MT9M114EBLSTCZH-GEVB

MT9M114 Evaluation Board User's Manual

ON Semiconductor®

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EVAL BOARD USER'S MANUAL

Evaluation Board Overview

The evaluation boards are designed to demonstrate the features of ON Semiconductor's image sensors products. This headboard is intended to plug directly into the Demo 2X system. Test points and jumpers on the board provide access to the clock, I/Os, and other miscellaneous signals.

Features

- Clock Input
  - Default – 24 MHz Crystal Oscillator
  - Optional Demo 2X Controlled MClk
- Two Wire Serial Interface
  - Selectable Base Address
- Parallel Interface
- MIPI Interface
- ROHS Compliant

Figure 1. MT9M114 Evaluation Board

Block Diagram

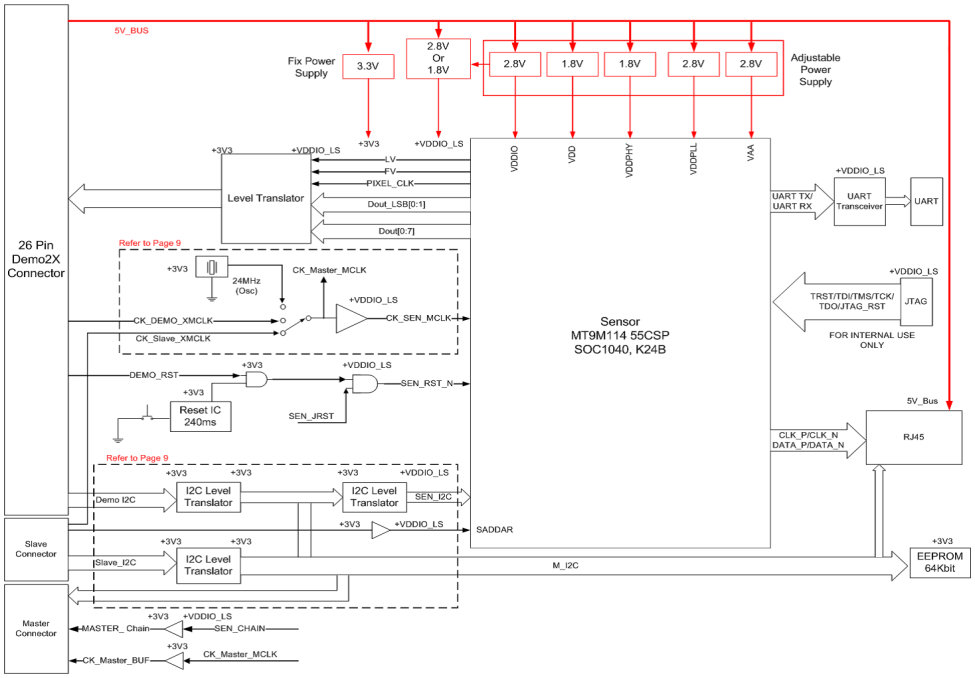

Figure 2. Block Diagram of MT9M114EBLSTCZH−GEVB

Top View

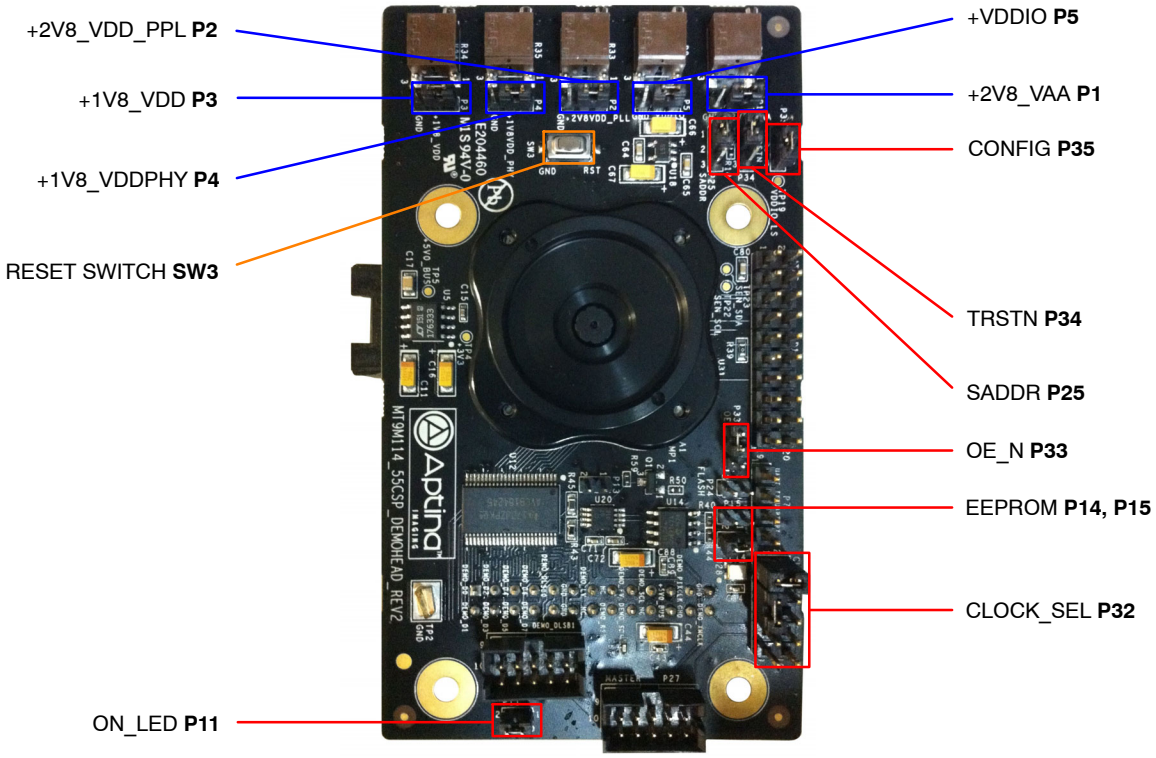

Figure 3. Top View of Evaluation Board − Default Jumpers

Bottom View

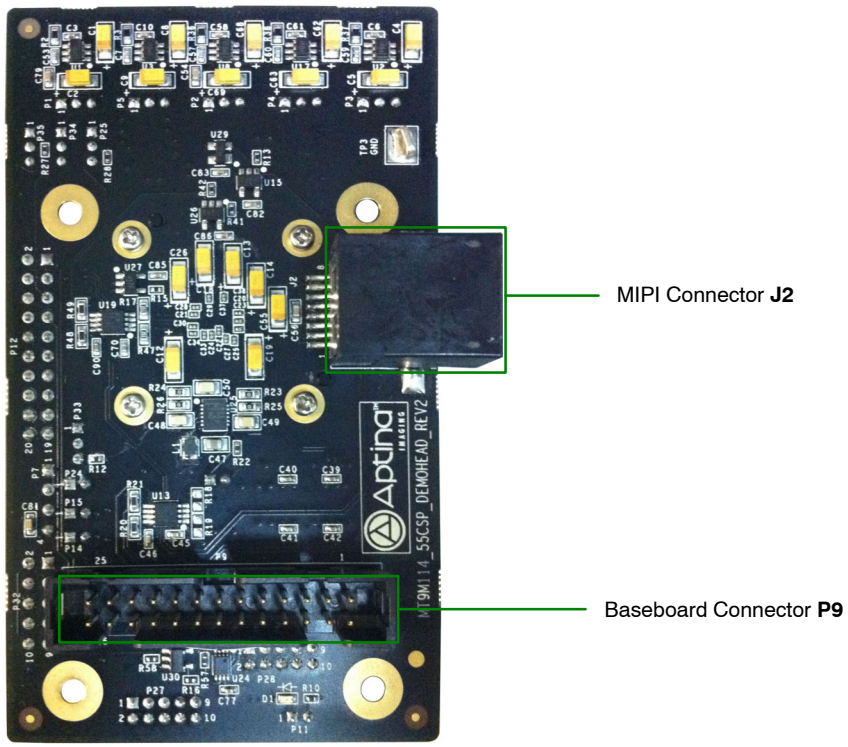

Figure 4. Bottom View of the Evaluation Board − Connectors

Jumper Pin Locations

The jumpers on headboards start with Pin 1 on the leftmost side of the pin. Grouped jumpers increase in pin size with each jumper added.

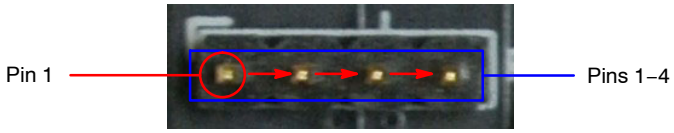

Figure 5. Pin Locations for a Single Jumper. Pin 1 is Located at the Leftmost Side and Increases as it Moves to the Right

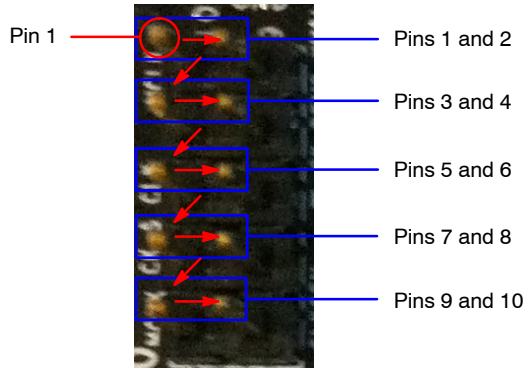

Figure 6. Pin Locations and Assignments of Grouped Jumpers. Pin 1 is Located at the Top-Left Corner and Increases in a Zigzag Fashion Shown in the Picture

Jumper/Header Functions & Default Positions

Table 1. JUMPERS AND HEADERS

Jumper/Header No.	Jumper/Header Name	Pins	Description
P1	+0V4_VDD_SLVS	2−3 (Default)	Connects to on-board +0V4_VDD_SLVS power supply
		1−2	External power supply connection
P1	+2V8_VAA	2−3 (Default)	Connects to on-board +2V8_VAA power supply
		1−2	External power supply connection
P2	+2V8_VDDPLL	2−3 (Default)	Connects to on-board +2V8_VDDPLL power supply
		1−2	External power supply connection
P3	+1V8_VDD	2−3 (Default)	Connects to on-board +1V8_VDD power supply
		1−2	External power supply connection
P4	+1V8_VDDPHY	2−3 (Default)	Connects to on-board +1V8_VDDPHY power supply
		1−2	External power supply connection
P5	+VDDIO	2−3 (Default)	Connects to on-board +VDDIO power supply
		1−2	External power supply connection
P11	ON_LED	1−2 (Default)	Connects to on-board LED to indicate “power on”

Table 1. JUMPERS AND HEADERS (continued)

Jumper/Header No.	Jumper/Header Name	Pins	Description
P14, P15	EEPROM ADDR	P15 Closed, P16 Open (Default)	EEPROM Address set to 0xA8
		P15 Open, P16 Open	EEPROM Address set to 0xAC
		P15 Open, P16 Closed	EEPROM Address set to 0xA4
		P15 Closed, P16 Closed	EEPROM Address set to 0xA0
P8	+2V8_VAAPIX	2−3 (Default)	Connects to on-board +2V8_VAAPIX power supply
		1−2	External power supply connection
P9	+0V4_VDD_SLVS	2−3 (Default)	Connects to on-board +0V4_VDD_SLVS power supply
		1−2	External power supply connection
P25	SADDR	1−2 (Default)	I^2C address set to 0x90
		2−3	I^2C address set to 0xBA
P32	CLOCK_SEL	3−5, 1−3 (Default)	Master mode on-board oscillator
		7−5, 9−10	Master mode Demo 2X Clock
		6−5, 4−2	Slave mode clock from master sensor unit
P33	OE_N	1−2 (Default)	Normal mode
		2−3	Suspend state
P34	TRSTN	1−2 (Default)	Normal mode
		2−3	Test mode
P35	CONFIG	1−2 (Default)	Normal mode
		2−3	Test mode
SW10	RESET	N/A	When pushed, 380 ms reset signal will be sent to MT9M114

Interfacing to ON Semiconductor Demo 2X Baseboard

The ON Semiconductor Demo 2X baseboard has a similar 26-pin connector and 13-pin connector which mate with J3 and J4 of the headboard. The four mounting holes secure the baseboard and the headboard with spacers and screws.

ADDITIONAL INFORMATION

TECHNICAL PUBLICATIONS:
Technical Library: www.onsemi.com/design/resources/technical−documentation
onsemi Website: www.onsemi.com

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