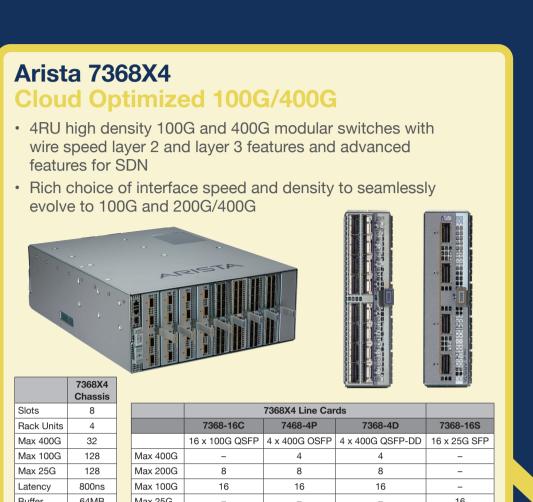
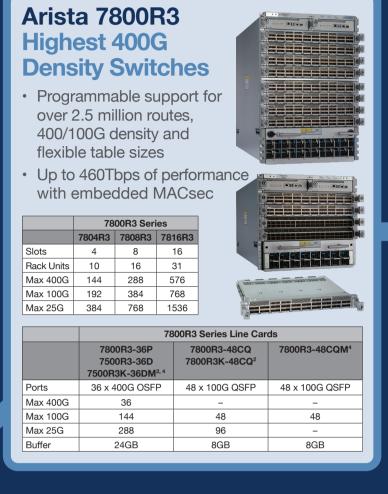
ARISTA Networking Platforms

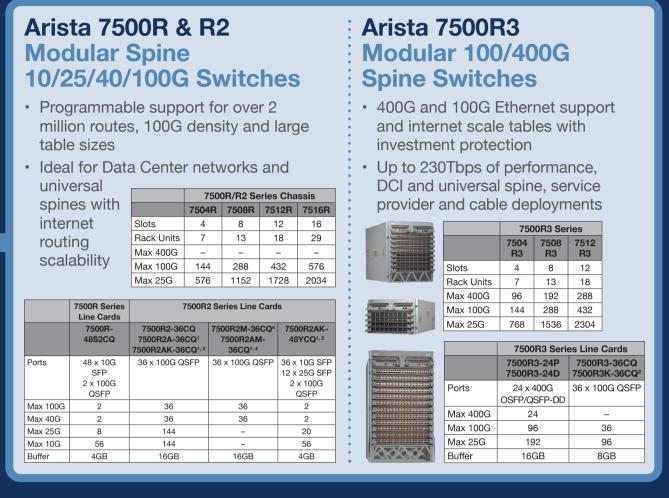
Why Arista? Quality • Innovation • Engineering Excellence • Evolution • Openness

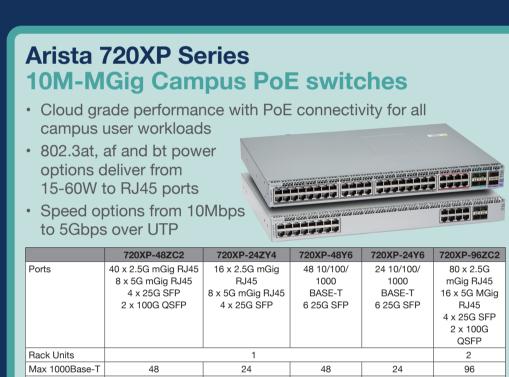


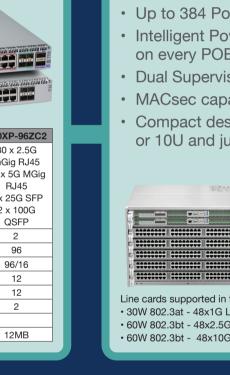




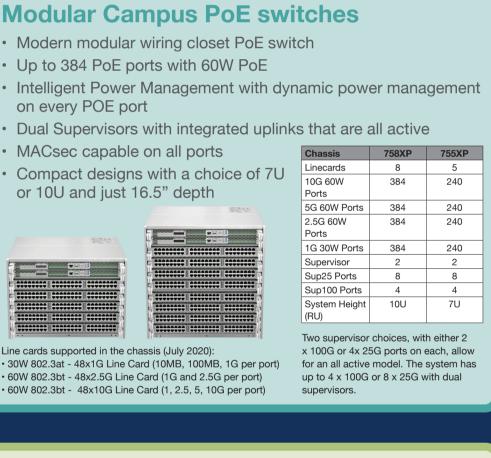


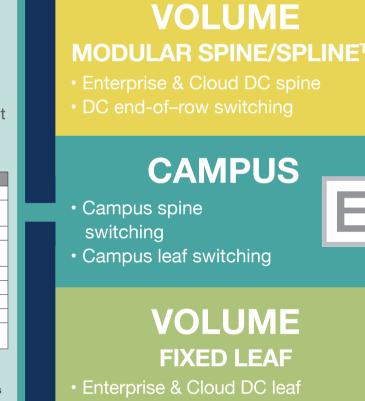






Arista 750XP Series





Smaller DC spine

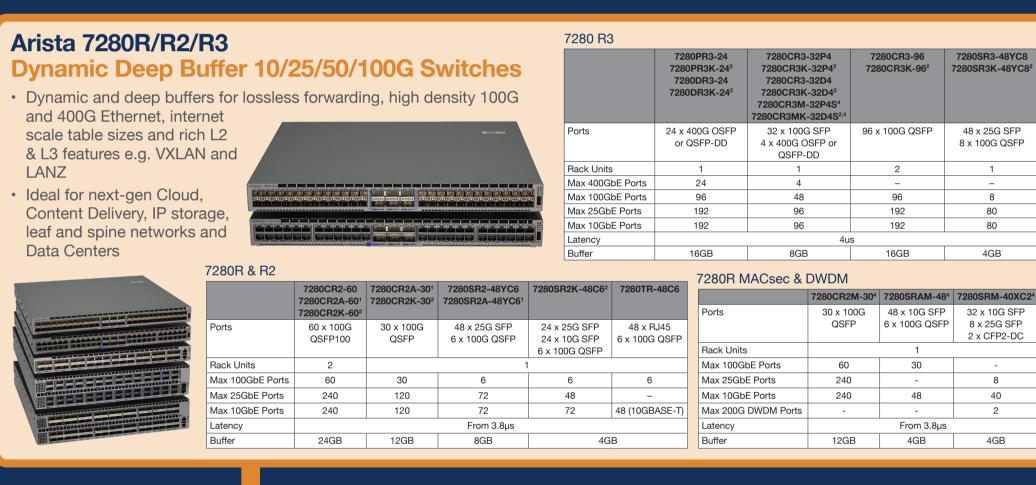


SP core

MODULAR SPINE/SPLINE™

Hyperscale Cloud DC spine

· Hyperscale Cloud DC edge



Arista 7060X

Max 2.5G/5G

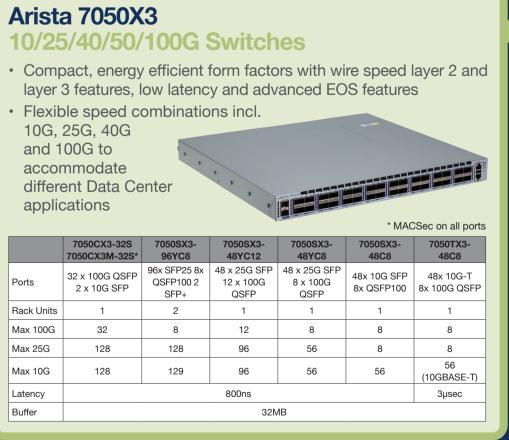
Max 100G

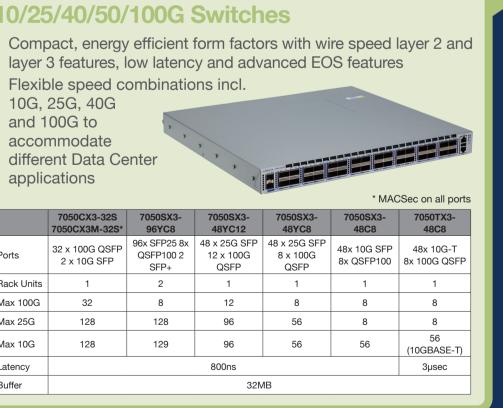
Cloud Optimised 10/25/40/50/100G Switches

• 1RU and 2RU fixed configuration 25G, 40G, 100G and 400G Data Center switches with wire speed layer 2 and layer 3 features

Rich choice of interface speed and density to seamlessly evolve from 10G/40G to 25G/100G and 200G/400G

		,		
	7060PX4-32	7060DX4-32		
Ports	32 x 400G OSFP 2 x SFP	32 x QSFP-DD 2 x SFP		
Rack Units	1	1		
Max 400G	32	32		
Max 100G	128	128		
Latency	700ns			
Buffer	64MB			
	7060CX2-32S	7060SX2-48YC6	7260CX3-64	
Ports	32 x 100G QSFP	48 x 25G SFP	64 x 100G QSFP	
	2 X 10G SFP	6 x 100G QSFP	2 X 10G SFP	
Rack Units	1	1	2	
Max 100G	32	6	64	
Max 25G	128	72	128	
Latanav	1	45000	·	







Arista

network-wide approach for

 Macro-Segmentation[™] built on Arista's open, Services (MSS) standards-based and fully programmable EOS® (Extensible Operating

controllers Topology View Hardware and Software Automated Network Abstraction Rollback

 State Streaming Telemetry for live monitoring and historic forensic troubleshooting

Features & Benefits:

Network

Centralized View of the

Integration with 3rd party

overlay and orchestration

 Full Change Management Framework Automated, zero touch

Provisioning and configuration management Smart System Upgrade (SSU) with a Network-Wide

Compliance Dashboard for

Security, Audit and patch management Open API Integration

Arista 713	80		
Ultra Low	latency L1	and FPGA	switches

switching in 4 ns MetaWatch Functionality MetaMux

Tapping, Tap aggregation and picosecond timestamping Data aggregation in 39 nanoseconds incl. Signal Low-latency firewall protection regeneration, Low-latency connection sharing between hosts ExchangeApp Inline timestamping enabling exchange fairness conversation, SwitchApp Low latency Layer 2 switching Port mirroring,

Telemetry, Dynamic patching/link management, Layer 1+ statistics on every link FPGA-enabled – Up to 3 Xilinx FPGAs on a single device

Powerful Network Applications

ı		7130 Connect Series	7130E Series	7130L/LB Series
ı	Description	Layer 1 Switch	FPGA switch	FPGA switch
	Ports	16/48/96	48	32/48/96
	#FPGA	None	Up to 3	1
ı	FPGA	NA	Xilinx Virtex® UltraScale+	Xilinx Virtex Ultrascale+®
ı	Off Chip RAM	NA	NA	Yes
	Clock Module	NA	NA	OCXO or Atomic



Arista 7010T 1GbE/10GbE Switching

· 48 100/1000BASE-T and four integrated 1/10G SFP ports

Non-blocking design with 40Gbps of uplink Power efficient solution in a compact footprint with scalable forwarding tables

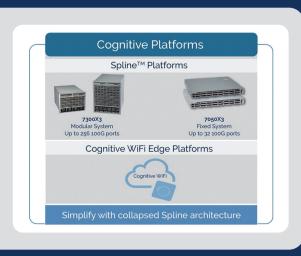
52 (48 RJ45 + 4 Max 10G Max 100G TITULE COLUMN CO

Cognitive Wi-Fi

Certifiable intelligent Wi-Fi 6 Access points

Cognitive Management

IE Universal Cloud Network



AI-Driven Network Detection and Response

AWAKE NUCLEUS Full Packet Analysis Machine Learning

Threat Detection (Adversarial Modeling™) Uncover the attack scope for insider and external threats

Situational Awareness (EntityIQTM) Discover, profile and track devices, users, applications and the supply chain

Al-based virtual security analyst to automate hunting, triage, investigation and response

Arista 7020R

1GbE/10GbE Switching

Dynamic and deep buffering with wirespeed 1/10G and 10/100G.

Features such as FlexRoute, VXLAN

1RU switches for storage, multimedia, content **Autonomous Security (AVATM)** delivery networks, edge compute and lossless Data Center designs



	7020SR-32C2	7020SR-24C2 7020SRG-24C2 ³	7020TR 7020TRA	
Ports	32 x 1/10G SFP 2 x 100G QSFP	24 x 1/10G SFP 2 x 100G QSFP	48 100/1000 BASE-T 6 10G SFP	
Rack Units	1			
Max 100G	2	2	-	
Max 25G	8	8	-	
Max 10G	32	24	6	
Max 1G	32	24	54 (48 RJ45 + 6 x SFP)	
Latency	Under 3.8µs			
Buffer	3GB			

7010T

48 10/100/1000

BASE-T

4 10G SFP