

MIC2920X

400 mA Low Dropout Regulators

Features

- · High Output Voltage Accuracy
- · Ensured 400 mA Output
- · Low Quiescent Current
- · Low Dropout Voltage
- · Extremely Tight Load and Line Regulation
- · Very Low Temperature Coefficient
- · Current and Thermal Limiting
- Input Withstands –20V Reverse Battery and +60V Positive Transients
- · Error Flag Warns of Output Dropout
- · Logic-Controlled Electronic Shutdown
- Output Programmable from 1.24V to 26V (MIC29202/MIC29204)
- Available in TO-220-3, TO-220-5, and Surface-Mount TO-263-5, SOT-223, and SOIC-8 Packages

Applications

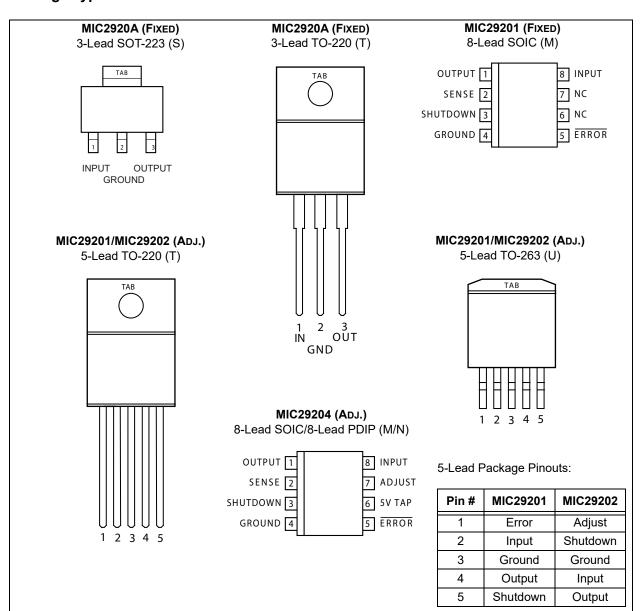
- · Battery-Powered Equipment
- · Cellular Telephones
- · Laptop, Notebook, and Palmtop Computers
- PCMCIA V_{CC} and V_{PP} Regulation/Switching
- · Barcode Scanners
- · Automotive Electronics
- SMPS Post-Regulators
- Voltage Reference
- · High-Efficiency Linear Power Supplies

General Description

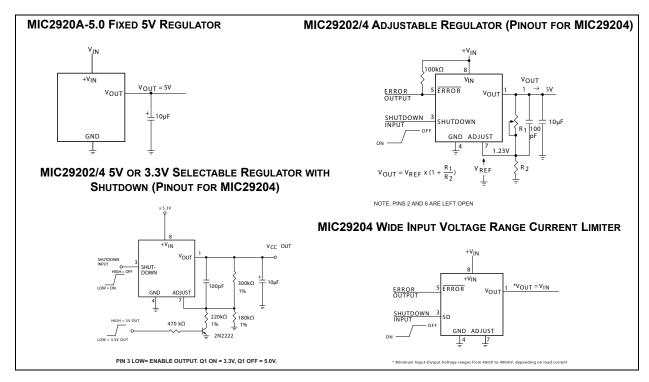
The MIC2920 family (MIC2920A, MIC29201, MIC29202, and MIC29204) are efficient voltage regulators with very low dropout voltage (typically 40 mV at light loads and 370 mV at 250 mA) and very low quiescent current (140 µA typical). The quiescent current of the MIC2920A increases only slightly in dropout, prolonging battery life. Key MIC2920A features include protection against reversed battery, fold-back current limiting, and automotive "load dump" protection (60V positive transient).

The MIC2920 family of devices are available in several configurations. The MIC2920A-x.x devices are 3-lead fixed-voltage regulators available in 3.3V, 4.85V, 5V, and 12V outputs. The MIC29201 is a fixed-voltage regulator that offers a logic-compatible ON/OFF (shutdown) input and an error flag output. This flag may also be used as a power-on reset signal. A logic-compatible shutdown input is provided on the adjustable MIC29202, which allows the regulator to be switched on and off. The MIC29204 8-lead SOIC adjustable regulator includes both shutdown and error flag pins and may be pin-strapped for 5V output or programmed from 1.24V to 26V using two external resistors.

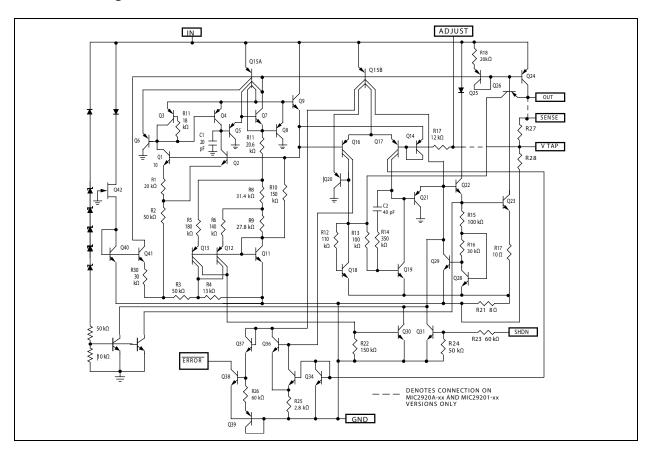
Package Types



Typical Application Circuits



Schematic Diagram



1.0 ELECTRICAL CHARACTERISTICS

Absolute Maximum Ratings †

Input Supply Voltage	–20V to +60V
Adjust Input Voltage (Note 1, Note 2)	
Power Dissipation (Note 3)	Internally Limited

Operating Ratings ‡

Operating Input Supply Voltage (Note 4)	+2V to +26V
Adjust Input Voltage (Note 1, Note 2)	
Shutdown Input Voltage	
Error Comparator Output Voltage	

- **† Notice:** Stresses above those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. This is a stress rating only and functional operation of the device at those or any other conditions above those indicated in the operational sections of this specification is not intended. Exposure to maximum rating conditions for extended periods may affect device reliability.
- **‡ Notice:** The device is not guaranteed to function outside its operating ratings.
 - Note 1: Comparator thresholds are expressed in terms of a voltage differential at the Adjust terminal below the nominal reference voltage measured at 6V input. To express these thresholds in terms of output voltage change, multiply by the error amplifier gain = V_{OUT}/V_{REF} = (R1 + R2)/R2. For example, at a programmed output voltage of 5V, the Error output is ensured to go low when the output drops by 95 mV x 5V/1.235V = 384 mV. Thresholds remain constant as a percent of V_{OUT} as V_{OUT} is varied, with the dropout warning occurring at typically 5% below nominal, 7.7% ensured.
 - 2: V_{SHUTDOWN} ≥ 2V, V_{IN} ≤ 26V, V_{OUT} = 0, with the Adjust pin tied to 5V Tap or to the R1, R2 junction (see the MIC29202/29204 Adjustable Regulator in Typical Application Circuits) with R1 ≥ 150 kΩ.
 - 3: The maximum allowable power dissipation is a function of the maximum junction temperature, $T_{J(MAX)}$, the junction-to-ambient thermal resistance, θ_{JA} , and the ambient temperature, T_A . The maximum allowable power dissipation at any ambient temperature is calculated using: $P_{(MAX)} = (T_{J(MAX)} T_A) \div \theta_{JA}$. Exceeding the maximum allowable power dissipation will result in excessive die temperature, and the regulator will go into thermal shutdown.
 - **4:** Across the full operating temperature, the minimum input voltage range for full output current is 4.3V to 26V. Output will remain in-regulation at lower output voltages and low current loads down to an input of 2V at 25°C.

ELECTRICAL CHARACTERISTICS

Electrical Characteristics: Limits in standard typeface are for T_J = +25°C and limits in **boldface** apply over the full operating temperature range. Unless otherwise specified, V_{IN} = V_{OUT} + 1V, I_L = 1 mA, C_L = 10 μ F. Adjustable versions are set for an output of 5V. The MIC29202 $V_{SHUTDOWN} \le 0.7V$. The 8-lead MIC29204 is configured with the Adjust pin tied to the 5V Tap, the Output is tied to Output Sense (V_{OUT} = 5V), and $V_{SHUTDOWN} \le 0.7V$. (Note 1)

Parameter	Symbol	Min.	Тур.	Max.	Units	Conditions	
		-1	_	1		Variation from factory trimmed V _{OUT}	
		-2	_	2		variation from factory triffined v _{OUT}	
Output Voltage Accuracy	\/	-2.5	_	2.5	%	1 mA ≤ I _L ≤ 400 mA, across temp range	
Output Voltage Accuracy	Vo	-1.5		1.5	70	MIC2920A-12 and MIC29201-12 only	
		- 3		3		WIG2920A-12 and WIG29201-12 Only	
		-4		4		1 mA ≤ I _L ≤ 400 mA, across temp range	
Output Voltage	ΔV _Ο /ΔΤ		20	100	ppm/°C	Note 2	
Temperature Coefficient	Δν0/Δ1	_	80	350	ррпі/ С	V _{OUT} > 10V only	
Line Regulation	$\Delta V_{O}/V_{O}$	_	0.03	0.10	%	V _{IN} = V _{OUT} + 1V to 26V	
Line Negulation	Δν0/ν0		_	0.40	/0	VIN - VOUT + 1V to 20V	
Load Regulation	$\Delta V_{O}/V_{O}$		0.04	0.16	%	I _I = 1 mA to 250 mA (Note 3)	
Load Negulation	Δν ₀ /ν ₀	_	_	0.30	/0	IL = 1 IIIA to 230 IIIA (Note 3)	
		_	100	150		- 1 m A	
			_	180		I _L = 1 mA	
	$V_{IN} - V_{O}$	_	250		mV	I _L = 100 mA	
Dropout Voltage (Note 4)		_	350	_		V _{OUT} > 10V only	
Dropout Voltage (Note 4)		_	370	_		I _L = 250 mA	
		_	500			V _{OUT} > 10V only	
		_	400	600		L = 400 mA	
		_	_	750		I _L = 400 mA	
		_	140	200		- 1 m A	
		_	_	300	μA	I _L = 1 mA	
0 15: 0 1		_	1.3	2		L = 100 mA	
Ground Pin Current (Note 5)	I_{GND}	_	_	2.5		I _L = 100 mA	
(Note 3)		_	5	9	mA	L = 250 mA	
		_	_	12		I _L = 250 mA	
		_	13	15		I _L = 400 mA	
Ground Pin Current at Dropout (Note 5)	I _{GNDDO}	_	180	400	μA	V_{IN} = 0.5V less than designed V_{OUT} , $(V_{\text{OUT}}$ = 3.3V), I_{O} = 1 mA	
0 11: :1			425	100		V _{OUT} = 0V	
Current Limit	I _{LIMIT}	_	_	1200	mA	Note 6	
Thermal Regulation	$\Delta V_{O}/\Delta P_{D}$	_	0.05	0.2	%/W	Note 7	
Output Noise Voltage		_	400	_		C _L = 10 μF	
(10 Hz to 100 kHz), I _L = 100 mA	e _n	_	260	_	μV _{RMS}	CL = 100 μF	

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ELECTRICAL CHARACTERISTICS (CONTINUED)

Electrical Characteristics: Limits in standard typeface are for T_J = +25°C and limits in **boldface** apply over the full operating temperature range. Unless otherwise specified, V_{IN} = V_{OUT} + 1V, I_L = 1 mA, C_L = 10 μ F. Adjustable versions are set for an output of 5V. The MIC29202 $V_{SHUTDOWN} \le 0.7$ V. The 8-lead MIC29204 is configured with the Adjust pin tied to the 5V Tap, the Output is tied to Output Sense (V_{OUT} = 5V), and $V_{SHUTDOWN} \le 0.7$ V. (Note 1)

	Parameter Symbol Min. Typ. Max. Units Conditions									
Parameter	Symbol	Min.	Тур.	Max.	Units	Conditions				
MIC29202, MIC29204										
Reference Voltage	\/	1.223	1.235	1.247	V	MIC29202				
Treference voltage	V _{REF}	1.210	_	1.260	V	INIGESEUZ				
Reference Voltage	V_{REF}	1.204	_	1.266	V	MIC29202, Note 8				
Reference Voltage	\/	1.210	1.235	1.260	V	MIC29204				
Reference voltage	V _{REF}	1.200		1.270	V	IVIIG29204				
Reference Voltage	V_{REF}	1.185		1.285	V	MIC29204, Note 8				
Adjust Pin Bias Current	1	_	20	40	nA					
Aujust Fili bias Cullent	I _{BIAS}			60	IIA					
Reference Voltage Temperature Coefficient	$\Delta V_{REF}/\Delta T$		20		ppm/°C	Note 7				
Adjust Pin Bias Current Temperature Coefficient	ΔI _{BIAS} /ΔΤ	_	0.1	_	nA/°C	_				
Error Comparator MIC29	201, MIC29	204								
Output Leakage Current		_	0.01	1.00	μA	\/ - 20\/				
Output Leakage Current	_			2.00	μΑ	V _{OH} = 26V				
Output Low Voltage	\/		150	250	mV	V = 4.5V L = 250 uA				
Output Low Voltage	V _{OL}	_		400	IIIV	$V_{IN} = 4.5V$, $I_{OL} = 250 \mu A$				
Upper Threshold Voltage	\ <u>\</u>	40	60		mV	Note 9				
Opper Threshold voltage	V _{UTH}	25	_	_	IIIV	NOIG 3				
Lower Threshold Veltage	V	_	75	95	mV	Note 0				
Lower Threshold Voltage	V_{LTH}	_	_	140	IIIV	Note 9				
Hysteresis	HYS	_	15	_	mV	Note 9				

ELECTRICAL CHARACTERISTICS (CONTINUED)

Electrical Characteristics: Limits in standard typeface are for T_J = +25°C and limits in **boldface** apply over the full operating temperature range. Unless otherwise specified, V_{IN} = V_{OUT} + 1V, I_L = 1 mA, C_L = 10 μ F. Adjustable versions are set for an output of 5V. The MIC29202 $V_{SHUTDOWN} \le 0.7$ V. The 8-lead MIC29204 is configured with the Adjust pin tied to the 5V Tap, the Output is tied to Output Sense (V_{OUT} = 5V), and $V_{SHUTDOWN} \le 0.7$ V. (Note 1)

, i	1, - 1		- 1	,	001 - 7	7 SHOTDOWN - 1	
Parameter	Symbol	Min.	Тур.	Max.	Units	Conditions	
Shutdown Input MIC2920)1, MIC2920	2, MIC29	9204				
		_	1.3	_		_	
Input Logic Voltage	_	_	_	0.7	V	Low (ON)	
		2.0	_	_		High (OFF)	
	I _{IN(SHDN)}	_	30	50	μΑ	- 2.41/	
Shutdown Pin Input		_	_	100		V _{SHUTDOWN} = 2.4V	
Current		_	450	600		- 26V	
		_	_	750		V _{SHUTDOWN} = 26V	
Regulator Output Current		_	3	10		Note 10	
in Shutdown		_	_	20	μA	Note 10	

- Note 1: Devices are ESD protected. However, handling precautions are recommended.
 - **2:** Output voltage temperature coefficient is defined as the worst case voltage change divided by the total temperature range.
 - 3: Regulation is measured at constant junction temperature using low duty cycle pulse testing. Changes in output voltage due to heating effects are covered by the thermal regulation specification.
 - **4:** Dropout Voltage is defined as the input to output differential at which the output voltage drops 100 mV below its nominal value measured at 1V differential. At low values of programmed output voltage, the minimum input supply voltage of 4.3V over temperature must be taken into account. The MIC2920A operates down to 2V of input at reduced output current at 25°C.
 - 5: Ground pin current is the regulator quiescent current. The total current drawn from the supply is the sum of the load current plus the ground pin current.
 - **6:** The MIC2920A features fold-back current limiting. The short circuit (V_{OUT} = 0V) current limit is less than the maximum current with normal output voltage.
 - 7: Thermal regulation is defined as the change in output voltage at a time "t" after a change in power dissipation is applied, excluding load or line regulation effects. Specifications are for a 200 mA load pulse at V_{IN} = 20V (a 4W pulse) for t = 10 ms.
 - 8: $V_{REF} \le V_{OUT} \le (V_{IN} 1V)$, $4.3V \le V_{IN} \le 26V$, $1 \text{ mA} < I_L \le 400 \text{ mA}$, $T_J \le T_{J(MAX)}$.
 - 9: Comparator thresholds are expressed in terms of a voltage differential at the Adjust terminal below the nominal reference voltage measured at 6V input. To express these thresholds in terms of output voltage change, multiply by the error amplifier gain = V_{OUT}/V_{REF} = (R1 + R2)/R2. For example, at a programmed output voltage of 5V, the Error output is guaranteed to go low when the output drops by 95 mV x 5V/1.235V = 384 mV. Thresholds remain constant as a percent of V_{OUT} as V_{OUT} is varied, with the drop-out warning occurring at typically 5% below nominal, 7.7% ensured.
 - **10:** $V_{SHUTDOWN} \ge 2V$, $V_{IN} \le 26V$, $V_{OUT} = 0$, with the Adjust pin tied to 5V Tap or to the R1, R2 junction (see the MIC29202/29204 Adjustable Regulator in Typical Application Circuits) with R1 ≥ 150 kΩ.
 - **11:** When used in dual supply systems where the regulator load is returned to a negative supply, the output voltage must be diode clamped to ground.
 - **12:** Maximum positive supply voltage of 60V must be of limited duration (<100 ms) and duty cycle (≤1%). The maximum continuous supply voltage is 26V.

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TEMPERATURE SPECIFICATIONS (Note 1)

Parameters	Sym.	Min.	Тур.	Max.	Units	Conditions		
Temperature Ranges								
Junction Temperature Range	TJ	-4 0	_	+125	°C	_		
Lead Temperature	_	_	_	+260	°C	Soldering, 5 sec.		
Package Thermal Resistance								
Thermal Resistance, SOT-223	θ_{JC}	_	15	_	°C/W	_		
Thermal Resistance, TO-220	θ_{JC}	_	3	_	°C/W	_		
Thermal Resistance, TO-263	θ_{JC}	_	3	_	°C/W	_		
Thermal Resistance, 8-Ld SOIC	θ_{JA}	_	160	_	°C/W	_		

Note 1: The maximum allowable power dissipation is a function of ambient temperature, the maximum allowable junction temperature and the thermal resistance from junction to air (i.e., T_A, T_J, θ_{JA}). Exceeding the maximum allowable power dissipation will cause the device operating junction temperature to exceed the maximum +125°C rating. Sustained junction temperatures above +125°C can impact the device reliability.

2.0 TYPICAL PERFORMANCE CURVES

Note:

The graphs and tables provided following this note are a statistical summary based on a limited number of samples and are provided for informational purposes only. The performance characteristics listed herein are not tested or guaranteed. In some graphs or tables, the data presented may be outside the specified operating range (e.g., outside specified power supply range) and therefore outside the warranted range.

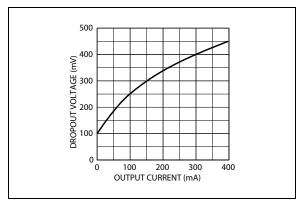


FIGURE 2-1: Dropout Voltage vs. Output Current.

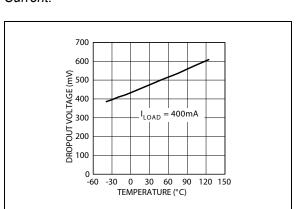


FIGURE 2-2: Dropout Voltage vs. Temperature.

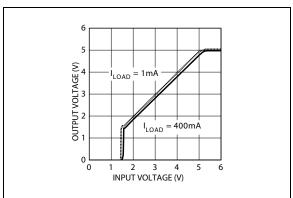


FIGURE 2-3: Dropout Characteristics.

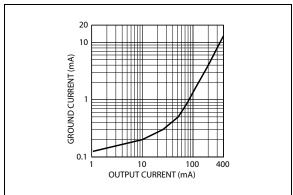


FIGURE 2-4: Ground Current vs. Output Current.

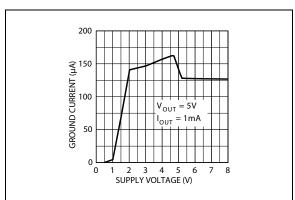


FIGURE 2-5: Ground Current vs. Supply Voltage.

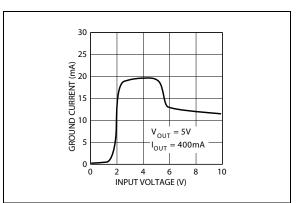


FIGURE 2-6: Output Voltage vs. Supply Voltage.

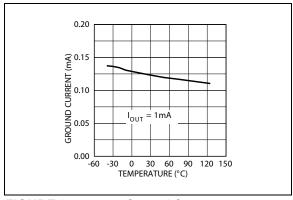


FIGURE 2-7: Temperature.

Ground Current vs.

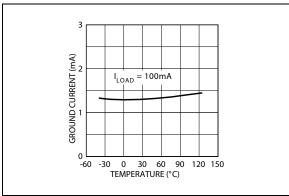


FIGURE 2-8: Temperature.

Ground Current vs.

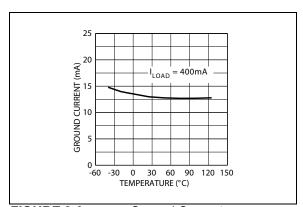


FIGURE 2-9: Temperature.

Ground Current vs.

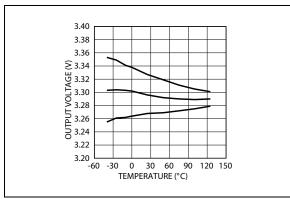


FIGURE 2-10:

Fixed 3.3V Output Voltage

vs. Temperature.

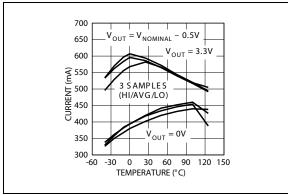


FIGURE 2-11: Short Circuit and Maximum Current vs. Temperature.

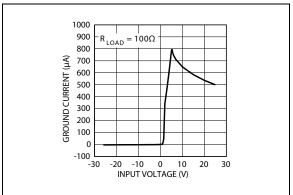


FIGURE 2-12: Voltage.

Ground Current vs. Supply

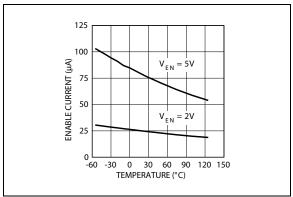


FIGURE 2-13: MIC29201/2 Shutdown Current vs. Temperature.

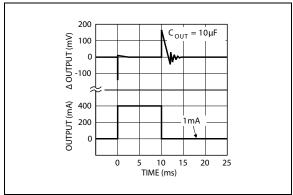


FIGURE 2-14: Load Transient.

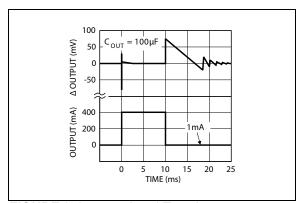


FIGURE 2-15: Load Transient.

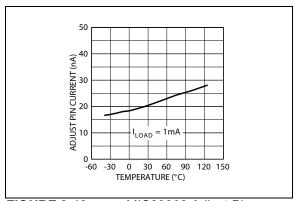


FIGURE 2-16: MIC29202 Adjust Pin Current vs. Temperature.

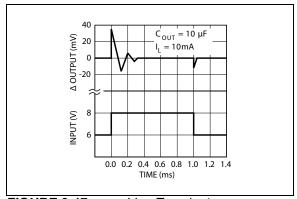


FIGURE 2-17: Line Transient.

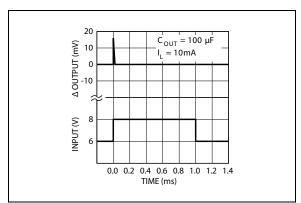


FIGURE 2-18: Line Transient.

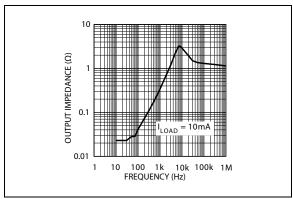


FIGURE 2-19: Frequency.

Output Impedance vs.

FIGURE 2-20:

Ripple Rejection.

3.0 PIN DESCRIPTIONS

The descriptions of the pins are listed in Table 3-1 through Table 3-4.

TABLE 3-1: MIC2920A PIN FUNCTION TABLE

Pin Number SOT-223/TO-220	Pin Name	Description
1	INPUT	Input Voltage Pin. V_{IN} between 2V and 26V at +25°C and between 4.3V and 26V across the full operating temperature range.
2	GROUND	Ground Pin.
3	OUTPUT	Output Voltage Pin. Can be 3.3V, 4.85V, 5V, and 12V depending of the variant of MIC2920A-x.x.

TABLE 3-2: MIC29201 PIN FUNCTION TABLE

Pin Number Fixed SOIC-8	Pin Number TO-220/TO-263	Pin Name	Description
1	4	OUTPUT	Output Voltage Pin. Can be 3.3V, 4.85V, 5V, and 12V depending of the variant of MIC29201-x.x.
2	_	SENSE	Input Pin. Must be connected to V _{OUT} (Pin 1) to ensure proper operation. The connection is not made internally.
3	5	SHUTDOWN	Shutdown Pin. Offers a logic-compatible ON/OFF input.
4	3	GROUND	Ground Pin.
5	1	ERROR	Error Pin. An output pin used as an error flag output. Can be used also as a power-on reset signal.
6	_	NC	Not connected.
7	_	NC	Not connected.
8	2	INPUT	Input Voltage Pin. V _{IN} between 2V and 26V at +25°C and between 4.3V and 26V across the full operating temperature range.

TABLE 3-3: MIC29202 PIN FUNCTION TABLE

Pin Number TO-263-5	Pin Number TO-220-5	Pin Name	Description
1	1	ADJUST	ADJUST is an input pin used to set the output voltage V _{OUT} from 1.24V to 26V using two external resistors.
2	2	SHUTDOWN	Shutdown Pin. Offers a logic-compatible ON/OFF input.
3	3	GROUND	Ground Pin.
4	4	INPUT	Input Voltage Pin. V _{IN} between 2V and 26V at +25°C and between 4.3V and 26V across the full operating temperature range.
5	5	OUTPUT	Output Pin. Programmable from 1.24V to 26V using two external resistors.

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TABLE 3-4: MIC29204 PIN FUNCTION TABLE

Pin Number Adj. SOIC-8	Pin Name	Description
1	OUTPUT	Output Pin. Programmable from 1.24V to 26V using two external resistors.
2	SENSE	Input Pin. Must be connected to V _{OUT} (Pin 1) to ensure proper operation. The connection is not made internally. It is used for connecting the internal resistor divider (which is not connected internally) necessary to set output to 5V by using 5V TAP (Pin 6).
3	SHUTDOWN	Shutdown Pin. Offers a logic-compatible ON/OFF input.
4	GROUND	Ground Pin.
5	ERROR	Error Pin. An output pin used as an error flag output. Can be used also as a power-on reset signal.
6	5V TAP	Input pin used to set the output voltage to 5V by using internal resistor divider by tying Pin 1 (Output) to Pin 2 (Sense) and Pin 7 (Adjust) to Pin 6 (5V Tap).
7	ADJUST	ADJUST is an input pin used to set the output voltage V _{OUT} from 1.24V to 26V using two external resistors.
8	INPUT	Input Voltage Pin. V _{IN} between 2V and 26V at +25°C and between 4.3V and 26V across the full operating temperature range.

4.0 APPLICATIONS INFORMATION

4.1 External Capacitors

A 10 μ F (or greater) capacitor is required between the MIC2920A output and ground to prevent oscillations due to instability. Most types of tantalum or aluminum electrolytics will be adequate; film types will work, but are costly and therefore not recommended. Many aluminum electrolytics have electrolytes that freeze at about –30°C, so solid tantalums are recommended for operation below –25°C. The important parameters of the capacitor are an effective series resistance of about 5 Ω or less and a resonant frequency above 500 kHz. The value of this capacitor may be increased without limit.

At lower values of output current, less output capacitance is required for output stability. The capacitor can be reduced to 2.2 μ F for current below 10 mA or 1 μ F for currents below 1 mA. Adjusting the MIC29202/29204 to voltages below 5V runs the error amplifier at lower gains so that more output capacitance is needed. For the worst-case situation of a 500 mA load at 1.23V output (Output shorted to Adjust) a 47 μ F (or greater) capacitor should be used.

The MIC2920A/29201 will remain in regulation with a minimum load of 1 mA. When setting the output voltage of the MIC29202/29204 versions with external resistors, the current through these resistors may be included as a portion of the minimum load.

A 0.1 μ F capacitor should be placed from the MIC2920A input to ground if there is more than 10 inches of wire between the input and the AC filter capacitor or if a battery is used as the input.

4.2 Error Detection Comparator Output (MIC29201/MIC29204)

A logic low output will be produced by the comparator whenever the MIC29201/29204 output falls out of regulation by more than approximately 5%. This figure is the comparator's built-in offset of about 75 mV divided by the 1.235V reference voltage. This trip level remains 5% below normal regardless of the programmed output voltage of the MIC29201/29204. For example, the error flag trip level is typically 4.75V for a 5V output or 11.4V for a 12V output. The out of regulation condition may be due either to low input voltage, extremely high input voltage, current limiting, or thermal limiting.

Figure 4-1 is a timing diagram depicting the ERROR signal and the regulated output voltage as the MIC29201/29204 input is ramped up and down. The ERROR signal becomes valid (low) at about 1.3V input. It goes high at about 5V input (the input voltage at which V_{OUT} = 4.75). Because the MIC29201/29204's dropout voltage is load-dependent (see curve in Typical Performance Characteristics), the input voltage trip

point (about 5V) will vary with the load current. The output voltage trip point (approximately 4.75V) does not vary with load.

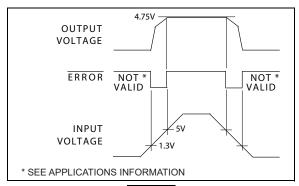


FIGURE 4-1: ERROR Output Timing.

The error comparator has an NPN open-collector output that requires an external pull-up resistor. Depending on system requirements, this resistor may be returned to the 5V output or some other supply voltage. In determining a value for this resistor, note that while the output is rated to sink 250 μA , this sink current adds to battery drain in a low battery condition. Suggested values range from 100 k Ω to 1 M Ω . The resistor is not required if this output is unused.

4.3 Programming the Output Voltage (MIC29202/MIC29204)

The MIC29202/29204 may be programmed for any output voltage between its 1.235V reference and its 26V maximum rating, using an external pair of resistors, as shown in the Typical Application Circuits.

The complete equation for the output voltage is:

EQUATION 4-1:

$$V_{OUT} = V_{REF} \times \left(1 + \frac{R1}{R2}\right) - \left|I_{FB}\right| \times R1$$

Where:

 V_{REF} = The nominal 1.235V reference voltage. I_{FB} = The Adjust pin bias current, nominally 20 nA.

The minimum recommended load current of 1 μ A forces an upper limit of 1.2 M Ω on the value of R2, if the regulator must work with no load (a condition often found in CMOS in standby), I_{FB} will produce a -2% typical error in V_{OUT} that may be eliminated at room temperature by trimming R1. For better accuracy, choosing R2 = 100 k Ω reduces this error to 0.17% while increasing the resistor program current to 12 μ A. Because the MIC29202/29204 typically draws 110 μ A at no load with SHUTDOWN open-circuited, this is a negligible addition. The MIC29204 may be

MIC2920X

pin-strapped for 5V using the internal voltage divider by tying Pin 1 (Output) to Pin 2 (Sense) and Pin 7 (Adjust) to Pin 6 (5V Tap).

4.4 Configuring the MIC29201-3.3YM

For the MIC29201-3.3YM, Output (Pin 1) and Sense (Pin 2), must be connected to ensure proper operation. They are not connected internally.

4.5 Reducing Output Noise

In reference applications, it may be advantageous to reduce the AC noise present at the output. One method is to reduce the regulator bandwidth by increasing the size of the output capacitor. This is relatively inefficient, as increasing the capacitor from 1 μF to 220 μF only decreases the noise from 430 μV_{RMS} to 160 μV_{RMS} for a 100 kHz bandwidth at 5V output. Noise can be reduced fourfold by a bypass capacitor across R1 because it reduces the high frequency gain from 4 to unity. Pick

EQUATION 4-2:

$$C_{BYPASS} = \frac{1}{2\pi R1 \times 200 Hz}$$

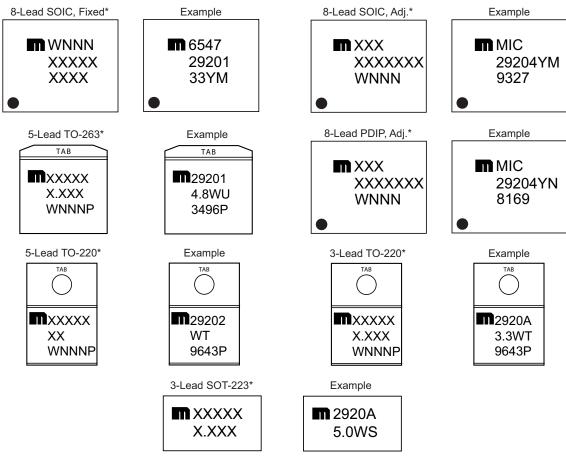
or about 0.01 µF. When doing this, the output capacitor must be increased to 10 µF to maintain stability. These changes reduce the output noise from 430 µV_{RMS} to 100 µV_{RMS} for a 100 kHz bandwidth at 5V output. With the bypass capacitor added, noise no longer scales with output voltage so that improvements are more dramatic at higher output voltages.

4.6 Automotive Applications

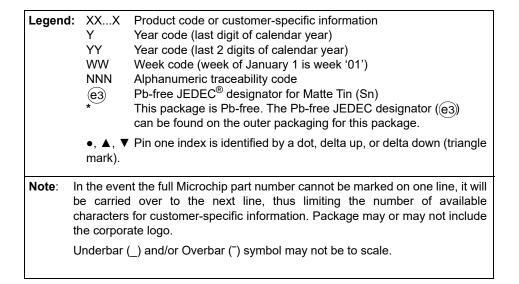
The MIC2920A is ideally suited for automotive applications for a variety of reasons. It will operate over a wide range of input voltages with very low dropout voltages (40 mV at light loads), and very low quiescent currents (100 μA typical). These features are necessary for use in battery-powered systems, such as automobiles. It is a robust device with the ability to survive both reverse battery (negative transients up to 20V below ground), and load dump (positive transients up to 60V) conditions. A wide operating temperature range with low temperature coefficients is yet another reason to use these versatile regulators in automotive designs.

5.0 PACKAGING INFORMATION

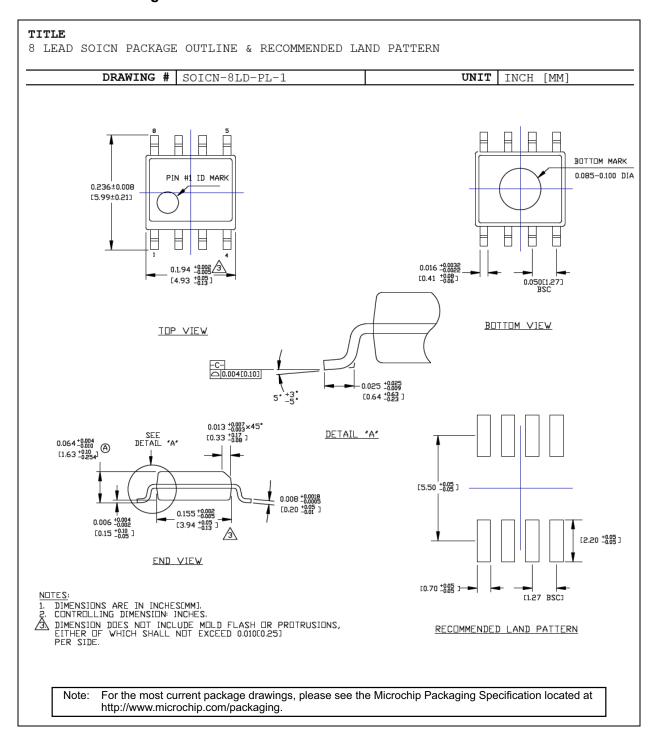
5.1 Package Marking Information



Some of the examples above have both fixed and adjustable versions. For the full list of fixed and adjustable options for these parts, be sure to see the Product Identification System.



8-Lead SOIC Package Outline and Recommended Land Pattern



8-Lead PDIP Package Outline and Recommended Land Pattern

TITLE 8 LEAD PDIP PACKAGE OUTLINE & RECOMMENDED LAND PATTERN DRAWING # PDIP-8LD-PL-1 UNIT INCH Lead Frame Copper Lead Finish | Matte Tin .375±.010 GAGE PLANE .015 .010 TYP R.010 MAX 0285±.005 .310 +.015 .120 MIN .150 MAX TOP VIEW END VIEW BASE MATERIAL SECTION A-A MAX ₽¥ 125 Ф .010**W** C .039 TYP .100 .010**₩** C SIDE VIEW For the most current package drawings, please see the Microchip Packaging Specification located at http://www.microchip.com/packaging.

5-Lead TO-263 Package Outline and Recommended Land Pattern

5 LEAD T0263 PACKAGE OUTLINE & RECOMMENDED LAND PATTERN DRAWING # T0263-5LD-PL-1 UNIT INCH/MM INCH MM PDS MIN MAX MIN 4.597 0.170 0.181 4.318 Α A1 0.000 0.012 0.000 0.305 0.026 0.036 0.660 0.914 0.023 0.305 0.012 0.584 C1 \bigcirc $_{\mathbb{A}}$ 0.330 0.361 8.392 9.169 θ1 0.396 0.420 10.058 10.668 1.575 0.062 0.072 1.829 0.045 0.055 1.143 1.397 \bigcirc A 0.575 0.625 14.605 15.875 Н J1 0.080 0.120 2.032 3,048 1.143 2.286 1.676 2.794 Κ 0.045 0.066 0.090 0.110 θ1 3° 10° 3° 10° θ 3 θ2 θ3 0° 8° 0° 8° θ4 18° 18° Gauge Plane 0.055 0,075 1,905 Q 1.397 TOP VIEW SIDE VIEW 1 U 0.256 Ref 6.502 Ref. V 0.305 Ref 7747 Ref θ1 11.18 9.91 1.32 **BOTTOM VIEW** SIDE VIEW 2 NOTE: 1. PACKAGE OUTLINE EXCLUSIVE OF MOLD FLASH & METAL RECOMMENDED LAND PATTERN 2. PACKAGE OUTLINE INCLUSIVE OF PLATING THICKNESS. (UNIT : mm) 3. FOOT LENGTH USING GAUGE PLANE METHOD MEASUREMENT

Note: For the most current package drawings, please see the Microchip Packaging Specification located at http://www.microchip.com/packaging.

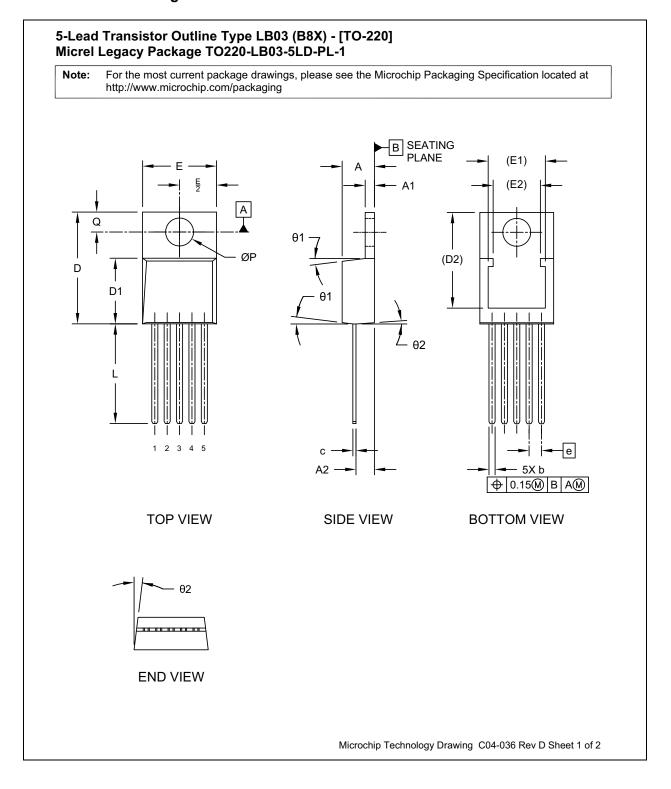
A PACKAGE TOP MARK MAY BE IN TOP CENTER OR LOWER

5. ALL DIMENSIONS ARE IN INCHES/MILLIMETERS.

0.010

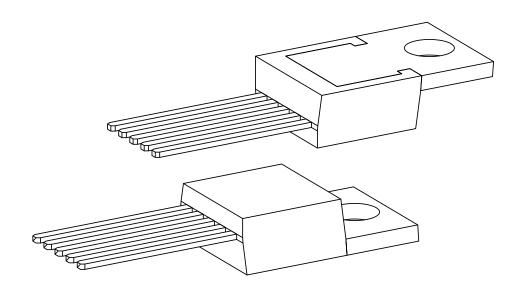
LEFT CORNER

5-Lead TO-220 Package Outline and Recommended Land Pattern



5-Lead Transistor Outline Type LB03 (B8X) - [TO-220] Micrel Legacy Package TO220-LB03-5LD-PL-1

For the most current package drawings, please see the Microchip Packaging Specification located at http://www.microchip.com/packaging



Dimension	Min	Min Nom Max			
Number of Leads	N		5		
Pitch	е		.067 BSC		
Overall Height	Α	.160	.175	.190	
Tab Height	A1	.045	.050	.055	
Seating Plane to Lead	A2	.080	.098	.115	
Lead Width	b	.025	.033	.040	
Lead Thickness	С	.012	.016	.020	
Lead Length	L	.500	.540	.580	
Total Body Length Including Tab	D	.542	.580	.619	
Molded Body Length	D1	.348	.354	.360	
Total Width	Е	.380	.400	.420	
Pad Width	E1		0.256 REF		
Pad Length	D2	0.486 REF			
Hole Diameter	ØP	.146 .151 .156			
Hole Center to Tab Edge	Q	.103	.108	.113	
Molded Body Draft Angle	θ1	3	7	10	
Molded Body Draft Angle	θ2	1	4	7	

Notes:

- Pin 1 visual index feature may vary, but must be located within the hatched area.
 Dimensioning and tolerancing per ASME Y14.5M
 BSC: Basic Dimension. Theoretically exact value shown without tolerances.

REF: Reference Dimension, usually without tolerance, for information purposes only.

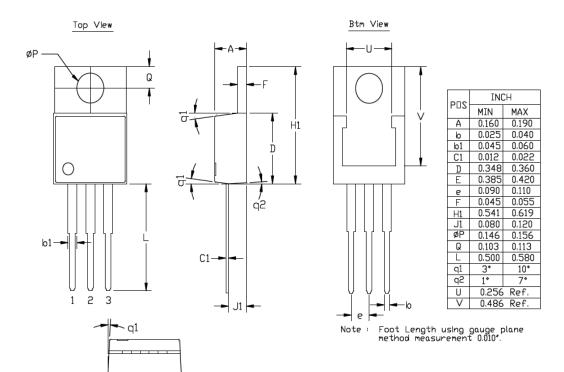
Microchip Technology Drawing C04-036 Rev D Sheet 2 of 2

3-Lead TO-220 Package Outline and Recommended Land Pattern

3 LEAD TO220 PACKAGE OUTLINE & RECOMMENDED LAND PATTERN

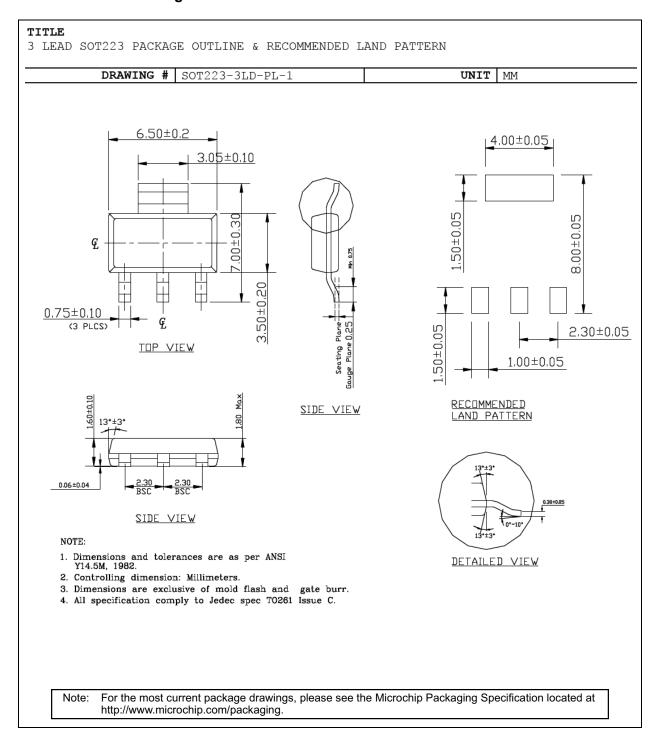
TITLE

DRAWING # TO220-3LD-PL-1 UNIT INCH
Lead Frame Copper Alloy Lead Finish Matte Tin



Note: For the most current package drawings, please see the Microchip Packaging Specification located at http://www.microchip.com/packaging.

3-Lead SOT-223 Package Outline and Recommended Land Pattern



APPENDIX A: REVISION HISTORY

Revision A (October 2021)

- Converted Micrel document MIC2920x to Microchip data sheet DS20006601A.
- · Minor text changes throughout.
- Removed all reference to discontinued leaded parts.



NOTES:

PRODUCT IDENTIFICATION SYSTEM

To order or obtain information, e.g., on pricing or delivery, contact your local Microchip representative or sales office.

PART No.	- <u>X.X</u>	X	X	- <u>XX</u>	Ex	amples:	
Device	Output Voltage	Junction Temp. Range	Package	Media Type	a)	MIC2920A-4.8WT:	400 mA Low Dropout Regulator 4.85V Output Voltage, -40°C to +125°C Temp. Range, 3-Lead TO-220, 50/Tube
Device:	MIC29204 MIC29201 MIC29202	I: 400 mA Lov with Logic-0 Flag	v Dropout Voltag v Dropout Voltag Compatible Shut v Dropout Voltag	je Regulator down & Error	b)	MIC29201-3.3YM-TR:	400 mA Low Dropout Regulator 3.3V Output Voltage, -40°C to +125°C Temp. Range, 8-Lead SOIC, 2,500/Reel
	MIC29204	with Logic-0 4: 400 mA Lov	Compatible Shut v Dropout Voltag Compatible Shut	down ge Regulator	c)	MIC29202WU:	400 mA Low Dropout Regulator Adjustable Output Voltage, -40°C to +125°C Temp. Range, 5-Lead TO-263, 50/Tube
Output Voltage:	3.3 = 4.8 = 5.0 =	3.3V 4.85V 5.0V	9202 & MIC2920	04 only)	d)	MIC29204YN-TR:	400 mA Low Dropout Regulator Adjustable Output Voltage, -40°C to +125°C Temp. Range, 8-Lead PDIP, 2,500/Reel
Junction Temperature	12 = W = Y =	12V -40°C to +125°C -40°C to +125°C			e)	MIC2920A-12WS-TR:	400 mA Low Dropout Regulator 12V Output Voltage, -40°C to +125°C Temp. Range, 3-Lead SOT-223, 2,500/Reel
Range:	M = N =	8-Lead SOIC 8-Lead PDIP			f)	MIC29201-5.0WT:	400 mA Low Dropout Regulator 5.0V Output Voltage, -40°C to +125°C Temp. Range, 5-Lead TO-220, 50/Tube
Package:	S = T = U =	3-Lead SOT-223 3- or 5-Lead TO-2 5-Lead TO-263	20		g)	MIC29202WU-TR:	400 mA Low Dropout Regulator Adjustable Output Voltage, -40°C to +125°C Temp. Range, 5-Lead TO-263, 750/Reel
Media Type:	(blank)= (blank)= TR =	50/Tube (TO-220, ⁻ 78/Tube (SOT-223) 95/Tube (SOIC opt 750/Reel (TO-263) 2,500/Reel (SOT-2	option) ion) option)	. ,	h)	MIC29204YM:	400 mA Low Dropout Regulator Adjustable Output Voltage, -40°C to +125°C Temp. Range, 8-Lead SOIC, 95/Tube
					No	catalog part num used for ordering the device packa	entifier only appears in the ber description. This identifier is purposes and is not printed on ge. Check with your Microchip ackage availability with the tion.



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