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(NASA-CR-169983) DESIGN AND DEVELCEMENT OF A FAST ICN MASS SPECTROMETER Final Report (Southwest Research Inst.) 158 p CSCL 14B Unclas HC ACE/ME AO1 G3/35 10026

DESIGN AND DEVELOPMENT OF A FAST ION MASS SPECTROMETER

by

J. L. Burch **Principal Investigator**

> W. C. Gibson **Project Manager**

> > **Final Report**

SwRI Project 15-5680 **NASA Contract NASW 3237**

prepared for

NATIONAL AERONAUTICS AND SPACE ADMINISTRATION Washington, D.C. 20546

February 1983



SOUTHWEST RESEARCH INSTITUTE Post Office Drawer 28510, 6220 Culebra Road San Antonio, Texas 78284

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I. INTRODUCTION

This project has resulted in the development of two Fault Ion Mass Spectrometers (FIMS A and FIMS B), culminating in their flight on two Project CENTAUR sounding-rocket payloads in December 1981. Analysis of data from these flights is now in progress.

This report summarizes the design, development, construction, calibration, integration, and flight of these new instruments, along with early results from the data analysis efforts. The goal of the program was to develop a medium-energy ion mass spectrometer that could cover mass-velocity space with significantly higher time resolution, improved mass resolution, (particularly for heavier ions), and wider energy range than existing instruments had achieved. The initial design consisted of a dual-channel cylindrical

electrostatic analyzer followed by a dual-channel cylindrical $\tilde{E} \times \tilde{B}$ velocity filter. As part of the early work on this instrument (FIMS A) investigations into the gain versus count-rate characteristics of the high-current channel electron multipliers (CEM's), which were chosen for ion detection, revealed a systematic behavior that could be used as a criterion for selection of CEM's for long counting lifetimes. This result was published in <u>Review of</u> Scientific Instruments (Hahn and Burch, 1980).

Meanwhile, a computer ray-tracing analysis was used to optimize the FIMS A ion optics, and a prototype instrument was fabricated. Since no suitable calibration facility existed at SwRI at that time, prototype calibration was carried out at Los Alamos National Laboratory. This calibration confirmed the ray-tracing analysis, and work on the FIMS A flight unit began.

Although the FIMS A instrument achieved the expected improvements in time resolution, heavy-ion mass resolution, and energy range, it still was able to sample in only one direction at a time. To improve further the time resolution for sampling in mass-velocity space an angular imaging capability was needed. The FIMS B instrument was designed to meet this goal. In FIMS B the cylindrical energy analyzer was replaced by a spherical sector analyzer that was capable of sampling a 35° x 5° angular fan that is resolved into sixteen 2.2° x 5° sectors by means of a microchannel plate detector at the output of

the cylindrical $\tilde{E} \times \tilde{B}$ velocity filter. The velocity filter was similar in similar in geometry to that of the FIMS A analyzer but with a magnetic field roughly twice as strong to give even better mass resolution for heavy ions. A FIMS B prototype also was calibrated at Los Alamos, and work was begun on a flight unit for Project CENTAUR. The design and early development work on the FIMS A and FIMS B instruments were described in a paper published in <u>Review of</u> <u>Scientific Instruments (Hahn, Burch and Feldman, 1981).</u> Following the initial FIMS calibrations at Los Alamos, work was begun on a calibration system at SwRI that could test the response of these instruments and their future derivatives over the full range of their capabilities. This calibration system was put into operation in time for calibration of the FIMS A and FIMS B flight units in September 1981.

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Integration of these instruments onto two separate Project CENTAUR payloads was accomplished with very few problems at SwRI. A long series of environmental tests of the full-up payloads at GSFC was followed by transport to Cape Parry, NWT, for launch operations. Both payloads were launched into the dayside magnetospheric cleft and operated nominally. Analysis of data from FIMS A and FIMS B, in cooperation with the other Project CENTAUR experimenters, is now underway.

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II. DESIGN AND EARLY DEVELOPMENT OF FIMS A AND FIMS B

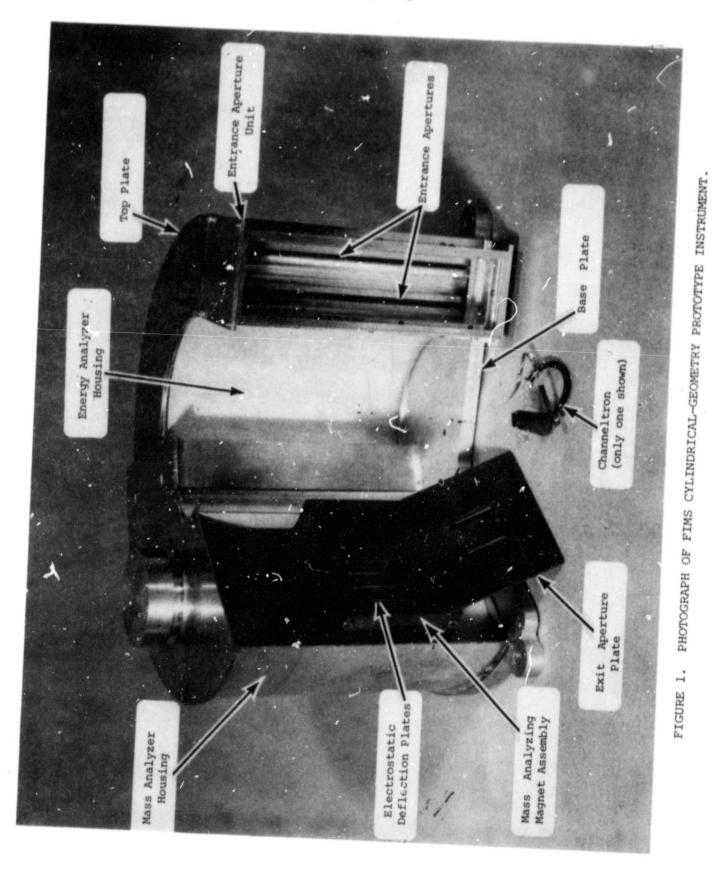
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The design and early development, through prototype calibration, of the FIMS A and FIMS B instruments are documented in the paper entitled "Development of a Fast Ion Mass Spectrometer for Space Research," by S. F. Hahn, J. L. Burch, and W. C. Feldman, which is reproduced in the following pages. Also reproduced in this section is a paper entitled "Exponential Decay and Exponential Recovery of Modal Gains in High Count Rate Channel Electron Multipliers," by S. F. Hahn and J. L. Burch.

Also shown for reference in Figures 1 and 2 at the end of this section are photographs of the FIMS A and FIMS B prototype units.

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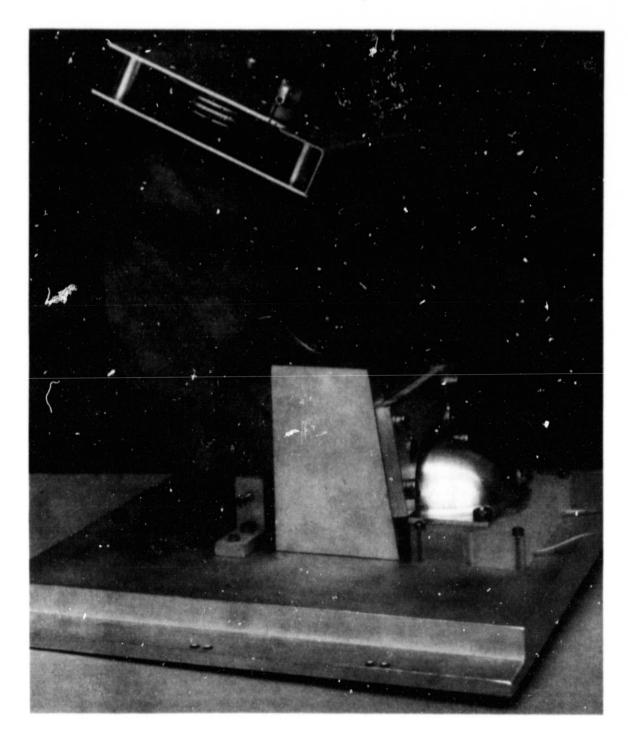
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FIGURE 2. PHOTOGRAPH OF FIMS PROTOTYPE INSTRUMENT WITH SPHERICAL ENERGY ANALYZER AND MICRO-CHANNEL PLATE DETECTOR.

Exponential decay and exponential recovery of modal gains in high count rate channel electron multipliers

S. F. Hahn and J. L. Burch

Southwest Research Institute, San Autonio, Texas 78284 (Received 17 July 1979; accepted for publication 5 February 1980) ORIGINAL PAGE 13

A series of data on high count rate channel electron multipliers revealed an initial drop and subsequent recovery of gains in exponential fashion. The FWHM of the pulse height distribution at the initial stage of testing can be used as a good criterion for the selection of operating bias voltage of the channel electron multiplier.

PACS numbers: 84,30.Wp, 29.60.Ef

Recently, high count rate channel electron multipliers (CEM's) developed specifically for the High Altitude Plasma Instrument and the Low Altitude Plasma Instrument for the NASA Dynamics Explorer project were examined in the laboratory to determine their performance characteristics. Although a number of different tests were performed, this note reports only on the results of pulse height analysis (PHA) examined as a function of total accumulated counts. The CEM's (Galileo Electro-Optics Series 4800) have typical channel resistance of $\sim 10^8 \Omega_{\star}$ and channel length-to-diameter ratios of 50 (with 1 mm inside diameters). The flat cone aperture has 6 mm × 20 mm rectangular dimensions to accommodate the instrument exit slit.

> The CEM's were tested in an extremely clean vacuum chamber with a cryogenic main pump which brings the chamber pressure to typically less than 5×10^{-8} Torr. Extra care was taken to reduce the number of outgassing sources by, for example, using bare solid wire for electrical connections, and adopting only glass, ceramic and vacuum compatible metal for mounting hardware..

> Measurements on CEM gain and pulse height distribution were obtained from a conventional charge sensitive preamplifier (ORTEC 142-PC) and wave shaper (ORTEC 460) combination, whose output was connected to either a CAMAC-controlled single channel analyzer or a multichannel analyzer (Nuclear Data 2200). The multichannel analyzer controls were set to match those of the manufacturer so as to achieve continuity in data taking. Data were obtained both at our facility and the manufacturer's.

> Figure 1 shows some of the typical data. To emphasize the exponential nature of the gain change as a function of total accumulated counts, a linear scale was used on the x axis. The counts were accumulated at a rate of $\sim 5 \times 10^4 \text{ s}^{-1}$ while the CEM modal gain was maintained at $\sim 10^8$ by adjusting bias voltage to ensure no premature fatigue induced by overdriving it. The PHA was performed at a count rate of 5×10^3 counts s⁻¹, which was well within the region where no gain change with count rate was observed.

> The data show a characteristic gain history with four distinct phases. The first phase involves an apparently exponential drop in gain. This initial gain reduction is

the result of a "clean-up" process, a term used by many.¹ It occurs in a relatively short period of total accumulated counts (TAC's) <u>usually</u> in the range of $3-5 \times 10^{\circ}$. Some units were observed to exceed 10° TAC's before reaching the second, minimum gain, phase. At the end of the first phase, the gain is generally one third to one half of the initial gain. For normal CEM's the minimum gain is maintained for a period which is positively correlated to that of the first phase, up to 10° TAC's. Those units that were subsequently judged defective had monotonically decreasing gain past 10^{10} TAC's, a test limit we set for convenience.

Even with the positive correlation between the accumulation periods of the first and the second phases, the onset of the third phase, gain recovery, is not very predictable. The third phase is characterized by a strong and fast recovery of gain, again in an exponential fashion. While the overall fluctuation of gain reduction and subsequent recovery has been observed by many, no report is known to us on the exponential nature of the gain recovery. The recovered gain is generally higher or comparable to the initial gain as opposed to the lower recovered gain of regular CEM's. The final gain plateau (the fourth and final phase of the gain history) is consistently close to or higher than the gain observed at the beginning of the test. The gain recovery typically plateaus near 10° TAC's, but some units took up to ~4 \times 10⁹ for full recovery. No appreciable change in gain was noticed in subsequent measurements up to 1010 counts, the set limit. We believe the gain stays more or less constant past this limit so long as no surface degradation due to contamination takes place, as evidenced in the literature.2.3

The gain drop and recovery phenomenon was observed after every exposure of the CEM to air, but the amount of change differed from the very first test values. The physiochemical process behind this gain fluctuation is presently not very well understood.

For statistical reasons it is important to know the FWHM of the CEM. Since the FWHM changes with gain, which is again subject to TAC's, it is better to determine the CEM operational voltage according to the actual FWHM measurements in addition to the usual

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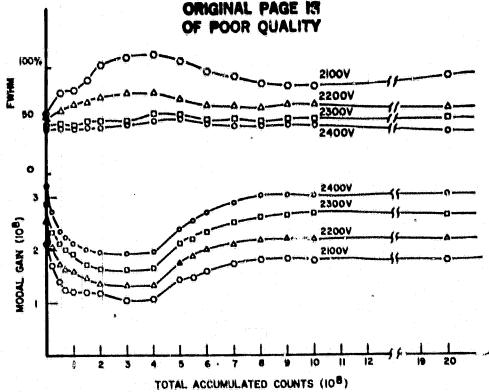


FIG. 1. Changes in modal gain and FWHM as functions of total accumulated counts.

practice of utilizing a gain (or count rate) vs. voltage curve, which does not give a clear-cut bending point. By testing FWMM at different bias voltages as a function of TAC's, one can safely set the operating voltage of a CEM with good statistical confidence. According to the data shown in Fig. 1, a bias voltage of 2300 V is needed if one wants better than 50% FWHM at any time during the operation of the CEM unless *in situ* aging of the CEM beyond the third phase is executed. This work was supported by NASA Contract Nos. NAS5-24301 and NASW-3237.

- ¹ E. A. Kurz, "Channel electron multipliers," Am. Lab. 67, (March 1979).
- ⁴ H. Rosenbauer, Remarks on the qualification of continuous channel electron multipliers (CEM's) for use as detectors in long term space flight missions, unpublished manuscript (May 1978).
- ² J. G. Timothy and R. L. Bybee, Rev. Sci. Instrum. 49, 1192 (1978).

Development of a fast ion mass spectrometer for space research

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(Received 29 August 1980; accepted for publication 6 October 1980)

An ion mass spectrometer with a cylindrical $\overline{E} \times \overline{B}$ analyzer and either a cylindrical or a spherical electrostatic analyzer is described in this paper. The instrument features two nested channels and a postacceleration between the electrostatic and $\overline{E} \times \overline{B}$ analyzers for wide ranges of energy and mass responses. The dual-channel construction not only doubles the data acquisition rate, but also makes it possible to reverse the electrostatic field orientation in the inner channel (radially outward, as opposed to radially inward in the outer channel) for lighter ions. The outer channel is then optimized for ions of medium to high mass numbers resulting in enhanced mass resolution for such heavy ions. A 90 °-deflection angle spherical electrostatic analyzer in combination with a cylindrical $\overline{E} \times \overline{B}$ analyzer provides an extremely wide viewing angle. The angular distribution of ions can be obtained from such geometry if a position-sensitive resistive anode or a detector array is used at the exit aperture. Details on the principles of operation, the instrument design, and calibration are given in this paper.

PACS numbers: 07.75. + h, 95.55.Lb

INTRODUCTION

Low to medium energy (a few eV/e-tens of KeV/e) ion mass spectrometers for ionospheric and magnetospheric research have gained great sophistication during the past two decades. The newer types of instruments have steadily increased the detectable ranges of ion energies and mass numbers, the detection sensitivity, and resolution. Recent progress has been concentrated on $\tilde{E} \times \tilde{B}$ type filters, in which a permanent magnetic field and a variable electrostatic field are used to form a tunable velocity analyzer. Such an $\tilde{E} \times \tilde{B}$ filter in combination with an electrostatic energy analyzer provides energyper-charge (E/q) and mass-per-charge (M/q) measurements of ions.

The geometry of the $\overline{E} \times \overline{B}$ filter has evolved from Cartesian^{1,2} to a cylindrical^{3,4} configuration, resulting in a more efficient use of volume and ion optics. A typicalmass spectrometer³ utilizes a cylindrical electrostatic energy analyzer followed by a cylindrical $\overline{E} \times \overline{B}$ analyzer which has radial electrostatic and axial magnetic fields formed by a set of coaxial electrostatic deflection plates placed between two magnet pole pieces. Such mass spectrometers are designed to be double focusing at selected mass and energy combinations achieving high sensitivity and good resolution as a whole device.

The energy ranges of such mass spectrometers have been increased by the adoption of a fixed preacceleration voltage applied between the grounded input aperture and the cylindrical electrostatic analyzer plates. The preacceleration increases the minimum energy of ions seen by the $\vec{E} \times \vec{B}$ analyzer to a level where the lightest ion (H⁺) can be deflected by the magnetic field alone. So the ion energies which the instrument must handle range only from E_{Acc}/q (energy gained by acceleration) to E_{max}/q (maximum design energy) rather than from 0 to $E_{\rm max}/q$. The preacceleration, however, results in deterioration of energy and angle resolution of the instrument at the lower ion energies, leads to an energy dependence of the total instrument sensitivity, and requires the use of a retarding potential analyzer (RPA) to obtain energy spectrum information at energies below the preacceleration energy. This approach not only adds to the complexity of the instrument, but also inadvertently decreases the sensitivity of the instrument due to lowered transmittance through a set of RPA grids.

This paper reports on the development of a Fast Ion Mass Spectrometer (FIMS) which is based on the cylindrical $\vec{E} \times \vec{B}$ velocity filter, but which achieves several significant improvements over previous instruments of this type. An isometric view of the calibrated instrument appears in Fig. 1. The energy and angle resolution at low energies has been improved by the use of postacceleration instead of preacceleration. In this approach the postacceleration potential is applied between the exit slit of the electrostatic analyzer and the entrance aperture of the $\vec{E} \times \vec{B}$ analyzer. This arrangement ensures the same wide dynamic operational energy range and still maintains uniform energy and angle resolutions of the basic electrostatic analyzer, eliminating the need of an

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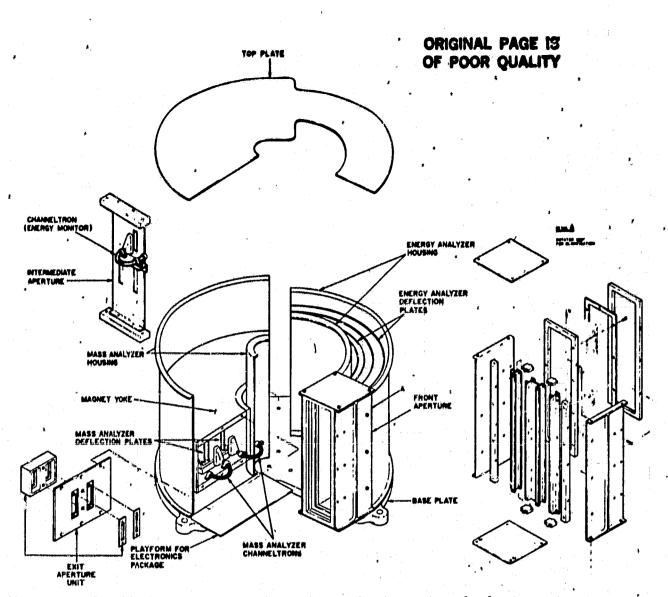


FIG. 1. A dual-channel fast ion mass spectrometer (FIMS) with cylindrical electrostatic and $\vec{E} \times \vec{B}$ analyzers.

RPA of the preacceleration instrument. The sensitivity of the instrument also remains constant for the entire energy range. Postacceleration also enhances the mass resolution of the instrument at low energies since the angle and energy spread past the electrostatic analyzer is reduced by the acceleration potential which precedes the $\bar{E} \times \bar{B}$ analyzer.

A second improvement is the incorporation of dualchannel operation, achieved by placing a second set of deflection plates nested within the radius of the first one. Such a nested geometry allows increase in instrument volume while providing a two-fold increase in data acquisition speed with minimal increases in weight and power consumption over those of the single-channel device. This dual-channel feature also allows the mass resolution of the instrument to be increased at higher mass numbers. As described above, single-channel $\vec{E} \times \vec{B}$ filters of this type have been designed so that low mass-number ions are detected when the radially inward electrostatic deflection force is minimized. An increase of the deflection voltage then allows the detection of higher mass-number ions, but with decreasing mass resolution since as $M/q \rightarrow \infty$, the $\bar{E} \times \bar{B}$ filter becomes essentially an electrostatic analyzer. With two channels, one can use a stronger magnetic field (or a larger radius), thereby optimizing the instrument at an intermediate mass number (e.g., O⁺ or C⁺). Then, the outer channel can acquire higher mass-number ions in the manner described above using a radially inward electrostatic deflection force, while the inner channel simultaneously covers the lower mass numbers by using a radially outward deflection electric field. This approach has been verified by trajectory computations, and a prototype instrument is now under construction.

An additional improvement has been achieved by the use of spherical electrostatic analyzers instead of the cylindrical units (Fig. 2). This geometry provides an extra large angle of view in at least one dimension (perpendicular to the plane of particle deflection), and, if a 90° deflection angle is used, enables one to measure the angular distribution of particles when a positionsensitive detector is used at the exit aperture.

The principle of device operation and detailed description of the prototype instruments as well as the use of

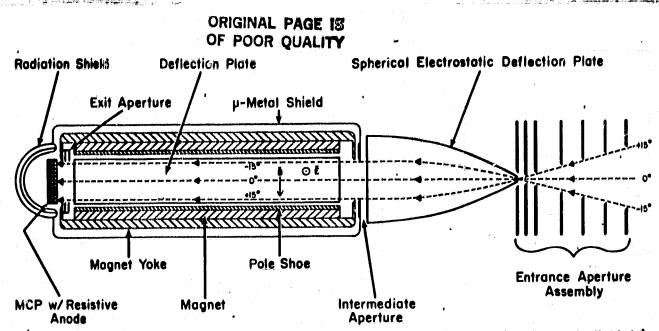


FIG. 2. A developed, cross-sectional side view of a mass spectrometer with dual spherical electrostatic analyzer and cylindrical $\vec{E} \times \vec{B}$ analyzer combinations. Such an instrument has an extra-wide field of view with high angular resolution if position sensitive detectors such as microchannel plates are used at the exit apertures.

spherical electrostatic analyzers are included in the following chapters.

I. OPERATIONAL PRINCIPLES

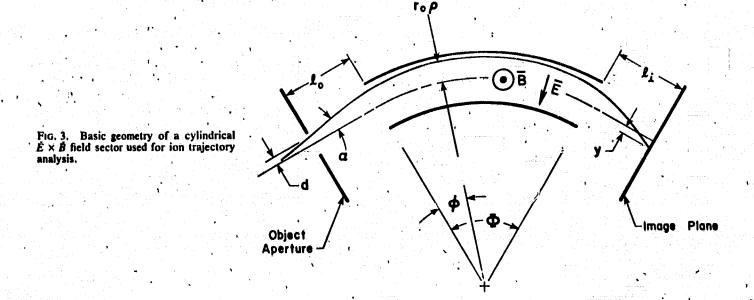
The analysis of charged particle optics in crossed \bar{E} and \bar{B} fields has been performed by some in cylindrical geometry,⁵⁻⁷ and a brief review is made in this chapter with the intention of clarifying the meanings of different parameters used in the instrument descriptions. The idealized field sector geometry is shown in Fig. 3 where, for simplicity, no fringing field effect is considered. The magnetic field \bar{B} is uniform within the sector boundary and is directed parallel to the z axis of the cylindrical coordinates of r, ϕ , and z. The radial electric field is generated by the use of a set of coaxial plates. Charged particles introduced into the field region are deflected by the combined effect of both electric and magnetic fields. Suppose the fields are adjusted so that an ion with mass per unit charge ratio of m_0 and velocity v_0 is deflected along the central trajectory of radius r_0 . The ratio, of centripetal force by electrostatic field to the total deflection force is defined as

$$f = eE_0/(m_0v_0^2/r_0^1), \tag{1}$$

where e is the unit positive charge and E_0 is the electric field at $r = r_0$. The quantity f is a measure of the relative deflection forces due to electrostatic and magnetic sources, and is either positive for radially-in or negative for radially-out \tilde{E} -field orientation. The magnetic field force on the ion is then

$$ev_0'B = (1 - f)(m_0v_0^2/r_0).$$
 (2)

The motion of a particle other than those on the central trajectory can be described by an equation of motion in



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cylindrical geometry, which can be solved analytically if first-order approximations are used for certain of the parameters as follows:

$$v = v_0(1 + \beta),$$
 [3(a)]

$$m = m_0(1 + \epsilon), \qquad [3(b)]$$

and

$$r = r_0(1 + \rho),$$
 [3(c)]

where β , ϵ and ρ are all <1.

Using the above approximations and the relationships between r, ϕ , and t, a second-order differential equation of ρ as a function of ϕ is obtained as

$$\frac{d^2\rho}{d\phi^2} + \rho(1+f^2) = \beta(1+f) + \epsilon, \qquad (4)$$

where only first-order terms in ρ , β , and ϵ are preserved. Solving for ρ with boundary conditions given in Fig. 3,

$$\rho(\phi) = A \sin k\phi + B \cos k\phi + [\beta(1+f) + \epsilon], \quad (5)$$

where

$$k^2 = 1 + f^2,$$

 $A = \alpha/k,$
 $B = (d + \alpha l_0)/r_0 - [\beta(1 + f) + \epsilon]/k^2,$

with α (≤ 1) = angle of incidence at the entrance aperture, d = displacement of particle entrance position from the aperture center, and l_0 = object distance.

After leaving the field region the particle follows a straight path to the exit aperture plane, assuming no fringing field effect. The displacement of the intercept point from the point of central ion trajectory at the exit aperture plane is then expressed as

$$D = \frac{d\rho}{d\phi} \bigg|_{\phi} [1 - \rho(\Phi)] l_i + \rho(\Phi) r_0, \qquad (6)$$

where l_i is the image distance and Φ is the field sector angle.

Equation (5) is used to evaluate D, and the resultant terms are grouped to represent effects of various sources of dispersions as

$$D = D_{\alpha} + D_{\beta} + D_{d}$$

= $\alpha [r_0/k - l_i l_0 k/r_0) \sin k\Phi + (l_0 + l_i) \cos k\Phi]$
+ $\beta (1 + f + \epsilon/\beta) [(l_i/k) \sin k\Phi + (r_0/k^2)(1 - \cos k\Phi)]$
+ $d [\cos k\Phi - (l_i k/r_0) \sin k\Phi].$ (7)

 D_{α} is the dispersion due to the angular divergence at the entrance aperture, while D_{β} results from the deviation of ion velocity and mass from those of the central trajectory ion. The last term is the magnification effect of the ion optics. For first order angular focusing ($D_{\alpha} = 0$), the terms inside the first bracket should reduce to zero, or

$$(l_i + l_0)/(r_0/k + l_i l_0 k/r_0) = -\tan k\Phi.$$
 (8)

For example, for a purely electrostatic cylindrical analyzer with $l_i = l_0 = 0$, angular focusing occurs when

 $k\Phi = \pi$ where $k = (1 + f^2)^{1/3} = 2^{1/3}$. Also, for a purely magnetic analyzer, k = 1 and the ions are focused in angle if $\Phi = \pi$. For any value of k between $1-2^{1/3}$, the focusing should occur between $\pi/2^{1/3}$ and π as both field components contribute in deflecting the ion. In other words, if the field sector angle Φ is set to a value between the two limits, there is a corresponding value of f for which the angular focusing occurs for the ions of given mass-per-charge and energy-per-charge ratios. Since the electrostatic energy analyzer preceding the $\tilde{E} \times \tilde{B}$ analyzer can be made to be angular focusing at all energies, the complete instrument can be designed for first-order angular focusing at selected ion energy and mass number combinations.

To achieve focusing in energy as well as in angle, the energy dispersion of the electrostatic analyzer should be compensated by the equal but opposite dispersion in the $\vec{E} \times \vec{B}$ analyzer while maintaining the angular focusing conditions. The velocity dispersion of an angularlyfocusing electrostatic analyzer is obtained from Eq. (7) by keeping $\epsilon = 0$ (uniform mass-per-charge), f = 1 and $k\Phi = \pi$, resulting in $D_{\beta E} = 2\beta r_{0E}$ where the subscript *E* is used to denote the electrostatic analyzer.

The velocity dispersion D_{β} in the $E \times B$ analyzer is given similarly:

$$D_{\beta} = \beta(1 + f_0)(r_0/k^2)(1 - \cos k\Phi)$$

= $\beta(1 + f_0)[2r_0/(1 + f_0^2)],$ (9)

where f_0 is the value of f for which the angular focusing condition is satisfied. For double focusing,

$$D_{\beta E} = D_{\beta}$$
, or $r_{0E} = r_0(1 + f_0)/(1 + f_0^2)$. (10)

The mean radius of the electrostatic analyzer, r_{0E} , ranges from $1.0r_0$ for $f_0 = 0-1.207r_0$ for $f_0 = 2^{1/2}$. Thus, the mass spectrometer can be made double focusing in energy and angle at a selected combination of ion mass and energy by adjusting the radius of the electrostatic analyzer within the limits with respect to that of the $\vec{E} \times \vec{B}$ analyzer.

Even for an instrument with postacceleration (an acceleration potential is applied between the electrostatic and $\vec{E} \times \vec{B}$ analyzers), the angular focusing condition previously discussed is still applicable as the two component analyzers are operating independently to achieve angular focusing as a complete instrument. The amount of angular dispersion at other than the focusing condition becomes smaller for the instrument with postacceleration, especially for ions whose initial energies prior to acceleration are less than those gained by the postacceleration. As explained in the previous section. the acceleration is required to accomplish a wide dynamic range of operation (from a few eV/e to tens of thousands of eV/e) without the benefit of an electromagnet by raising the minimum energy of the ions to the level required by the permanent magnet. In contrast to postacceleration, a preacceleration scheme (applying acceleration before the ions are introduced into the electrostatic analyzer) required an additional analyzer (a Retarding Potential Analyzer) for low-energy ions,

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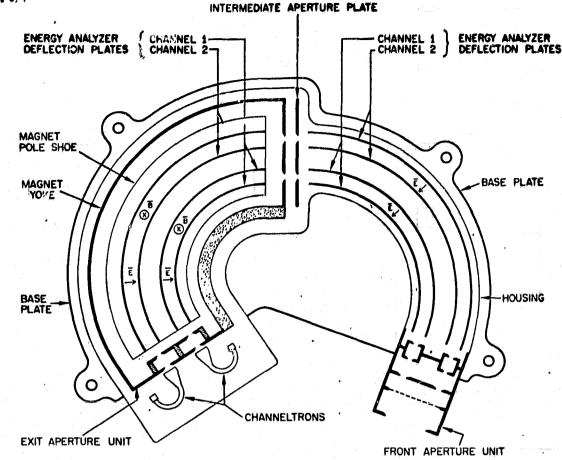


FIG. 4. Schematic presentation of the dualchannel fast ion mass spectrometer (FIMS).

adding to the complexity of the basic instrument. The double-focusing characteristic is also obtained in the instrument with postacceleration, but at a different value of r_{0E} from that of an otherwise identical preacceleration instrument. In the limiting case for which the initial ion energy per charge is much higher than the energy gained by the postacceleration, a similar double-focusing condition applies to both. For low-energy, low-mass ions, the angular focusing occurs near $\phi = \pi/2$ in deflection angle as all the ions are essentially monoenergetic and parallel in motion when introduced into the $E \times B$ analyzer. This angle is smaller than the minimum possible angle of $\pi/2^{1/2}$ for angular focusing of an $E \times B$ analyzer. In other words, there is a lower limit in energy for which the double focusing occurs for such an instrument. This limit, however, does not necessarily degrade the mass resolution of the instrument at low energies since the angular dispersion becomes very small for the same low-energy level.

The preceding discussions were verified through raytracing calculations on a computer graphics terminal. The design of the instrument was initially based on the results obtained from the simulation. Details of the instrument are given in the following section.

II. INSTRUMENTATION

Figure 4 shows a schematic drawing of the instrument with cross-sectional plane parallel to the base plate. The

prototype was built and calibrated in the laboratory. The calibration results are discussed in the next section. The instrument utilizes two ion channels which enable us to collect data at twice the speed of a single-channel device. The two channels are in a nested configuration, so that the additional weight and volume amounts to only a fraction of those for a single channel instrument. Five major functional units make up the physical structure of the instrument excluding the electronics system. These units include the front aperture, the electrostatic analyzer unit, the intermediate aperture, the $\vec{E} \times \vec{B}$ analyzer unit, and the exit aperture/detector unit.

A. Front aperture

The front aperture unit works as a mechanical limiter of the viewing angle of the instrument. The unit consists of a four-stage baffle and a grounded grid which shields ions from the electrostatic leakage field of the deflection plates. The last two baffles are machined to exact opening slit sizes $(2.5 \times 120 \text{ mm})$ and viewing angles $(\pm 10^{\circ}$ azimuthal $\times \pm 12^{\circ}$ polar) and are biased to a slightly positive potential with respect to the instrument body to repel ions of energies lower than the design limit. Particles within the viewing angle and direction are introduced into the two cylindrical electrostatic analyzer channels with little disturbance from the front aperture as there is only one grid of high transparency ($\leq 91\%$).

Mass spectrometer for space

ORIGINAL PAGE IS B. Electrostatic analyzer OF POOR QUALITY

The two channels of the electrostatic analyzer have mean radii of 6.92 and 8.92 cm each for inner and outer channels, respectively. The ratio of plate separation to mean radius $(\Delta r/r_0)$ of a channel is set the same for both of the channels to have the same energy resolution since $\Delta E/E \propto \Delta r/r_0$ in a first-order approximation. The same $\Delta r/r_0$ ratio for both channels also results in ions of the same mean energy being deflected through the central radii of the two channels, thus facilitating direct comparison of data between the two even when a common power supply is used for both.

The sector angles of the two analyzer channels are both 112°. Equation (8) demands that the sum of the image and object distances be different between the two channels if the same sector angle is used for both. For practical reasons, however, the same image and object distance of 1.0 cm is used for the two channels. The top and bottom edges of the deflection plates are machined to have blunt ribs with circular cross sections. This feature helps to maintain the mechanical rigidity of the thin aluminum deflection plates and also to reduce the electrostatic fringing field effect near the edges. The plates are mounted on the base plate and top plate of the housing through Kel-F insulating mounts. The internal area of the deflection plates, which is exposed to the channel, is gold blackened for the effective absorption of energetic photons and the prevention of multiple ion scattering, both of which can cause high background count. The gold black is obtained through carefully controlled evaporation of gold in a vacuum chamber with backfilled gas (98% N_2 and 2% O_2) maintained at a pressure of ~2 Torr. The common practice of serrating the internal surface of the deflection plates is omitted for ease of machining in the prototype instrument.

C. Intermediate aperture

Energy-selected ions from the electrostatic analyzers are angularly focused to a pair of grounded intermediate apertures of widths 3 and 4 mm for inner and outer channels, respectively. The intermediate aperture selects ions within a predefined energy bandwidth and then passes them to the postacceleration region between the grounded plane and another set of matching apertures located on the $\vec{E} \times \vec{B}$ analyzer unit. The postacceleration potential should add enough energy to the ions (-3400 e V/e for the prototype) to guide the lightest ion (H+) through the inner channel at the given radius and flux density of the $\vec{E} \times \vec{B}$ analyzer. The intermediate aperture unit also includes a channel electron multiplier as an ion detector, which is located against the inner channel of the electrostatic analyzer, but vertically offset from the main aperture slit. This detector is used as an energy monitor which measures the energy distributions of the total ion population. All aperture openings have beveled knife edges to reduce any off-angle scattering of particles.

D. É × B analyzer unit

The energy-filtered, postaccelerated ions are introduced into the two channels of $\vec{E} \times \vec{B}$ analyzer unit. The unit consists of a magnet assembly and two sets of electrostatic deflection plates. The magnet assembly uses two SmCo_b permanent magnets, pole shoes, and a voke system which completely encloses the magnetic field region, except for the aperture openings at the inlet and outlet. The entire $\vec{E} \times \vec{B}$ analyzer unit is biased to -3400 V from the instrument body for postacceleration. The initial yoke design was verified by a two-dimensional magnetostatic computer code to achieve uniform flux density inside the field region at minimum weight and low-leakage flux. The complete analyzer unit weighs 2.8 kg in the present design, where elaborate machining was avoided for simplicity. The magnet weight can be reduced, if required, by as much as 25% by further trimming of the yoke. The magnets generate an average flux density of 1330 G in a 3.0 cm gap between pole shoes, and the maximum flux density change within the volume of the channel is approximately 1% of the average value except for the regions near the entrance and exit sides of the channels, where a fringing field effect arises.

The magnetic leakage was measured at not more than 0.75 G at 10 cm from the surface of the magnet yoke in any direction. Further reduction in leakage to less than 1 γ at 1 m seems easily obtainable if a high- μ foil is \Im sed for shielding around the yoke. The sector angle defined by the physical boundary of the magnets was selected through computer programming of the beam trajectory calculation, and was set at 123°, which is the same for the electrostatic deflection plates mounted between the magnet pole shoes. This angle is chosen to have angular focusing condition at a value of f near unity, corresponding to essentially electrostatic deflection of ions of heavy mass and/or high energy. Considering that the angular focusing of a purely electrostatic analyzer with cylindrical geometry is achieved at $\Phi = \pi/2^{1/2}$ if the object and image distances are zero, it is clear that the $\vec{E} \times \vec{B}$ analyzer with 123° sector angle and 1.5 cm object and image distances will satisfy focusing conditions when the electrostatic deflection force is perdominant over the magnetic force.

With the same object and image distances for both channels, the angular focusing occurs at f = 0.75 and 0.82 for inner and outer channels, respectively, if no fringing field effect is considered. The mean radii of inner and outer channels are 6.0 and 8.0 cm, respectively. The fringing field of the magnet assembly was measured *in situ*, and the effect was calculated for various ion energy and mass combinations. For a 3.0 keV H⁺ ion, the magnetic fringing field effect works as if the field boundary is approximately 0.3 cm beyond the pole shoes, and the virtual field sector angle becomes 128.7° in contrast to the physical magnet sector angle of 123°. The electrostatic deflection plates in the $\overline{E} \times \overline{B}$ analyzer unit are machined and gold blackened as described above for the energy analyzer plates.

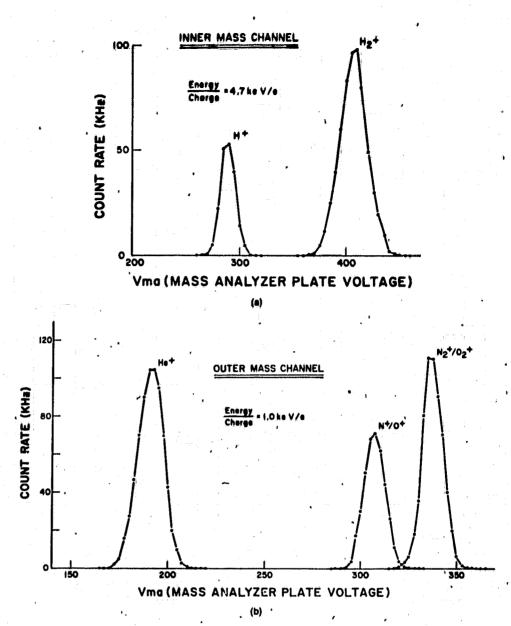
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FIG. 5. Calibration data from the dual-channel FIMS of cylindrical geometry. Each set of curves is a composite of separate measurements for different ion species. (a) Innerchannel data for 4.7 keV/e, H⁺ and H₂⁺ ions. (b) Outer channel data for 1.0 keV/e, He⁺, N⁺/O⁺ and N₂⁺/O₂⁺ ions. The label N⁺/O⁺ indicates that the peaks are due to nitrogen.



E. Exit aperture and detector unit

The exit apertures are located at 1.5 cm from the field sector boundary, and are composed of two layers of baffles and a set of two limiting slits on a soft-iron plate. The plate forms part of a magnetic yoke system which completely encloses the magnetic field region, minimizing the leakage flux. Ions with selected mass-per-charge emerge from the apertures and are detected by two high count rate (\geq 5 MHz) channel electron multipliers (CEM's) set against the apertures. The CEM (Galileo Electro-Optics' 4800 Series) has an input cone of rectangular (6×20 mm) opening, which is larger than the exit aperture slit sizes. The bias voltage is individually adjusted to achieve a gain of $\sim 1 \times 10^8$, and ranges from 2200-2700 V. The bias voltage of the CEM's is obtained from the postacceleration potential of the $E \times B$ analyzer unit, thus eliminating the use of an extra power supply. Different types of material are being considered for the shielding of CEM's against penetrating high-energy radiation, including gold and polyethylene combinations.

III. INSTRUMENT CALIBRATION

The calibration of the instrument has been performed using an ion accelerator facility at Los Alamos Scientific Laboratory. The accelerator main chamber contains a giraballed platform on which the instrument is mounted. Ions are generated by a duo-plasmatron type gun, and are postaccelerated to a desired energy level. A massselecting magnet is set between the gun and the flight tube of approximately 3 m in length. The beam intensity and cross sectional distribution are monitored by a set of electron multipliers inside the main chamber next to the beam opening. The chamber is pumped by a combination of ion and turbomolecular pumps to a low 10⁻⁶ Torr range in pressure. Calibration parameters include the ion species, energy, the orientation of the instruments in two angles, electrostatic analyzer plate voltages, and the $\vec{E} \times \vec{B}$ analyzer plate voltages to determine the energy and mass resolutions, viewing angles, and deflection sensitivity of the electrostatic plates. The instrument response to ions of isotropic distribution is simulated by integrating the data over all angle and energy settings

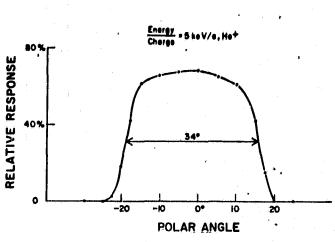


FIG. 6. The response curve in polar viewing angles for a spherical electrostatic and cylindrical $\vec{E} \times \vec{B}$ analyzer combination.

of the instrument. Figure 5 shows some of the data obtained using 4.7 keV/e H⁺ and H₂⁺ ions on the inner channel. The two mass peaks show resolutions ($\Delta M/M$) of ~0.15 for H⁺ and 0.25 for H₂⁺ at FWHM. The resolution gets better than 0.1 for H⁺ ions of energies below 1.0 keV.

The outer channel calibration data at ion energy of 1.0 keV/e are given in Fig. 5(b). Only He⁺ and heavier ions were used in the calibration of the outer channel. The separation of peaks between N⁺⁺/O⁺⁺ group and N⁺/O⁺ group ions is large enough for unambiguous identification, which is true for energies up to and above 10 keV/e. The instrument viewing angles vary with the change of ion energies, but generally range between $\pm 5^{\circ}$ and $\pm 8^{\circ}$ for both polar and azimuthal angles. The energy resolution is quite uniform over entire energy range, and is typically about 7%. The geometric factor, slightly dependent on the ion energies, is estimated at $\sim 1 \times 10^{-3}$ cm²-ster ($\Delta E/E$).

IV. USE OF SPHERICAL ELECTROSTATIC ANALYZER IN COMBINATION WITH CYLINDRICAL $\vec{E} \times \vec{B}$ ANALYZER

On a spin-stabilized satellite, an instrument with its viewing direction perpendicular to the spin axis can scan the whole 4π steradian solid angle per spin if the viewing angle on a plane including the spin axis is 180°. It is thus very advantageous to develop an instrument which has an extra large viewing angle in at least one plane with an ability to resolve the angular response. The mass spectrometer discussed in preceding sections has viewing angles in both polar and azimuthal directions of between $\pm 5^{\circ}$ and $\pm 8^{\circ}$, while a wider angular acceptance is often desirable. A modified mass spectrometer has been developed to meet this objective through the use of a spherical electrostatic energy analyzer as shown in Fig. 2. The spherical analyzer has a 90° deflection angle in the plane of trajectory and the apex of the 1/8 of a sphere is used for particle inlet aperture.

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OF POOR QUALITY control analyzer transforms ion flow such that small area-wide polar angle flux at the inlet aperture converts itself to a large area --- parallel flux at the exit aperture after the particles have traced out great circle trajectories of 90° in deflection. As before, the particles emerging from the exit aperture are introduced into the $\vec{E} \times \vec{B}$ analyzer channels for mass analysis. Since the exit positions of parallel particle trajectories correlate uniquely to the particle inlet angles, and the relative positions of particles are conserved in the $\vec{E} \times \vec{B}$ analyzer where there is no force in z-direction, the original particle inlet angle can be recovered at the detector plane if a position sensitive detector is used. A microchannel plate with resistive anode appears the most promising candidate for the detector while an array of channel electron multipliers could also be used. A prototype of the instrument combining 96° spherical electrostatic analyzer with a cylindrical $\vec{E} \times \vec{B}$ analyzer was chosen to verify the feasibility of the novel idea. 96° deflection angle was selected in the prototype device to have focusing in polar angles to a point detector located beyond the exit aperture of $\vec{E} \times \vec{B}$ analyzer. The calibration data shown in Fig. 6 shows the wide (34° at FWHM) angle of acceptance in polar angle.

It was also verified that the mass resolutions of the spectrometer with such configuration was quite comparable to those of cylindrical geometry. The magnetic field sector angle must be changed from that of the cylindrical configuration since the ions incident on $\vec{E} \times \vec{B}$ analyzer are all parallel in the plane of deflection (azimuthal angle). The initial angular focusing (in the deflection plane) occurs at a deflection angle between $\pi/2^{3/2}$ and $\pi/2$ for the parallel ions, which makes the $\int \overline{B} \times dI$ factor too small to differentiate high AMU ions. A sample run of trajectory calculations is given in Fig. 7. The previously mentioned postacceleration still applies to the new scheme with the similar effects, raising the minimum energy of ions before being analyzed in the $\vec{E} \times \vec{B}$ unit and also straightening the ion trajectories for less angular spread.

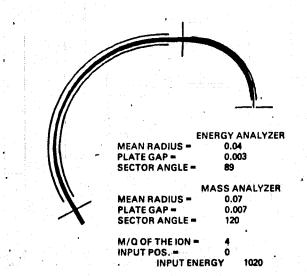


FIG. 7. Ray tracing of ions in a spherical electrostatic and cylindrical $\vec{E} \times \vec{B}$ analyzer combination.

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V. DISCUSSIONS

The use of cylindrical electrostatic and $\vec{E} \times \vec{B}$ analyzers in space-borne ion mass spectrometers has led to the achievement of high mass resolutions over wide ion energy ranges. Further improvements have been made possible by the use of more than one channel and the adoption of postacceleration between electrostatic and $\vec{E} \times \vec{B}$ analyzers as established by the calibration data of a laboratory prototype. The use of multiple channels increases the data acquisition speed with little penalty on weight and volume. When a high mass resolution is needed for ions with high mass-to-charge ratios. the magnetic flux density and the postacceleration potential of the $E \times B$ analyzer can be optimized for such ions on the outer channel. Ions of lower mass-to-charge ratios are then analyzed through the inner channel. which not only provides naturally smaller channel radius, but also work with reversed (radially-outward) electrostatic field to guide the ions through the channel. The ⁷ postacceleration brings the mass resolution of the instrument to a still higher level, especially when the ion energy per charge is lower than the acceleration voltage. A mass spectrometer of this design has a mass resolution which is high enough to distinguish between O^{++} . O^+ and NO⁺ up to several keV/e.

For a large polar angle response of the mass spectrometer, a spherical electrostatic analyzer was tried in place of the cylindrical unit. A laboratory prototype provides up to 34° FWHM response angle with slightly reduced geometric factor when the same magnet was used in both. It can also provide angular resolutions in polar angles if a position sensitive detector is used on the exit aperture plane.

ACKNOWLEDGMENTS

The authors wish to express their gratitude to all who contributed to this project. Special thanks go to Bill Tomlinson, Russ Williams, John Brune, and Tom Booker for their work involving instrument design and fabrication, and to Drs. Jack Asbridge, John Lynch, and Sam Bame for many discussions and detailed designs leading to the incorporation of spherical section front end electrostatic analyzer, and for their help in calibrating the instrument. This project was partially supported by NASA Contract Nos. NAS5-24301 and NASW-3237, and by the Southwest Research Institute, Internal Research Panel, and the U.S. Department of Energy.

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III. FLIGHT UNIT CONSTRUCTION

The two FIMS flight instruments were constructed in the laboratories of the Department of Space Sciences at Southwest Research Institute during the period of June through September 1981. A considerable amount of the flight unit construction was performed in clean room facilities owing to the sensitivity of the solid state detectors to contamination from airborne particulate and vapor contaminants. Figures 3 and 4 are photographs of the FIMS A and B flight units.

Appendices A and B of this report contain complete hardware and software reference manuals for the two flight instruments. It will be noticed that the instruments each have an associated Central Electronics Package (CEP) which provides control and data acquisition services for the spectrometers. The CEP associated with the FIMS A instrument was constructed primarily from widetemperature CMOS digital circuitry. The system consisted of three primary subsystems - the channeltron scalers, parallel-to-serial converters, and the programmable power supply (PPS) controllers. The output of each of the three FIMS A channeltrons was connected to a 16-bit linear accumulator. All three sets of accumulators employed tri-state output buffers so that a single common data bus could be constructed from their commonly connected outputs.

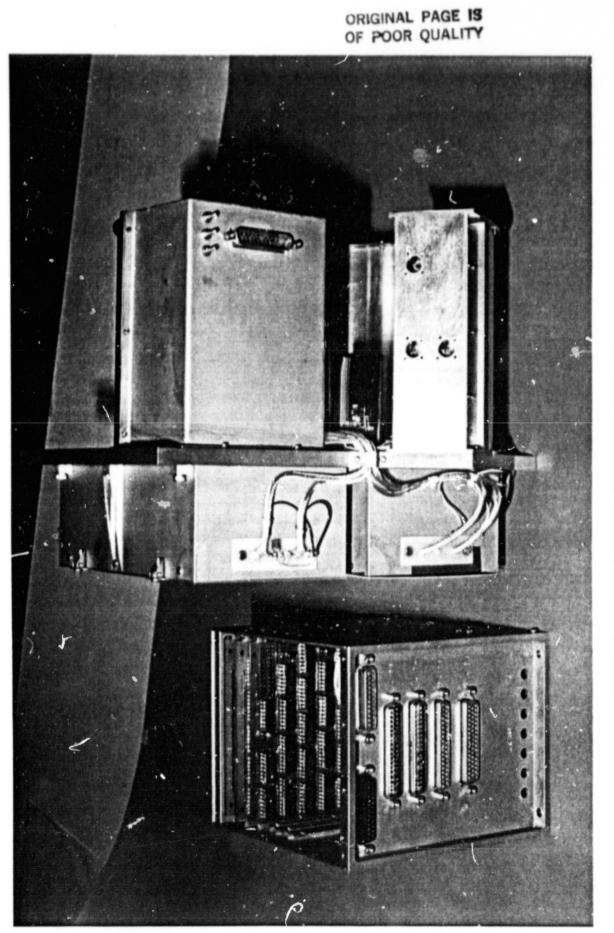
This data bus, in turn, was connected to a single, 16-bit parallel-toserial converter. The parallel-to-serial converter was used to format channeltron (and PPS command) data for subsequent transmission to the CENTAUR rocket's P.C.M. telemetry encoder. Control of the PPS's was provided by a PROM-stored sequence digital controller. At the P.C.M. system's minor frame rate, a binary counter within the PPS controller was incremented. The counters' output was used to form an effective address into the stored PPS command word table. Two such circuits were utilized, one each for the mass and energy programmable high-voltage power supplies. This rather simple procedure ensured that the power supply step levels were synchronized with the telemetry system's clock.

The CEP for FIMS B was a microprocessor-based system. As shown in the schematic diagrams contained in Appendix B, the FIMS-B controller was constructed from a few LSI microprocessor components. The system used an 8085 microprocessor with software stored in a single 8755 PROM/peripheral interface adaptor chip. The interface to the microchannel plate detector electronics was made through one of the parallel I/O ports on an 8155 RAM/I/O/timer chip. A "ready" flag from the microchannel plate electronics was used to strobe data into the 8155 and to interrupt the 8085. The function of the FIMS B microprocessor system was similar to that of a conventional laboratory multi-channel analyzer. Two 64-channel data memories were maintained by the system during spectrometer operation. As the detector electronics strobed its 7-bit wide digital data word into the 8155, the interrupted 8085 would read this data word as a pointer into data memory. The data word pointed to would then be incremented by one.

Separate interrupts were routed to the 8085 from the P.C.M. system's minor and major frame rate clocks. These additional interrupts were used to increment 8085 internal registers which, in turn, were used as pointers into a table of PPS command words. In a manner analogous to that of the FIMS A CEP, the FIMS B CEP was able to use the P.C.M. clock-interrupts just described to synchronize its PPS settings to telemetry timing. Measurements stored in the data memory were sent to the telemetry system through simple 8-bit shift registers.

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The two FIMS instruments, each equipped with a pair of programmable high voltage power supplies and a central electronics package, were integrated into their respective rocket payloads in early September, 1981. After a brief series of interface tests with the rockets' systems, both instruments were turned over to the payload integrator on 13 September 1981.



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FIGURE 3. FIMS-A FLIGHT INSTRUMENT WITH CEP

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IV. FLIGHT UNIT CALIBRATION

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The FIMS A and FIMS B flight units were calibrated in the SWRI RADIAC (Right Angle Deflecting Ion Accelerator) system in September 1981. A photograph of the calibration system appears in Figure 5, and a schematic diagram of its operation is shown in Figure 6. Close-up views of the ion generation system appear in Figure 7. The system is fully computer-automated except for the ion generator, which requires some manual settings. Automation is accomplished with a Hewlett-Packard 2113E minicomputer, which communicates with the calibration system, and the instrument under test, through a CAMAC interface system. This system controls the beam energy, the orientation of the instrument relative to the beam axis, and the various deflection potentials required in the instrument, while acquiring and storing the output data. An example of the excellent mass discrimination obtained with the mass spectrometer that is part of the ion accelerator is shown in Figure 8, in which relative beam current is plotted against the magnetic field produced by the 90° deflection system electromagnet. Purely electrostatic deflection can also be employed when a mixture of beam masses is desired.

An example of the calibration data acquired with the FIMS B instrument in a beam of N_2 ions is shown in Figure 9. Literally hundreds of such plots were required to characterize the response of the instrument for the full range of parameters over which it operated.

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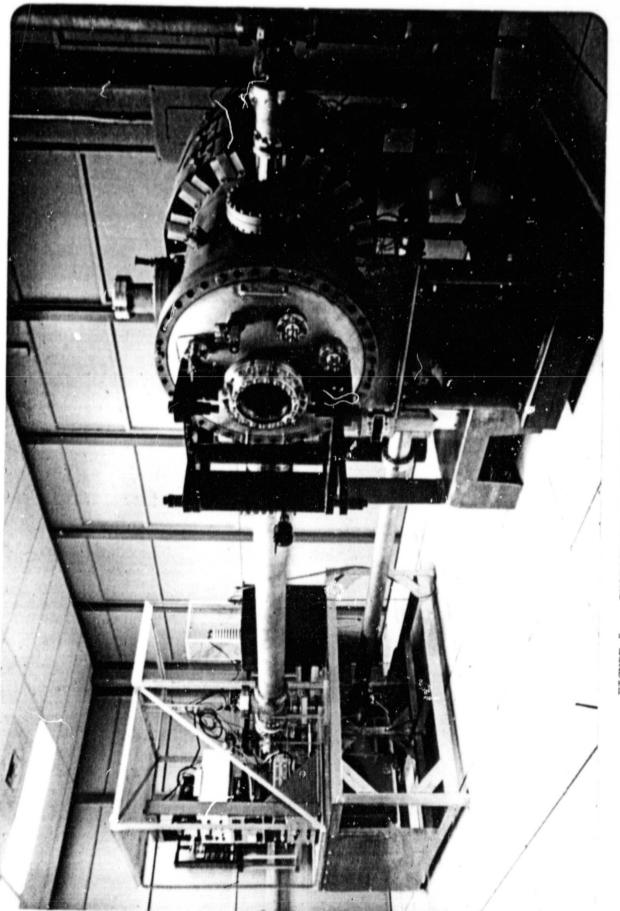
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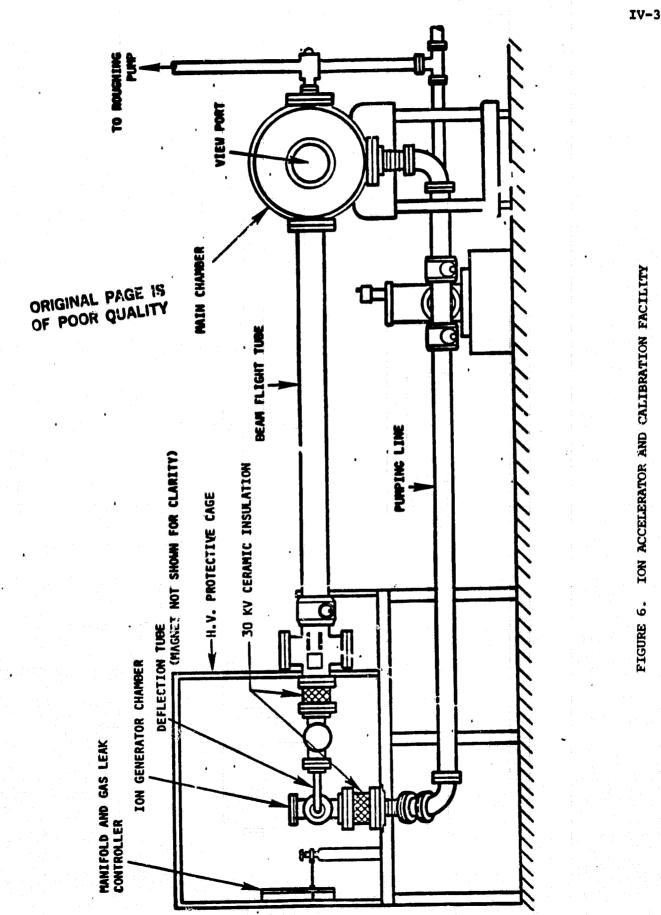
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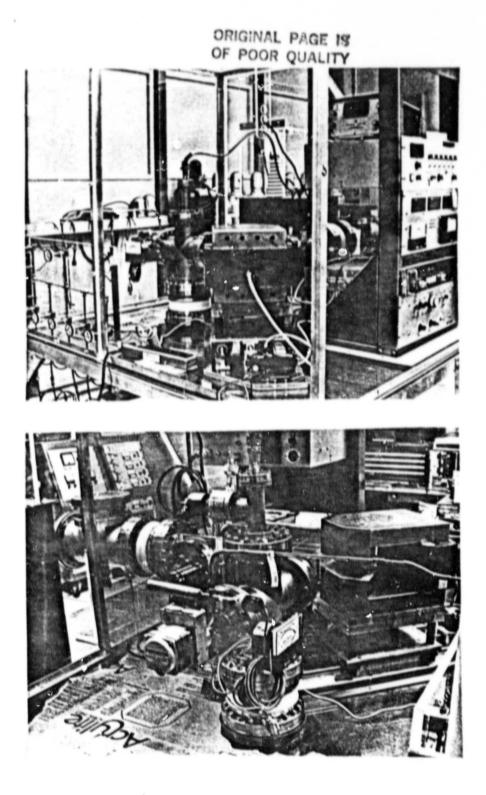
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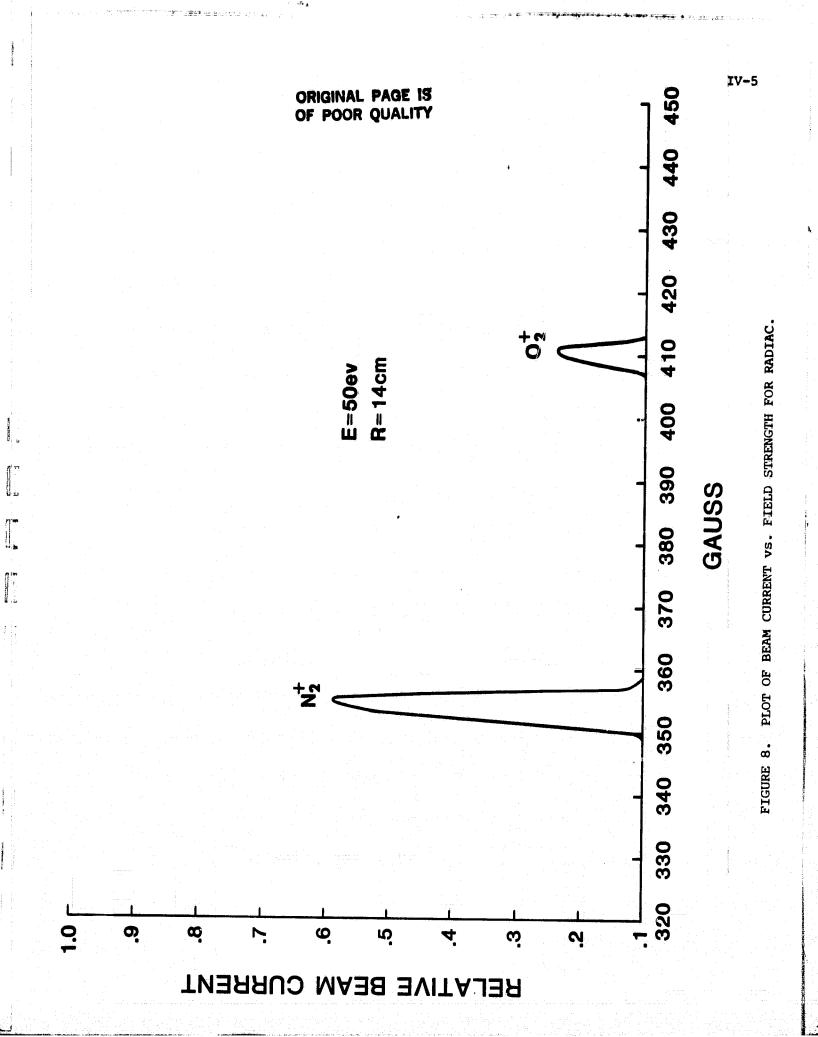
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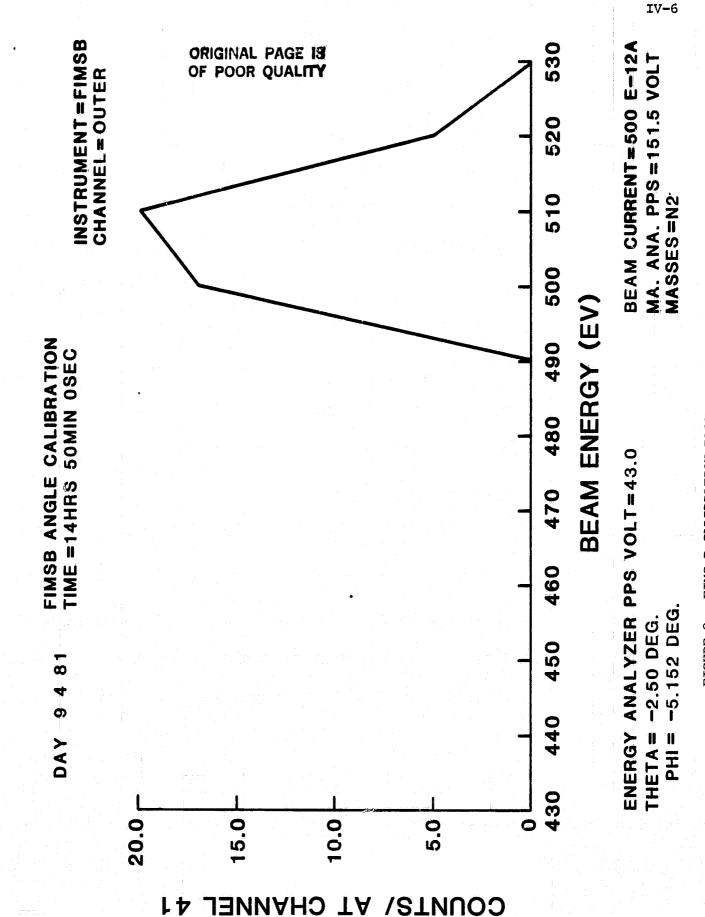
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FIGURE 7

ION BEAM GENERATION SYSTEM AND MASS SELECTION SYSTEM FOR THE FIMS ION ACCELERATOR SYSTEM. Top Photograph: View from behind the mass-selection electromagnet. Bottom Photograph: View from ion gun side, showing the electromagnet in its retracted position.





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FIGURE 9. FIMS-B CALIBRATION PLOT.

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V. FLIGHT INTEGRATION

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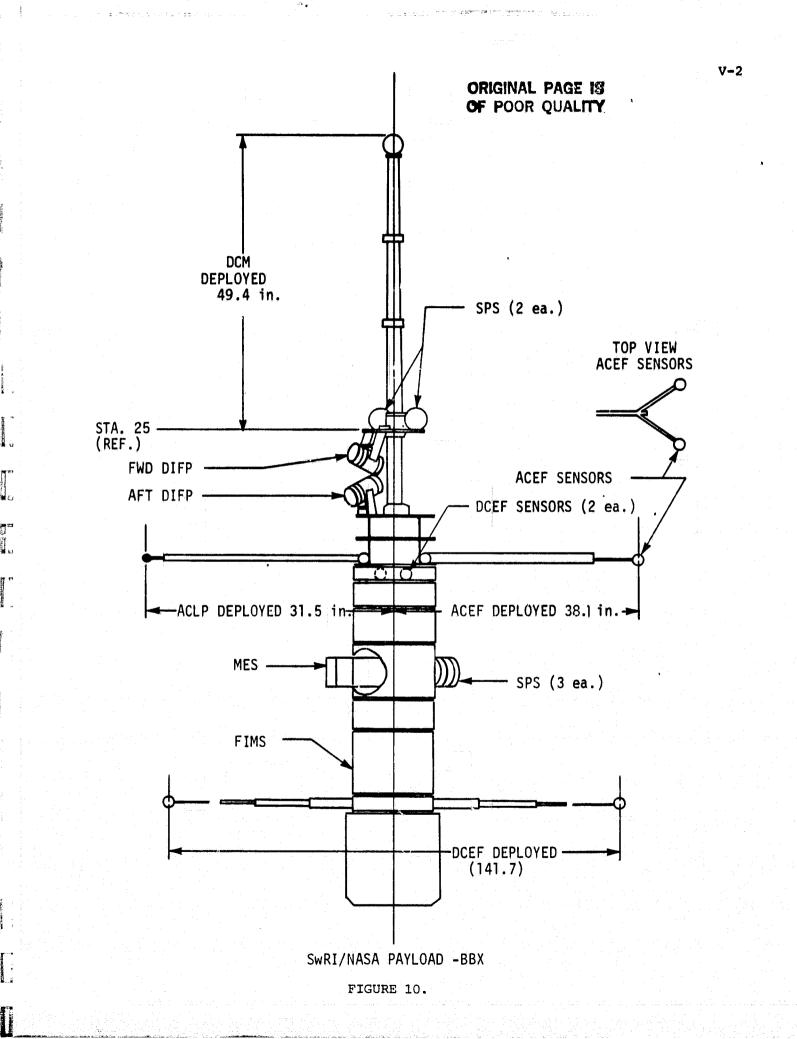
The two FIMS instruments were delivered to the payload integrator at SWRI in early September, 1981. Figures 10 and 11 show the locations within each of the two payloads where the instruments were mounted. The entrance apertures of the instruments were oriented 30° up from the X-Y plane as shown in Fig 10.

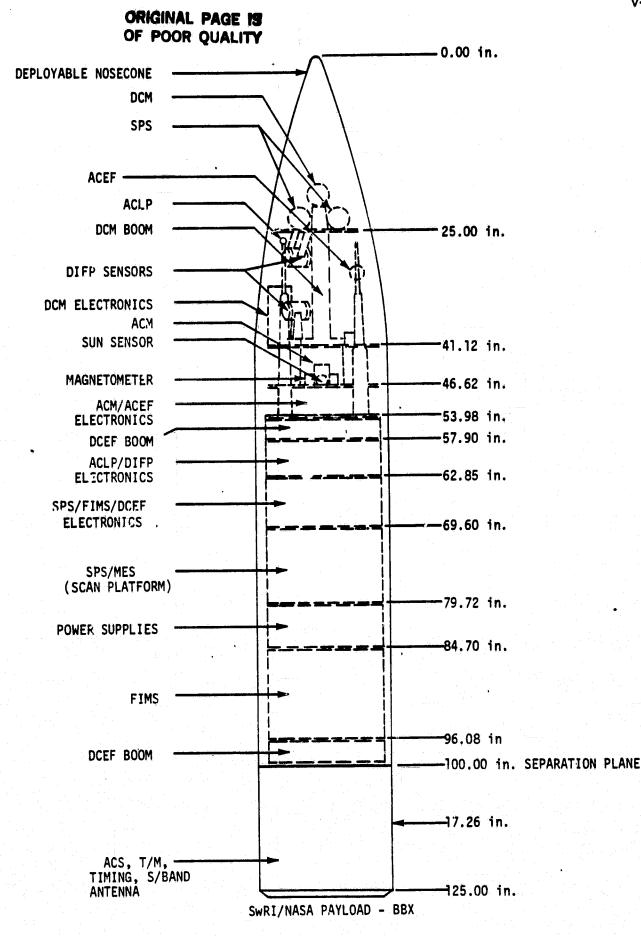
After a brief "fit check", the instruments underwent a series of interface tests with the rockets' telemetry and power systems. Only one minor change had to be made in the rockets' telemetry format for the FIMS A instrument in order to accommodate a minor design problem with the instrument's controller. Neither instrument seemed to be sensitive to EMI generated by other instruments or by the rockets' systems (i.e. scan platform) during ground testing, although FIMS B did encounter some EMI problems in flight from the scan platform.

Once the two payloads were checked out they were shipped from SwRI to the Goddard Space Flight Center. Arriving at GSFC on 18 September, the payloads were subjected to the usual series of integration and environmental tests. The payloads were later shipped to the NASA Wallops Island Tracking Station where the final assembly of the rockets took place and the integrated systems awaited shipment to Cape Perry, N.W.T., Canada.

The expedit on arrived at Cape Perry on 18 November 1981 and began making immediate arrangements for flight. During the preparations for flight no problems were experienced with either of the two FIMS instruments. The rocket carrying FIMS B (X35.001) was launched on 2 December 1981 at 01:38:01 local time. Telemetry records showed nominal performance from the microprocessorbased controller at launch and throughout the remainder of the flight. High voltage was applied to the instrument approximately 122 seconds after launch. Monitor circuits on the outputs of the programmable high-voltage power supplies reported normal performance for both supplies. The rocket was at approximately 600,000 ft. altitude when high voltage was applied. Telemetry records indicate normal behavior of all instrument systems for the remainder of the flight.

The rocket bearing the FIMS A instrument (X35.002) was launched on 13 December 1981 at 22:54:25 local time. Again telemetry records showed normal behavior of all instrument systems throughout the flight. High voltage was applied to the instrument at approximately 121 seconds after launch. No signs of high voltage-breakdown were seen in telemetry data records for the duration of the flight. Neither of the two rockets was recovered following flight.





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FIGURE 11.

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VI. DATA ANALYSIS

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Analysis of data from the two Project CENTAUR sounding rockets is being performed on the SwRI Space Sciences Data Analysis facility. The primary computational system is the VAX 11/750 CPU with 120 MB disk and tape drive that were partially funded through this contrct. Data display is being performed on our Chromatics CG 7900 color graphics system, which, for this application, is employed as a terminal to the VAX 11/750.

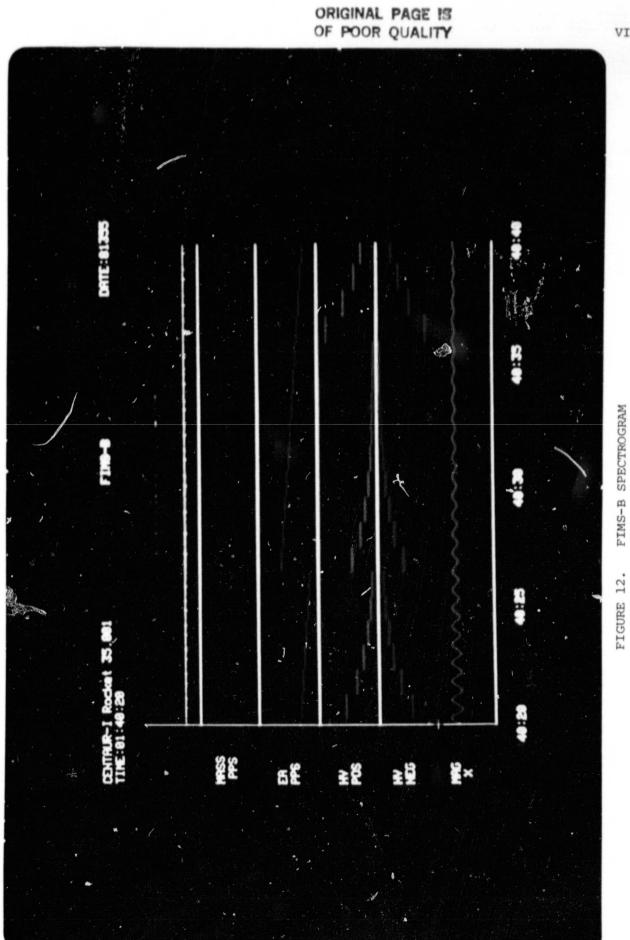
Up to this time only cursory examinations of the data have been performed, since the VAX system has only recently been placed into operation. Detailed data analysis will be performed over the next six months and the results will then be published in appropriate journals. In particular, the <u>Canadian Journal of Physics</u> plans to have a special issue on Project CENTAUR, and one or more papers on the FIMS results will be submitted.

Examples of the FIMS data displays now being generated are shown in Figures 12 and 13. In Figure 12 data from FIMS B for a segment of the flight of the first CENTAUR payload are presented in spectrogram format as follows. In the top panel, count rates are displayed as grey-scale intensi-ties versus angular channel number (vertical scale) and time (horizontal scale). Plotted below the spectrogram are the mass analyzer program power supply (PPS) steps, the energy analyzer PPS steps (both positive and negative), and the payload magnetometer data. Since the payload was oriented

nearly along B, the FIMS B channels sampled nearly constant pitch angles in the range of 30° to 60°. Note in Figure 12 that each of the 16 energy steps are held constant while a mass sweep is made, and that a complete energy-mass cycle requires approximately 20 seconds.

In the spectrogram at the top of Figure 12, where a few of the angular channels for each of the two FIMS B mass channels are plotted as grey-scale intensities, multiple and distinct mass peaks are seen for nearly every mass analyzer PPS sweep. However, one noisy angular channel is apparent in the lower of the two traces, and this particular channel will have to be disregarded in subsequent analyses.

In Figure 13 the FIMS B data are plotted in a different format, but with the same information content as in Figure 12. In these figures each 20-second segment of data is plotted with mass step on the horizontal axis, total count rate for all angular channels on the vertical axis, and energy on the 3rd (or depth) axis. Each energy "plane" is coded in a different color to aid in its identification. The effect of the one noisy angular channel appears as the high baseline or "plateau" that appears on all traces. Although this noisy channel became quiet later in the flight, it does not affect the quality of the data from the other 31 channels. The distinct mass peaks, which we identify as H⁺, He⁺⁺, and at times O⁺, are easily seen in the plots of Figure 13. These plots, and similar ones for FIMS A, will be the primary means of initial scanning of the data, which will then be followed by the required detailed analysis and interpretation.



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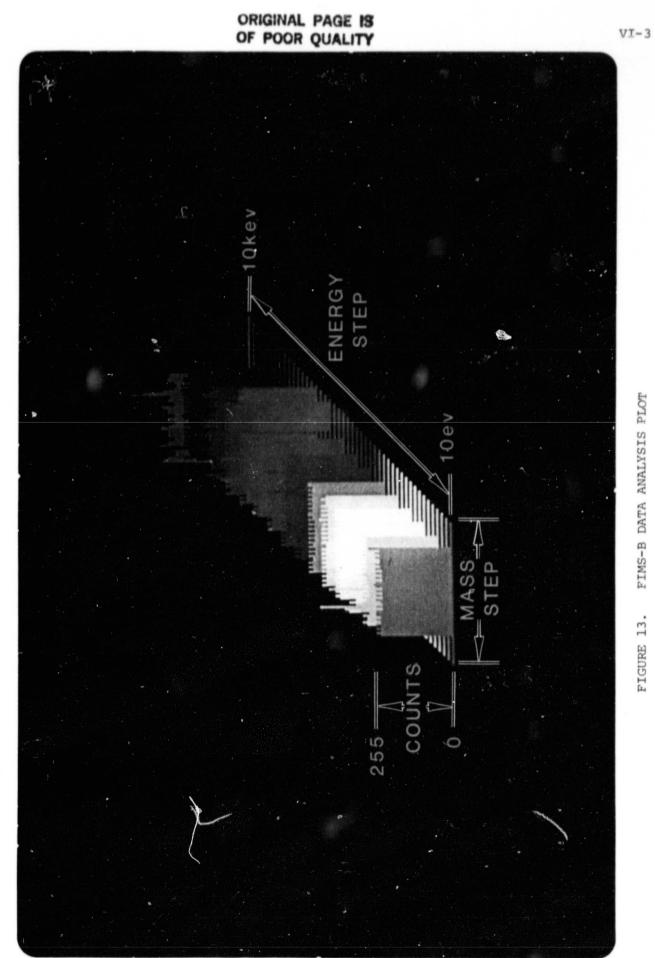
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APPENDIX A

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FIMS MODEL A

HARDWARE/SOFTWARE REFERENCE MANUAL

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FAST ION MASS SPECTROMETER MODEL A HARDWARE/SOFTWARE REFERENCE MANUAL

Submitted to

The National Aeronautics and Space Administration NASA Headquarters Office of University Affairs

By

The Space Science Department Instrumentation Research Division Southwest Research Institute Project 15-5680

The work performed under NASA Contract NASW-3237, the Development of a Fast Ion Mass Spectrometer.

September, 1981

Approved:

John R. Barton

John R. Barton, Vice President Instrumentation Research Division

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I. INTRODUCTION

This document provides a single location for all pertinent FIMS operational, calibration and repair information. It is hoped that this document will be of assistance in the field operation of the FIMS instrument should any problems arise with the instrument integrated onto its launch vehicle. Sufficient information is contained herein to troubleshoot the instrument should the occasion arise. All test and calibration records will be kept in this document as well.

The second purpose of this document is to aid in the development of future generations of similar ion mass spectrometers. The experience gained in the laboratory and the field with the FIMS A instrument will be of critical assistance in the development of similar instruments for the OPEN program or for other sounding rocket applications.

The introductory section of this document contains a copy of a paper published in the Review of Scientific Instruments which describes in some detail the geometry of the analyzer sections of the FIMS-A and FIMS-B instruments. As is described in this paper, the primary difference between FIMS-A and FIMS-B is in the area of the electrostatic energy analyzer and in the detectors used. The nomenclature "FIMS-A" refers to that instrument which uses the cylindrical energy analyzer and an array of three channel electron multipliers. The "FIMS-B" instrument uses a spherical energy analyzer and a microchannel plate array with resistive anodes. The Central Electronics Packages for the two instruments are also different. The FIMS-A instrument will be flown for the first time on the A rocket payload of the two payloads produced by the SwRI Department of Space Sciences during the summer of 1981.

This document will serve as a combination Design Specification and Instrument Log Book and should be kept with the FIMS B instrument at all times. Figures I-1 and I-2 are pictures of the fully assembled FIMS A instrument. Figure I-3 is a block diagram of the total instrument system.

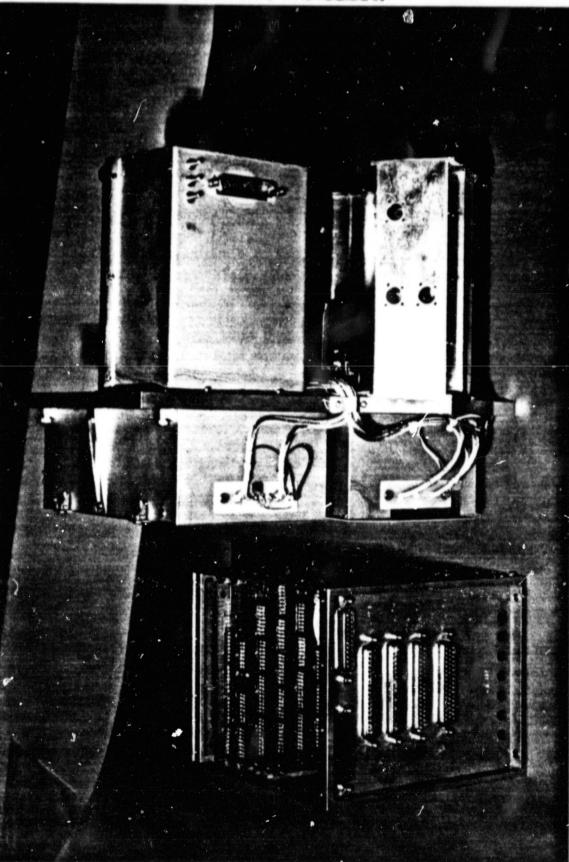


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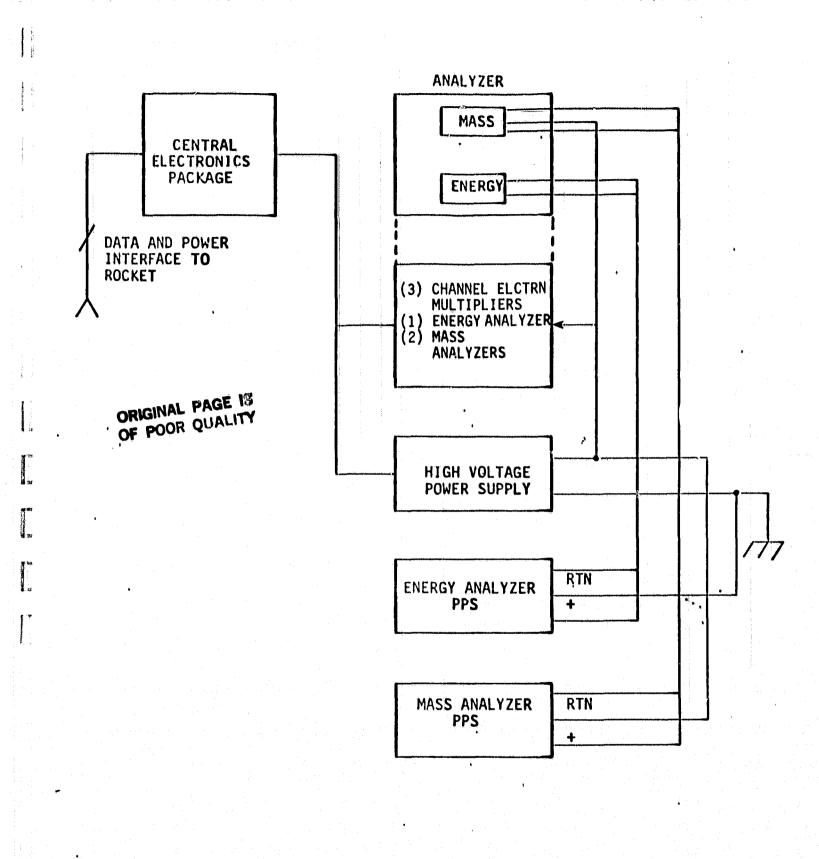


FIGURE I-3. FIMS INSTRUMENT BLOCK DIAGRAM

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II. QUICK-LOOK TEST INFORMATION

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This section presents a short summary of the data produced by the FIMS A instrument, the telemetry locations for these data sources and the procedures to be used to stimulate the various sections of the instrument to obtain responses viewable on PCM telemetry. Table I shows a list of the FIMS A data channels, their respective telemetry word locations and word lengths.

To describe the expected responses, via PCM, of the FIMS A instrument it will be assumed initially that the instrument is operating with only its low voltages and without the externally applied stimulus to its test input connectors. At the onset of power to the FIMS Central Electronics Package, the controllers contained therein will begin to produce stepping counts to the two programmable power supplies. These two 9-bit digital words (S31 and S32) are located in word 8, frame 19 and word 9, frame 20, respectively. Figure 1 is an example strip-chart recording of the output of the S31 data channel. The data contained in this channel is produced by the energy analyzer PPS command words. The stepping rate for this word is approximately 1 step/second. It should be noted that Figure 1 is a strip chart recording made with the deflection calibrated for 10 counts/major division. Since the strip chart recorders used in the PCM ground station are normally calibrated for a deflection of approximately 100 counts/major division, the recorder must be recalibrated using the PCM simulator if an exact correspondence is to be obtained with Figure 1. Such exactness is not necessary for routine operations and a general waveform analysis will be considered adequate.

Telemetry channel S32 corresponds to the mass analyzer program; power supply command words. S32 is located in word 9, frame 20 and this 9-bit value range from a minimum of zero counts to a maximum of 63 counts. Figure 2 is an example strip chart recording of the waveforms produced by the Central Electronics Package under normal operation. The mass analyzer commands steps at a rate of 1/major frame, or approximately 1 step every 25 milliseconds. Figure 2 is a copy of a stripchart recording made with the deflection sensitivity again set for 10 counts/division. The stripchart recorder speed was 25 mm/sec.

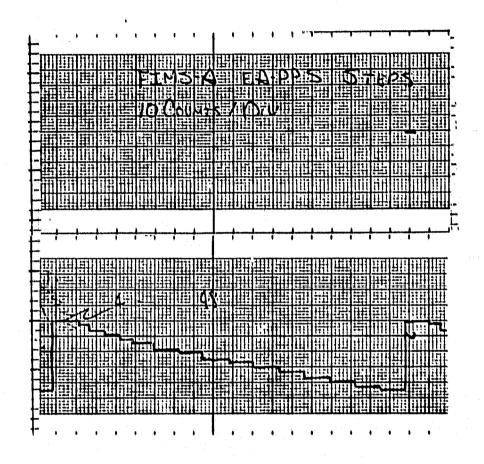
The FIMS A instrument produces a total of 5 channels of serial/digital data and 3 channels of analog data. At the onset of low voltage power only, only the S31 and S32 channels will show any information. Channels 28, 29, and 30 serial/digital words are derived from the instrument scalers and without high voltage beingoperated, no counts will appear in these channels. Likewise, analog channels 38, 39 and 40, corresponding to the float potential monitor, energy analyzer program power supply positive monitor and energy analyzer program power supply negative voltage monitor, will show 0 readings initially.

During the execution of a signal input test to the FIMS A instrument, which is described in the next section f this document, count values will begin to appear at serial data channels 28, 29 or 30, depending upon which input is being stimulated. It should be noted that all three of these channels are 18 bit words with the second of the two words in each case

corresponding to the 9 least significant bits of the count. Serial channel 28 (S28) contains the 18-bit digital count from the energy analyzer. Channel S29 corresponds to the outer mass channel (MA1). The third channel, S30, is used by the inner or second mass analyzer channel. As stated earlier, all three of these serial/digital channels are 18 bits in length. The S31 and \$32 channels are 9 bits each. As the test input stimulus is applied to the energy analyzer input, word no. 3 of frame 7 will begin to show counts. Word 2 of frame 7, corresponds to the nine most significant bits of this scaler word. If the setup test procedure for the input signal test is followed properly, it should be possible to see the counts building up as the pulse repetition rate is increased for the energy analyzer input by "iewing words 2 and 3 of frame 7. If the test input is applied to mar, analyzer channel 1, (S29), counts will begin to appear in words 2 and 3 of frame 23. Again, word 3 of frame 23 is the least significant 9-bits of the 18-bit word. Finally, as counts are applied to the mass analyzer channel 2, (S30), counts will begin to appear in words 8 and 9 of frame 3 with word 9 representing the 9 least significant bits of the 18-bit scaler. The procedure described in the next section of this document should produce a count of approximately 2300 counts in the 18-bit scaler words.

When high voltage is applied to the FIMS instrument by turning the timed 28V switch on, the three analog data channels will respond. Analog channel 38, corresponding to the float potential monitor, is located in word 7, frame 23. This monitor will assume an output of approximately 3.2 volts with the onset of high voltage. There should be no modulation on this channel since it represents the output of the PICO-PAK high voltage power supply and is not programmable. Analog channel 39, word 6, frame 27, corresponds to the positive output of the energy analyzer program power supply and its output can be seen stepping at the same rate as the digital word described earlier in channel S31. The output range for this monitor will be 0 - 2.4 volts. The third analog output from the FIMS A instrument is located in analog channel 40, word 6, frame 19, and it represents the analog output of the negative voltage monitor from the energy analyzer program power supply. It will also step at the same rate as analog channel 39 and its outputs should be of the same magnitude as channel 39 (word 6, frame 27). Only when high voltage is applied and the externally applied stimulus is connected will data be seen on all three analog and three of the five digital channels. Again, with low voltage only applied to the instruments, only channels S31 (word 8, frame 19) and channel S32 (word 9, frame 20) will show any counts. Figures 1 and 2 are examples of the waveforms which should be obtained from these two digital data channels with a stripchart recorder properly calibrated for 10 counts/ major division.

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FIGURE II-1

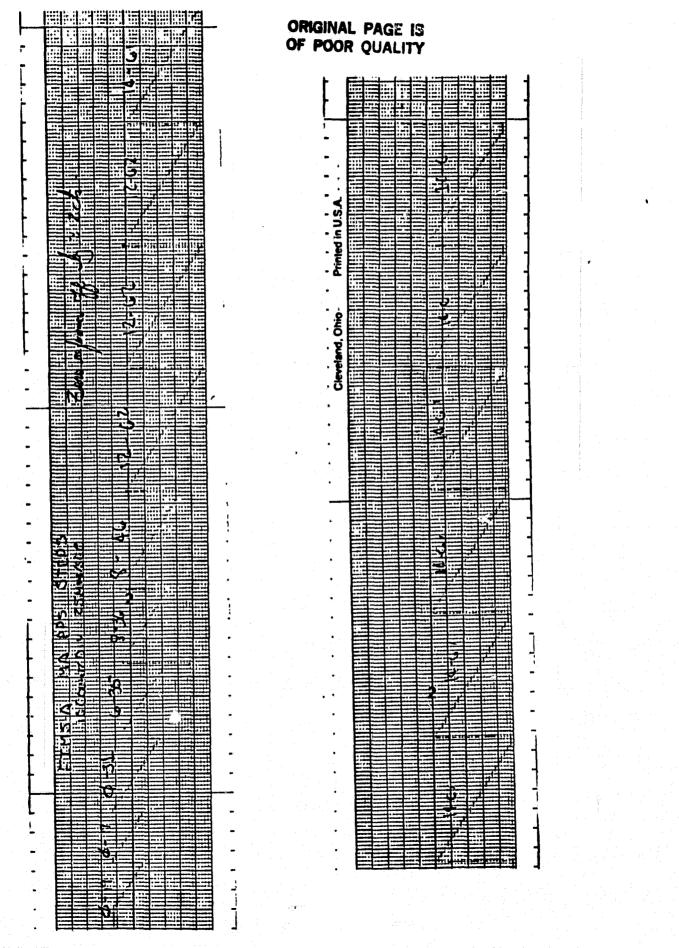


FIGURE TI-2

TABLE II-1

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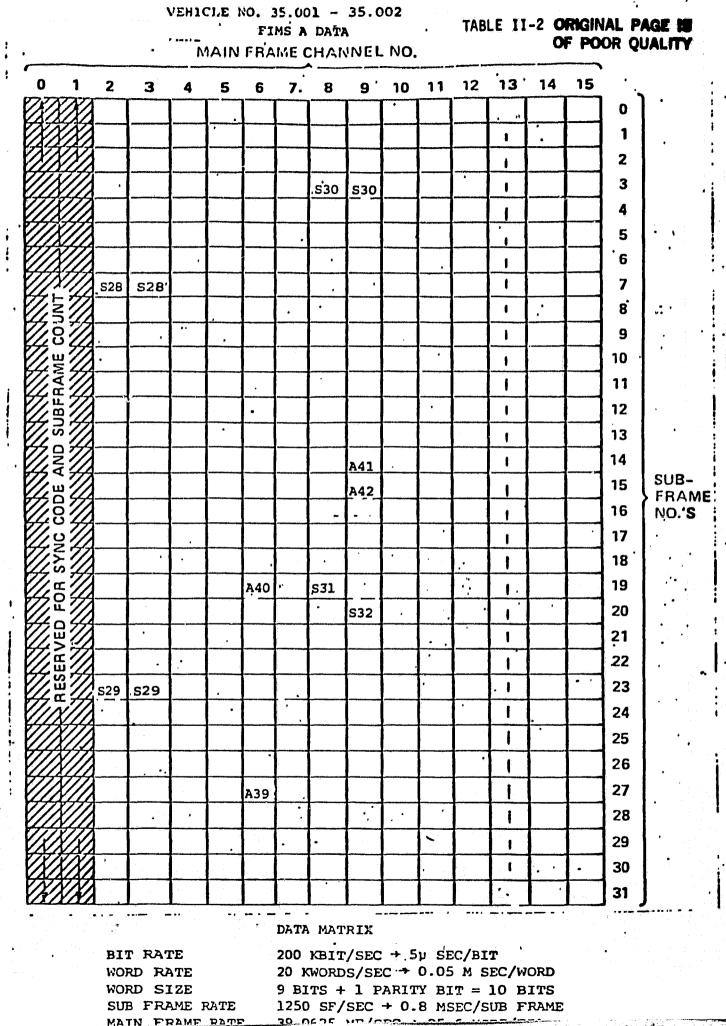
FIMS A PCM DATA MATRIX LOCATIONS

Digital Serial Data

Signal Name	Word Length	<u>Matrix No.</u>	Word	Frame
Energy Analyzer Data	18 bit	S28	2 and 3	7
Mass Analyzer Data No. 1 (inner)	18 bit	S29	2 and 3	23
Mass Analyzer Data No. 2 (outer)	18 bit	S3 0	8 and 9	3
EA PPS Step No.	9 bit	S31	8	19
MA PPS Step No.	9 bit	S32	9	20

Analog Data

Signal Name	Voltage Level	Matrix No.	Word	Frame
Float Potential (-HVPS)	0~3.2V	A38	7	23
EA PPS + HV Monitor	0~2.5V	A39	6	27
EA PPS -HV Monitor	0~2.5V	A40	6	19



MATN FRAME PATE

III. SIGNAL INPUT TEST PROCEDURE

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The following equipment will be needed to perform the signal input test of the FIMS A instrument:

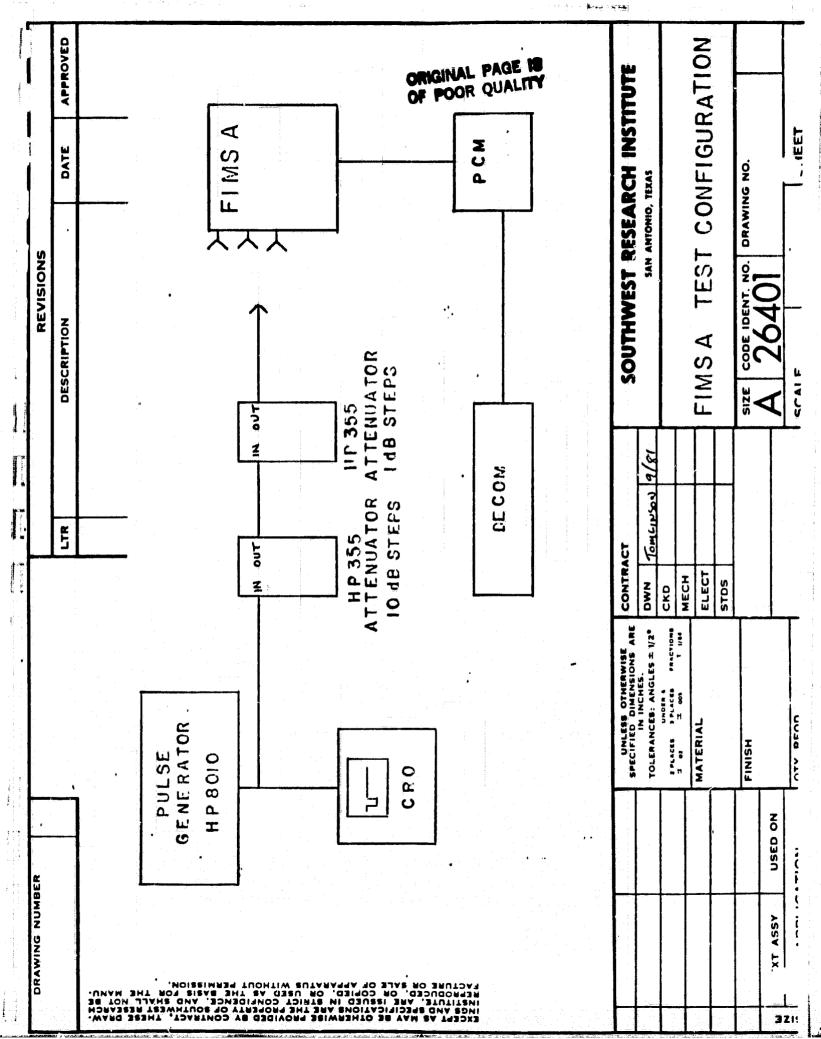
- 1. A pulser or function generator capable of producing IV negative going pulses, approximately 50 ns wide into a 50 ohm termination.
- 2. Two variable HP 355 VHF attenautors.
- 3. An oscilloscope with performance capable of accurately demonstrating the 50 ns negative-going waveform produced by the pulser.
- 4. Four lengths of coaxial cable, each terminated with a male BNC type connector.
- 5. A 50 ohm terminator contained in a standard BNC connector shell.
- 6. A short length of coaxial cable with a BNC on one end and a SELECTRO No. 51-424-3188 connector on the other end.

To perform the signal input tests, perform the following steps:

- Connect the output of the pulser to the input of the oscilloscope, through a BNC "T", the other side of which is connected to the 50 ohm termination.
- 2. Adjust the output of the pulser as viewed on the oscilloscope to produce a negative-going pulse approximately 1V in amplitude, 50 ns wide, with a 20 ns fall time, a 20 ns baseline time and a 20 ns rise time. The baseline voltage for this waveform should be 0 V, with the peak amplitude of -1 V.
- 3. With the pulser adjusted for the proper parameters to produce the waveform described, install the FIMS test cable described earlier and set the pulse repetition rate to approximately 100,000 pulses/second.
- 4. Set the HP attenuators in such a manner that an attenuation of approximately 29 dB is obtained. If no counts are seen in TM_r lower the attenuation in 1 dB steps to a minimum level of 20 dB until counts appear.
- 5. Apply low voltage to the FIMS Central Electronics Package and observe the corresponding count rates on the telemetry channel assigned to whichever input channel is being used on the instrument. For the energy analyzer, the telemetry assignments are words 2 and 3 of frame 7. For mass analyzer channel 1, the telemetry assignments are words 2 and 3 of frame 23; for the second mass analyzer channel, the telemetry assignments are words 8 and 9 of frame 3. With the pulse repetition rate set

to the value mentioned earlier, it should be possible to observe a count of approximately midscale on both words of the 18-bit word scaler. Since the sample period for each major frame is 23.2 ms, the 18-bit accumulator is capable of count rates of greater than 10 mHz.

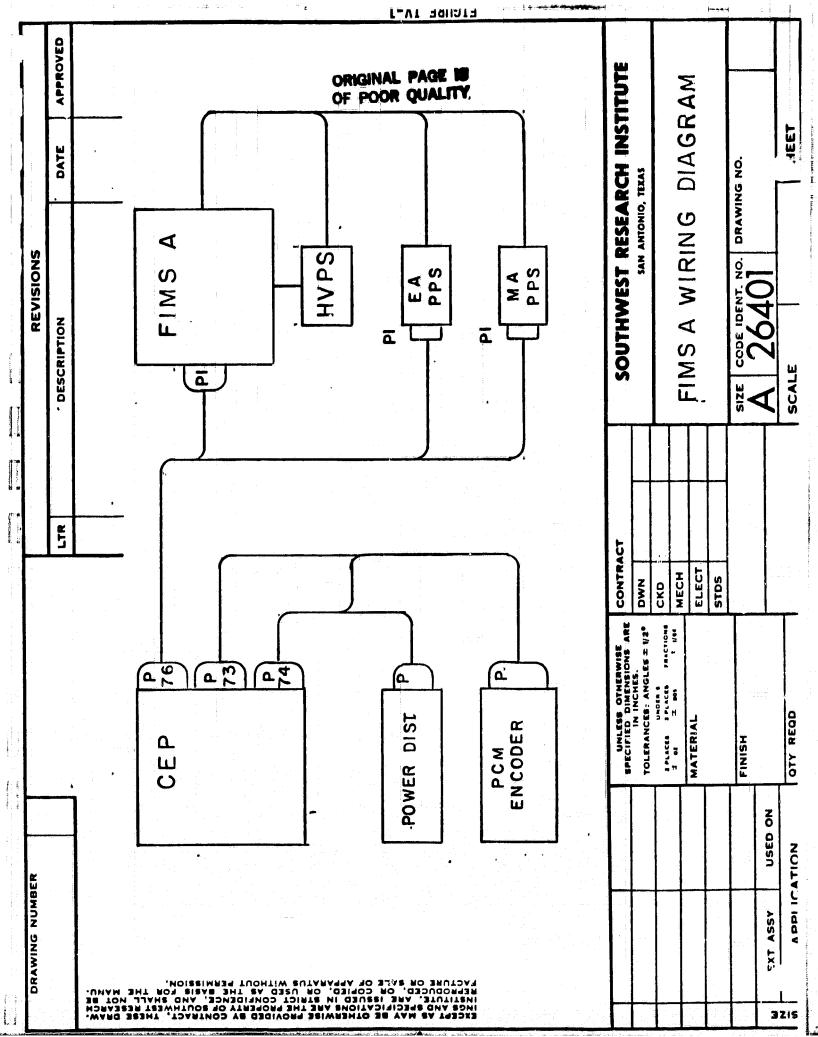
Figure III-1 shows the test configuration.



IV. INSTRUMENT CARLING INFORMATION

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This section contains the wiring lists and cable pin connections which were used in the construction of the FIMS instrument. Any problems arising from miswiring can be solved by referring to the original wiring lists for the instrument.



SOUTHWEST	RESE/	ARCH INS	TITUTE	Code ID,		Number	Rev. Ltr.	Date
ONNECTOR DD50(P) N	MB	TITLE FIMS	"A" CEP	TO INST	RUMENT	& PPS	SI	
FROM CONNECTOR	PIN NO,	WIRE ID.	LGTH. FT,	SIG	SNAL	DESCRIPTION	PIN NO	CONNECTO
P76	1			EA PPS	BITØ		W	EA-PPS-P1
88	2	• 		11	" 1		Т	11
H	3			06	"2		Р	1 1
•	4			11	" 3		R	
9 1	5			H	" 4	·	x	11
ii -	6			11	" 5		U	
80	7			- - -	RETURN	l	M	i)
11	8			MA PPS	ANODE	BIT Ø	V	MA-PPS-PI
IJ	9			MA PPS	CATHO	DE BIT Ø	W	11
11	10) ;		MA PPS	ANODE	BIT 1	т	11
11	11			MA PPS	CATHO	DE BIT 1	Α	11
00	12			MA PPS	ANODE	BIT 2	S	0)
11	13			MA PPS	CATHO	DE BIT 2	P	n
01	14			MA PPS	ANODE	BIT 3	R	Ně .
11	15			MA PPS	CATHO	DE BIT 3	J	11
11	16			MA PPS	ANODE	BIT 4	M	ti
14	17			MA PPS	CATHO	DE BIT 4	X	
	18			MA PPS	ANODE	BIT 5	U	lî
	19			MA PPS	CATHO	DE BIT 5	N	\$i
11	20			EA PPS	+V MO	VITOR	F	EA-PPS-P1
U	21				RETUR		N	11
D.	22			EA PPS	-V MOI	NITOR	V	41
1.11	23				RETUR	1	N	11
91	24							
Û.	25				· · · · · · · · · · · · · · · · · · ·	ORIGINAL P	CE R	
P76	26					OF POOR QL		

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FIGURE IV-2.1

SOUTHWES	T RESE	ARCH INS	TITUTE	Code ID, Number R	evi Ltr.	Date
ONNECTOR DD50(P) NM	1B	TITLE FIMS '	'A" CEF	TO INSTRUMENT & PPS	SHE	ET 2OF_2_
FROM CONNECTOR	PIN NO.	WIRE ID.	LGTH, FT,	SIGNAL DESCRIPTION	PIN NO,	TO CONNECTO
P76	27			TIMED 28V TO EA PPS	В	EA-PPS-P1
	28	*		CHASSIS (SHIELD)	С	MA-PPS-P1
11	29			28V RTN TO EA-PPS	E	EA-PPS-P1
et.	30			TIMED 28V TO MA-PPS	D	MA-PPS-P1
ài.	31			28V RTN TO MA-PPS	Е	MA-PPS-P1
61	32			MA PAD #1 DATA	1	FIMS P1
ji)	33			RETURN	2	
11	34			MA PAD #2 DATA	15	
	35			RETURN	16	
11	36			EA PAD DATA	4	
0)	37	ange og den for en ander het den for den fyldere		RETURN	5	FIMS P1
U)	38	tarina du mujaria yakina kuta yakang ku		SHIELD (CHASSIS)	С	EA-PPS-P1
11	39) all its good and an	FLOAT POT. MON	20	FIMS-P1
H	40	Min Tarakan Principality and		RETURN	8	;j
0	41			SHIELD (NO CONNECTION AT CE	P 21	1)
	42			+8V	18	11
ti .	43			-8V	19	01
U)	44	in an		COMMON (8V)	6	
14	45		a Yana da maya na mana na mana Na mana na mana Na mana na mana	hard a fin de anna an a	-	ŧ1
00	46	*****		+5V	9	è)
tt .	47			5V RETURN	10	•
11	48		1 <mark></mark>	TIMED 28V TO INSTRUMENT	13	1 1 1 1 1
	49			28V RETURN	25	
H	50		,	CHASSIS	21	FIMS-P1
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FIGURE IV-2.2

SOUTHWES	T RESE	ARCH INST	ITUTE	Code ID, Number	Rev. Ltr.	Date
CONNECTOR P73 (37S)		TITLE		FIMS/SPS CEP	SHE	ET_1_OF_2_
FROM CONNECTOR	PIN NO.	WIRE ID.	LGTH. FT,	SIGNAL DESCRIPTION	PIN NO,	
P73 (37S)	1			+5v from LVPS #8 (900mA)	8	P97
	2.	2 E		COMMON (5 RET)	7	19
U.	3			+8 VDC (50mA)	5	08.
11	4			-8 VDC	6	88. ¹¹
60	5		2	COMMON (8V)	12	11
0	6			-6 VDC	10	H
H	7	1.		+6 VDC	9	N
	8			COMMON (6V)	NC	USE 8V COMMON
11	9			-5 VDC	11	P97
11	10			COMMON	NC	USE 8V Common
91 -	11			(Ki TIMED 30 VDC to FIMS HVPS	B) 6	P93
	12		а А.	30 V RETURN	25	P93
H	13			MF2 ³	4	P100
01	14			SF2 ⁴	5	P100
H	15			SIGNAL RTN (GND)		NC
1)	16			GATED CLOCK	9	P100
11	17			MF 2 ³	14	P101
81	18			SIGNAL RTN (GND)		NC
H	19			FIMS "B" DATA S27	1	J107
11	20			" S28	2	J1 07
01	21	ORIGINA	L PAG	19 " \$29	3	J107
N	22	OF POC		" S30	4	J1 07
11	23			" S31	5	J1 07
01	24			" S32	6	J 107
	25			DIGITAL SIGNAL RTN (GND)	7	J107
	26			FIMS ENABLE GATE EG27	8	J107
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SOUTHWES	T RESE		TITUTE	Code ID.	Number	Rev. Ltr.	Date
ONNECTOR P73 (37S)		TITLE		FIMS/SPS	CEP	SH	EET _2_OF _2_
FROM CONNECTOR	PIN NO.	WIRE ID.	LGTH, FT,	SIGNAL	DESCRIPTION	PIN NO.	TO CONNECTO
P73 (37S)	27			ENABLE GATE	EG 28	9	J107
••	28			FIMS	EG 29	10	0
1)	29	· · ·		11	EG 30	11	
a)	30			ji	EG 31	12	"
IJ	31				EG 32	13	01
	32			FIMS ANALOG	DATA A38	38	J109
11	33			FIMS	A39	39	J109
••	34				A40	40	J109
11	35				A41	41	J109
¥	36				A42	42	J109
11	37			ANALOG RTN (GND)	43	J109
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SOUTHWES	T RESE	ARCH INS	TITUTE	Code ID.	Number	Rev	<i>i.</i> L1 r.	Date
CONNECTOR (3 P74	17S)	TITLE FIMS/S	SPS CEP)			SHE	ET_LOF
FROM CONNECTOR	PIN NO.	WIRE ID.	LGTH. FT.	SIGNAL	DESCRIPTION)	PIN NO.	
P74(37S)	1			SPS DIGITAL	DATA S12 (SPS)	19	J106
11	2	•		01	S13		20	10
.	3				S14		21	Û.
¢0	4			0	S1 5		2 2	30
98	5			ţţ.	S1 6		23	80 <u>.</u>
11	6			11	\$1 7		24	88 -
1)	7			11	S1 8		25	91
88	8			11	S19		26	81
	9			10	S20		27	11
11	10			•••	<u>S21</u>		28	11
11	11			11	S22		29	
n	12	-		DIGITAL RTN	(GND)		30	
11	13			ENABLE GATE	EG 12		32	11
ţi.	14			lt	13		32	0
U .	15				14		33	н
11	16		anat P	AGE IS "	15		34	00 g
31	17	ORIG OF P	OORQ		16		35	1
11	18		 	10 a.c.	17		36	
11	19	<u></u> +			18		37	. It
H	20			81	19		38	
 1)	21			0	20		39	
	22			0	21		40	, Ú
01	23			H	22		41	11
8	24			CHASSIS			NC ·	••
11	25			200 kHz CLOC	ĸ		20	P100

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SOUTHWEST	RESE		TITUTE	Code ID.	Num	ber	Rev, Ltr.	Date
ONNECTOR P74 (37S)		TITLE		FIMS/SPS CE	P		SHE	ET_2_OF_2_
FROM CONNECTOR	PIN NO.	WIRE ID.	LGTH. FT.	SIGNAL	DESC	RIPTION	PIN NO.	
P74(37S)	27			SF 2 ⁴			9	P101
••	28	•		SIGNAL RETU	RN		NC	
II	29			ANALOG DATA	A12 S	PS 3 kv	12	J110
11	30			SPS 3 kv	A13	-3 kv	13	n 1
••	31			SPS PPS	A14	PPS	14	••
	32			ANALOG RTN	(GND)			NC
ł)	33			CHASSIS CON	NECTIO	N TO FIM	s	NC
	34			SF 2 ³			17	P100
u	35			5 µs GATE		 	21	P101
	36			landa aya ay a				
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V. INSTRUMENT TEST RECORDS/HISTORICAL LOG

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This section of the system reference manual will be used to store all instrument test records taken during the field test and laboratory calibrations of the instruments. Records of shipment and installation and removal of the instrument will be maintained in this section of the document.

FIMS TEST RECORD/HISTORICAL LOG

DATE

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LOCATION

TEST DESCRIPTION

PROFILE STREET

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PROBLEMS/COMMENTS:

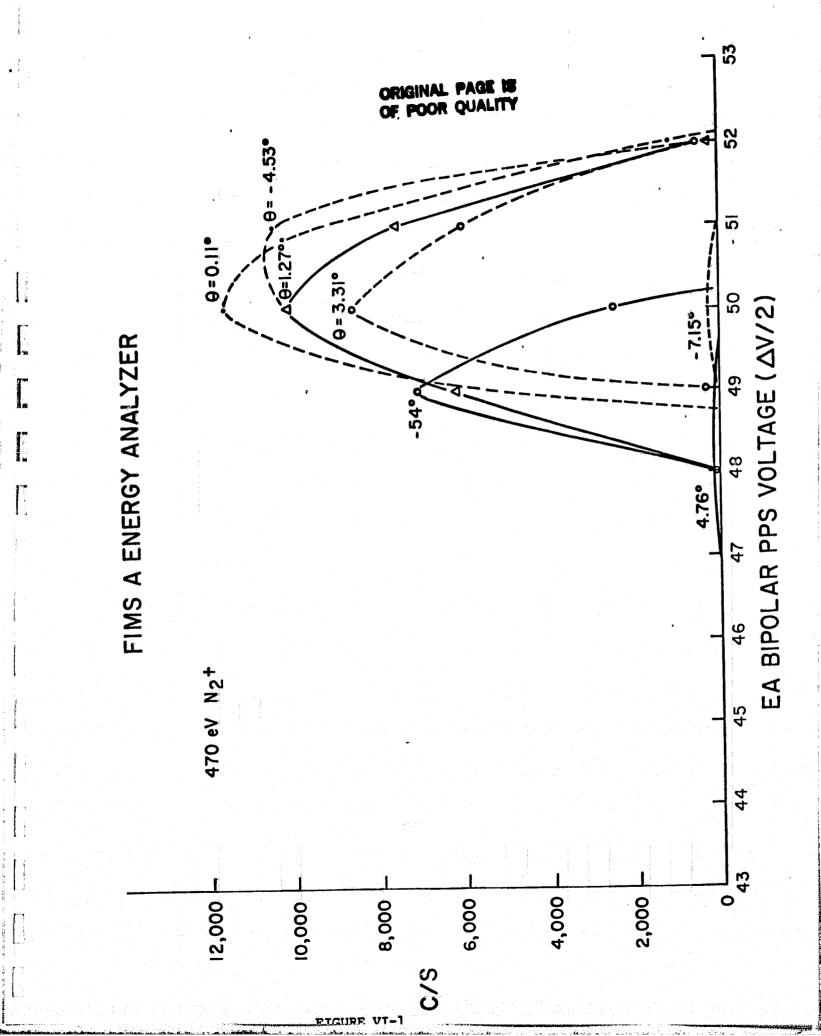
FIGURE V-1

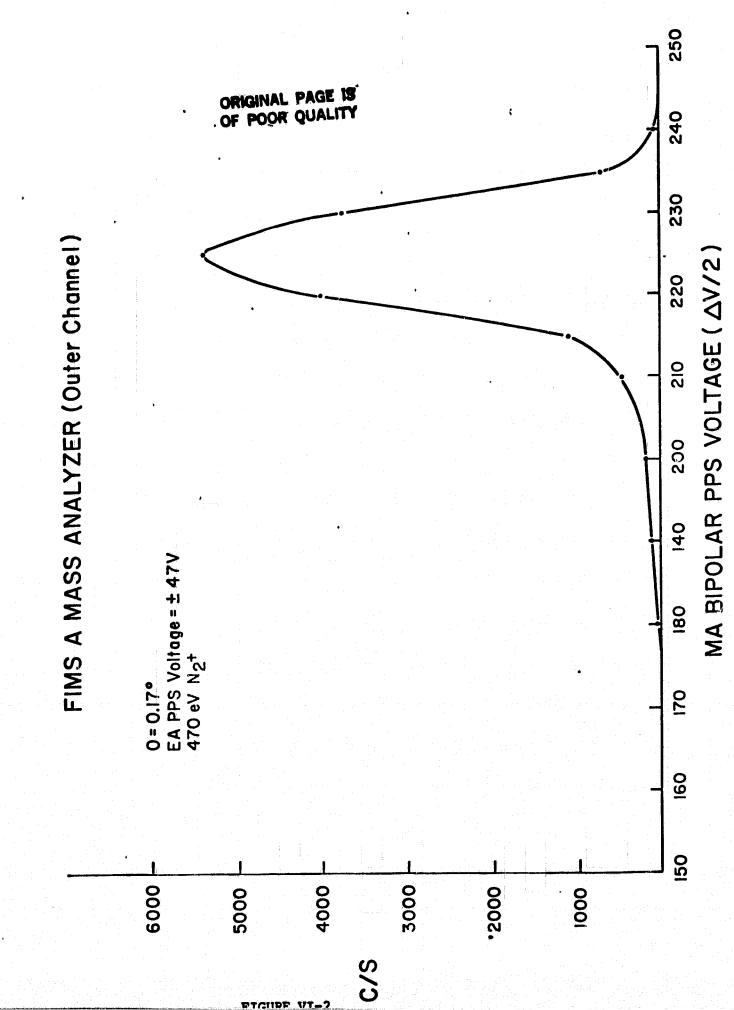
VI. INSTRUMENT CALIBRATION RECORDS

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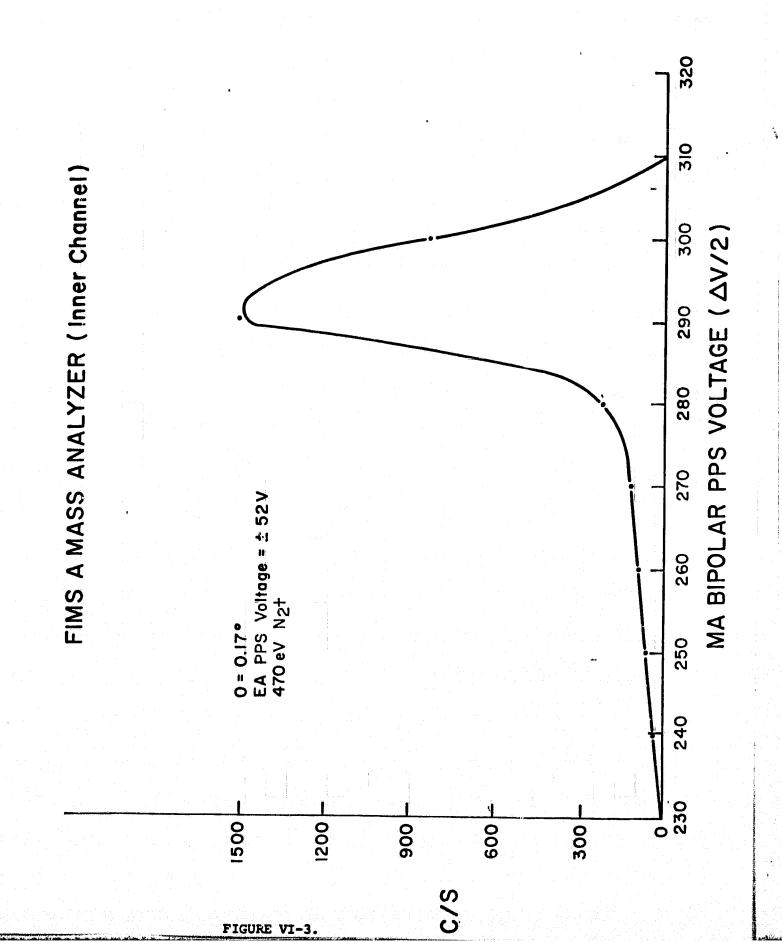
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This section of the system reference manual contains all of the original calibration information taken for the FIMS A instrument. Any subsequent calibrations or modifications to the instrument will be noted both in this section and in the previous section.





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VII. INSTRUMENT DESIGN INFORMATION

A. Analyzer Design

The details of the design information pertinent to the energy and mass analyzer sections of the FIMS instrument are described in some detail in Section I of this document. In particular, in the scientific paper published by S. F. Hahn and J. L. Burch in the Review of Scientific Instruments, February 1981. No further comments on the analyzer design will be made in this section of the reference manual.

B. CEP Design

Figures 1, 2 and 3 are the schematic diagrams of the Central Electronics Package used to control the operation of the FIMS A instrument. As can be seen, the system consists of three circuit boards populated almost entirely of CMOS logic with additional help from low-power Schotky prescalers in the area of the high-speed digital accumulators. Board 1 of the three-board set contains all three digital accumulator channels and their respective output buffer latches with tristate drivers. The control lines to support the operation of the accumulators is provided by a decoded state controller located on Board 2. Basically the accumulators are allowed to acquire counts from their respective pulse-amplifier discriminators located within the detector assembly of the instrument for all but 3 minor frames of the telemetry format. With the receipt of an SF 2^4 rising edge, the controller on Board 2 inhibits the accumulation of counts from all three accumulators on the scaler board and transfers those accumulated counts into the buffer-store latchets shown on the Board 1 schematic as 54C 374's. As the time arrives for the transmission of the accumulated count information to the telemetry system, the selected digital data channel is enabled and the tri-state outputs of the octal buffer latches are turned on, thus applying the accumulated count output to the input of the systems one and only shift register assembly. The shift register hardware is located on Board 2 of the set. Each of the three accumulator channels is read out at its respective time in the PCM format. For a period of 3 minor frames, the accumulators are disabled by the controller on Board 2. This dead time is necessary because of the flyback period of the programmable power supplies used on FIMS. Since the power supply outputs are not stable for a period of 3 minor frames, it is desired that data counts not be accumulated during this period. As seen in the schematic of Board 1, each accumulator channel consists of a 14-bit CMOS synchronous counter string preceded by a 4-bit LS counter. The QA output of the first stage LS counter is brought out as the clock input to the following CMOS counters. This configuration results in the LS counter being used as a high-speed prescaler for the CMOS counters that follow. Since the output of the PAD amplifiers located within the instrument are only 50 ns wide, it is not possible to apply this input directly to the slow CMOS counters, thus the need for the LS prescaler. The board layout for Board 1 is shown as Figure 4 of this document.

Card 2 of the FIMS Central Electronics Package contains all of the instrument controller functions as well as two erasable programmable read-only memory chips in which are stored the PPS commands for both power supplies. As seen on the schematic, the instrument controller is keyed to the rising edge of the SF 2^4 timing signal. Each time a transition is seen on this line, the controller clocked by the 200 kHz clock, goes through 7 states which are used to transfer the accumulated count data in the scalers into their respective buffer latches, to fetch and store the next PPS command from the erasable PROMS into low-power octal latches and to induce the 3 minor frame delay before resumption of count accumulations on the accumulator board. Card 2 thus contains all of the instrument controller functions and it functions basically by keying its activities to the rising edge of the SF 2^4 clock to perform its duty as controller for the two PPS's and the accumulators. (Board 2 layout is Figure 5.)

Card 3 of the FIMS 8-card set contains the interface logic used to receive the incoming signals from the PCM subsystem. As seen in the schematic, the typical electrical interface consists of a 10K ohm pull-up resistor and shunt, with a 100 picofarad capacitor. The line receivers used in this application are 54C of 914's, yielding a high-noise immunity, medium-speed interface circuit. (Fig. 6 is board 3 layout.)

The FIMS A instrument central electronics package is completely hardware controlled; thus, only the PPS commands are stored in PROM and no other software operations are involved. Figure 7 shows component list for the CEP.

The intra-instrument cable details for the FIMS A CEP are shown in Figure 8 of this document. They are provided here should any changes be necessary or construction of any additional cables be required.

C. Programmable Power Supplies

1

FIMS-A uses two programmable high voltage power supplies. Both power supplies were constructed from the design drawings prepared by the Goddard Space Flight Center for use on the HAPI and LAPI instruments on the Dynamics Explorer Satellite.

A few design changes were made to the power supplies to adapt them to the scientific and operational requirements of the FIMS/Centaur rocket program. The two power supplies used by FIMS are referred to as the energy and mass units, corresponding to the two sections of the instruments analyzer. The energy supply is very similar to the standard D. E. design. Table VII-1 shows the relationship between the 6-bit programming code word and the high voltage output.

The mass analyzer power supply is considerably different from the basic P.P.S. design, the principal difference being that this supply has its high voltage output floated at the -3200 VDC bias used for the mass analyzer float potential. To realize this objective, a considerable effort was invested in insulating the electronics within the supply from the chassis-grounded case of the power supply. The inner surfaces of the case of the power supply are lined with a fiberglass material to prevent high voltage breakdown. The second major change was the addition of a set of six optical couplers to interface with the 6-bit parallel command interface to the CEP. In summary, the mass P.P.S. has its high voltage return connected to the minus high weltage output of the HVPS, thus floating most of the circuits within the supply at -3200 V. A 50-megohm resistor was placed in series with the high voltage return line to the minus high voltage output of the bias power supply (PICO=PAK).

The third major change was the modification of the input multiplexer circuit and the output voltage range. The resistor network for $R_{\rm IN}$ and $R_{\rm F}$ was changed to allow the output voltage to step from a maximum of 982 V to a minimum of 25 V in 64 steps.

Table VII-2 shows the relationship between the 6-bit programming code and the high-voltage output of the mass supply.

D. High Voltage Power Supply (HVPS)

-

The FIMS-A instrument uses a single HVPS to provide the float potential for the mass analyzer deflection plates and the bias for the channe electron multiplier. A standard PICO-PAK Model PP9N provides the bias potential. The pages following contain specification sheets for the unit used on FIMS-A. The high voltage output has been adjusted to -3200 VDC for this application.

E. Fims Instrument Detector Assembly

The detector assembly as shown in the introductory section contains two (2) printed circuit boards containing the charge/preamplifiers for threas channel electron multipliers, analog-buffer circuit for monitoring the float or bias potential and the necessary voltage divider resistor networks for the CEMs. The schematic diagram for the instrument is shown in Figure 9 and the component parts list is shown in Figure 10.

SPECIFICATIONS I.

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A.	Ele	<u>Electrical</u>									
·	1.	Input Voltage: 28 ± 4 volts; negative ground.									
	2.	Regulation: Line: < ± 0.4% Load: (50 to 250 mW output): < ± 1.5%									
	·3.	Ripple: <0.007% peak to peak. *									
	4.	Output power: 250 mW maximum.									
	5.	Output voltage: See Table 1 below.									
٠	6.	Input current: See Figure 3.									
	7.	Temperature drift: < ±1% from -30°C to ±71°C									
	8.	Dynamic load regulation: Equivalent power supply source impedance < 100 K Ω at 1 KHz to < 1 K Ω at 0.5 MHz.									
•											

MODEL	TABLE 1	• MAX WEIGHT IN Without Output Protect 'n	With Output
PP-5	400-725 volts	110	150
PP-6	725-1300 volts	110	15 0
PP-7	1150-2000 volts	140	3.80
PP-8	1500-2600 volts	140	180
PP-9	2300-4000 volts	160	200

A "P" or "N" in the model number indicates positive or negative output polarity.

The letter "L" in the model number indicates current limited to protect against short-circuit or overload damage.

Example: PP-7-N is negative, not output protected; PP-7-PL is positive, and is output short-circuit protected.

* Ripple is measured with input and output returns connected to case.

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A. <u>Electrical</u> (Continued)

- 9. Stability: After two hours operation, drift less than 0.1% per day.
- 10. Noise induced into 28 volt power line: less than 25 millivolts across 1 ohm impedance.
- 11. Impedance between either input and output returns and case is > 10 megohms and < 100 pf.</p>

B. Environmental

Pico-Pacs have been designed and tested to meet or exceed the environmental conditions indicated below:

Altitude: Operational from sea level to 200,000 feet (exposure at reduced pressure > 30 minutes).

Acceleration*: 120 g for 30 seconds - any axis.

Mechanical Shock*: 80 g, 15 milliseconds - any axis.

Thermal Shock*: -55° C to +65° C and +65° C to -55° C, each in less than five minutes.

Sinusoidal Vibration#:

5-28 Hz, 0.55 inch double amplitude 28 - 3000 Hz, ± 20 g -any axis

Random Vibration*: 0.1 g 2/Hz, 3 σ , 20-2000 Hz, 90 seconds - any axis.

* Units energized following, but not during, tests.

Two Teenman Grand Maumeo Keencaux, M Pu No.1-Tor and Puu Borron -	FIMS A CEP Bourd Ho-L - \$ JULV. \$1 15-5880- GA.F.	ORIGINAL PA OF POOR QU	
	$ \begin{bmatrix} & 0 & 0 & 0 & 0 & 0 & 0 & 0 & 0 & 0 & $	GRND 36 FIMS CEP COMPONENT SIDE COMPONENT LAYOUT	

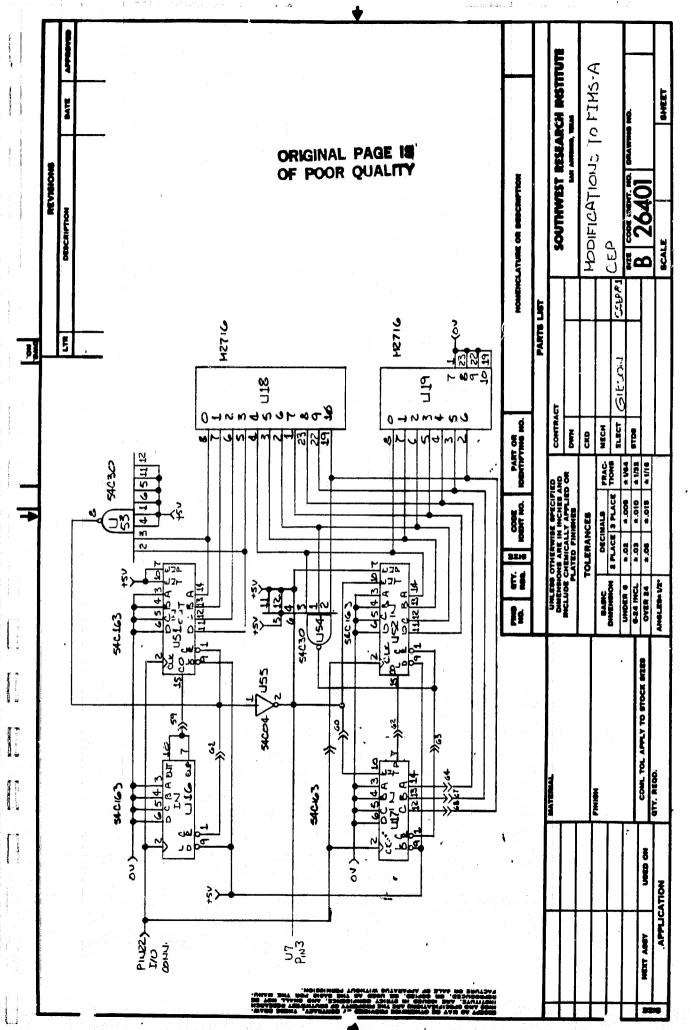
FIGURE VII-4

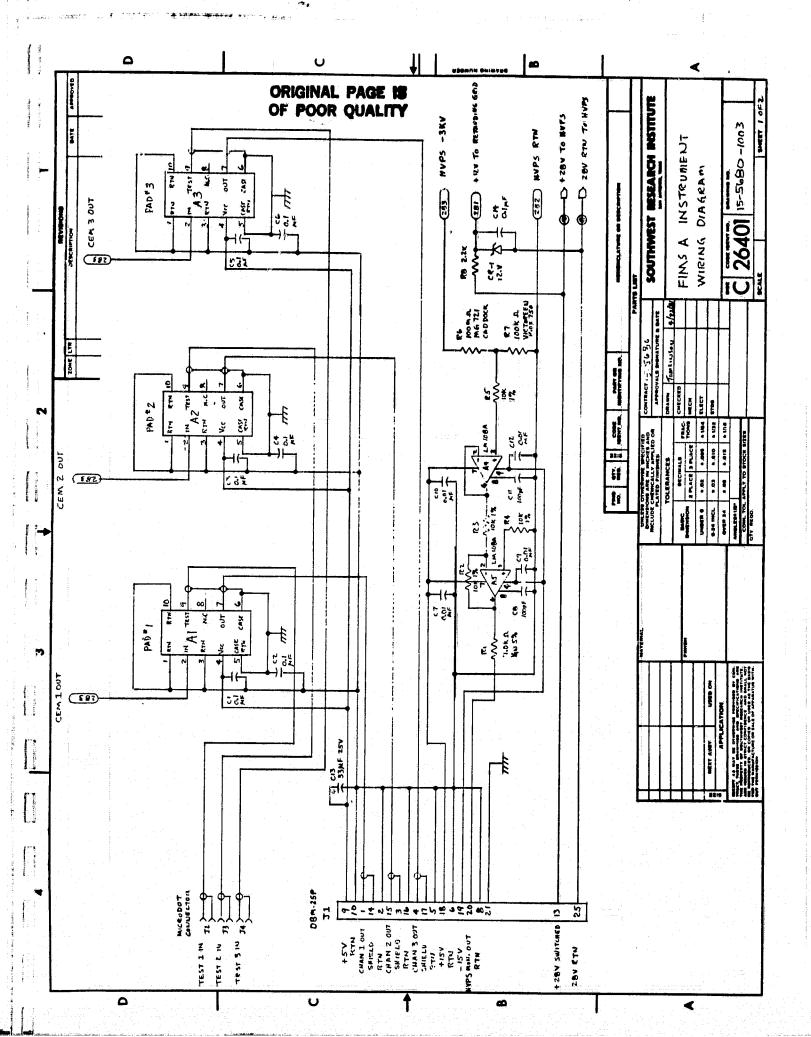
Tue Teanmar Communs Pourte Verreaus, there Pur No-1. Tee, And An No.2. Borren.	ORIGINAL PAGE 13 OF POOR QUALITY BOB VO No No No 2 - 2000 - 2000 - 2000 2 -	
	Processes	FIGURE VII-5

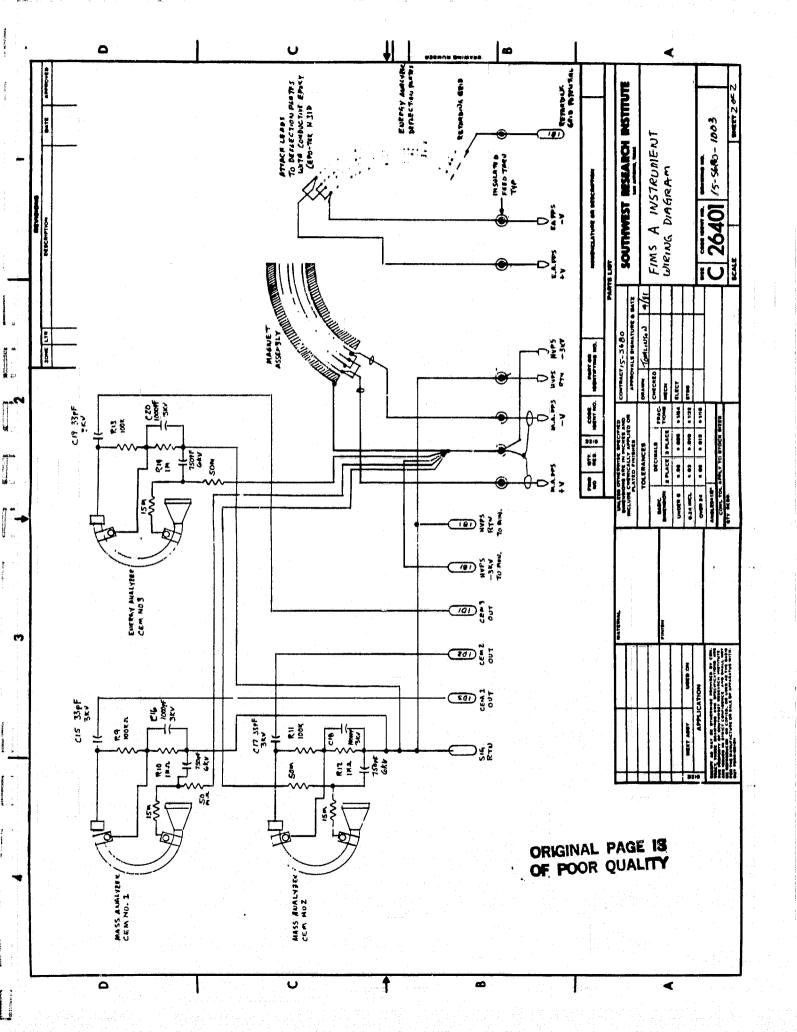
. . .

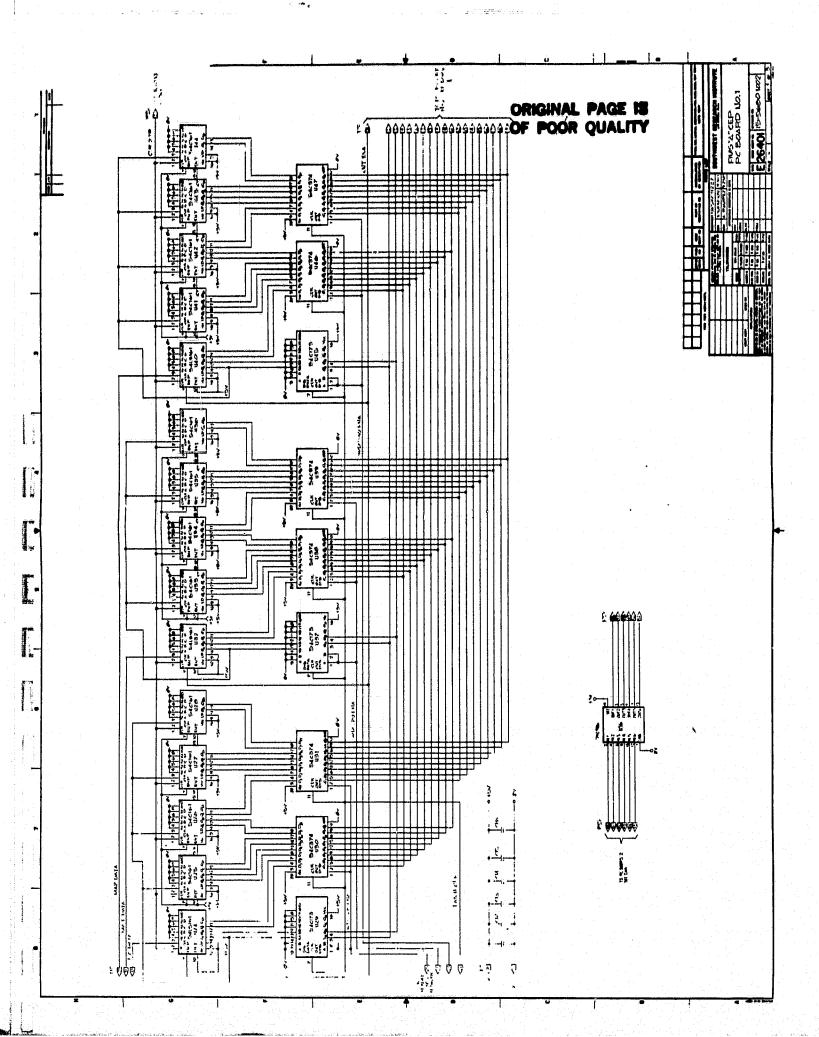
	•	Two Teanimars Components Mounted Vernicary, Have Pin No.L- Top, And Pan Ha.Z- Borton-	FIMS A СЕР Волко No.3. ЗЈину ві	15-5680- 15-5680-	ORIGINAL OF POOR	PAGE IS QUALITY	
					S. S.	FIMS CEP COMPONENT SIDE COMPONENT LAPOUT	FIGURE VII-6

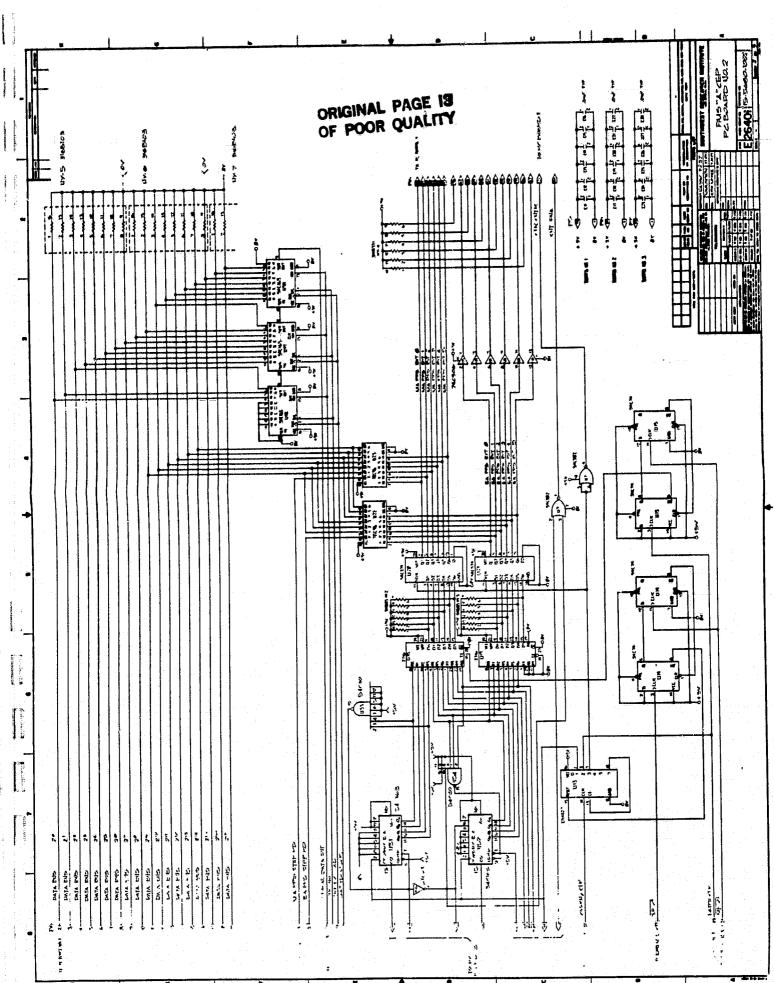
/

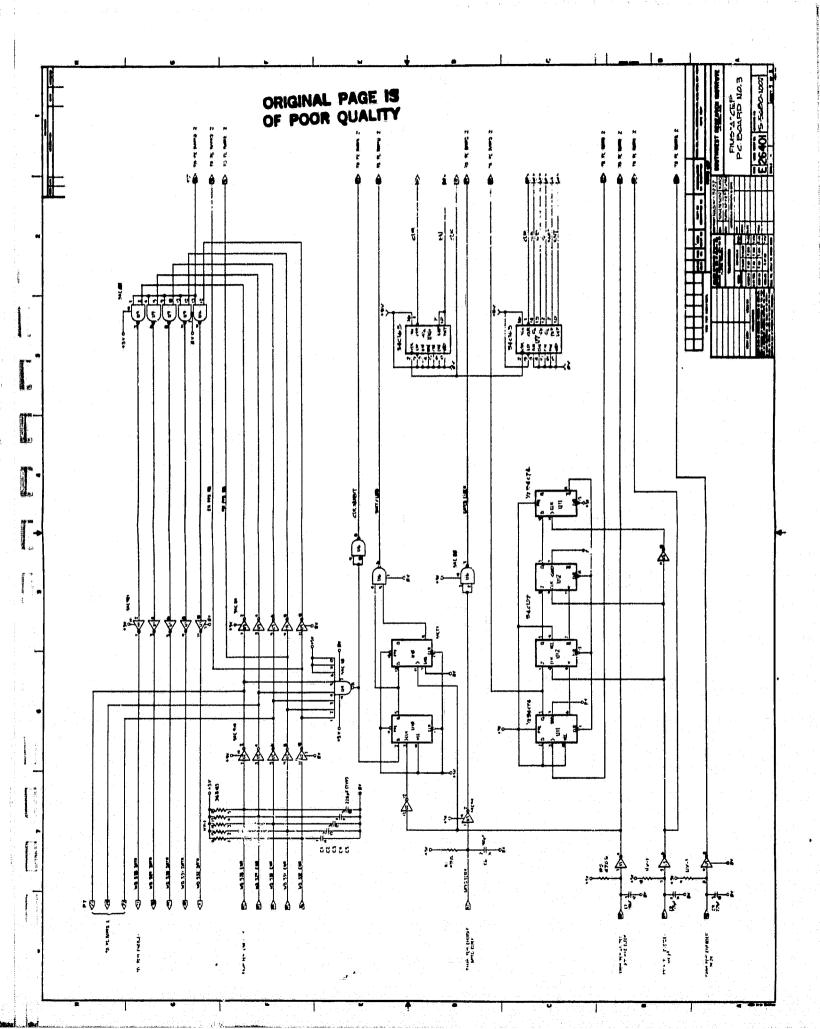












<u>רא</u> צה	5-7		1511	-	1000	M ICTANUIOOC	NEJE-111-14 114231		
DRAWING TITLE	NG TITLE: FIMS A	E.	CENTRAL I	ELECTR	ELECTRONICS PACKAGE				SHEET 1 OF 2
¥ V	OTY REQ PER ASSY/INSTALL	TALL	-	UNIT	FIND	CODE	PART OR	NOMENCLATURE	
				OF MEAS.	NO.	IDENT. NO.	IDENTIFYING NO.	DESCRIPTION	
			2			U6,9	54C003	•	
			2			U5 , 55	54C04J	HEX INVERTER	
			m			14,53,54	54C30J		
			4			U10,11, 14,15	54C74J	DUAL D FLIP FLOP	
FIG.			12	•		U 25-28 U 33-36 U 41-44	54C161J ·		•
VII			4				54C163J	•	
7.1		•	n			U48,49 · 50	54Ĉ165J .		
			e			45	54C173J		OR OF
			œ		•	U30,31,38 39,46,47	54C374J		2INAL POOP
	·		1		-	IU	5403013	•	PAG
			1			U 56	54C906J	•	
			2			U2, 3	54C914J		
						U7 .	54LS02J		
			m		1	U24,32 49	54LS161J		
						u 13	CD4022B		
			2			U22 , 23	70C96	•	
			2			U18,19	12716	E-PROM	

Γκαη-Ι ΩΩ2	SMEET 2 of 2						· · · · · · · · · · · · · · · · · · ·	•		orig of P	NAL	PAGI QUAL	15					• •	
· · · ·		NOMENCLATURE	DESCRIPTION	HEX CMOS TO • TTL BUFFER	•	100PF 100V CERAMIC CAP	220 PF 100V	.01 µF 50V CERAMIC CAP		14 PIN RESISTOR NETWORK 10 Kg	16 PIN RESISTOR NETWORK 10 Kg			*				•	
SUULTVESI NESEAKUN INSIIU	(AGE	PART OR	IDENTIFYING NO.	DS1630		CK05101K	CK05221K	CK05103K		3148 103	3168 103				-			•	•
N ICAN	ELECTRONICS PACKAGE	CODE	NO.	18			•			Hy2,3	ΗΥ1,5,6,7					·			
	ELECTR	FIND	o v																•
	CENTRAL	UNIT	OF MEAS.			-	2	18		~	4	<u> </u>							
- S							•						•		<u> </u>				•
- Si	SMI	OTY REQ PER ASSY/INSTALL				·								<u> </u>			 		
P.AP.TS	DRAWING TITLE	0TY ASSY			•				<	FIG.	VII	-7.2							

SOUTHWES ONNECTOR (50 Pin Sub	50S)	TITLE		· INTERNAL WIRING		1
	PIN NO.	WIRE ID.	LGTH.	SIGNAL DESCRIPTION	PIN NO.	ET _1_OF _5_ TO CONNECTO
J-76	1			E.A. PPS BIT Ø	46	J5
j)	2			" 1	47	"
N	3			" 2	48	11
H	4			H 3	49	11
0 1	5			" 4	50	11
\$1	6			" 5	51	
	7			SIG RETURN	36	Ĵ6
07	8			MA PPS ANODE BIT Ø	46	H
UI	9			" CATHODE BIT Ø	47	11
11	10			" ANODE BIT 1	48	j j
N	11			" CATHODE BIT 1	49	11
14	12			" ANODE BIT 2	50	88
11	13			* CATHODE BIT 2	51	11
11	14			" ANODE BIT 3	52	11 II
ţı	15		4	" CATHODE BIT 3	53	11
ji.	16			" ANODE BIT 4	54	- - - 11
	17			" CATHODE BIT 4	55	11
0	18			" ANODE BIT 5	56	11
88	19			" CATHODE BIT 5	57	11
U	20			E.A. PPS +V MON (A39)	33	J73
N	21			RETURN	37	J1
0	24					
11	25			ORIGINAL PAGE IS OF POOR QUALITY		
J-76	26					
			:			

FIGURE VII- .1

OUTHWEST	RESE4	ARCH INS	TITUTE		lev. Ltr.	Date
ONNECTOR 50 PIN Sub	(50S) "D"	TITLE FIMS	"A" CEP	INTERNAL WIRING	SHE	ET_2OF.5_
FROM CONNECTOR	PIN NO.	WIRE ID.	LGTH. FT.	SIGNAL DESCRIPTION	PIN NC,	
J76	27			TIMED 28V to EA PPS	. "71	
	28	•		CHASSIS		GND LUG
ii	29			28V return to EA PPS	T2	
•	30	· · ·		Timed 28V to MA PPS	<u>T1</u>	- '
J76	31		1	28V return to MA PPS	T2	-
0	32	" " " " "		MA PAD #1 DATA	38	J5
11	33			RETURN	68	11
	34			MA PAD #2 DATA	37	10
11	35			RETURN	36	II
••	36			EA PAD DATA	39	
11	37			RETURN	69	••
0	38			CHASSIS		GND LUG
••	39			FLOAT POT. MON. (A38)	32	J73
0	40			RETURN	37	J72
0	41			CHASSIS		GND LUG
11	42			+8V to INSTRUMENT	3	J73
•	43			-8V to INSTRUMENT	4	J73
IJ	44			COMMON (8V)	5	J73
11	45		_			
Û,	46			+5V to INSTRUMENT	35	J7
	47			5V RETURN	1	J7
ļi .	48			TIMED 28V to INSTRUMENT	T1	
Û	49			28V RETURN	T2	
J 76	50					
J73	11			TIMED 28V from SUPPLY	T1	
J73	12			28V RETURN	T2	

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FIGURE VII-' .2

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SOUTHWEST	RESE	ARCH INST	TITUTE	Code ID.	Number	Rev. Ltr.	Date
CONNECTOR (37 Pin Sub	37P) "D"	TITLE	5 "A" C	EP		SHE	ET <u>3</u> OF <u>5</u>
FROM CONNECTOR	PIN NO.	WIRE ID.	LGTH. FT.	SIGNAL	DESCRIPTION	PIN NO,	
J73	20			FIMS DATA S2	8	37	J7 .
0	21			FIMS DATA S2	9	38	U
64	22			FIMS DATA S3	0	39	01
10:	23			FIMS DATA S3	1	40	
11	24			FIMS DATA S3	2	41	· 11
J73	25			SIGNAL RETUR	N	36	; 1 1.
		:					
J73	27	: :		ENABLE GATE	EG28	42	••
	28	- wining		ENABLE GATE	EG29	43	10
11	29			ENABLE GATE	EG30	44	u
	30			ENABLE GATE	EG31	45	0
	31			ENABLE GATE	EG32	46	. ()
11	16			GATED CLOCK	, 	47	. 11
J3	10			200 kHz CL.OC	K	48	11
J73	17			MF 2 ³		49	11
J74	27			SF 2 ⁴		50	0
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			•			•	

FIGURE VII- .3

SOUTHWES	T RESE		TITUTE	Code ID.	Number	Rev. Ltr.		Date
ONNECTOR		TITLE		FIMA "A" CE	p		SHEE	T4_OF_5
FROM CONNECTOR	PIN NO.	WIRE ID.	LGTH. FT.	SIGNAL	DESCRIPTION	PIN I	VO.	
J5	2			BOARD INTER	CONNECT	2		
()	3		1		8	3_		. 0
H	4			ŧI		4		
0	5					5		
11	6					6		¢1
11	7		-	11		7		<u></u>
11	8		.:	11		8		
11	9			0	:	. 9		
11	10			" 0		18 10		
11	11			. 0	F POOR QUAL			11
11	12			0		12		61
01	13			H		13		"
11	14			88		14		' 11
11	15			0	: 	15		0
11	16				· · · · · · · · · · · · · · · · · · ·	16		43
11 11	17					17		<u> </u>
11	18			n de la companya de l La companya de la comp		18		
BI	19					19		11
H	22			84		- 23		H
11	23			11		23		J7
1)	24					24		11
11	25			0		25		21
J6	22	•		IJ		22		I
11	26			0		26		
11	27			11		27		11
H	28			11		28		J7

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SOUTHWES	T RESE	ARCH INST	ITUTE	Code ID.	Number	Rev. Ltr.	Date
CONNECTOR		TITLE		FIMS "A" CEP		SHE	ET <u>5</u> OF <u>5</u>
FROM CONNECTOR	PIN NO.	WIRE ID.	LGTH. FT.	SIGNAL	DESCRIPTION	PIN NO.	TO CONNECTO
J6	29			BOARD INTERC	ONNECT	29	J7
11	30			91		30	
: I I	31			0		31	11
••	32		-	0		32	11
ę)	33					33	
11	34			<u>n</u>		34	
11	59			•		59	
	60			ŧi		60	ti
. 11	61			0		61	
	62					62	0
11	63			" OF	POOR QUALIT	15 63	
0	64			" OF	POOR QUALI	64	01
11	65			11		65	"
81	66			18		66	0
0	69			. 0		69	μ
11	40					40	J5
0	41					41	11
M	42			08		42	0
\$ J	43			01		43	81
. 11	44			8		44	88
11	45			le de la companya de La companya de la comp		45	n
					• 		
· · · · · · · · · · · · · · · · · · · ·							
0	67					67	J7
))	68			11 		68	41

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				 A set of the 		
STEP NO.	VOLTAGE LEVEL	+V0	<u>-vo</u>	+TM	-TM	STEP NO.
0	2500.0 V	2515	2486	5.02	4.95	0
1	2164.9	2179	2154	4.36	4.29	1
2	1874.7	1884	862	3.77	3.71	2
3	1623.4	1630	1611	3.26	3.21	3
4	1405.8	1442	1396	2.83	2.79	4
5	1217.4	1223	1209	2.45	- 2.41	5
6	1054.2	1061	1049	2.13	2.09	6
7	91 2.92	9 18	90 8	1.84	1.816	7
8	79 0.55	7 94 ·	78 5	1.59	1.57	8
9	684.59	6 88	681	· 1.3 8	1.36	9
10	59 2.83	5 95	589	1.19	1.18	10
11	513.37	5 15	510	1.03	1.02	11
12	444.56	446	442	.894	.884	12
13	384.97	386	383	.774	.766	13
14	33 3.37	335	332	.672	.664	14
15	28 8.68	290	287	.581	.575	15
16	249.99	250	247	.500	.496	16
17	216.48	216	214	.434	.430	17
18	187.47	187	186	.375	.372	18
19	162.34	162	161	.324	.322	19
20	140.58	140	139	.280	.279	20
21	121.74	121	121	.243	.242	21
22	105.42	105	105	.210	.210	22
23	91. 288	91	90.6	.182	.1820	23
24	79.052	79.2	78.9	.158	.1584	24
25	6 8.456	68.5	68.4	.137	.1375	25
26	59.281	59.2	59.2	.118	.119	26
27	51,335	51.2	51.3	.102	.103	27
28	44.454	44.3	44.5	.088	.0895	28
29	38.495	38.3	38.5	.0763	.0776	29
30	3 3.336	33.2	33.4	.0660	.0675	30
31	2 8.867	28.6	29.0	.0569	.0585	31
32	24.998	25.0	24.92	5.02	4.83	32
33	21.647	21.7	26.61	4.35	4.18	33
34	18.746	18.78	18.7	3.76	3.61	34
35	16.233	16.26	16.2	3.25	3.12	35
36	14.057	14.10	14.04	2.82	2.70	- 36

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TABLE VII-1. ENERGY ANALYZER PPS STEP CODE

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	•					
STEP NO.	VOLTAGE LEVEL	<u>+V0</u>	<u>-V0</u>	+TM	-TM	STEP NO.
37	12.173	12.20	12.16	2.44	2.33	37
38	10.541	10.6	10.56	2.12	2.02	38
39	9.1285	9.17	9.13	1.84	1.74	39
40	7.9049	7.94	7.90	1.59	1.50	40
41	6.8454	6.88	6.85	1.38	1.30	41
42	5.9278	5.95	5.93	1.195	1.12	42
43	5.1333	5.15	5.13	1.035	0.96	43
44	4.4452	4.46	4.45	0.898	0.832	44
45	3.8494	3.86	3.85	0.380	0.716	45 、
46	3.3334	3.36	3.35	0.678	0.618	46
47	2.8866	2.906	2,896	0.588	0.530	47
48	2.4997	2.503	2.498	0.508	0.453	48
49	2.1646	2.169	2.165	0.442	0.389	49
50	1.8745	1.876	1.874	0.384	0.333	50
51	1.6232	1.623	1.620	0.334	0.282	51
52	1.4057	1.407	1.407	0.292	0.243	52
53	1.2173	1.218	1.217	0.255	0.207	53
54	1.0541	1.057	1.056	0.223	0.176	54
55.	0.91281	0.914	0.913	0.195	0.149	55
5 6	0.79046	0.797	0.801	0.172	0.127	56
57	0.6841	0.691	0.693	0.151	0.1067	57
5 8	0.59276	0.598	0.600	0.133	0.0890	58
59	0.51331	0.516	0.518	0.1172	0.0734	59
60	0.44450	0.448	0.452	0.1039	0.060	60
61	0.38492	0.388	0.390	0.0921	0.0488	61
62	0.33333	0.331	0.336	0.0820	0.039	62
63	0.289	0.280	0.279	0.075	0.028	63

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TABLE VII-1.2 ENERGY ANALYZER PPS STEP CODE

STEP	RI	RF	<u></u>	<u>-V0</u>	+V0.,	<u>-V0</u>	+TM	- TM
1	127K	127K	-7.50	-982.22	982	982	4.91	4.903
2		120K	-7.08	-926.62	934	934	4.67	4.663
3		113K	-6.67	-874.17	874	874	4.37	4.363
4	i .	107K	-6.30	-824.69	829	829	4.145	4.138
5		101K	-5.94	-778.01	774	773	3.87	3.863
6		95K	-5.60	-733.97	739	738	3.69	3.687
7		90K	-5.29	-692.43	700	70 0	3.501	3.495
8		84K	-4.99	-653.23	655	654	3.27	3.268
9	202K	127K	-4.71	-616.26	619	618	3.09	3.088
10		120K	4.44	-581.38	588	58 8	2.94	2.937
11		113K	-4.19	-548.47	550	550	2.75	2.748
12		107K	-3.95	-517.42	522	522	2.610	2.606
13		101K	-3.73	-488.13	487	487	2.436	2.433
14		95K	-3.52	-460.50	465	465	2.325	2.322
15		9 0K	-3.32	-434.44	441	441	2.204	2.201
16		84K	-3.13	-409.85	412.6	412.1	2.06	2.058
17	323K	127K	-2.95	-386.65	386.1	385.6	7.928	1.926
18		120K	-2.79	-364.76	367.2	366.7	1.834	1.831
19		113K	-2.63	-344.12	343.5	343.0	1.715	1.713
20		107K	-2.48	-324.64	325.9	325.4	1.627	1.625
21		101K	-2.34	-306.26	304.2	303.7	1.519	1.517
22		9 5K	-2.21	-288.93	290.4	289.9	1.450	1.448
23		9 0K	-2.08	-272.57	275.3	274.8	1.374	1.373
24		84K	-1.96	-257.14	257.4	25 6.9	1.285	1.283
25	514K	12 7K	-1.85		243.2			1.213
26		12 0K			231.4			1.153
27		113K	-1.65		216.5			1.079
28		10 7K			205.4			1.024
29		10 1K	-1.47	-192.15	191.7	191.2	.956	.9 56
30		9 5K	-1.38	-181.28	183.0	182.5	.913	.9 12
31		9 0K	-1.31	-171.02	173.5	173.0	.865	.865
32		84K	-1.23	-161.33	162.2	161.7	.809	.8 08
33		127K	-1.16	-152.20	152.2	151.7	.759	.7 58
34	820K	120K	-1.10	-143.59	144.8	144.2	.721	.721
35		113K	-1.03	-135.46	135.5	134.9	.675	.675
36		107K	98	-127.79	128.5	128.0	.640	.640

TABLE VII-2.1 MASS ANALYZER PPS STEP CODE

ORIGINAL PAGE IS OF POOR QUALITY

							· · · · · · · · · · · · · · · · · · ·	· · · · · · ·
<u>STEP</u>	RI	RF	<u>V1</u>	-V0	+V0.,	-V0	+TM	<u>- TM</u>
37		101K	92	-120.56	120.0	119.4	.597	. 597
38		95K	87	-113.73	114.5	114.0	.570	.570
39		90K	82	-107.30	108.6	108.0	.541	.541
40		84K	77	-101.22	101.5	101.0	.505	.5 05
41	1.306M	127K	73	-95.49	95.9	95.4	.478	.477
42		120K	69	-90.09	91.3	90.7	.4546	.4547
43		113K	65	-84.99	85.4	84.9	.4253	.4255
44		107K	61	-80.18	81.0	80.5	.4034	.4036
45		101K	58	-75.64	75.7	75.1	.3765	.3768
46		95K	54	-71.36	72.2	71.7	.3594	.3597
47		9 0K	51	-67.32	68.5	67.9	.3406	.3410
48		84K	48	-63.51	64.0	63 .5	.3185	.3189
49	2.082M	127K	46	-59.91	60.2	59.6	.2989	.2993
50		120K	43	-56.52	57.2	56.7	.2843	.2847
51		113K	41	-53.32	53.6	53.0	.2659	.2664
52		107K	38	-50.31	50.8	50.3	.2522	.2527
53		101K	36	-47.46	47.5	46.9	.2354	.2359
54		9 5K	34	-44.77	45.3	44.8	.2247	.2252
55		9 0K	32	-42.24	43.0	42.4	.2129	.2135
56		84K	30	-39.8 5	40.2	29.6	.1991	.1997
57	3.318M	127K	29	-37.59	38.0	37.5	.1882	.1887
58		120K	27	-35.46	36.2	35.6	.1789	.1796
59		113K	26	-33.56	33.9	33.3	.1674	.1680
60		107K	24	-31.56	32.2	31.6	.1587	.1594
61		101K	23	-29.78	30.0	29.5	.1481	.1488
62		9 5K	21	-28.09	28.7	28.1	.1414	.1421
63		9 0K	20	-26.50	27.2	26.7	.1340	.1347
64		84K	19	-25.00	25.5	24.9	.1253	.1260

ORIGINAL PAGE IS OF POOR QUALITY

TABLE VII-2.2 MASS ANALYZER PPS STEP CODE

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FIMS-A EA-PPS COMMAND

	DATA	ADD	DATA	ADD	DATA	ADDRESS
	33	40	21	20	06	00
ORIGINAL PAGE IN OF POOR QUALITY	33	41	21	21	06	01
AOUTI	33	42	21	22	09	02
	06	43	21	23	09	03
	06	44	21	24	0C	04
	06	45	24	25	00	05
	U .	46	24	26	0C	06
		47	24	27	OF	07
	••	48	24	28	OF	08
		49	24	29	OF	09
	U	4A	27	2A	12	OA
		4B	27	2B	12	Ов
	а. 11 ал	4C	27	2C	12	0C
	-87	4D	27	2D	15	OD
	in.	4E	27	2E	15	OE
		4F	2A	2F	15	OF
	99		2A	30	15	10
		1	2A	31	18	11
	**		2A	32	18	12
	91		2A	33	18	13
	\$1		2D	34	18	14
			2 D	35	18	15
	••		2D	36	1B	16
	**		2D	37	1B	17
	•		2D	38	18	18
	•		30	39 -	18	19
			30	ЗА	18	18
	10		30	3B	1 E	18
	11		30	3C	1E	10
			30	3D	1e	1D
	ti -		33	3E	1E	le
			33	3F	1E	lF

TABLE VII-3

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FINAL FLIGHT PROMS-FIMS-A

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MASS ANALYZER PPS COMMANDS

ORIGINAL PAGE IS OF POOR QUALITY

Each	ADD	DATA	Èach	ADD	DATA	Each	ADD	DATA	Each	ADD	DATA
6	00	00	9	33	00	12	66 ,	1B	15	<u>99</u>	33
91	OI	01		34	OE	H 44	68				
0	02	02		35	OF			10	18 #	AO	08
H	03	03		36	10		68	1D		A 1	09
e ņ	04	C4	99	37	J.1		69	16	*	λ2	0
	05	05	06	38		15 "	70	06		A 3	OB
	06	06		39	12	•	71	07	**	А4	0C
	07	07	12		13		72	08		A 5	0D
•	08	08	, T , T ,	40	01		73	09	0	A 6	OE
				41	02	• •••	74	OA	H . 1	A 7	ÔF
+÷	09	09		42	03		75	OB	91	A 8	10
U,	10	OA	Q B B B B B B B B B B	43	04	01	76	OC	• +	A9	11
- +i	11	OB	Ħ	44	05	••	77	OD		BO	12
	12	0C	tri∎ina ana	45	06	90 - D	78	OE	**	Bl	13
•	13	OD	17	46	07	00	79	OF	-9	B2	14
	14	0E		47	08		80	10		B 3	15
H	15	OF	89	48	09	11	81	11	. ••	В4	16
101	16	10	. 90	49	OA		82	12		B 5	17
00 ,	17	11		50	OB	. 11 -	83	13	• • •	B6	18
89-	18	12	01	51	OC	11	84	14	••	B7	19
6	19	13		52	OD	· • ·	85	15		BB	1A '
9	20	00		53	OE		86	16	17 .	в9	1B
••	21	01	-01	54	OF	84	87	17	00	C0	10
••	22	02		55	10	ŬT.	88	18	••	C1	1D
H	23	03	ţ.	56	11	81	89	19	0	C2	le
	24	04		57	12	91	90	la		C3	lF
-	25	05		58	13	ŧĨ.	91	1B	: 91	C4	20
	26	06	9 1	59	14	Û.	92	1C		C5	21
U	27	07		60	15		93	1D	4 	C6	22
90	28	08	••	61	16		94	lE		C7	23
w	29	09	81	6 2	17	ŕ.	95	1F		C8	24
1010 - 1010 11	30	0A	ŧi	63	18		96	20	18	C9	25
•	31	OB		64	19	88	97	21	18	DO	08
11	32	00	12	65	1A	15	98	22	21	DI	09

TABLE VII-4.1

FIMS & MA-PPS COMMANDS

ORIGINAL PAGE IS OF POOR QUALITY

									JOUR &		
Each	ADD	DATA	Each	ADD	DATA	Each	ADD	DATA	Each	ADD	DATA
21D	D2 _H	ON"H	21 _D	105 _H	28 _H	24 _D	138 _H	29 _H	27 _D	171 _H	18 _H
**	D3	0B		06	2C	•	39	22	# #	72	19
	D4	0C		07	2D	**	40	2B		73	18
**	D5	OD.	**	08	2E	**	41	2C		74	1B
++	D6	0E	21	09	2F		42	2D	••	75	1C
81	D7	OF	24	110	OD	99	43	3E		76	1D
	D8	10		11	0E	· • • • • • •	44	2 <i>F</i>	-17	77	1E
	D9	11	H	12	OF	. 80	45	30	11	78	1F
#1	EO	12	.97	13	10	88	46	31	¢1	79	20
68	El	13		14	11	n.	47	32	99	80	21
**	E2	14		15	12	**	48	33	\$9	81	22
	E3	15	,	16	13		49	34	10	82	23
**	E4	16	. 11	17	14	**	50	35	+1	83	24
	E5	17	•,	18	15	ü+	51	36	90	84	25
u i	E 6	18	••	19	16		52	37	11	85	26
9	E7	19	60	120	17		53	38		86	27
ti	E8	1A	00	21	18		54	39	**	87	28
	E9	18		22	19	.07	55	3A		88	29
• ••	FO	10		23	18		56	3B		89	2A
	Fl	1D		24	lB	•	57	30	.01	90	2B
*1	F2	1E	Ú1 .	25	1C	5 <u>7</u>	58	3D	11	91	2C
* '	F3	lF		26	10	24	59	3E		92	- 2D
49	F4	20	••	27	lE	27	60	OD		93	2E
	F 5	21	11	28	1F	ţ,	61	OE	-11	94	2F
an H ipeg	F6	22		29	20	· • .	62	OF	11, .	95	30
H	F7.	23	· 10 ⁻¹	30	21	ŧĭ.	63	10	••	96	31
PR	F8 ⁻	24	••	31	22	tţ	64	11	- #	97	32
1	F9	25	11	32	23		65	12		98	33
Ħ	100	26	11	33	24	•	66	1.3	U	199	34
	01	27	ii -	34	25	Ħ	67	14		1A 0	35
	02	28	11	35	26	11	68	15		Al	36
	03	28		36	27		69	16		A2	37
21 _D	04	29	24 _D	37 _H		27 _D	70	17	27 _D		38

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B6 12 E8 33 " 21 25 " 54 " B6 13 " 1E9 34 " 22 26 " 55 " B7 14 " 1F0 35 " 23 27 " 56 " B8 15 " F1 36 " 24 28 " 57 " B9 16 " F2 37 " 25 29 " 58 " 1C0 17 " F3 38 " 26 2A " 259 " C1 18 " F4 39 " 27 2B " 260 " C2 19 " F5 3A " 28 2C " 61 " C3 1A " F6 3B " 230 2E " 63 " C4 1B " F7 3C " 230	13
B0 13 1E9 34 " 22 26 55 " B7 14 " 1F0 35 " 23 27 " 56 " B8 15 " F1 36 " 24 28 " 57 " B9 16 " F2 37 " 25 29 " 58 " 1C0 17 " F3 38 " 26 2A " 259 " C1 18 " F4 39 " 27 2B " 260 " C2 19 " F5 3A " 28 2C " 61 " C3 1A " F6 3B " 230 2E " 63 " C4 1B " F7 3C " 230 2E " 63 " C4 1B " F7 3C " 31	14
D7 14 170 35 23 27 86 " $B8$ 15 " $F1$ 36 " 24 28 " 57 " $1B9$ 16 " $F2$ 37 " 25 29 " 58 " $1C0$ 17 " $F3$ 38 " 26 $2A$ " 259 " $C1$ 18 " $F4$ 39 " 27 $2B$ " 260 " $C2$ 19 " $F5$ $3A$ " 28 $2C$ " 61 " $C2$ 19 " $F6$ $3B$ " 229 $2D$ " 62 " $C4$ $1B$ " $F7$ $3C$ " 230 $2E$ " 63 " $C4$ $1B$ " $F7$ $3C$ " 230 " 65 " $C6$ $1D$	15
36 15 $F1$ 36 24 28 8 57 " $1B9$ 16 " $F2$ 37 " 25 29 " 58 " $1C0$ 17 " $F3$ 38 " 26 $2A$ " 259 " $C1$ 18 " $F4$ 39 " 27 $2B$ " 260 " $C2$ 19 " $F5$ $3A$ " 28 $2C$ " 61 " $C2$ 19 " $F6$ $3B$ " 229 $2D$ " 62 " $C4$ $1B$ " $F7$ $3C$ " 230 $2E$ " 63 " $C5$ $1C$ " $F8$ $3D$ " 31 $2F$ " 64 " $C6$ $1D$ 30_D $1F9$ $3E$ " 32 30 " 65 "	16
10 10 12 37 12 25 29 15 58 10 17 " F3 38 26 2A 259 10 17 " F3 38 26 2A 259 11 18 " F4 39 " 27 28 " 260 11 18 " F4 39 " 27 28 " 260 " C2 19 " F5 3A " 28 2C " 61 " C3 1A " F6 3B " 230 2E " 62 " C4 1B " F7 3C " 230 2E " 63 " C5 1C " F8 3D " 31 2F " 64 " C6 1D 30 _D 1F9 3E " 32 30 " 65 " C7 1E <td>17</td>	17
100 17 F3 38 126 2A 259 " C1 18 " F4 39 27 2B 260 " C2 19 " F5 3A " 28 2C " 61 " C2 19 " F5 3A " 28 2C " 61 " C3 1A " F6 3B " 229 2D " 62 " C4 1B " F7 3C " 230 2E " 63 " C5 1C " F8 3D<"	18
$C1$ 16 $F4$ 39 27 28 260 " $C2$ 19 " $F5$ $3A$ " 28 $2C$ " 61 " $C3$ $1A$ " $F6$ $3B$ " 229 $2D$ " 62 " $C4$ $1B$ " $F7$ $3C$ " 230 $2E$ " 63 " $C5$ $1C$ " $F8$ $3D$ " 31 $2F$ " 64 " $C6$ $1D$ 30_D $1F9$ $3E$ " 32 30 " 65 " $C7$ $1E$ 33_D 200 10 " 33 31 " 66 " $C8$ $1F$ " 01 11 " 34 32 " 67	19
$C2$ 15 $F5$ $3A$ 28 $2C$ 61 " $C3$ $1A$ " $F6$ $3B$ " 229 $2D$ " 62 " $C4$ $1B$ " $F7$ $3C$ " 230 $2E$ " 63 " $C5$ $1C$ " $F8$ $3D$ " 31 $2F$ " 64 " $C6$ $1D$ 30_D $1F9$ $3E$ " 32 30 " 65 " $C7$ $1E$ 33_D 200 10 " 33 31 " 66 " $C8$ $1F$ " 01 11 " 34 32 " 67	14
" C4 1B " F7 3C " 230 2E " 63 " C4 1B " F7 3C " 230 2E " 63 " C5 1C " F8 3D " 31 2F " 64 " C6 1D 30 _D 1F9 3E " 32 30 " 65 " C7 1E 33 _D 200 10 " 33 31 " 66 " C8 1F " 01 11 " 34 32 " 67	18
" C5 1C " F8 3D " 31 2F " 64 " C6 1D 30 _D 1F9 3E " 32 30 " 65 " C7 1E 33 _D 200 10 " 33 31 " 66 " C8 1F " 01 11 " 34 32 " 67	1C
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" C7 1E 33 _D 200 10 " 33 31 " 66 " C8 1F " 01 11 " 34 32 " 67	le
" C7 1E 33 _D 200 10 " 33 31 " 66 " C8 1F " 01 11 " 34 32 " 67	lF
	20
	21
" C9 2 <u>0</u> " O2 12 " 35 33 " 68	22
" 1D0 21 " 03 13 " 36 34 " 269	23
" D1 22 " 04 14 " 37 35 " 70	24
" D2 23 " 05 15 " 38 36 " 71	25
" D3 24 " 06 16 " 239 37 " 72	26
" D4 25 " 07 17 " 240 38 " 73	27
" D5 26 " 08 18 " 41 39 " 74	28
30_{D} D6 27 33_{D} 09 19 33_{D} 4] 3A 36_{D} 275	29

TABLE VII-4.3

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ORIGINAL PAGE IS OF POOR QUALITY

Each	ADD	DATA	Each	ADD	DATA	Each	ADD	DATA	Each	NDD	DATA
36 _D	276 _H	27 1	39 _D	2 79 H	19 _H ;	³⁹ D	2E2 _H	3A _H	42 _D	315 _H	29 _H
Ħ	77	2B		2B0	גו	11	E3	3B		16	2٨
•	78	2C		Bl	18		E4	3C		17	2B
**	7 9	2D	99 -	B 2	1C	.0	E5	3D	Ħ	18	2C
••	280	2E	93	B3	1D	**	E6	3E	•	19	2D
•	81	2F	#	B4	lE		E7	3F	H	320	2E
	82	30	••	B5	1F	9	E8	3E	#1	21	2F
07	83	31	••	B6	20	39 _D	2E9	3F	••	22	30
•	84	32		B7	21	42 _D	2F0	10	- ++	23	31
- 11	85	33	**	B 8	22	61	Fl	11		24	32
••	86	34	6 6	289	23	n	F2	12		25	33
81	87	35	11	200	24		F3	13		26	34
11	88	36	16	Cl	25	Ű	F4	14	· •	27	35
	289	37	! !	C2	26	•	F 5	15	÷.	28	36
₹ ₽	290	38	**	C3	27		F 6	16	"	329	37
99	91	39	••	C4	28	H	F 7	17		330	38
	92	3Å	11	C5	29	H (F8	18	49	31	39
01	93	3B		C6	2A		F9	19		32	3A
	94	3C		С7	2B	Ú)	300	18	.01	33	ЗВ
(9)	95	3D	91. ···	C8	2C	•	01	18	ti .	34	30
**	96	3E	••	209	2D	**	02	10	. 11	35	3D
ti -	97	3F	,,,,	200	2E	H	03	1D		36	3E
**	98	3F		Dl	2F	**	04	le	11	37	3F
36 _D	299	ЗF	100 - 100 - 100 - 100 - 100 - 100 - 100 - 100 - 100 - 100 - 100 - 100 - 100 - 100 - 100 - 100 - 100 - 100 - 100	D2	30		05	lF	91	38	3F
39 _D	2A 0	10	•	D3	31		06	20	ţ.	3 3 9	3F
	Al	11	2 10 -	D4	32		07	21	**	340	10
•	A2	12	**	D5	33	H	08	22	1	41	11
H	A3	13		D6	34		09	2,3		42	12
н Н	A4	14		D7	35		310	24		43	13
	A5	15	1)	D8	36	ti and	11	25	- O Ţ.	44	14
11	A6	16		209	37	U	12	26		45	15
	A7	17	11	2E0	38		13	27	Ħ	46	16
39	288	18	39 _D	El	39	42	314	28	D	47	17

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TABLE VII-4.4

ORIGINAL PAGE IS OF POOR QUALITY

Each	ADD	DATA	Éach	ADD	DATA	Each	ADD	DATA	Each	ADD	DATA
45 _D	·· 348	18 _H	45 _D	380 _H	38 _.	48 _D	382 _H	26 _H	51 _D		
и.	49	19	н Н	81	З9		В3	27	D	E5 :	14 _H 15
	350	18	•• ,	82	3А		B4	28	* 69.	EG	16
,	51	1B	-94-	83	3B	••	B5	29		E7	17
€ <u>1</u>	52	10		84	3C		B6	22		E8	18
••	53	1D	••	85	3D	11	B 7	2B *	99	3E9	19
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TABLE VII-4.5

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ta .	23	3B	00	57	0
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TABLE VII-4.6

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	OTY REO PER ASSY/INSTALL		UNIT	FIND	CODE	PART OR	NOMENCLATURE		
			OF MEAS.	vo	NO.	IDENTIFYING NO.	DESCRIPTION		
		8			A1, A2 A3	BBRC 52881-1	Hybrid Circuit • Fad	CTI	
		2			A4, A5	LM108A	OP-AMP	QPL	
		1			CR-1	1N963	12.0V 1/4W Zener	QPL	
		2			C1,2,3, 4,5,6,14	CK06BX104K	0.1µF 100V Ceramic	dpL.	
FIG.		. 4	5		C7,9,10 12	CK05BX103K	0.01µF 100V Ceramic Cap	QPL .	
VII-	_	2			C8, C11	CK05BX101K	1. 2	- OR OF Tab	
					C13	CSR13F336K	33µF 35V Tantalum Cap	GINA POO Tdù	
	-	3			X1,2,3	6x20mm Series 4800	Channel Electron Multiplier	Galileo Galileo	
•	-	1			R1	RCR07G102J	1.0Km 1/4W 5% carbon	GE IS	
		4			R2,3, 4,5	RN55D1002J	10Kn 1/8W 1% film resistor	QPL	
	<u> </u>	-			R6	MG721	100Ma 1% 5kv resistor	CADDOCK	
		4			R7,9,11 13	MOX 750	100Kg 1% 5kv resistor	VICTOREEN	
		1			R8	RCR07G222J	2.2kg 1/4W 5% carbon	QPL	
	-	6			R10,12, 14	MOX 750	1 megn 1% 5FV resistor	VICTOREEN	
	-	4			R15,16, 17,18	MOX 750	10 megn 1% 5KV resistor	VICTOREEN	•
	-	3			C15,17, 19	X5F	33PF 5KV Ceramic Cap	ERIE	.
	-	3	•		C16,18, 20	Åsғ.	1000PF 5KV Ceramic Cap	ERIE	
	•						50	1	

APPENDIX B

FIMS MODEL B

HARDWARE/SOFTWARE REFERENCE MANUAL

SOUTHWEST RESEARCH INSTITUTE Post Office Drawer 28510, 6220 Culebra Road San Antonio, Texas 78284

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FAST ION MASS SPECTROMETER MODEL B HARDWARE/SOFTWARE REFERENCE MANUAL

Submitted to

The National Aeronautics and Space Administration NASA Headquarters Office of University Affairs

By

The Space Science Department Instrumentation Research Division Southwest Research Institute Project 15-5680

The work performed under NASA Contract NASW-3237, the Development of a Fast Ion Mass Spectrometer.

September, 1981

Approved:

John R. Barton

John R. Barton, Vice President Instrumentation Research Division

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INTRODUCTION	I
QUICK-LOOK TEST INFORMATION	II
SIGNAL INPUT TEST PROCEDURE	III
INSTRUMENT CABLING INFORMATION	IV
INSTRUMENT TEST RECORDS/HISTORICAL LOG	V
INSTRUMENT CALIBRATION RECORDS	VI
INSTRUMENT DESIGN INFORMATION	VII

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I. INTRODUCTION

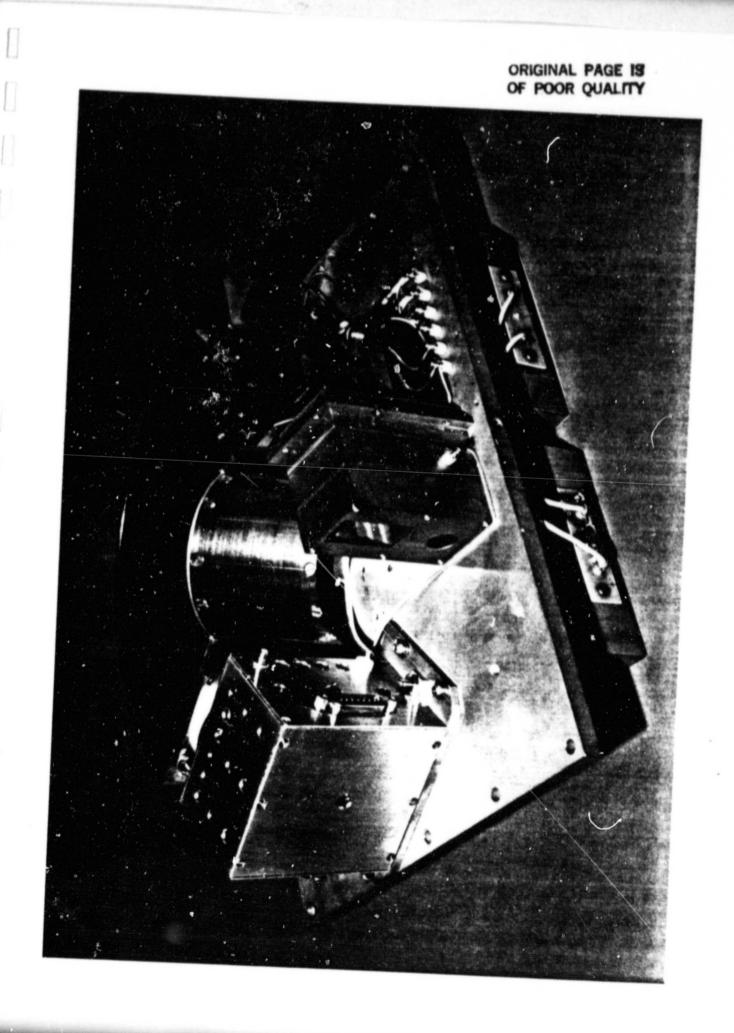
This document provides a single source for all FIMS operational, calibration and repair information. It is hoped that this document will be of assistance in the field operation of the FIMS instrument should any problems arise. Sufficient information is contained herein to troubleshoot the instrument in the field. All test and calibration records will be kept in this document as well.

19 a.

The second purpose of this document is to aid in the development of future generations of similar ion mass spectrometers. The experience gained in the laaboratory and the field with the FIMS B instrument will be of critical assistance in the development of similar instruments for other satellite and rocket-borne instruments.

The introductory section of this document contains a copy of a paper published in the Review of Scientific Instruments which describes in some detail the geometry of the analyzer sections of the FIMS-A and FIMS-B instruments. As is described in this paper, the primary difference between FIMS-A and FIMS-B is in the area of the electrostatic energy analyzer and the detectors. The nomenclature "FIMS-A" refers to that instrument which uses the cylindrical energy analyzer and an array of three channel electron multipliers. The "FIMS-B" instrument uses a spherical energy analyzer and a microchannel plate array with a resistive anode following. The central electronics packages for the two instruments are also different. The FIMS-A instrument will be flown for the first time on the A rocket payload of the two payloads produced by the SwRI Department of Space Sciences during the summer of 1981.

This document will serve as a combination Design Specification and Instrument Log Book and should be kept with the FIMS B instrument at all times. Figure I-1 is a picture of the fully assembled FIMS-B instrument. Not shown in the picture is the instrument's Central Electronics Package. Figure I-2 is a block diagram of the total instrument system.

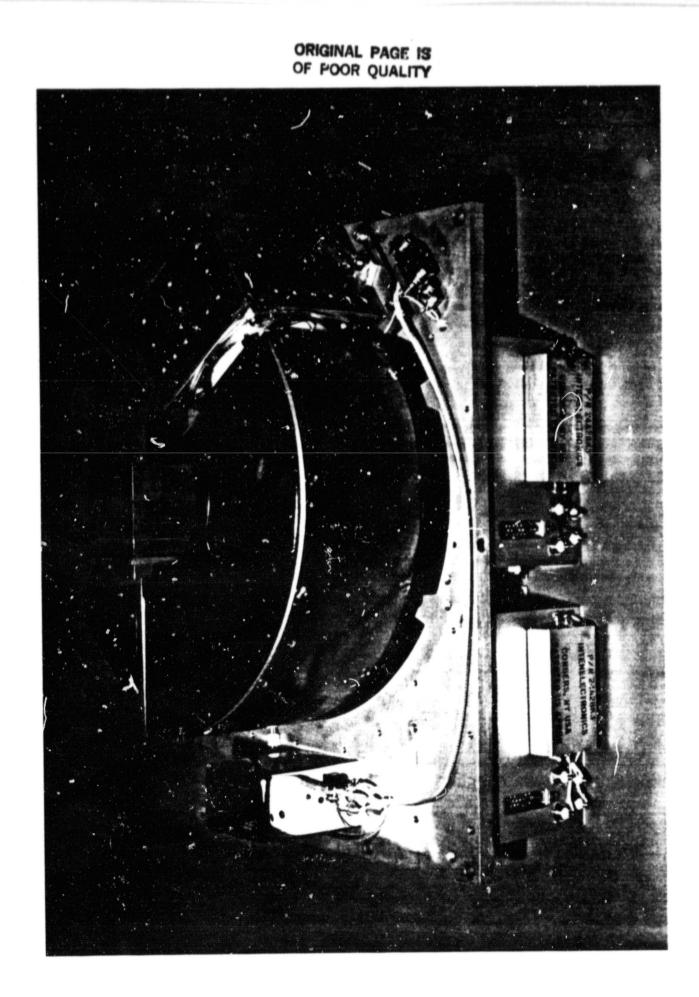


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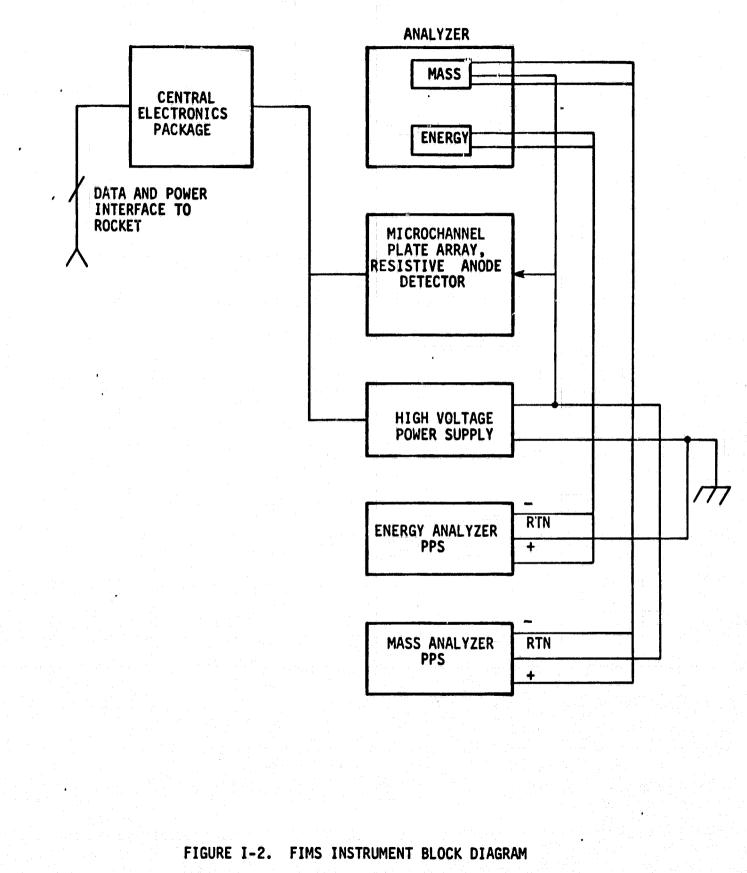
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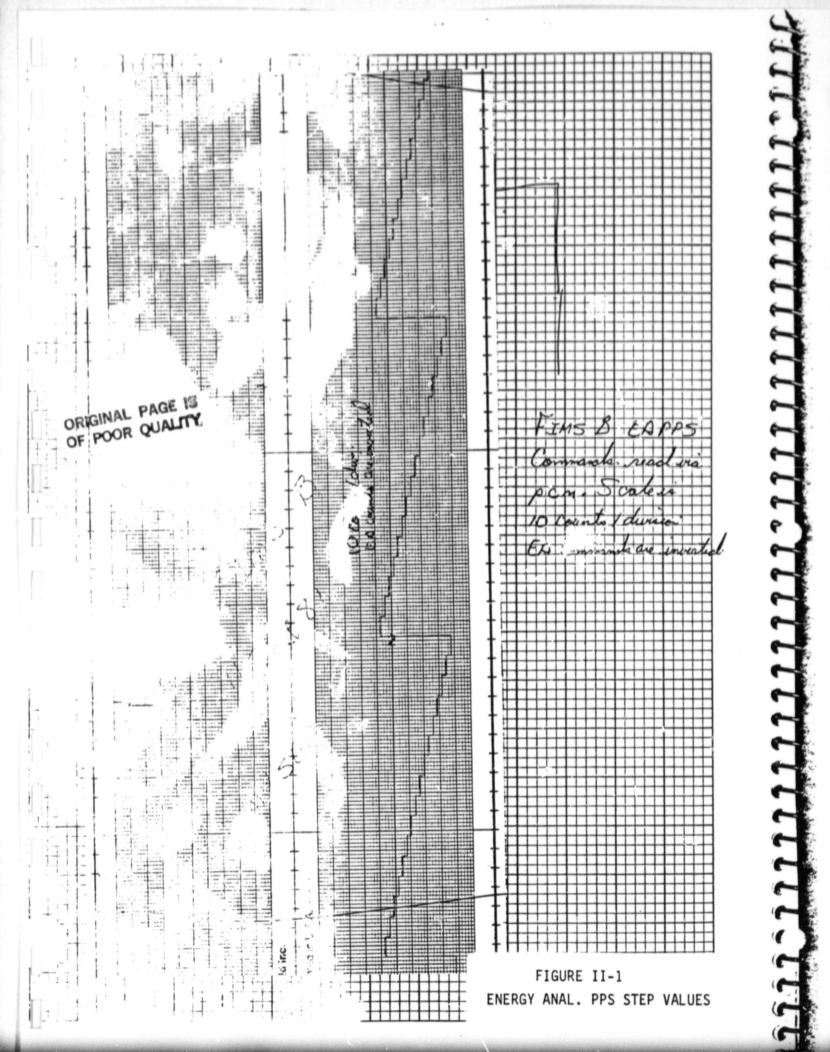
QUICK-LOOK TEST INFORMATION II.

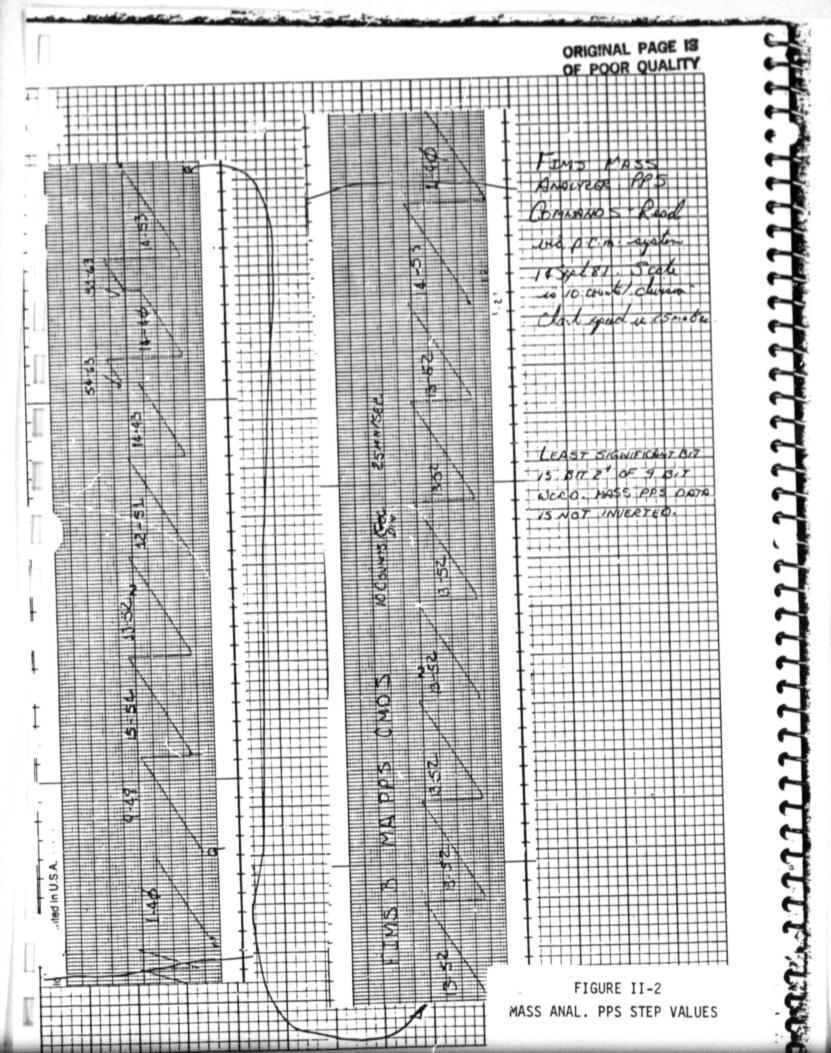
The FIMS-B instrument produces three digital data channels, 527, 531 and 532, FIMS-B also produces two analog signals noted as A39 and A40. Digital chanel S27 is located in word 13, all frames, and contains the FIMS B scientific information. It is a 9-bit digital word with all of the information contained in the 8 most significant bits. The least significant bit of the 9-bit word is not used by the instrument.

Channel S31 (word 8, frame 19) is used to transmit the mass analyzer program power supply command word. It is also a 9-bit digital word with the instrument information again contained in the 8 most significant bits of that word. The third digital channel is \$32 (word 9, frame 20) and is the case with the other two digital channels, channel 32 uses only the 8 most significant bits. The two analog channels, A39 (word 6, frame 27) and A40 (word 6, frame 19) contain the analog monitors from the energy analyzer program power supply. Channel <u>A19</u> contains the positive output monitor signal and <u>A40</u> the negative output.

At the onset of low voltage to the FIMS Central Electronics Package, a stepping value will be seen on both S31 and S32. Figures II-1 and II-2 are sample stripchart recordings made of these two channels under normal operating conditions. It should be noted that the strip chart recorder which produced these signals was carefully calibrated for a deflection of 10 counts/major division with the knowledge that the 2' bit of the 9-bit data word is the least significant bit of the word. In other words, when calibrating the stripchart recorder for use in FIMS B testing, use the second least significant bit rather than the least significant bit to represent a count of "1" for the calibration. It is not necessary for routine testing to recalibrate the stripchart recorder just for a FIMS B test. It is possible to observe the general waveform of these two stepping counts to ensure basic function of the Central Electronics Package. Word S27 will not contain any count information unless the signal input test is being conducted. Details for the signal input test for the FIMS B instrument are described in the following section of this manual.

Likewise, analog channels 39 and 40 will not contain any information unless the FIMS B high voltages have been turned on. If the high voltages have been applied, then a stepping signal will be seen on these two channels such as is shown in Figure II-3. Both of these channels should step at the same rate and contain approximately the same signals. It is expected that under normal circumstances these two signals should track each other to within 10%. Again, no data will be obtained from these two data channels unless high voltages are applied to the instrument.





III. SIGNAL INPUT TEST PROCEDURE

A. Equipment Required

To perform this test the following items will be required:

- 1. Pulser (i.e. HP8010A) capable of producing a 50ns wide, 100mv pulse at a repetition rate of 8500/sec.
- Two dial-selectable attenuators. One attenuator should be capable of attenuating in 1 dB steps, while the second should attenuate in 10 dB steps.
- Single channel oscilloscope, with vertical bandwidth of 100 mHz.
- 4. One 50 ohm terminator, packaged in a standard BNC-type connector shell.
- 5. Frequency counter capable of counting 100mv, 50ms wide pulses.

B. Pulser Adjustment

1

- 1. Configure the equipment listed above in the manner shown in Figure III-1.
- 2. With all equipment operating, adjust the output of the pulser to produce a 100mv low-going pulse, 50ms wide. The low-going pulse should drop to OV, with the high level at +100mv. The leading and trailing edge transition times should be adjusted to approximately 20ms each, although experience has shown that the instrument is not particularly sensitive to transition times. It is assumed that at the time of these adjustments the two dial-selectable attenuators are set to zero and the 50 ohm attenuator is attached to the pulser.
- 3. Adjust the repetition rate of the pulser to produce an 8500 pulse/second rate.
- 4. Turn off the pulser, remove the 50 ohm terminator and attach the FIMS-B test cable to the attenuators. Set the dial selectable attenuators for a total of 25 dB.

C. Instrument/Payload Operation

1. With the instrument power off and the pulser off, attach the FIMS-B test cable to the test input connector on the instrument's detector.

- 2. Apply low voltage only to the FIMS-B instrument, then to the pulser.
- 3. On the rocket's P.C.M. decommutator dial up word 13, frame 28.

16 MR 42 24 1940

- 4. With the pulser at 8500 pulses/second, the decommutator should be reading approximately full scale (8 MSB's set). If it is not, adjust the dial slectable attenuators in 1dB steps until counts appear. It should not be necessary to reduce the attenuation below 20dB if the instrument is functioning normally.
- 5. Select word 13, frame 27, on the decommutator. Under normal operating conditions the instrument will produce a few counts in this channel when frame 28 is near full scale. No other frames for word 13 should have any counts.

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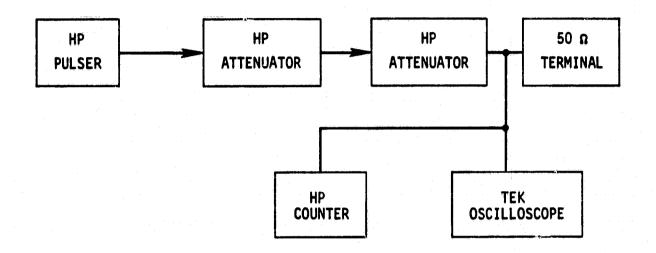


FIGURE III-1. SIGNAL INPUT TEST CONFIGURATION

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VII. DESIGN INFORMATION

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A. Analyzers

Barrison and Andrewson

The energy and mass analyzer design aspects of FIMS-B were described earlier in this document, and no furhter information on the analyzers will be presented. The following pages of this section of the document will present a description of the instruments Central Electronics Package.

B. Central Electronics Package

The Central Electronics Package designed for the FIMS B instrument is radically different from that used by the FIMS A instrument. This system is designed around the INTEL 8085 A-2 microprocessor. Figures VII-1 through VII-5 are the detailed schematic diagrams of the FIMS B central electronics package. This system is contained completely on a single printed circuit board. The microprocessor-based controller is a totally interrupt-driven system with interrupting sources being the SF 2^4 timing signal, the MF 2^3 timing signal and the data ready (D.R.) flag from the microchannel plate array detector.

In normal operation the microprocessor is interrupted every 25ms by the SF 2^4 signal to indicate the start of a new major frame of data. Responding to this interrupt, the microprocessor updates the commands to the two program power supplies based on values stored earlier in a look-up table, the values for which were derived from the instrument calibration. After updating the two command words to the PPS line drivers, the microprocessor makes a 1 to 0 to 1 transition on the shift-load line of the two shift registers which are used to send power supply step values to the telemetry system. In other words, the shift load command for the two shift registers used to send PPS values to telemetry is software generated.

Following the update of the two PPS commands, the 8085-based controller disables its science data acquisition for a period of 3 minor frames, or for approximately 2.4ms. This dead time is allowed for PPS settling. However, even during the 2.4ms dead time for scientific data acquisition, the microprocessor still is able to send data acquired during the previous major frame to telemetry based on the arrival of the MF 2^3 timing signal which generates the third processor interrupt. When interrupted by MF 2^3 , the microprocessor performs an address pointer manipulation to fetch the next stored value of science data word to telemetry.

Science data information is presented to the Central Electronics Package of the FIMS B instrument in the form of a 6-bit wide address with handshaking flag. The four least significant bits of this 6-bit word represent the binary address of the X-location on the resistive anode detector where a particle event has occured. The two most significant bits of the 6-bit address represent the binary address of the Y location on the resistive anode of the particle event. To the microprocessor, the interface appears to be a simple, 6-bit memory address with a handshaking flag that is used both to latch the data into the 8155 hardware interface

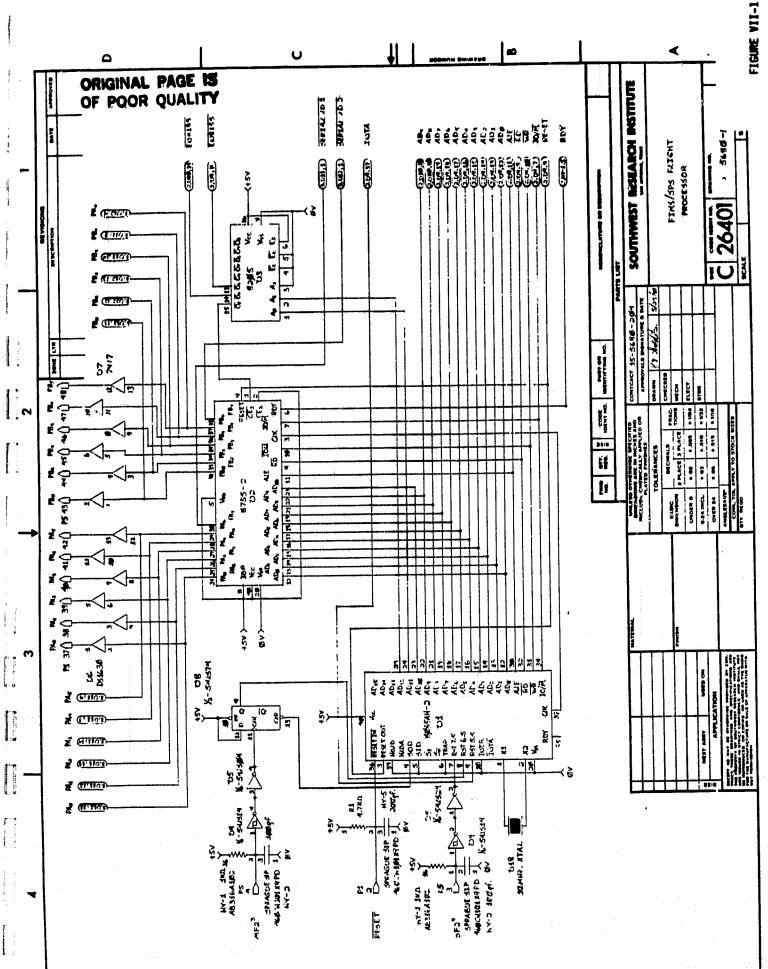
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chip and to generate the software interrupt to the 8085. Handshaking with the microchannel plate detector instrument is not completed until the 8085 has had a chance to read the data from the port B interface of the 8155, thus clearing the B buff-full handshaking flag. This ensures that the instrument will not attempt to send new science information to the CEF until the previous information has been properly digested. Stated in its most elemental form, the FIMS B CEP functions as a 64-channel multi-channel analyzer. Each time a 6-bit address is presented to the microprocessor and an interrupt generated, the corresponding memory location in the 64-word science data buffer memory is fetched, incremented by 1 and stored back. This is exactly the same procedure used with any laboratory-style multichannel analyzer.

The flight CEP described here contains a double-buffered memory system whereby two sets of 64 locations are reserved for the science data counts. Two buffers are preserved so that one can be reserved for data incoming from the instrument while the second buffer is used for sending data out to telemetry. At the end of each major frame, signalled by a high-going transition on the SF 2^4 timing signal, the buffer pointers are switched and the buffer previously used for storing incoming science information now becomes the output buffer for data going to telemetry. This technique preserves the integrity of the data as a block thus data that is presented in minor frame 1 has the same staleness as the data presented in minor frame 32.

As mentioned earlier, the microprocessor based controller is totally interrupt driven. The SF 24 timing flag initiates the process whereby the microprocessor updates the PPS commands to both program power supplies and also loads these new PPS commands in the telemetry data output shift registers. The SF 24 timing flag also initiates a 3-minor frame long blanking period where no new science data interrupts are acquired. Within the three minor frame dead time, however, interrupts are allowed from the MF 2^3 timing flag and are used to initiate the process of sending the next available science data word to telemetry. Stated another way, the first MF 2^3 interrupt which arrives at the microprocessor after the SF 24 interrupt is used to flag to the microproprocessor that it should present data from its buffer location 1 to telemetry, likewise the 32nd MF 2^3 interrupt to the microprocessor flags to the software that it should send the 32nd memory location from its science data buffer to telemetry. This results in a very simple mapping technique. The only aspect of this operation which is the least bit involved is the process whereby the numer of data channels sent to telemetry is reduced from the 64 channels produced by the instrument down to the 32 channels forwarded to telemetry. The 32 middle channels of the 64 total are sent to telemetry. This mapping function was arrived at by the simple knowledge of geometry of the instrument, that is channels 0 through 15 are channels which are not struck by particles under normal operation of the analyzer section of the instrument. Likewise, channels 48 through 64 do not contain any scientific information from the instrument.

Contained in the following few pages of this section of the document is a copy of the software listings which are used to control the operation of the FIMS B microprocessor based Central Electronics Package. As seen, the software was written in 8085 assembly language and is now stored in the erasable prom section of the 8755 integrated circuit.

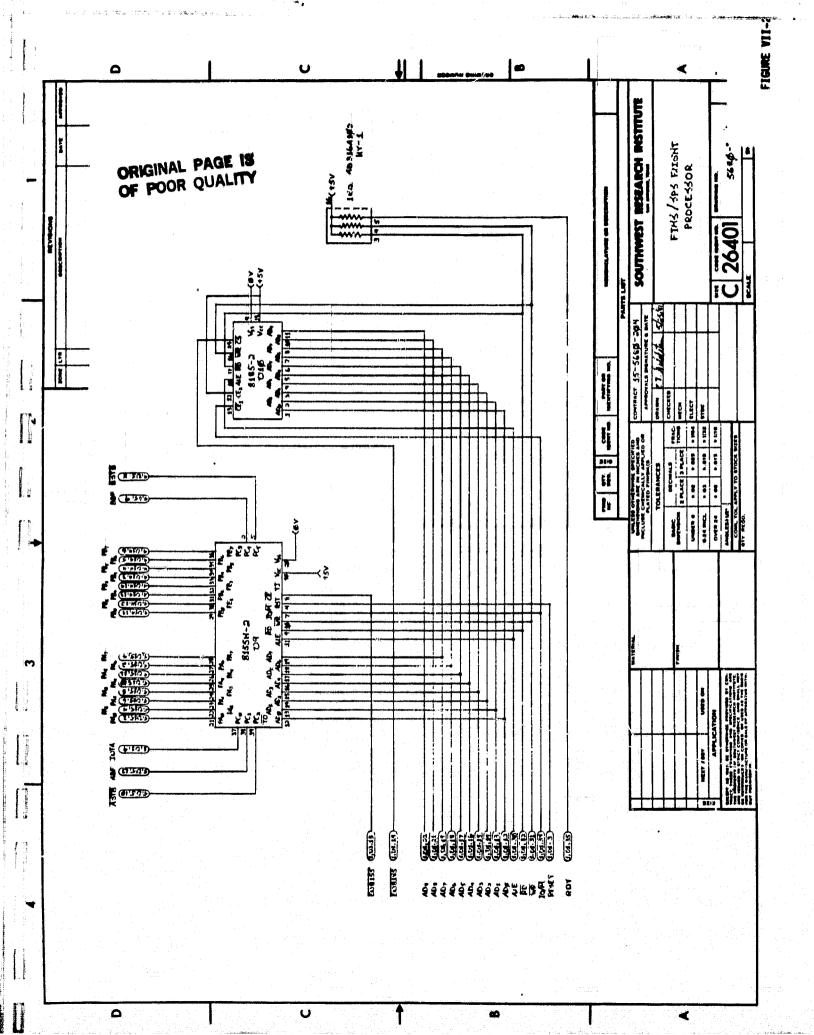


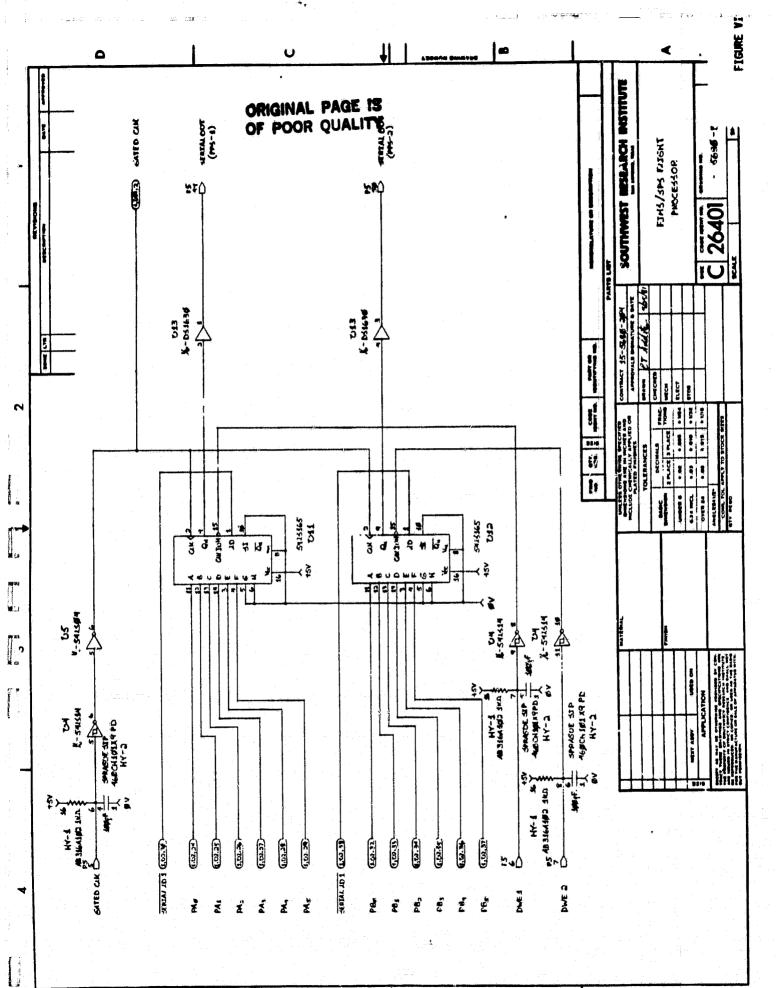
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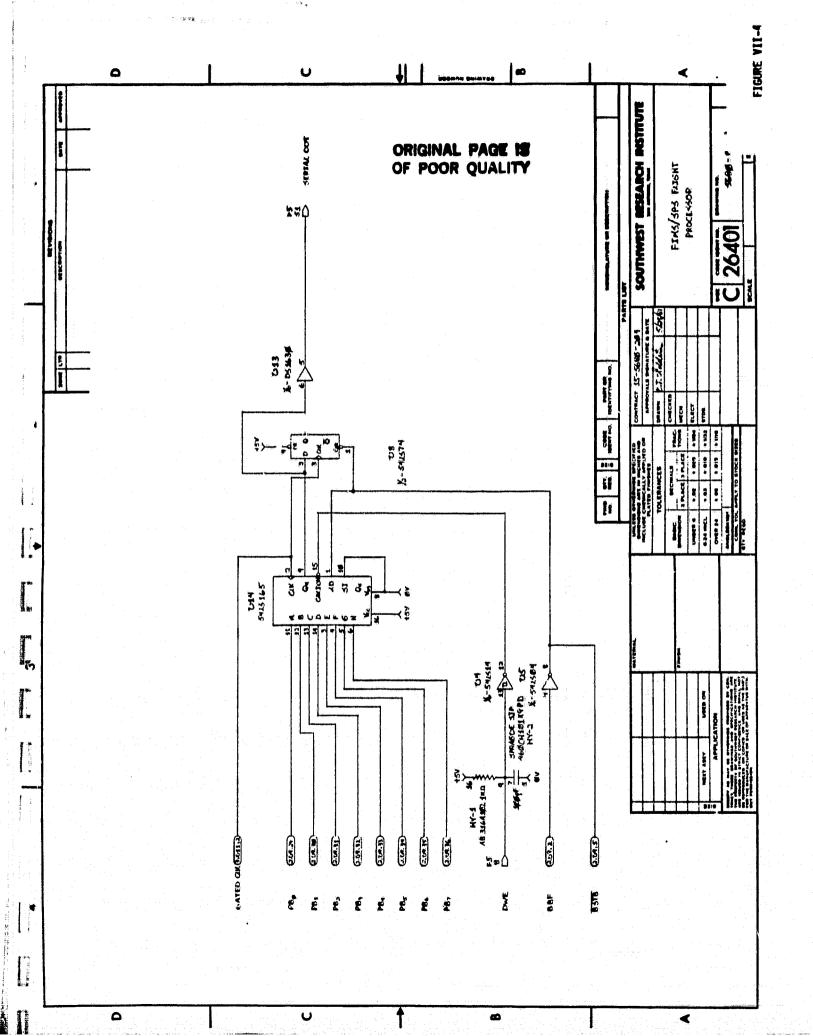
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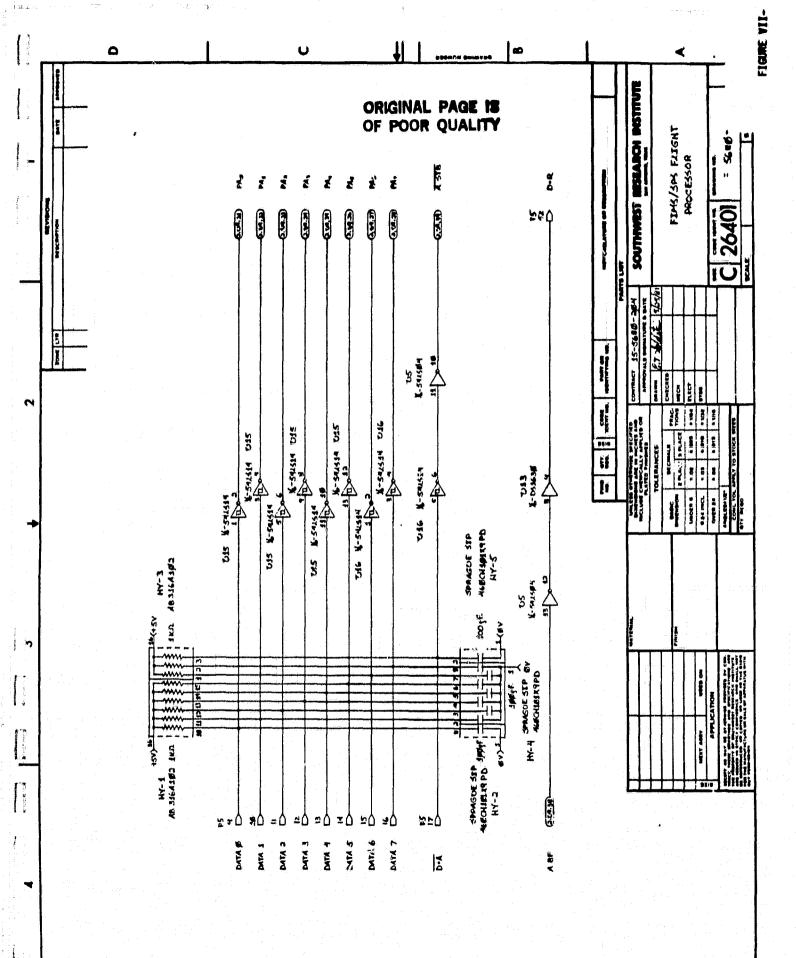
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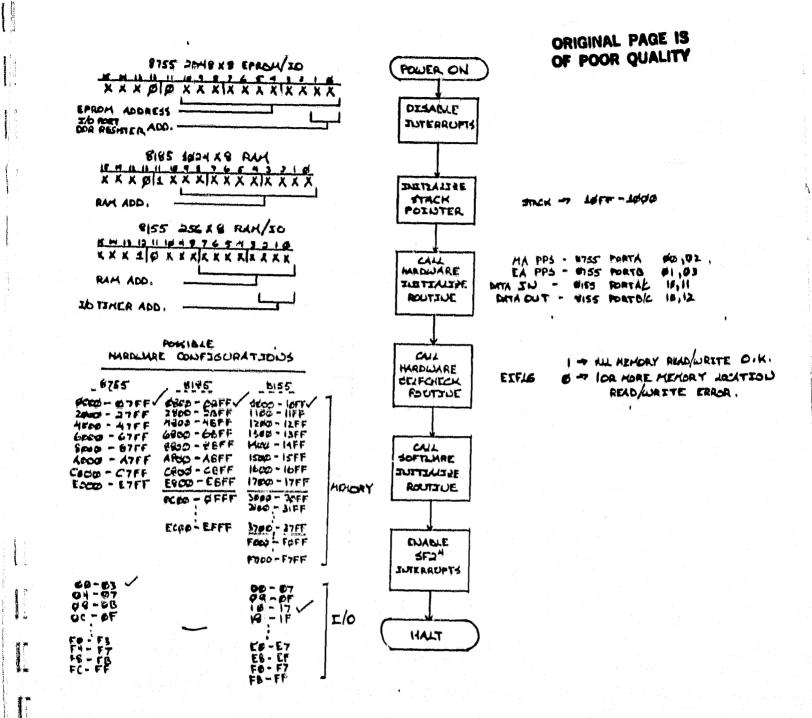
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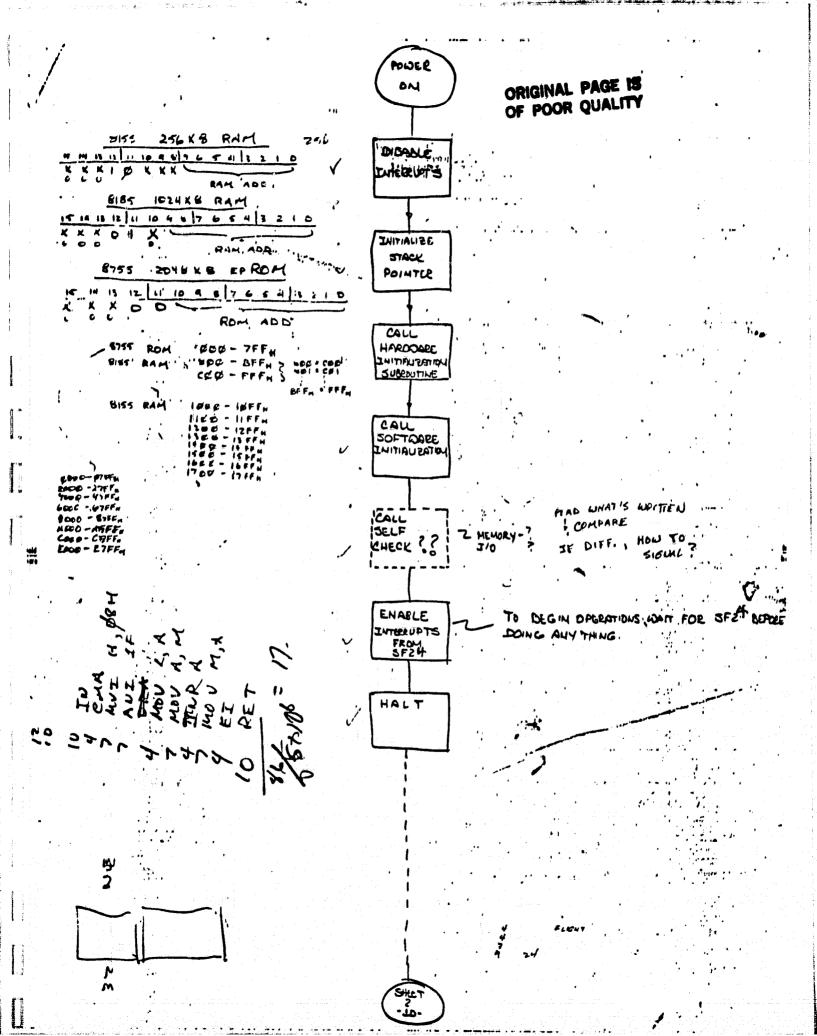
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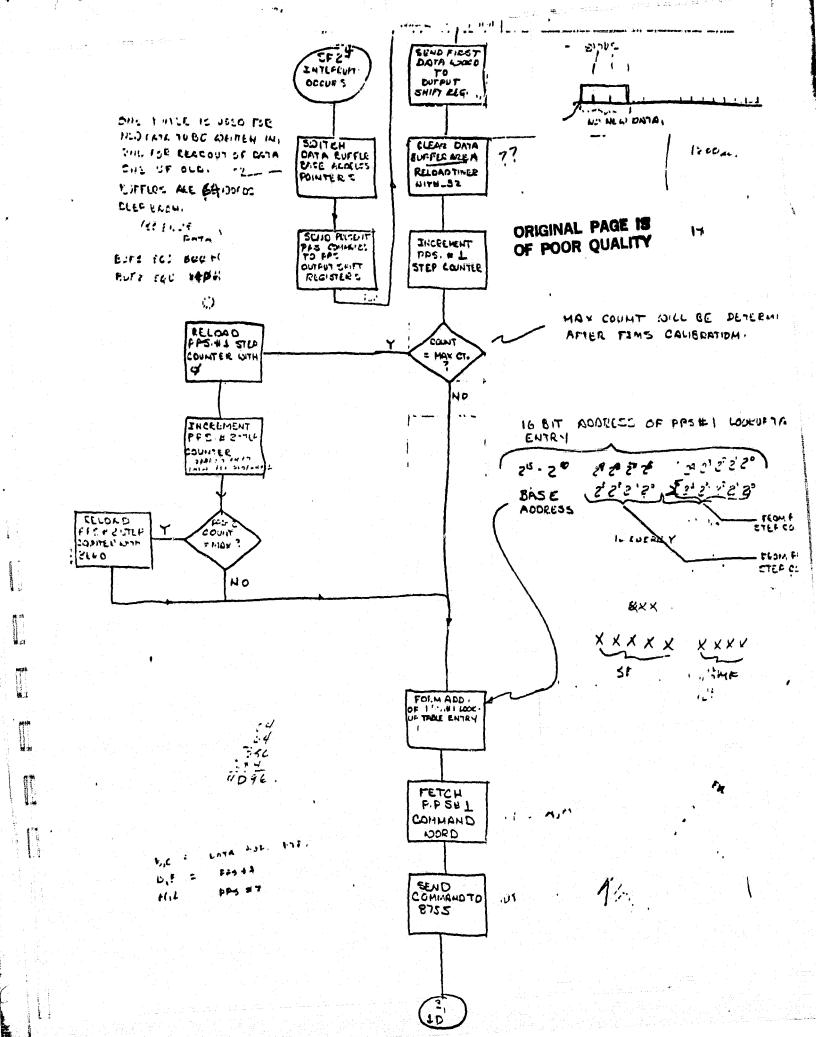
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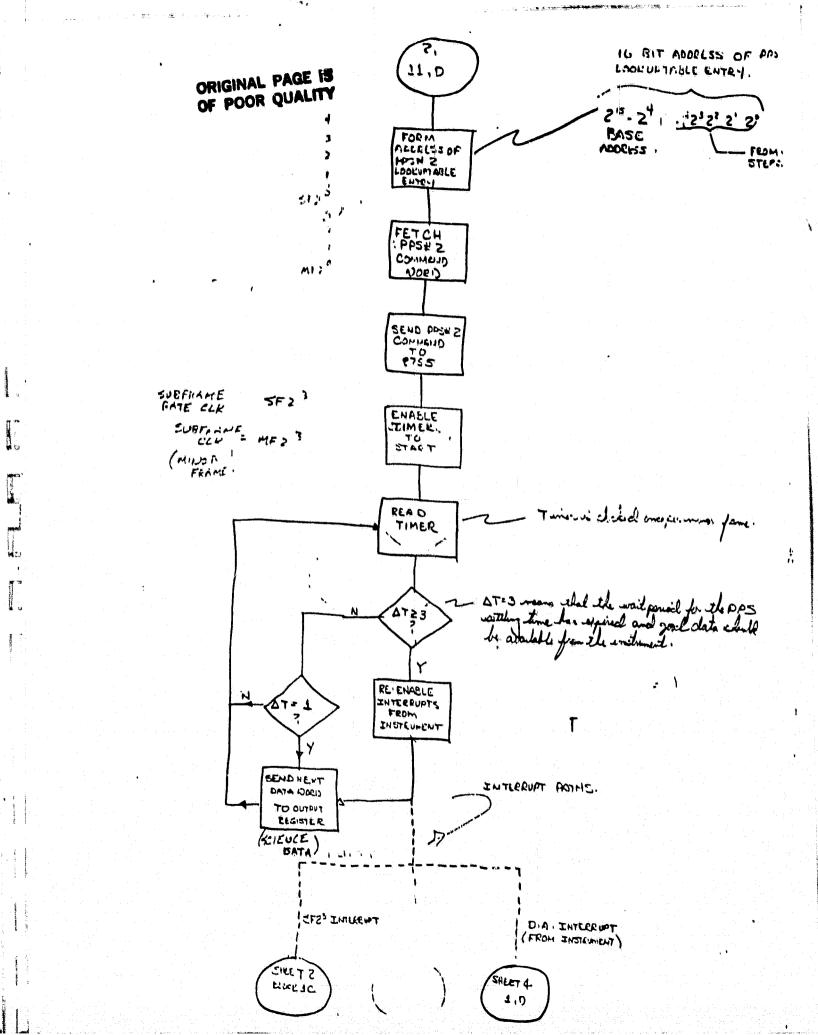
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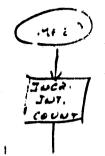


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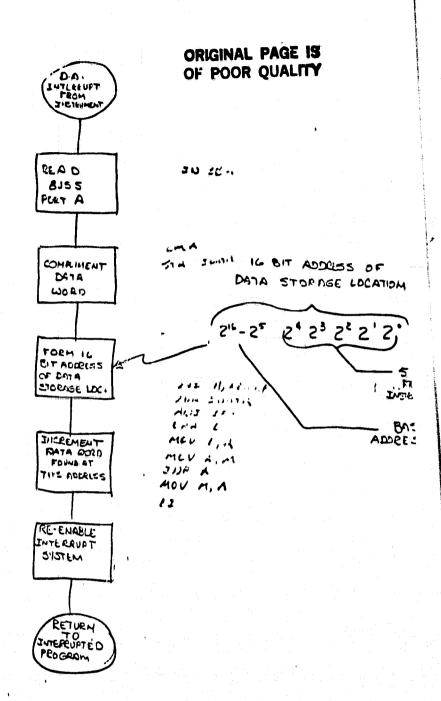


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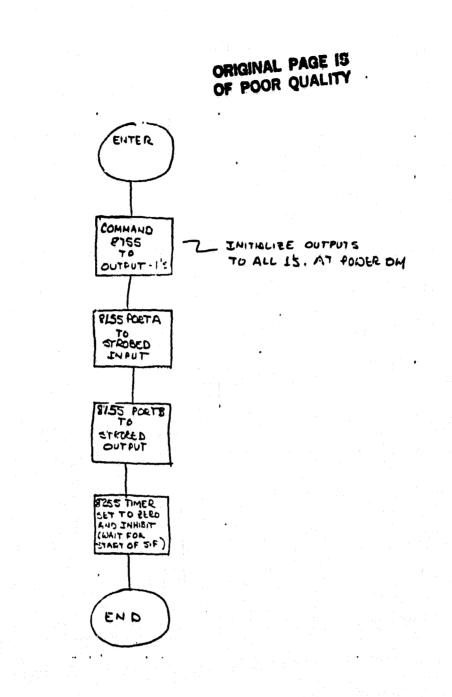
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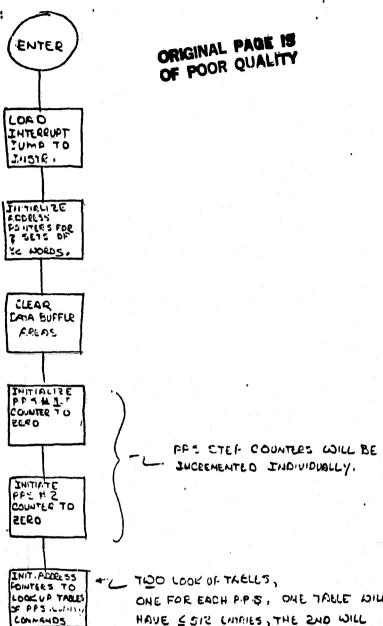
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1 0098 11 E 2755 WILL BE CLEARED HLDNG WITH ELFLG, 1 0098 12 EAVING THE INTERRUPT SYSTEM DISABLED. 1 0098 3600 CHRAW LXI H.GUGH 18185 ADDR. IN H.L PAIR. 11 0098 3600 CHRAW LXI H.GUGH 18185 ADDR. IN H.L PAIR. 11 0098 3600 CHRAW LXI H.GUGH 18185 ADDR. IN H.L PAIR. 11 0098 76 MUY H.M IMETE 0 TO 8185. 11 0098 3600 CHL WH 12 ENT SET IF A-REG. = 0. 11 0090 226000 JN2 WATER 11F 2-BIT = 0. ERNDR. 12 0046 726 MUY H.M IMETE IF A = FFH. 13 0046 CFEFF CPI UFH 12 ENT SET IF A = FFH. 14 0046 CABS00 JZ LKE6 11F 2-BIT SET. CHECK L. 14 0046 CABS00 JZ LKE6 11F 2-BIT SET. CHECK L. 14 0046 CABS00 JZ LKE6 11F 2-BIT SET. CHECK L. 15 0046 CABS00 JZ LKE6 11F 2-BIT SET. CHECK L. 16 0052 CSB00 JMP L8355 10 HERCARES. BUMP H.L. 110 0050 CAS00 JMP L8185 10 HERCARES. BUMP H.L. <th>4</th> <th></th> <th></th> <th></th> <th></th> <th></th> <th>1.0</th> <th></th>	4						1.0	
1 0098 10098 10098 1 0098 10098 10098 1 0098 10098 10098 1 0098 10098 10098 1 0098 10098 10098 1 0098 20008 10088 10080 1 0098 20008 10088 10080 10088 1 0098 20008 1008 10081 10081 10085 1 0098 20008 1008 10085 10085 10085 10085 1 0008 26000 JN2 WRTER 117 20081 20085 11085 116 117 20081 20081 118 20081 118 20081 118 20081 118 20081 118 20081 118 20081 118 20081 118 </th <th>1917 - 8</th> <th></th> <th></th> <th></th> <th></th> <th></th> <th></th> <th></th>	1917 - 8							
1 0098 11 E 2755 WILL BE CLEARED HLDNG WITH ELFLG, 1 0098 12 EAVING THE INTERRUPT SYSTEM DISABLED. 1 0098 3600 CHRAW LXI H.GUGH 18185 ADDR. IN H.L PAIR. 11 0098 3600 CHRAW LXI H.GUGH 18185 ADDR. IN H.L PAIR. 11 0098 3600 CHRAW LXI H.GUGH 18185 ADDR. IN H.L PAIR. 11 0098 76 MUY H.M IMETE 0 TO 8185. 11 0098 3600 CHL WH 12 ENT SET IF A-REG. = 0. 11 0090 226000 JN2 WATER 11F 2-BIT = 0. ERNDR. 12 0046 726 MUY H.M IMETE IF A = FFH. 13 0046 CFEFF CPI UFH 12 ENT SET IF A = FFH. 14 0046 CABS00 JZ LKE6 11F 2-BIT SET. CHECK L. 14 0046 CABS00 JZ LKE6 11F 2-BIT SET. CHECK L. 14 0046 CABS00 JZ LKE6 11F 2-BIT SET. CHECK L. 15 0046 CABS00 JZ LKE6 11F 2-BIT SET. CHECK L. 16 0052 CSB00 JMP L8355 10 HERCARES. BUMP H.L. 110 0050 CAS00 JMP L8185 10 HERCARES. BUMP H.L. <td></td> <td></td> <td></td> <td>i.</td> <td></td> <td></td> <td></td> <td></td>				i.				
i 0099 i iLetvine THE INTERRUPI SYSTEM DISABLED. 11 0098 i i 10 0098 i i 11 0098 i i i 11 0098 i i i i 11 0090 i i i i i 11 0090 i i i i i i 11 0090 i	- 1 - F							
11 0098 1 10 0098 210008 CHRKHW LNI H,800H 18185 ADDR. IN H-L PAIR. 11 0098 CARKHW LNI H,800H 18185 ADDR. IN H-L PAIR. 11 0098 CARKHW LNI H,800H 18185 ADDR. IN H-L PAIR. 11 0090 CAF000 JN2 WRTER 112-2017 = 0 0. ERKDR. 12 0008 366F MU M, M, WFH WRTE FFH TD 8185. 116 12 0008 366F MU M, MFH WRTE FFH TD 8185. 116 14 0008 CEP000 JN2 WRTER 317 2-517 = 0, ERKDR. 14 0008 CEP000 JN2 WRTER 317 2-517 = 0, ERKDR. 14 0008 CEP000 JN2 WRTER 317 2-517 = 0, ERKDR. 14 0008 CE900 JN2 WRTER 317 2-517 = 0, ERKDR. 14 0082 C39800 JMH H 12-515 = 517 EH = 06H. 131 12 0082 C39800 JMH L 18185 = 10000 = 10000 = 100000 118 2-511 SET 1F A = FFH. 131 0082 C39800 JMH L 18185 = 100000 = 100000 = 118 2-511 SET 1F A = 000000 = 118 2-5000 = 10000 = 118 2-511 SET 1F A = 0000000 = 118 2000 = 100000 = 118 2000 = 100000								
110098 210008CHKHW LXI H,BUCH18185 ADDR. IN H-L PAIR.110090 7EMDV H+MWHITE UT D 8185.118 0096 7EMDV H+MWHITE UT D 8185.118 0096 7EMDV H+MSZ-BIT SET IF A-REG. = 0.12 0040 C2F000CPI 004SZ-BIT SET IF A-REG. = 0.12 0040 7EMVI M,UFFHSWITE FFH TD 8185.12 0047 7EMVI M,UFFHSWITE FFH TD 8185.12 0048 7CMDV H,HSREAD 8185.12 0048 7CMDV H,HSFEIT = U, ERKDR.124 0048 7CMDV H,HSFEIT = U, ERKDR.125 0046 CFE0BCFI UBHSZ-BIT SET IF A = FFH.126 0046 CFE05JZ LKE6SIF Z-BIT SET, CHECK L.127 0051 23INX HSCHECK NEXT LDCATION.126 0052 C39B00JMP L8185SCHECK NEXT LDCATION.127 0058 CABFUUJZ C3155SIF Z-BIT SET IF L = FH.131 0088 CABFUUJZ C3155SIF Z-BIT SET IF L = FH.132 0086 C3FB00JMP L8185SCHECK NEXT LDCATION.134 0086 C4F00JMP L8185SCHECK NEXT LDCATION.135 0000 SIFF0BLXI SP.0BFFHSHEW SETURN ADD.136 0002 C55PUSH BSUSH SETURN ADD.137 0002 75MOV A,MSKEAE SIDR. IN H,L PAIR.138 0007 3600LS155 MVI M,004SUSH RETURN ADD.139 0002 73MOV A,MSKEAE SIDR. IN H,L PAIR.130 0005 74MOV A,MSKEAE SIDR. IN H,L PAIR.137 0002 75MOV A,MSKEAE SIDR. IN H,L PAIR.138 0007 3600LXI SP.0BFFHSKEAE SIDR. IN H,L PAIR.139				SUEHV)	INP 1	HE INTERRU	JPT SY	STEM DISHBLED.
11 0090 7E MUY H, WHTE U 10 8185. 117 0090 7E MUY H, M IREAD 8185. 118 0096 7E00 JL2 WRTER 31F 2-BIT SET 1F A-REG. = 0. 12.0003 36FF MUY H, M IREAD 8185. 140046 7EE MDY H, H IREAD 8185. IF 140048 7C MDY H, H IREAD 8185. IF 1410048 7E JDH RMUSER IF 2-BIT = U, ERROR. 142 OORG 7E00 JDL LREG IF 2-BIT = U, ERROR. 142 OORG 7E00 JZ LREG IFF 2-BIT SET IF A-REG. 142 OORG 7E00 JZ LREG IFF 2-BIT SET IF A-REG. 143 OORG 7E00 JP LE85 ICHECK NEXT LDCATION. 142 OORG 7EF CFI UFFH IF 2-BIT SET IF A-SEG. 143 OUBE 2C INK H IDHERMISE, BUMP L. 144 OUBE 2C INK H IDHERMISE, BUMP L. 145 </td <td></td> <td></td> <td></td> <td>3</td> <td></td> <td></td> <td></td> <td></td>				3				
17 0.090 7E MDV HN IRED 8185. 18 0.040 C2F000 CPI 0.044 IZ=bIT SET IF A=REG. = 0. 11 0.040 C2F000 JN2 WRTER IF Z=bIT = 0. ERROR. 12: 0.063 S6FF MVI M.VFH IMRITE FFH TO 8185. 12: 0.066 C2F000 JN2 WRTER IF Z=bIT = 0. ERROR. 14: 0.066 C2F000 JN2 WRTER IF Z=bIT = 0. ERROR. 14: 0.066 C2F000 JN2 WRTER IF Z=bIT = 0. ERROR. 14: 0.066 C2F000 JN2 WRTER IF Z=bIT = 0. ERROR. 14: 0.066 C29000 JZ LRE6 IF Z=bIT = 0. ERROR. 14: 0.066 CFEFF CPI UFH IZ=bIT SET IF A = FFH. 15: 0.066 CFEFF CPI UFH IZ=bIT SET IF A = FFH. 16: 0.066 CFEFF CPI UFH IZ=bIT SET IF A = FFH. 16: 0.062 C39B00 JNP L8185 ICHECK NEXT LDCATION. 17: 0.082 C39B00 JNP L8185 ICHECK NEXT LDCATION. 13: 0.002 SF0 LNI K IF Z=bIT SET IF L = FFH. 13: 0.003 IFF 0B LXI SP.00FFH INEWISE SHUP L.								
110040C2F00CH100412-bit set 1f A-REG. = 0.110040C2F000JN2WRTER11F 2-bit = 0.ERKDR.12.0043S6FFMUYM.VFHJWRIE FFH TD 8185.12100457EMDYH.MIKEAD 8185.1220046FEFFCP10FH12-bit set 1F A = FFH.120046FEFFCP10FH12-bit set 1F A = FFH.1240046FEOBCP10FH12-bit set 1F A = FFH.1240046FEOBCP10FH12-bit set 1F A = FFH.1240046FEOBCP10FH12-bit set 1G A-REG.1240046FEOBCP10BH12-bit set 1G A-REG.1240046FEOBCP10BH12-bit set 1G A-REG.1260062C9800JMPL81851CHECK NEXT LDCATION.127005123INKH12-bit set 1F L = FFH.1310058CBFFCP10FH12-bit set 1F L = FFH.1310058CCINKL10HERWISE, BUMP L.1320056CS5PDF SSAVE RETURN ADD.IN S.C.1340056CS5PDF SSAVE RETURN ADD.IN S.C.1350057CS155PDF SSAVE RETURN ADD.IN S.C.1360057CS155PDF SSAVE RETURN ADD.IN S.C.1360057CS155PDF SSAVE RETURN ADD.IN S.C.136 <td></td> <td></td> <td></td> <td>L8185</td> <td></td> <td></td> <td></td> <td></td>				L8185				
1100AU C2F000JNZ WRTER11F Z-BIT = 0. ERROR.12. 00AS 36FFMV1 M, UFHJMRITE FFH TO 8185.12. 00AS 7EMDV A, MJMRITE FFH TO 8185.14. 00AS 7EMDV A, MJREAD 8185.15. 00AS C2F000JNZ WRTERJIF Z-BIT SET IF A = FFH.14. 00AB 7CMDV H, HJFETCH H-REG. IN A-REG.154. 00AB 7CMDV H, HJFETCH H-REG. IN A-REG.154. 00AE CAB500JZ LREGJIF Z-BIT SET, CHECK L.157. 00B1 23INX HJDHERWISE, BUMP H-L.168. 00AE CAB500JZ LREGJIF Z-BIT SET, CHECK L.177. 00B1 23INX HJDHERWISE, BUMP H-L.168. 00AE CABFUUJZ C8155JIF Z-BIT SET, CHECK L.179. 00B8 CABFUUJZ C8155JIF Z-BIT SET, CHECK 8155.170. 00B8 CABFUUJZ C8155JIF Z-BIT SET, CHECK 8155.171. 00B8 CABFUUJZ C8155JIF Z-BIT SET, CHECK 8155.172. 00B8 CCINR LJUHERWISE, BUMP L.173. 00C3 C5PUSH BJCHECK NEXT LDCATION.174. 00C4 C10 C01 C C8155 MDF BJF9.00FFH175. 00C3 C5PUSH BJPUSH RETURN ADD. IN B, C.176. 00C4 7E0LR155MI M, 0UH177. 00C9 7EMDV A, M178. 00C7 7EMDV A, M179. 00C4 7E0JNZ WRTER179. 00C4 7E0JNZ WRTER179. 00C4 7E0JNZ WRTER184. 00D4 C2F000JNZ WRTER197. 00C5 7EFMV1 M, 0UF144. 00D2 CF 2000JNZ WRTER157. 00D4 C2F000JNZ WRTER167. 00D4 C2F000<								
12. 0043 36FF MVI N, UFFH WRITE FFH TD \$185. 12 0046 FEFF CP1 0FH 32-BIT SET IF A = FFH. 13. 0046 CF2000 JN2 WRIER 31F 2-BIT SET IF A = FFH. 14. 0046 CF2000 JN2 WRIER 31F 2-BIT SET IF A = FFH. 14. 0046 CF200 JN2 WRIER 31F 2-BIT SET. CHERDR. 14. 0046 CABS CP1 UBH 32-bit SET. FORCH H-REG. 14. 0046 CABS DP LB165 IFF 2-BIT SET. CHECK L. 127 0081 CS IF A SET. BUM HECG. Net. 128 0085 CB DP LB185 IFFC-BIT SET. IF A FFH. 131 0085 CB DP LB185 IFFC-BIT SET. IF A FFH. 132 0085 CB DNF LB185 IF C-BIT SET. IF A IF A 131 0085 CB DNF LB185 IF C-BIT SET. IF A					10 Y 11			
12100H57EMDVA,MIREAD 3185.1200H6FEFFCP10FFHIZ-BIT SET IF A = FFH.12400H8CZF000JNZWRTERIF 2-BIT SET.12400H87CMDVH,HIFETCH H-REG.12400H87CMDVH,HIFETCH H-REG.12400H87CMDVH,HIFETCH H-REG.12400H2C39500JMPLRE6IIF12700B123INXHIFETCH L-REG.IN A-REG.12800B2C39800JMPL8185ICHECK NEXT LDCATION.12900B2CINXHIF 2-BIT SET.CHECK 8155.12900B2CINXLIF 2-BIT SET.CHECK 8155.12900B2CINXLIF 2-BIT SET.CHECK 8155.13400B2CINXLXISP+0BFFHINEW SITE CHECK 8155.13400B2CINXH,100HIS155AUPL L.13500C031FF0BLXISP+0BFFHINEW SITE CHECK 8155.13600C031FF0BLXISP+0BFFHINEW SITE CHECK 8155.13600C031FF0BLXISP+0BFFHINEW SITE CHECK 8155.13600C031FF0BLXISP+0BFFHINEW SITE CHECK 8155.13700C031FF0BLXISP+0BFFHINEW SITE CHECK 8155.13700C025PUSH BIF 2-BIT SET 1F A = 00FH.136 </td <td></td> <td></td> <td></td> <td></td> <td></td> <td></td> <td></td> <td></td>								
12 00H6 FEFF CP1 0FFH 32-BIT SET IF A = FFH. 124 00H8 7C MDV H+H IF Z-BIT set IF A = FFH. 124 00H6 FC0 MDV H+H IF Z-BIT set IF A = 0BH. 124 00H6 FE0B CP1 UBH SZ-BIT SET CHECK L 124 00B1 23 INX H IF Z-BIT SET CHECK L 127 00B1 23 INX H IF Z-BIT SET CHECK L 126 00B1 23 INX H IF Z-BIT SET CHECK L 128 00B5 7D LKE6 MDV H+L IF Z-BIT SET CHECK L 131 00B5 CBFFH CP1 UFH IZ-BIT SET CHECK REG. 134 00B6 CC JMP LS185 IUHEFMISER BUHP L 134 00B7 CB S55 IPF B SPACK RETURN ADD. N B.C. 135 00C0 SFF0B LX1 SPACK IPINER IN J.C. 136	12.							
1200AS C2F000JN2WRTERIF 2-BIT = 0, ERRDR.12400AB 7CMDVH+HIFETCH H-REG. IN A-REG.12400AC FE0BCPI URHIZ-BIT SET, CHECK L.12500AE CABS00JZLREG12700B1 23INX HIDTHERWISE, BUMP H+L.12800B2 C39BU0JMP L8185ICHECK NEXT LDCATION.12700B2 C39BU0JMP L8185ICHECK NEXT LDCATION.12800B2 C39BU0JMP L8185ICHECK NEXT LDCATION.12900B2 C2INX HIF 2-BIT SET, CHECK 8155.13100B6 CABFUUJZC8155IF 2-BIT SET, CHECK 8155.13200B2 C2INX LIUFFHIST IDCA NEXT LDCATION.13400B2 C39B00JMP L8185ICHECK NEXT LDCATION.13500C0 31FF 08LXI 3P; 0BFFHINEW STACK POINTER.13500C3 C5PUSH BIFPUSH RETURN ADD.13600C7 3600L8155 MVI M; 000HISTS ADDR. IN H; L FAIR.13700C4 FE00CFI 00HIFFH13800C7 760LXI M; 00HIFE 2-BIT SET IF A = 00H.14100C4 FE00CFI 00HIN2 WRTER14200C7 760JN2 WRTERIF 2-BIT SET IF A = 0FFH.14300D1 7EMDV A;MIREAD 8155.14400D2 FEFFCFI 0FFHIZ-BIT SET IF A = 0FFH.15500D4 C2F000JN2 WRTERIF 2-BIT 0, ERRDR.14300D2 FEFFCPI 0FFHIZ-BIT 3ET IF A = 0FFH.15000D4 C2F000JN2 WRTER<	121	UUA5 7	'E		MEIY	fi • M		
12400HB7CMDVH,HFFETCH H-REG. IN A-REG.12500HCFEDBCPI08HFZ-BIT SET, CHECK L.12700B123INXHSDTHERMISE, BUMP H,L.12600B2C39BU0JMPL8165SCHECK NEXT LDCATION.13100B57DLREGMDVH,LSFETCH L-REG. IN A-REG.13200B6FEFFCPIUFFHSFETCH L-REG. IN A-REG.13300B8CABFUUJZC8155SCHECK NEXT LDCATION.13400B6CEFFCPIUFFHSETCH NAMEL13400B7C1C8155FDPB13500C0SIFF 0BLX1SP+00FFHSAVE RETURN ADD. IN B; C.13500C3C5PUSH BSP+00FFHSAVE RETURN ADD.13500C3C5PUSH BSP+00FHSAVE RETURN ADD.13600C3C5PUSH BSP+00FHSAVE RETURN ADD.13600C3C5PUSH BSP+00FHSAVE RETURN ADD.13600C3C5MU M, WOHASAVE RETURN ADD.13600C4210010LX1H;1000HSAVE RETURN ADD.13600C73600LX1SP+00FHSAVE RETURN ADD.13600C4210010LX1H;1000HSAVE RETURN ADD.13700C97EMDVA;SAVE RETURN ADD.13600C7S6FFMU M;WRITESIT13700C9C4MU M	12	OOA6 P	FFF		CP1	ÚFFH		Z-BIT SET IF H = FFH.
12400HB7CMDVH,HFFETCH H-REG. IN A-REG.12500HCFEDBCPI08HFZ-BIT SET, CHECK L.12700B123INXHSDTHERMISE, BUMP H,L.12600B2C39BU0JMPL8165SCHECK NEXT LDCATION.13100B57DLREGMDVH,LSFETCH L-REG. IN A-REG.13200B6FEFFCPIUFFHSFETCH L-REG. IN A-REG.13300B8CABFUUJZC8155SCHECK NEXT LDCATION.13400B6CEFFCPIUFFHSETCH NAMEL13400B7C1C8155FDPB13500C0SIFF 0BLX1SP+00FFHSAVE RETURN ADD. IN B; C.13500C3C5PUSH BSP+00FFHSAVE RETURN ADD.13500C3C5PUSH BSP+00FHSAVE RETURN ADD.13600C3C5PUSH BSP+00FHSAVE RETURN ADD.13600C3C5PUSH BSP+00FHSAVE RETURN ADD.13600C3C5MU M, WOHASAVE RETURN ADD.13600C4210010LX1H;1000HSAVE RETURN ADD.13600C73600LX1SP+00FHSAVE RETURN ADD.13600C4210010LX1H;1000HSAVE RETURN ADD.13700C97EMDVA;SAVE RETURN ADD.13600C7S6FFMU M;WRITESIT13700C9C4MU M	12	0088 0	2F000		JN2	WRIER		SIF 2-BIT = U, ERROR.
1100HE CABS00JZLRE6 $IF Z-BIT SET, CHECK L.$ 12700B1 23INXH $IF DTHERWISE, BUMP H_L.$ 12600E5 C39BU0JMP L8165ICHECK NEXT LDCATION.13100E5 C7DLRE6MDVH,L14100E5 C7DLRE6MDV14200E6 FEFFCPI UFFHIF Z-BIT SET, CHECK B155.13200E6 CABFUUJZCS155IF Z-BIT SET, CHECK 8155.13200E5 C30JMP L8185ICHECK NEXT LDCATION.13300E5 C30JMP L8185ICHECK NEXT LDCATION.13400E5 C1CS155 FDP BISAVE RETURN ADD. IN B,C.13500C0 31FF0BLX1 SP,0EFFHINEW STACK PDINTER.13600C3 C5PUSH BIPUSH RETURN ADD.13700C4 210010LX1 H,1000HIS155 HDDR. IN H_L PAIR.13800C7 3600LB155 MV1 M;00HIWRITE D1 SET IF A = 00H.13400C4 7E00LB125 MV1 M;00HIWRITE EFF D; CFI 00FH13400C7 3600LB125 MV1 M;00HIWRITE HFH TD 8155.14500C7 36FFMV1 M;0FFHIFETCH L-RE6. IN A-KEG.14400C7 36FFMV1 M;0FFHIFETCH L-RE6. IN A-KEG.14500D4 C2F000JM2 WRIERIF Z-BIT SET IF A = 0FFH.14500D4 C2F000JM2 WRIERIF Z-BIT SET		00AB 7	'C		MDV	H+H		FETCH H-REG. IN A-REG.
1100HE CABS00JZLREGIF Z-BIT SET, CHECK L.12700B1 23INXHITF Z-BIT SET, CHECK L.12600E5 C39BU0JMP L8185ICHCK NEXT LDCATION.13100B5 7DLREGMOVH,L13100B8 CABFUUJZC8155IF Z-BIT SET, CHECK NEXT13100B8 CABFUUJZC8155IF Z-BIT SET, CHECK 8155.13400B8 CCINRLIDTHERWISE, BUMP L.13500B8 CCC8155FDPB13600B7 C1C8155FDPB13700C3 C5PUSH BIPUSH RETURN ADD. IN B,C.13500C4 210010LX1SP,0BFFHINEW STACK PDINTER.13600C4 210010LX1H,1000HIB155 HDDR. IN H,L FAIR.13700C7 7EMDV A,MIREAD 8155.14100C6 7EFMDV A,MIREAD 8155.14100C7 7EMDV A,MIREAD 8155.14200CF 36FFMVI M,0FFHIWRITE FFH TD 8155.14300D4 C2F000JN2WRIERIF Z-BIT SET IF A = 0FFH.14400D2 FEFFCFI 0FFHIF Z-BIT SET IF A = 0FFH.14500D4 C2F000JN2WRIERIF Z-BIT SET IF A = 0FFH.14600C6 736FFMVI M,0FFHIFETCH L-REG. IN A-KEG.14700D1 7EMDV A,KIF Z-BIT SET IF A = 0FFH.14800D4 C2F000JN2WRIERIF Z-BIT SET IF A = 0FFH.14900D4 C2F000JN2WRIERIF Z-BIT SET IF A<	185	UUHC F	EOB		CPI	UBH		32-B11 SET IF H = 0BH.
1270081 23INXHIDTHEWISE, BUMP H.L.1280082 C39B00JMP L8185ICHECK NEXT LDCATION.1280085 7DLRE6MDVH.L1310085 7BLRE6MDVH.L1310088 CASF00JZC3155IF 2-BIT SET IF L = FFH.1310088 CASF00JZC3155IF 2-BIT SET.1340088 CASF00JJPL8185ICHECK NEXT LDCATION.1350088 CASF00JJPL8185ICHECK NEXT LDCATION.1350085 C1CS155 PDPBISAVE RETURN PDD. IN B.C.1350006 C5PUSH BISAVE RETURN PDD. IN B.C.1360007 C5PUSH BIPUSH B1370006 726MOV A:MIWETE 0 TD 8155.1410007 72MDV A:MIWETE 0 TD 8155.1410007 72MDV A:MIF 2-BIT SET IF A = 00H.1410007 75MDV A:MIF 2-BIT SET.1420007 70MDV A:LIF 2-BIT SET IF A = 0FFH.1430017 75MDV A:LIF 2-BIT SET IF A = 0FFH.1440026 FEFFCPI 0FFHIF 2-BIT SET IF A = 0FFH.1450017 70MDV A:LIF 2-BIT SET IF A = 0FFH.1460026 C3700JNZUKTERIF 2-BIT SET IF A = 0FFH.150005 3EUMVI A:PFFHIFETCH L-RE6.1610051 3EFFCPI 0FFHIF 2-BIT SET IF A = 0FFH.1500052 700JNZUKTER1610051 3EFFCHI 0H179005	12	OOHE C	AB500		JZ	LREG		\$ IF 2-BIT SET, CHECK L.
1280082C39800JMPL81653 CHECK NEXT LOCATION.1300857DLREGMDVH+L3FEICH L-REG. IN A-REG.130086FEFFCFIUFFH3Z-BI1 SET IF L = FFH.1310086CASF 00JZC3155SIF 2-BI1 SET . CHECK 8155.13*0086CASF 00JMPL81853CHECK NEXT LDCATION.13*0087C1CS155FDPSAVE RETURN ADD.13*0087C1CS155FDFSAVE RETURN ADD.13*0003C5PUSHSHENG KENC PDINTER.13*0003C5PUSH BSPUSH RETURN ADD.13*0003C5PUSH BSPUSH RETURN ADD.13*0003C5PUSH BSPUSH RETURN ADD.14*0004C55MVI M:00HSWETE 015*000736FFMDV A:MSREAD 8155.14*00077EMDV A:MSREAD 8155.14*0007GFFFCFI 00HSZ-BIT SET IF A = 00H.14*00077DMDV A:MSREAD 8155.14*0008FEFFCFI 00FHSZ-BIT SET IF A = 0FH.15*00077DMDV A:LSFETCH L-REG. IN A-REG.14*0008FEFFCFI 00FHSZ-BIT SET IF A = 0FH.15*00077DMDV A:LSFETCH L-REG. IN A-REG.14*0008FEFFCFI 00FHSZ-BIT SET IF A = 0FH.15*0004C2F000JME WRITERSFETCH		00B1 8	23		INX	н		SOTHERWISE, BUMP H.L.
13. 00B6 FEFFCP1UFFH 32 C8155 $31F$ 2-B11 SET 1F L = FFH.13. 00B8 CABF UUJZC8155 $31F$ 2-B11 SET 1F L = FFH.13. 00B0 CABF UUJZC8155 $31F$ 2-B11 SET 1F L = FFH.13. 00B0 CABF UUJZC8155 $31F$ 2-B11 SET 1F L = FFH.13. 00B0 CABF UUJZC8155 $31F$ 2-B11 SET 1F L = FFH.13. 00B0 CABF UUJZC8155 $31F$ 2-B11 SET 1F L = FFH.13. 00B0 CABF UUJZC8155 $31F$ 2-B11 SET 1F L = FFH.13. 00C3 C5FUSH BSAVE RETURN ADD.IN B.C.13. 00C3 C5FUSH BSAVE RETURN ADD.IN B.C.13. 00C3 C5FUSH BSAVE RETURN ADD.IN B.C.13. 00C4 210010LX1 H.1000HSB155 HDR. IN H.L PAIR.13. 00C7 7EMDV A.MSREAD 8155.14. 00C4 FE00CP1 UUHSZ-BIT SET IF A = 00H.141 00CC C2F00UJN2 WRTERSIF 2-BIT = 0, ERRDR.142 00D2 FEFFCP1 0FFHSREAD 8155.144 00D2 FEFFCP1 0FFHSFETCH L-REG. IN A-REG.15 00D7 7DMDV A.LSFETCH 1/D 20FFH.16 00D8 FEFFCP1 0FFHSET 8755 HDRT A, BIT 7.16 00D8 C3C700JMP L8155SCHECK NEXT LDCATIDN.17 00E8 31FF10LX1 SP.10FFHSET 8755 HDRT A, BIT 7.16 00E3 1300DU1 0UHSET 8755 HDRT A, BIT 7.17 00E8 31FF10LX1 SP.10FFHSFETCH 1/D DATA.15 00EA C1PDP BSFETCH RETURN ADD.15 00EA C1PDP BSFETCH RETURN ADD.15 00EA C1P	128	00B2 C	39BU0		JMP	L8185		JCHECK NEXT LOCATION.
13. 00B6 FEFFCP1UFFH 32 C8155 $31F$ 2-B11 SET 1F L = FFH.13. 00B8 CABF UUJZC8155 $31F$ 2-B11 SET 1F L = FFH.13. 00B0 CABF UUJZC8155 $31F$ 2-B11 SET 1F L = FFH.13. 00B0 CABF UUJZC8155 $31F$ 2-B11 SET 1F L = FFH.13. 00B0 CABF UUJZC8155 $31F$ 2-B11 SET 1F L = FFH.13. 00B0 CABF UUJZC8155 $31F$ 2-B11 SET 1F L = FFH.13. 00B0 CABF UUJZC8155 $31F$ 2-B11 SET 1F L = FFH.13. 00C3 C5FUSH BSAVE RETURN ADD.IN B.C.13. 00C3 C5FUSH BSAVE RETURN ADD.IN B.C.13. 00C3 C5FUSH BSAVE RETURN ADD.IN B.C.13. 00C4 210010LX1 H.1000HSB155 HDR. IN H.L PAIR.13. 00C7 7EMDV A.MSREAD 8155.14. 00C4 FE00CP1 UUHSZ-BIT SET IF A = 00H.141 00CC C2F00UJN2 WRTERSIF 2-BIT = 0, ERRDR.142 00D2 FEFFCP1 0FFHSREAD 8155.144 00D2 FEFFCP1 0FFHSFETCH L-REG. IN A-REG.15 00D7 7DMDV A.LSFETCH 1/D 20FFH.16 00D8 FEFFCP1 0FFHSET 8755 HDRT A, BIT 7.16 00D8 C3C700JMP L8155SCHECK NEXT LDCATIDN.17 00E8 31FF10LX1 SP.10FFHSET 8755 HDRT A, BIT 7.16 00E3 1300DU1 0UHSET 8755 HDRT A, BIT 7.17 00E8 31FF10LX1 SP.10FFHSFETCH 1/D DATA.15 00EA C1PDP BSFETCH RETURN ADD.15 00EA C1PDP BSFETCH RETURN ADD.15 00EA C1P	14	00B5 7	'I)	LREG	MOV	HIL		FETCH L-REG. IN A-REG.
13*00BB2CINRLJUTHERWISE, BUMPL.13*00BF C1C39500JMPL8185\$CHECK NEXT LDCATION.13*00C031FF 0BLX1SP,0BFFH\$New STACK PDINTER.13*00C3C5PUSH B\$PUSH RETURN ADD.13*00C4210010LX1H,1000H\$B15513*00C73600L8255MV1H,000H13*00C77EMDVA,M\$READ 8155.14*00C72600CP1U0H\$2-BIT SET IF A = 00H.14100C726F00JN2WRTER\$1F 2-BIT = 0, ERROR.14200C736FFMV1M,0FFH\$WRITE FFH TD 8155.14*00D2FEFFCP10FFH\$2-BIT SET IF A = 0FFH.14500D4C2F000JN2WRTER\$1F 2-BIT = 0, ERROR.14*00D77DMDVA,M\$READ 8155.15*00D8FEFFCP10FFH\$2-BIT SET IF A = 0FFH.14*00D87DMDVA,M\$READ 8155.15*00D4C2F000JN2WRTER\$1F 2-BIT = 0, CHECK DK.16*00D6C2F000JN2WRTER\$1F 2-BIT = 0, CHECK DK.16*00D77DMDVA,M\$READ 8155.16*00D8FEFFCPI0FFH\$2-BIT \$ET IF A = 0FFH.16*00D8FEFFCPI0FFH\$2-BIT \$ET IF A = 0FFH.16*00D77DMDV<	11	00B6 F	EFF		CP1	UFFH		32-B13 SET 1F L = FFH.
13500BF C1C8155 PDPBSAVE RETURN ADD. IN B;C.13500C0 31FF0BLX1SP:0BFFHNEW STACK PDINTER.13600C3 C5PUSH BNEW STACK PDINTER.13700C4 210010LX1H:1000HSB155 ADDR. IN H:L PAIR.13800C7 3600L8255 MV1M:00HWRITE 0 TD 8155.13700C9 7EMDVA:MREAD 8155.14100CC C2F000JN2WRTERIF 2-BIT SET IF A = 00H.14200CF 36FFMV1M:0FFHWRITE FFH TD 8155.14300D1 7EMDV A:MREAD 8155.14400D2 FEFFCPI 0FFHIF 2-BIT SET IF A = 0FFH.14500D4 C2F000JN2WRTERIF 2-BIT SET IF A = 0FFH.14500D4 C2F000JN2WRTERIF 2-BIT SET IF A = 0FFH.14500D4 C2F000JN2WRTERIF 2-BIT SET IF A = 0FFH.14600D6 C3C700JN2WRTERIF 2-BIT SET IF A = 0FFH.14700D1 2CINRLIF 2-BIT SET IF A = 0FFH.14800D6 C3C700JM2WRTERIF 2-BIT SET IF A = 0FFH.14900D1 2CINRLIF 2-BIT SET IF A = 0FFH.14900D2 CINRLIF 2-BIT SET IF A = 0FFH.15100E1 3EFFCHKDKMVI H: 0FFHIF 2-BIT SET IF A = 0FFH.15200E3 D300DU10UHSET 8755 PDRT A:15300E5 3E01MVI H: 0FFHIFETCH L-REG.15400E3 SE01MVI H: 01HIFREE. <td></td> <td>0088-0</td> <td>iabf uu</td> <td></td> <td>JZ</td> <td>C8155</td> <td></td> <td>\$1F 2-BIT SET, CHECK 8155.</td>		0088-0	iabf uu		JZ	C8155		\$1F 2-BIT SET, CHECK 8155.
13500BF C1C8155 PDPBSAVE RETURN ADD. IN B;C.13500C0 31FF0BLX1SP:0BFFHNEW STACK PDINTER.13600C3 C5PUSH BNEW STACK PDINTER.13700C4 210010LX1H:1000HSB155 ADDR. IN H:L PAIR.13800C7 3600L8255 MV1M:00HWRITE 0 TD 8155.13700C9 7EMDVA:MREAD 8155.14100CC C2F000JN2WRTERIF 2-BIT SET IF A = 00H.14200CF 36FFMV1M:0FFHWRITE FFH TD 8155.14300D1 7EMDV A:MREAD 8155.14400D2 FEFFCPI 0FFHIF 2-BIT SET IF A = 0FFH.14500D4 C2F000JN2WRTERIF 2-BIT SET IF A = 0FFH.14500D4 C2F000JN2WRTERIF 2-BIT SET IF A = 0FFH.14500D4 C2F000JN2WRTERIF 2-BIT SET IF A = 0FFH.14600D6 C3C700JN2WRTERIF 2-BIT SET IF A = 0FFH.14700D1 2CINRLIF 2-BIT SET IF A = 0FFH.14800D6 C3C700JM2WRTERIF 2-BIT SET IF A = 0FFH.14900D1 2CINRLIF 2-BIT SET IF A = 0FFH.14900D2 CINRLIF 2-BIT SET IF A = 0FFH.15100E1 3EFFCHKDKMVI H: 0FFHIF 2-BIT SET IF A = 0FFH.15200E3 D300DU10UHSET 8755 PDRT A:15300E5 3E01MVI H: 0FFHIFETCH L-REG.15400E3 SE01MVI H: 01HIFREE. <td>134</td> <td>UOBB 2</td> <td>2C</td> <td></td> <td>INR</td> <td>I.</td> <td></td> <td>SUTHERWISE, BUMP L.</td>	134	UOBB 2	2C		INR	I.		SUTHERWISE, BUMP L.
13500BF C1C8155 PDPBSAVE RETURN ADD. IN B;C.13500C0 31FF0BLX1SP:0BFFHNEW STACK PDINTER.13600C3 C5PUSH BNEW STACK PDINTER.13700C4 210010LX1H:1000HSB155 ADDR. IN H:L PAIR.13800C7 3600L8255 MV1M:00HWRITE 0 TD 8155.13700C9 7EMDVA:MREAD 8155.14100CC C2F000JN2WRTERIF 2-BIT SET IF A = 00H.14200CF 36FFMV1M:0FFHWRITE FFH TD 8155.14300D1 7EMDV A:MREAD 8155.14400D2 FEFFCPI 0FFHIF 2-BIT SET IF A = 0FFH.14500D4 C2F000JN2WRTERIF 2-BIT SET IF A = 0FFH.14500D4 C2F000JN2WRTERIF 2-BIT SET IF A = 0FFH.14500D4 C2F000JN2WRTERIF 2-BIT SET IF A = 0FFH.14600D6 C3C700JN2WRTERIF 2-BIT SET IF A = 0FFH.14700D1 2CINRLIF 2-BIT SET IF A = 0FFH.14800D6 C3C700JM2WRTERIF 2-BIT SET IF A = 0FFH.14900D1 2CINRLIF 2-BIT SET IF A = 0FFH.14900D2 CINRLIF 2-BIT SET IF A = 0FFH.15100E1 3EFFCHKDKMVI H: 0FFHIF 2-BIT SET IF A = 0FFH.15200E3 D300DU10UHSET 8755 PDRT A:15300E5 3E01MVI H: 0FFHIFETCH L-REG.15400E3 SE01MVI H: 01HIFREE. <td>13.</td> <td>OURC C</td> <td>39B00</td> <td></td> <td></td> <td></td> <td></td> <td></td>	13.	OURC C	39B00					
135 00C0 31FF0B LX1 SP,0BFFH INEW STACK PDINTER. 137 00C3 C5 PUSH B IPUSH RETURN ADD. 138 00C7 3600 L8255 MUY A, M SESS ADDR. IN H, L PAIR. 137 00C9 7E MUY A, M IREAD 8155. II 141 00C7 2F01 DH SETS ADDR. IN H, L PAIR. 142 00C7 2F01 DH SETS ADDR. IN H, L PAIR. 144 00C7 CF1 UH SETS ADDR. IN H, L PAIR. 144 00C7 SEFF MUY A, M IREAD 8155. IF A = 00H. 144 00C7 SEFF MUY A, M SERDR. IF A = 0FFH. 143 00D1 7E MUY A, M SEERDR. IF A = 0FFH. 144 00D2 FEFF CP1 OFFH SET STACK IF A = 0FFH. 145 00D4 C2F000 JNZ WRTER IF Z-BIT SET IF A = 0FFH. 145 00D4 CAFLOR JZ = BIT SET IF A = 0FFH. IF Z-BIT SET IF A = 0FFH. 148 00D7 CE JNP LS15S	134			08155				
13. 00C3 US PUSH B #PUSH RETURN ADD. 134 00C4 210010 LX1 H,1000H #B155 ADDR. IN H,L PAIR. 138 00C7 3600 L8155 MV1 M, 00H #B155 ADDR. IN H,L PAIR. 137 00C9 7E MDV A,M #READ 8155. 141 00CC C2F000 CP1 00H #Z-BIT SET IF A = 00H. 142 00CC C366F MV1 M, 0FFH #WITE +FH TD 8155. 143 00D1 7E MDV A,M #READ 8155. 143 00D1 7E MDV A,M #READ 8155. 144 00D2 FEFF CPI 0FFH #WITE +FH TD 8155. 143 00D4 C2F000 JN2 WRTER #F 2-BIT = 0, ERRDR. 145 00D4 C2F000 JN2 WRTER #F 2-BIT = 0, ERRDR. 145 00D4 C2F000 JN2 WRTER #F 2-BIT = 0, CHRDR. 146 00D4 C2F000 JN2 WRTER #F 2-BIT = 0, CHRDR. 147 00D5 FEFF CPI 0FFH #Z-BIT SET IF A = 0FFH. 148 00DA CAE100 J2 CHKUK #IF 2-BIT = 0, CHECK DK. 149 00DD 2C INR L #DTHERWISE, BUMP L-REG. 149 00DD 2C INR L #DTHERWISE, BUMP L-REG. 150 00EA C3C700 JMP L8155 #CHECK MEXT LDCATION. 151 00ES 3E01 MV1 H, UFFH #FETCH L/D DA								
$134'$ 0004 210010 $LX1$ $H_{2}1000H$ 38155 $ADDR.$ $IN H_{2}L PAIR.$ 138 0007 3600 $L8255$ MVI $M, 00H$ $WRITE$ 0008 $1008155.$ 137 0007 $7E$ MV A, M $3READ$ $8155.$ $AIRCR141000CC27000JN2WRTER31F2-B1TSETIFA = 00H.14200CF36FFMVIM_{1}WRTER31F2-B1TSETIFA = 00H.144000C226000JN2WRTER31F2-B1TSETIFA = 00H.14500D17EMVIM_{1}WRTER31F2-B1TSETIFA = 0FFH.14500D4C2F000JN2WRTER31F2-B1TSETIFA = 0FFH.14500D77DMDVA_{1}L3FETCH L-REG.INA = 0FFH.14500D77DMDVA_{1}L3FETCH L-REG.INA = 0FFH.14600D77DMDVA_{1}L3FETCH L-REG.INA = 0FFH.14500D77DMDVA_{1}L3FETCH L-REG.INA = 0FFH.14600D77DMDVA_{1}L3FETCH L-REG.INA = 0FFH.14600D7200DDTMPB$	13							
138 $00C7$ 3600 L8155MV1M, 00H\$WRITE 0 TD 8155.137 $00C9$ 7EMDVA, M\$READ 8155.141 $00CC$ $CP1$ $00H$ \$Z-BIT SET IF A = 00H.142 $00CF$ $36FF$ MV1M, UFFH\$WRITE FFH TD 8155.142 $00CF$ $36FF$ MV1M, UFFH\$WRITE FFH TD 8155.143 $00D1$ 7EMDVA, M\$READ 8155.144 $00D2$ FEFFCP1 $0FFH$ \$READ 8155.144 $00D2$ FEFFCP1 $0FFH$ \$READ 8155.145 $00D4$ C2F000JN2WRTER\$IF 2-BIT = 0, ERROR.145 $00D4$ C2F000JN2WRTER\$IF 2-BIT = 0, ERROR.145 $00D4$ C2F000JN2WRTER\$IF 2-BIT = 0, CHECK.146 $00D4$ C2F000JN2CHKUK\$IF 2-BIT = 0, CHECK.147 $00D5$ CCINR\$IF 2-BIT SET IF A = 0FFH.148 $000A$ CAE100J2CHKUK\$IF 2-BIT = 0, CHECK.149 $00D5$ 2CINR\$IF 2-BIT SET IF A = 0FFH.15000E4C1UPL8155\$CHECK.161 $00D6$ 2CINR\$IF 2-BIT SET IF A = 0FFH.151 $00E5$ SE00JNPL815516200E3SE00JNPL815517900D1CINR\$IF 2-BIT SET IF A = 0FFH.17900E3S1610LX1SP,10FFH\$FETCH I/D DATA. <td< td=""><td></td><td></td><td></td><td></td><td></td><td>—</td><td></td><td></td></td<>						—		
137 $00C9$ 7EMDVA, M\$READ 8155.141 $00C4$ FE00CP1UUH\$2-BIT SET IF A = 00H.141 $00CC$ C2F00UJM2WRTER\$1F 2-BIT = 0, ERROR.142 $00C7$ 36FFMV1 M, UFFH\$WITE FFH TD 8155.143 $00D1$ 7EMUVA, M\$READ 8155.144 $00D2$ FEFFCF10FFH\$2-BIT SET IF A = 0FFH.155 $00D4$ C2F000JM2WRTER\$1F 2-BIT = 0, ERROR.145 $00D4$ C2F000JM2WRTER\$1F 2-BIT = 0, CHECK.14600D7 7DMDV A, L\$FETCH L-REG. IN A-KEG.14700D8 FEFFCP10FFH\$2-BIT SET IF A = 0FFH.14800D4 CAE100J2CHKUK\$1F 2-BIT = 0, CHECK DK.14900D2 C2INR\$5 CHECK NEXT LOCATION.15100E1 3EFFCHKUK MVI H, UFFH\$ETCH L/R DATA.15200E3 J300DU10UH\$SET 8755 PDR1 A, BIT 7.15300E5 3EU1MVI H, UFFH\$FETCH RETURN MDD.17500E8 31FF10LX1SP,10FFH\$REST STACK PDINTER.15800EF C9RET\$RETURN.15900F0 3E2FFWRTER MVI A, 7FH\$REST 8755 PDR1 A, BIT 7.15000F2 J300DUTUUH\$RESET 8755 PDR1 A, BIT 7.15000F3 20600STA EIFLG\$FETCH L/D DATA.15000F3 20600STA EIFLG\$REST BRE RETURN ADD.15400F4 320600STA EIFLG\$FETCH RETURN ADD.15500F4 320600 <t< td=""><td>3</td><td></td><td></td><td>L8155</td><td></td><td></td><td></td><td></td></t<>	3			L8155				
1400CH FE00CP1UUH $32-B1T SET IF A = 00H.$ 14100CC C2F00UJN2WRTER $3IF 2-B1T = 0$, ERRDR.14200CF 36FFMV1M, UFFH $3WRTE FFH$ TD 8155.14300D1 7EMDVA, M $3KEAD 8155.$ 14400D2 FEFFCPI0FFH $3Z-B1T SET IF A = 0FFH.$ 14500D4 C2F000JN2WRTER $3IF 2-B1T = 0$, ERRDR.14500D7 7DMDVA, L $3FETCH L-REG. IN A-REG.$ 14600D8 FEFFCPI0FFH $3Z-B1T SET IF A = 0FFH.$ 14700D8 FEFFCPI0FFH $3Z-B1T SET IF A = 0FFH.$ 14800D4 C2F 000J2CHKUK $3IF 2-B1T = 0$, CHECK DK.14900D4 C2F 000J2CHKUK $3IF 2-B1T SET IF A = 0FFH.$ 14800D4 CAE100J2CHKUK $3IF 2-B1T SET IF A = 0FFH.$ 14800D4 CAE100J2CHKUK $3IF 2-B1T SET IF A = 0FFH.$ 14900D4 C2F 000JMPL8155 $3CHECK DK. DK.$ 14900D4 C2CINR $SET 8755 FDRT A.$ 15100E1 3EFFCHKDK MVI H, UFFH $3FETCH I/D DATA.$ 15200E3 D300DU10UH $3EET B755 FDRT A.$ 15300E4 C1PDPB $3FETCH RETURN ADD.$ 15400E7 320600STA EIFLG $3EST DRE RETURN ADD.$ 15500E8 C5PUSH B $3RESTDRE RETURN ADD.$ 15800EF C9RET $3RESTDRE RETURN ADD.$ 15900F0 3E7FWRTER MVI A, 7FH $3FET$								
141 OUCC C2F000 JN2 WRTER \$ 1F 2-BIT = 0; ERRDR. 142 OUCF 36FF MV1 M; UFFH \$ WRITE FFH TD 8155. 14 OUD1 7E MDV A; M \$ READ 8155. 144 OUD2 FEFF CP1 OFFH \$ Z-BIT SET IF A = OFFH. 145 OUD4 C2F000 JN2 WRTER \$ IF Z-BIT = 0; ERRDR. 145 OUD4 C2F000 JN2 WRTER \$ IF Z-BIT SET IF A = OFFH. 145 OUD4 C2F000 JN2 WRTER \$ IF Z-BIT = 0; ERRDR. 145 OUD4 C2F000 JN2 WRTER \$ IF Z-BIT = 0; ERRDR. 148 OUD4 C2F000 JN2 WRTER \$ IF Z-BIT = 0; ERRDR. 148 OUD4 C2F000 JN2 WRTER \$ IF Z-BIT = 0; ERRDR. 148 OUD4 C2F000 JN2 CHKUK \$ IF Z-BIT = 0; ERRDR. 148 OUD4 C2F000 JN2 CHKUK \$ IF Z-BIT = 0; ERRDR. 149 OUD2 C INR L \$ DTHERWISE, BUMP L-REG. 150 OUE C3C700 JMP L8155 \$ CHECK NEXT LDCATION. 151 OUE3 3800								
142 00CF 36FF MV1 M, UFFH \$WRITE FFH TD 8155. 143 00D1 7E MDV A, M \$READ 8155. 144 00D2 FEFF CF1 0FFH \$Z-BIT SET IF A = 0FFH. 145 00D4 C2F000 JN2 WRITER \$IF 2-BIT = 0, ERROR. 145 00D7 7D MDV A, L \$FETCH L-REG. IN A-REG. 148 00D7 ACE CPI UFFH \$Z-BIT SET IF A = 0FFH. 148 00D7 CD MDV A, L \$FETCH L-REG. IN A-REG. 149 00D0 CC INR L \$CHKUK \$IF 2-BIT = 0, CHECK DK. 149 00D0 2C INR L \$OTHERWISE, BUMP L-REG. 149 00D1 2C INR L \$OTHERWISE, BUMP L-REG. 149 00D2 CC INR L \$OTHERWISE, BUMP L-REG. 150 00E1 3EFF CHKUK MVI #.UFFH \$FETCH NEXT LDCATION. 152 00E3 3E01 MVI #.UFFH \$FETCH NEXT LDCATION. \$IF 300E CS 151 00E4 C1 POP B \$FETCH RETURN ADD. \$IF 300E CS								
1300D17EMDVA, M;READ\$READ\$8155.14400D2FEFFCPI0FFH;2-BITSETIFA = 0FFH.14500D4C2F000JNZWRTER; IF2-BITSETIFA = 0FFH.14500D4C2F000JNZWRTER; IF2-BITSETIFA = 0FFH.14500D77DMDVA, L; FETCHL-REG.INA-REG.14700D8FEFFCPI0FFH; Z-BITSETIFA = 0FFH.14800DACAE100J2CHKUK; IF2-BITSETO + ECG.14800DACAE100J2CHKUK; IF2-BITSETO + ECG.14900DB2CINRL; DTHERWISE, BUMP L-REG.I14900DEC3C700JMPL8155; CHECKNEXTLDCATION.15100E13EFFCHKDKMVIH, UFFH; FETCHIJD ATA.15200E3J300DU10UH; SETSETFETCHA, BIT15400E53EU1MVIH, UFFH; FETCHIFIF155U0EA155U0EASTAEIFLG; SETSTACKPDINTER.15500EAC1PDPB; FETCHKESTSTACKPDINTER.15900F03E7FWRTERMVIA, 7FH; FETCHIZD ATA.SETSTAC								
14400D2FEFFCPI0FFH $;Z-BIT$ SETIFA = 0FFH.14500D4C2F000JM2WRTER $;IF$ $;IF$ $;IF$ $;IF$ $;IF$ 14500D77DMDV A_{FL} $;IF$ $;IF$ $;IF$ $;IF$ $;IF$ 14800D7CAE100J2CHKUK $;IF$								
14500D4C2F000JN2WRTER $iFZ-BIT = 0$, ERRDR.1.500D77DMDVA+LiFETCH L-REG.IN A-REG.1.200D8FEFFCPI0FFHiZ-BITSETIF A = 0FFH.14800DACAE100J2CHKUKiIF Z-BIT= 0, UHECK DK.14900DD2CINRLiDTHERWISE, BUMP L-REG.1.400DEC3C700JMPL8155iCHECK NEXT LDCATION.15100E13EFFCHKOKMVIH, UFFH15200E3D300DUT0UHiSET15200E3320600STAEIFLGiSET15300E53EU1MVIH, UIFFHiFETCH RETURN ADD.15400E7320600STAEIFLGiSET15500EAC1PDPBiFETCH RETURN ADD.17500E33IFF10LX1SP,1UFFHiRESET1700E2D300DUTUHiRESET1700E4C5FUSHBiRESTDRE1700E5D0F0SE7FWRTERMVI1800F2D300DUTUHiRESET1900F03E7FWRTERMVIA, 7FH1900F2D300DUTUHiRESET1900F2D300DUTUHiRESET1000F2D300DUTUHiRESET1000F4320600STHEIFLG								
1.5 $00D7$ 7DMDV A_{FL} ; FETCH L-REG. IN A-REG.1.2 $00D8$ FEFFCPI $0FFH$; 2 -BIT SETIF A = 0FFH.148 $00DA$ CAE100J2CHKUK ; 1 F 2-BIT = 0, CHECK DK.149 $00DD$ 2CINR L ; DTHERWISE, BUMP L-REG.1.0 $00DE$ C3C700JMPL8155 ; CHECK NEXT LDCATION.151 $00E1$ SEFFCHKDK MVI H, UFFH ; FETCH 1/D DATA.152 $00E3$ J300DUT0UH ; SET 8755 PORT A, BIT 7.153 $00E5$ SEU1MVI H, U1H ; H-RE6. = 1.154 $00E7$ 320600STA EIFLG ; SET EIFLG.155 $00EA$ C1PDP B ; FETCH RETURN ADD.175 $00E3$ 31FF10LX1 SP; 1UFFH ; RESTDRE RETURN ADD.176 $00E7$ B300DUT U0H ; RESTDRE RETURN ADD.159 $00F0$ SE7FWRTER MVI A; 7FH ; FETCH 1/D DATA.10 $00F2$ D300DUT U0H ; RESET 8755 PORT A; BIT 7.10 $00F2$ D300MVI H; 00H ; RESET 8755 PORT A; BIT 7.1000F2 D300MVI A; 7FH ; FETCH 1/D DATA.1000F2 D300MVI A; 00H ; A-REG. = 0.16200F6 320600STA EIFLG ; CLEAR EIFLG.1300F9 C1PDP B ; FETCH RETURN ADD.1400FA 31FF10LX1 SP; 10FFH ; RESET STACK PDINTER.								
1.700D8FEFFCPI0FFH $32-BIT$ SE1IFA = 0FFH.14800DACAE100J2CHKUK $31F$ $2-BIT$ = 0, CHECKDK.14900DD2CINRL $3DTHERWISE, BUMP L-REG.$ 14900DEC3C700JMPL8155 $3CHECK$ NEXT15100E13EFFCHKUKMVIH, UFFH $3FETCH$ $1/2$ 15200E3D300DU100H $3SET$ 8755PORT15200E33E01MVIH, UFFH $3FETCH$ $1/2$ DATA.15200E33E01MVIH, UIH $3FETCH$ $1/2$ DATA.15300E53E01MVIH, UIFFH $3FETCH$ RETURN ADD.17500E831FF10LX1SP, 10FFH $3FETCH$ RETURN ADD.15800EFC9RET $3RESTDRE$ RETURN ADD.15900F03E7FWRTERMVIA, 27FH $3FETCH$ $1/2$ 15900F33200DUT00H $3RESET$ 3755 $7DR1$ A, BIT $7.$ 15400F43200MVIH, 00H $3FETCH$ $1/2$ $DATA.$ 15500F6320600STAE1FLG $3CETR$ $1/2$ $1/2$ 16200F6320600STAE1FLG $3FETCH$ $1/2$ $1/2$ 16200F6320600STAE1FLG $3FETCH$ $1/2$ $1/2$ 16300								
14800DACAE100J2CHKUK $\$IF 2-BIT = 0$; CHECK DK.14900DD2CINRINR $\$ITHERWISE$; BUMP L-REG.14000DEC3C700JMPL8155 $\$CHECK NEXT LDCATION$.15100E13EFFCHKOKMVIH; UFFH $\$FETCH 1/D$ 15200E3D300DU10UH $\$SET 8755$ POR1 A; BIT 7.15300E53EU1MVIH; 01H $\$H-REG. = 1$.15400E7320600STAEIFLG $\$SET EIFLG.$ 15500EAC1PDPB $\$FETCH RETURN ADD.$ 17500EB3IFF10LX1SP; 10FFH $\$RESET \$TACK POINTER.$ 15800EFC9RET $$RETURN.$ 15900F03E7FWRTERMVIA; 7FH16000F43200MVIA; 00H $$RESET \755 POR1 A; BIT 7.16200F6320600STAEIFLG $$GETCH 1/D DATA.$ 16200F6320600STAEIFLG17300F9 C1PDPB $$FETCH 1/D DATA.$ 17400FA31FF10LXISP; 10FFH1753000DUT00H $$RESET \755 POR1 A; BIT 7.176300F9 C1PDPB $$FETCH RETURN ADD.$ 177400FA31FF10LXI17830F9 C1PDPB $$FETCH RETURN ADD.$ 17930F9 C1PDPB $$FETCH RETURN ADD.$ 17430F9 C1PDPB <td>· ·</td> <td></td> <td></td> <td></td> <td></td> <td></td> <td></td> <td></td>	· ·							
149 00DD 2C INR L IDTHERWISE, BUMP L-REG. 10 00DE C3C700 JMP L8155 ICHECK NEXT LDCATION. 151 00E1 3EFF CHKOK MVI H, 0FFH IFETCH 1/D DATA. 152 00E3 D300 DU1 0UH ISET 8755 PORT A, BIT 7. 13 00E5 3E01 MVI H, 01H IM-REG. = 1. 14 00E7 320600 STA EIFLG ISET EIFLG. 155 00EA C1 POP B IFETCH RETURN ADD. 17 00EE C5 PUSH B IRESTORE RETURN ADD. 158 00EF C9 RET IRETURN. 159 00F0 3E7F WRTER MVI A, 7FH IFETCH 1/D DATA. 10 00F2 D300 DUT 00H IRESET 8755 PORT A, BIT 7. 11 0 00F4 3E00 MVI A, 00H IRESET 8755 PORT A, BIT 7. 162 00F6 320600 STA EIFLG ICLEAR EIFLG. 13 00F9 C1 PDP B IFETCH I/D DATA. 14 00FA 31FF10 LXI SP, 10FFH IRESET 8755 PORT A, BIT 7.								
1.000DEC3C700JMPL8155\$CHECKNEXTLDCATION.15100E13EFFCHKOKMVIH, UFFH\$FETCH1/0DATA.15200E3D300DU100H\$SET8755POR1A, BIT7.11300E7320600STAEIFLG\$SETEIFLG.1.1.11300E7320600STAEIFLG\$SETEIFLG.1.15500EAC1PDPB\$FETCHRETURN ADD.17500EB31FF10LX1SP;10FFH\$RESET\$TACKPOINTER.17500EEC5PUSHB\$RESTORERETURN.15800EFC9RET\$RETURN.\$RETURN.15900F03E7FWRTERMVIA, 7FH\$FETCH1/D16200F4320600DUTUUH\$RESET\$755POR1A, BIT7.16200F6320600STAEIFLG\$CLEAREIFLG.\$CLEAREIFLG.1<3								
151 $00E1$ $3EFF$ $CHKDK$ MVI $H, 0FFH$ $FETCH$ I/D $DATA$.152 $00E3$ $D300$ $D01$ $00H$ $SE1$ 8755 $PDR1$ A, BIT $7.$ 113 $00E5$ $3E01$ MVI $H, 01H$ $H-REG. = 1.$ $1.$ 114 $00E7$ 320600 STA $EIFLG$ $SE1$ $EIFLG.$ 115 $00E8$ $C1$ PDP B $SFETCH$ $RETWRN$ $ADD.$ 175 $00E8$ $31FF10$ $LX1$ $SP, 10FFH$ $SRESTDRE$ $RETWRN$ $ADD.$ 176 $00E8$ $C5$ $PUSH$ B $SRESTDRE$ $RETURN.$ 158 $00EF$ $C9$ RET $SRETURN.$ $SFETCH$ I/D 159 $00F0$ $3E7F$ $WRTER$ $MV1$ $A, 7FH$ $SFETCH$ I/D 159 $00F2$ $B300$ DUT $U0H$ $SRESET$ 8755 $PDR1$ A, BIT $7.$ 159 $00F4$ 320600 $MV1$ $A, 00H$ $SRESET$ 8755 $PDR1$ A, BIT $7.$ 159 $00F4$ 320600 $MV1$ $A, 00H$ $SRESET$ 8755 $PDR1$ A, BIT $7.$ 162 $00F6$ 320600 STA $EIFLG$ $SFEICH$ $EIFLG.$ $SFEICH$ $RETWRN$ $ADD.$ 162 $00F6$ 320600 STA $EIFLG$ $SFEICH$ $SFEICH$ $RETWRN$ $ADD.$ 162 $00F6$ 320600 STA $EIFLG$ $SFEICH$ <td< td=""><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td></td<>								
152 00E3 D300 DU1 00H \$SET 8755 PDR1 A, BIT 7. 14 00E5 3E01 MV1 H+01H \$H-RE6. = 1. 14 00E7 320600 STA E1FLG \$SET E1FLG. 155 00EA C1 PDP B \$FETCH RETURN ADD. 175 00EB 31FF10 LX1 SP,10FFH \$RESET STACK PDINTER. 17 00EE C5 FUSH B \$RESTDRE RETURN ADD. 158 00EF C9 RET \$RETURN. \$RESTDRE RETURN. 159 00F0 3E7F WRTER MV1 A,7FH \$FETCH 1/D DATA. 159 00F0 3E7F WRTER MV1 A,7FH \$FETCH 1/D DATA. 159 00F0 3E7F WRTER MV1 A,7FH \$FETCH 1/D DATA. 160 00F4 3E00 MV1 A,00H \$A-REG. 0. 0. <tr< td=""><td>- <u>-</u></td><td></td><td></td><td>CHROK</td><td></td><td></td><td></td><td></td></tr<>	- <u>-</u>			CHROK				
1300E53E01MV1 H_{2} 01H H_{2} HREG. = 1.11400E7320600STAE1FLG H_{2} SETE1FLG.1500EAC1PDPB H_{2} FETCH RETURN ADD.1700EB31FF10LX1SP210FFH H_{2} SETSTACK1700EEC5PUSHB H_{2} SET DRERETURN ADD.15800EFC9RET H_{2} SET DRERETURN.15900F03E7FWRTERMV1A, 7FH H_{2} SET CH $I \ge D$ DATA.1000F2D300DUTU0H H_{2} SEST H_{2} A, BIT H_{2} 1000F43E00MV1A, 00H H_{2} REG. = 0. H_{2} 16200F6320600STAE1FLG H_{2} CLEARE1FLG.1300F9C1PDPB H_{2} SFETCH RETURN ADD.1400FA31FF10LX1SP, 10FFH H_{2} SESTSTACK				and the second				
1 $\$$ 0067 320600STAEIFLG\$SET EIFLG.155 00EA C1PDP B\$FETCH RETURN ADD.175 00EB 31FF10LX1SP,10FFH\$RESET STACK PDINTER.17 00EE C5PUSH B\$RESTDRE RETURN ADD.158 00EF C9RET\$RETURN.159 00F0 3E7FWRTER MV1A,7FH\$FETCH 1/D DATA.10 00F2 D300DUTU0H\$RESET 8755 PDR1 A, BIT 7.10 00F4 3E00MV1A,00H\$A-REG. = 0.162 00F6 320600STAE1FLG\$CLEAR E1FLG.13 00F9 C1PDP B\$FETCH RETURN ADD.14 00FA 31FF10LX1SP,10FFH\$RESET STACK PDINTER.								
155UDEAC1PDPB#FETCH RETURN ADD.17500EB31FF10LX1SP;10FFH#RESET STACK PDINTER.1700EEC5PUSH B#RESTORE RETURN ADD.15800EFC9RET#RETURN.15900F03E7FWRTER <mv1< td="">A;7FH#FETCH 1/D15900F2D300DUTU0H#RESET 87551000F2D300DUTU0H#RESET 87551000F43E00MV1A;0UH#A-REG. = 0.16200F6320600STAE1FLG#CLEAR1300F9C1PDPB#FETCH RETURN ADD.1400FA31FF10LX1SP;10FFH#RESET</mv1<>								
175 00EB 31FF10LX1 SP,10FFH#RESET STACK PDINTER.1700EE C5PUSH B#RESTORE RETURN ADD.158 00EF C9RET#RETURN.159 00F0 3E7FWRTER MV1 A,7FH#FETCH 1/D DATA.10 00F2 D300DUT 00H#RESET 8755 PDR1 A, BIT 7.10 00F4 3E00MV1 A,00H#A-REG. = 0.162 00F6 320600STA EIFLG#CLEAR EIFLG.1 3 00F9 C1PDP B#FETCH RETURN ADD.1 4 00FA 31FF10LX1 SP,10FFH#RESET STACK PDINTER.								
1700EEC5PUSH B\$RESTORE RETURN ADD.15800EFC9RET\$RETURN.15900F03E7FWRTERMV1A,7FH\$FETCH 1/01000F20300DUT00H\$RESET87551000F43E00MV1A,00H\$A-REG. = 0.16200F6320600STAE1FLG\$CLEARE1FLG.1300F9C1PDPB\$FETCHRETURN ADD.1400FA31FF10LX1SP,10FFH\$RESETSTACKPDINTER.								
158 00EF C9 RET ;RETURN. 159 00F0 3E7F WRTER MV1 A;7FH ;FETCH 1/D DATA. 1 0 00F2 D300 DUT 00H ;RESET 8755 PDR1 A; BIT 7. 1-1 00F4 3E00 MV1 A;0UH ;A-REG. = 0. 162 00F6 320600 STA EIFLG ;CLEAR EIFLG. 1 3 00F9 C1 PDP B ;FETCH RETURN ADD. 1 4 00FA 31FF10 LXI SP;10FFH ;RESET STACK PDINTER.								
159 00F0 3E7F WRTER MV1 A, 7FH \$FETCH 1/0 DATA. 1 0 00F2 D300 DUT 00H \$RESET 8755 PDR1 A, BIT 7. 1 0 00F4 3E00 MV1 A, 00H \$RESET 8755 PDR1 A, BIT 7. 1 0 00F4 3E00 MV1 A, 00H \$RESET 8755 PDR1 A, BIT 7. 162 00F6 320600 STA E1FLG \$CLEAR E1FLG. 1 3 00F9 C1 PDP B \$FE1CH RETURN ADD. 1 4 00FA 31FF10 LXI SP, 10FFH \$RESET STACK PDINTER.						~ '		
1 0 00F2 0300 DUT 00H \$RESET 8755 PDR1 A, BIT 7. 1 0 00F4 3600 MVI A, 00H \$A-REG. = 0. 162 00F6 320600 STA E1FLG \$CLEAR E1FLG. 1 3 00F9 C1 PDP B \$FEICH RETURN ADD. 1 4 00FA 31FF10 LXI SP, 10FFH \$RESET STACK PDINTER.				HETED		a.754		
1_100F43E00MVIA,00H\$A-REG. = 0.16200F6320600STAEIFLG\$CLEAREIFLG.1300F9C1PDPB\$FETCHRETURNADD.1400FA31FF10LXISP,10FFH\$RESETSTACKPDINTER.				WR. 1 E.K.				
162 00F6 320600STA EIFLG\$CLEAR EIFLG.1 3 00F9 C1PDP B\$FETCH RETURN ADD.1 4 00FA 31FF10LXI SP,10FFH\$RESET STACK PDINTER.	. 1							
1 3 00F9 C1PDP B#FEICH RETURN ADD.1 4 00FA 31FF10LXI SP,10FFH#RESET STACK PDINTER.								
1 4 OUFA 31FF10 LXI SP,10FFH SRESET STACK PDINTER.								
TO UND CO FUER BUILD POER BUILD POER BUILD								
	16	0070 0			rusm	5		FRESTURE RETURN MUD.

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DM SUSURED FELDE-MALAU ASSEMBLER VER 1.0

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	166	UOFE	C9		RET			;RETURN.
	16-7	UUFF		;				****
	1	OUFF						ERRUPT PROCESSING ++++++
	1	UUFF		;*** **	****	******	******	*****
		UUFF	BED9	SF24	MV1	H, ODSH		FETCH INTERRUPT MASK.
		0101	00		NDP		i.	ISET SOD. ENABLE RST 7.5.
		0102		ş.			-	6.5. DISABLE RST 5.5.
	173	0102	SHOHOO	LPPS1	LDH	PPS1		FETCH PPS #1 VOLTAGE.
	174	01.05	EEBF		HH1	OBFH		\$LDAD BY CLEARING BIT 6.
	1 5	0107	F680		OF:1	80H		ISET BIT 7.
		0109	D300		TUD	UUH		BUTPUT TO 8755, PORT A.
	177	01 ÚB	F6CŬ		UR1	OCOH		1SET BIT 6.
		0100			DUT	00H		BUIPUT TO 8755, PORT A.
i.	1 9	01 0F	SAUBOU	LFPS2	LDA	PPS2		FETCH PPS #2 VOLTAGE.
		0112			HH1	UBFH		FLOAD BY CLEARING BIT 6.
		0114			DR I	80H		\$SET 817 7.
		0116			DUT	01H		JOULPUT TO 8755, PORT B.
		0118			DF(1	UC:UH		\$SE7 B17 6.
		U11A			DUT	01H		DUTPUT TO 8755, PORT B.
		0110		SWICH		H•10		FETCH BUFFER 1/2 FLAG.
			FEOU		CPI	00H		\$2-B11 SET IF A-REG. = 0.
			CA3401		JZ	BUFR2		FIF 2-B1T = 1, BUFFER #2.
			210200	BUFR1		H+ BUF 1		FBUFFER #1 ADD. IN H.L.
			220800			NEWBF		SAUD. IN NEWBE, NEWBE + 1.
			210300		LX1	H, BUF2		BUFFER #2 ADD. IN H.L.
			220900			DLDBF		SADD. IN DLUBF, DLUBF + 1.
		012E			MV1	HUOH		ICLEAR H-REG.
		0130			MUY	Da H China Marta		BD-REG. = BUFFER 1 ADD.
	194		C34301 210300	BUFR2		ULRNB H, BUF2		<pre>\$GD CLEAR NEW BUFFER. \$BUFFER #1 ADD. 1N H*L.</pre>
			220800	EUP P.C		NEWBE		FADD. IN NEWBF, NEWBF + 1.
			210200		LX1			BUFFER #2 AND. IN H.L.
			220900			DLDBF		JADD. IN OLDBF, OLDBF + 1.
	· Ŧ	0130 0140			MVI			A-REG. = 40H.
•		0142			MOV			$\mathbf{i}\mathbf{D}-\mathbf{REG}$. \Rightarrow $\mathbf{EUFFER} \ge \mathbf{ADD}_{\mathbf{N}}$
			2AU800	CLENE				INEW DATA ADD. IN H.L.
	1							CLEAR A-REG.
	23	0148	3E00 47 70	CNBLP	MOV	B.H		CLEAR B-REG.
	204	0149	70	CINELP	MDY	M.B		CLEAR B-REG. CLEAR DHTA LDCATION. 2-BIT SET IF A = 31D. FIF 2 = 1, INHIBIT S. D.
			FE1F		UPI	310		32-B11 SET IF A = 31D.
			CA5401		JZ	INHSD		\ddagger IF Z = 1, INHIBIT S. D.
		014F			INX	H		
					INR	Ĥ		BUMP H-REG.
	2 3	0151	3C C34901		JMF	CHBLP		CLEAR NEXT LOCATION.
	2.0	0154	381300	INHSD	LDH	SDINH	1	<pre>\$BUMP A-REG. \$CLEAR NEXT LOCATION. \$FETCH DATA INHIBIT FLAG. \$2-BIT SET IF A-REG. = 1.</pre>
	8 Y 1					01H		#Z-BIT SET IF M-REG. = 1.
	£' 2	0159	CAD901	BPPS1	JZ	ENHEI		91r 2 = 19 EMHBLE DHIN.
	2 3	0150	300000	EPPS1	LDH	FICHT		#FETCH PPS #1 STEP COUNT.
	214	015F	30		INE	Ĥ		\$INCREMENT.
			320000		STA	PICNI		SAVE PPS #1 STEP COUNT.
	2 6	0163	FE28		CPI	401		<pre>\$FETCH PPS #1 STEP COUNT. \$INCREMENT. \$SAVE PPS #1 STEP COUNT. \$Z-BIT SET 1F MAX. COUNT. \$IF 2-BIT = 0, FORM ADD.</pre>
			028201		JHZ	F1HDD	÷ 1	SIF $Z-BIT = 0$, FORM ADD.
;		0168			MVI	He HIH		TRIHERMISEN A-REG. = A.
			320000		STH	F1CH1		RESET PPS #1 STEP COUNT. BUMP A-REG.
	2. 0	016D	30		INR	H		; BUMP A-REG.
						ORIGINAL	PAGE IS	
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221	016Ë	321300		SIH	SDINH		ISET DATA INHIBIT FLAG.
218	0171	SHODUO	EPPS2		PECNT		IFETCH PPS #2 STEP COUNT.
2 3	0174	SAODUO SC		INR	Ĥ		FINUREMENT.
2	0175	SZODOO		SIH	PECNI		SAVE PPS #2 STEP COUNT.
	0178			CF1	160	1	\$2-BIT SET IF MAX. COUNT.
		C28201					
				JHZ	P1ADD		$\$1F \ Z-B11 = 0, \ FDRM \ ADD.$
2.7		SEQU		MYI	H, UUH		IDTHERWISE, A-REG. = 0.
		320D00		STA	Pecni		FRESET PPS #2 STEP COUNT.
		3A1200	PIADD		FIRST		FETCH FIRST TIME FLAG.
2 0	0185	FEQU		CF1	00H		32-B1T SET IF A-REG. = 0.
231	0187	CA9301		JŻ	P17AB		IF Z-BIT = 1, FORM ADD.
232	018A	3E00		MV1	H. UUH		IDTHERWISE, CLEAR A-REG.
		323200		STA	FIRST		FRESET FIRST TIME FLAG.
	U18F			1NR	Ĥ		IBUNP A-REG.
		321300		STA	SILINH		SET DATA INHIBIT FLAG.
	11122	210400	PITAB		HIPPS1T		PPS 1 TABLE HDD. IN H.L.
	0170 0102	210400 3AUC00					
4	0170	3800.00		LUA	PICNT		FETCH PPS #1 STEP COUNT.
	0199			AH1	SFH		IMASK PICNT.
	019B			MDV	B+H		ISAVE IN B-REG.
		SAUDUO		LIH	PECNT		FETCH PPS #2 STEP COUNT.
241	019F	E603		HN1	03H		FMHSK BITS U HND 1.
242	01H1	0F		RRC			FOTATE RIGHT 2 BITS.
	0182	0F		RRC			
4	0163			UH:A	F		FDR WITH PPS #1 COUNT.
	0184			MOV	Ē, H		SAVE IN L-REG.
		SHODOO		LDH	PECHT		FETCH PPS #2 STEP COUNT.
	0148				UCH		
				HN1	UCH .		IMASK BITS 2 HND 3.
	Ú1AA			RRC			FROTATE RIGHT 2 BITS.
249 d	UIAB			RRC			
đ	01AC			UR:H	H		IOR WITH PPS #1 BASE ADD.
	01AD	67		MOV	HH		SAVE IN H-REG.
252	01AE	GEFF		MV1 -	H, DEFH		FETCH 8755 1/D MDDE.
873	01B0	1302		DUT	UZH		REINITIHLIZE PORT A.
3 4	0182	1383		DUT	03H		FREINITIALIZE PORT B.
	U184	7E		MOV	H+ M		FETCH PPS #1 VOLTAGE.
		320800		STH	PPS1		SAVE VOLTAGE AT PPS1.
2.17	0188			DR1	OCUH		SET BITS 6 AND 7.
	UIBA						1001PU1 10 8755, PDRT A.
		<u>1500</u>	A LOCATION		00H		
207	UIDU:	210500	LC I UD				PPS 2 TABLE ADD. IN H.L.
		3AODU0		LIH			FETCH PPS #2 STEP COUNT.
	0102	EGUE		HN1	UFH		MASK PECNT.
		B2		ORH	L		JOR WITH PPS #2 BASE ADD. JSAVE IN L-REG. JEFTCH 8255 LZD MODE.
843	0105	6F	4	MUV	Lift		SAVE IN L-REG.
2 54	0106	3EFF 1302 1303		MV1	HEUFFH		
285	0108	0302		001	UZH		FREINITIALIZE PORT A.
266	U1CH	0303		DU1			FREINITIALIZE PORT B.
	A 4 4 4 4 4 4 4 4 4			MOV	H.M		ISETCH PPS #2 VOLTALE
	0100						SCAUE UNITALE AT DECO
02.00 102.00	011010	76 320800 F6C0		10 F 17 10 E 1			FETCH PPS #2 VOLTAGE. SAVE VOLTAGE AT PPS2. SET BITS 6 AND 7. DUTPUT TO 8755, PORT B.
		FOLU		CO 13	0000 010		7067 DI10 D MAD 7. 10017003 10 0055 0007 0
		D 301	7.8.4 . 8 .		U I M		HOTPOT TH 8755, PURT B.
	01104	SEUU	INIEN	NV I	H, UUH		JULEHK H-RED.
	$01D_{\odot}$	47		MEW	B H		FRESET MAIN FRAME COUNT.
	01107			E1			CLEAR A-REG. FRESET MAIN FRAME COUNT. FENHBLE INTERRUPT SYSTEM.
	01D8			KET			
		3E00	ENHBI	MV1	H, 00H		CLEAR A-REG.
				*** ₩	····		an a

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FR6E 0006

. F					
276 U1DE		5	57A 3	IIINH	FRESET DATA INHIBIT FLAG.
277 UIIE	C3D401		JMF	INTEN	GU ENABLE INTERRUPT SYSTEM.
2 3 U1E1		*****		*****	*****
2 01E1	· · · · · · · · · · · · · · · · · · ·		- MH1	NECHME DATE IN	TERRUPT PROCESSING +++++
200 01E1					
2(, U1E1	- 11 L	1-23 1	4846) 4846)	*****	PARTY AND A RATE OF COMMAND AND A REAL AND A REAL
	NU 1 5440		YÜP		FREAD INTERRUPT MASK.
262 01E2			1111	40H	MASK 17.5 BIT.
283 01E4			CF 1		12-B11 SET IF A = 40H.
261 01E6	CAFI01	•	JZ	SFEXT	FIF Z-BIT SET RETURN.
2)) (1E9	280900 E	IUUT L	HLU		FOLD BUFFER HDD. IN H.L.
286 01EC					FETCH DHTA.
287 U1ED	1312		101	12H	JOUIPUT TO 8155, PORT B.
2 3 01EF	23		INX -	H	JEUMP H.L PAIR.
209 01F0	220900	5	HLD		SAVE NEXT ADD. IN DLDBF.
290 01F3	(14		INR	R	SINCREMENT MAIN FRAME COUNT.
2) 1 01F4			101	Ĥ, B	FETCH MF2(3) COUNT.
2 2 01F5			281	917 AP	FC=B1T = 1 IF A < USH.
293 01F7			JNC		i = 0, ENABLE DATA.
			J}70- 		
294 01FH	3E40 P	FEXT N			FETCH CLEAR SOD COMMAND.
2 5 HIFC 256 JIFD	00		HOP		ICLEAR SOD.
256 01FU	3EC9 3	HEXT N			FETCH SET SDD COMMAND.
297 U1FF	00		IDF'		SET SOD. LEAVE MASK WITH
2[3 0200 2[3 0200					KST 5.5 INHIBITED.
2 9 0200	FB	E	1		FENHBLE INTERRUPT SYSTEM.
300 0201	09	F	(ET		JRETURN.
301 0202	3A1300 E	NDAT L	DH	SDINH	#FETCH DATA INHIBIT FLAG.
3 2 0205					\$2-B17 SET IF A-REG. = 1.
	CAFA01				FIF Z-BIT SET, RETURN.
304 020A					FETCH CLEAR SOD COMMAND.
3 0200			金尸		CLEAR SOD.
30200	SEC8				FETCH SET SOD COMMAND.
307 020F			1UP		SET SDD. ENHBLE RST 5.5,
307 020F 368 0210 399 0210	•				RST 6.5, RST 7.5.
3 9 0210	DB11	1	111		CLEAR RST 5.3 PENDING.
310 0212			-1		FENABLE INTERRUPT SYSTEM.
311 0213	C9	h h	ζE1		;RETURN.
3 2 0214		*****	****	**********	*********
3.3 0214	;	*****	H SC	IENCE DATA INT	ERRUPT PROCESSING ++++++
314 0214	an an Eili	*****	}	*********	*****************
3 5 0214	DB11 S	CHCE 1	ÎN 1	1H	INPUT FROM 8155, PORT A.
6 0216			:MA		COMPLIMENT.
317 0217					DATA BASE HDD. IN H-REG.
318 0218					MASK B115 0 - 4.
3 9 0219					DR WITH BUFFER BASE ADD.
320 021A					MAR WITH BOFFER BASE ADD.
320 021H					
					FINCREMENT THAT LOCATION.
5 2 0210			1		SENABLE INTERRUPT SYSTEM.
3 3 021D			ΈT		FRETURN.
324 U21E	0000	E	END	FIMSB	

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P: 1 L 015C BPPS2 L 0171 BUF1 % 0602 BUF2 % 0 # 1 L 0122 BUFR2 L 0134 CB155 L 00BF CHKHW L 0 # L 00E1 CLRMB L 0143 CMBLP L 0149 DBASE % 0 1A & X 0011 EIFLG X 0006 ENABD L 01D9 ENDAT L 0 1M B L 0000 FIRST X 0012 IDLE L 004F INHSD L 0 1M B L 0000 FIRST X 0012 IDLE L 004F INHSD L 0 1M B L 0000 FIRST X 0012 IDLE L 004F INHSD L 0 1MW L 0053 IMSW L 0064 INTEN L 01D4 L8155 L 0 3185 L 009B LDRAM L 006C, LPPS1 L 0102 LPPS2 L 0 RE - L 00B5 MF23 L 01E1 MFCN1 X 0010 MFEX1 L 0 DF A X 0000 NEWBF X 0008 DDATA X 0001 DDUT L 0 LDBF X 0009 P1ADD L 0182 P1CNT X 0000 P1MAX X 0 17 4B L 0193 P2CNT X 0000 P2MAX X 000F P2TAB L 0 P1 X 000A PPS1T X 0004 PPS2 X 000B PPS2T X 0 SEDM X 0014 PWRDN L 003F SCNCE L 0214 SDINH X 0				Aliment and a second seco
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FINS-B PPS LOOKUP TARLE DATA

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- <u>54</u> H	_ UD::	-124 H		_ 20011	<u>ــــــــــــــــــــــــــــــــــــ</u>
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Øbii	_ 61H _	- <u>1461</u>		85411	2411
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¢====	_ С1н	118H		821 N	6911
- \$*? H		4494			ØG H
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ØBII	CII			1	69,1
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10H	C14				69,1
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131		4534		<u>801 H</u>	<u> </u>
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1611			JEN.	YØJ N	Ø9.11
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<u>- ЗАц</u>	<u>.</u>	<u> </u>	1A.H	1 802 H	<u>ÉC:</u>
THC II		<u>18CH</u>	1B+	8:324	ØC11
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	2211		1FH	800%	JC4
111	0211	-191N	ЭØн	5024	¢C+1
42 11	004	4924	<u>31 H</u>	32211	
1311	Ø2 H	4936	2211	80211	
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15n	0311	-195H	24 ₁₁	5.52.	<u> </u>
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181	02:1	-198n	27H	<u> <u>8</u> 7 2 1 1</u>	ØCI
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26H	- 6211	41611	3511		CC+1
27H	ಯಗ	HX74	364	ۯ2H	Or n
	03:1	4:04	Ør H	5Ø3.4	ØFn
Øla .	C311	<u></u>	¢ i k		BEH.
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	<u>C311</u>	4634	10 H	<u>8'63 н</u>	ØFri
. CH	<u></u>	HCHH	114	80311	CFri
Ő5n	.¢3	HC51	12.4	803.	ØF.I
264	03.1	Чсен	13.11	<u>\$\$03</u> "	ØF11
57 m	<u>¢'3:</u>	<u> ЧС7н</u>		\$ø3x	QF.n
_C.8.1	d:	40811	15.4	8,73 H	ØFN
1911	Ø3.	-1694	161-	<u>\$</u> \$\$;	ØF.H
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1733.L_	<u> (</u> (5:1	HCEN	18.11	\$Ø3.K	ØFN
	<u> </u>	4664	194	<u>803</u> ,	ØF.
FD.	ظ3،،	4C D11	144	<u><u></u><u></u><u></u><u></u><u></u><u></u><u></u><u></u><u></u><u></u><u></u><u></u><u></u><u></u><u></u><u></u><u></u><u></u><u></u></u>	ØFH
EEN_	031	ACEH	1BH	°ø34	ØF
CFH_	23.1	HCFH	1Сн	° Ø3.	ØF
	331	HDOH	10H	4034	ØFn
	<u> (13.11</u>	4D1.	1 1 EH	3,0311	ØF11
	· <u>: : : : : : : : : : : : : : : : : : :</u>	4024	1FH	× Ø311	ØF15
3	<u> </u>	4D3H	3Ø4	503 N	OF.
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-54		4D5H	3211	- Ø3 K	Ør.
/ ·	630	<u>Ч</u> р6н	<u>дзн</u>	· 03H	CF.
- 115	C'S.(ND7H	<u> 24 m</u>	8:03.1	ØF.N
18,1	G	ADRA	354	× ±311	ØFN
	03,,	4094	<u> </u>	1.034	ØF.,
_: ٨٠		HDAN	37H	<u>8 øзн</u>	ØFit
	CRI	4DBH	28 H	<u>т</u> øзн	Ø.F.N
	C/G.n	-40C4	39 H	96311	ØF.11
- <u></u>		HDDH	344	4534	ØF.N
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24 4		4E+4	31н	523,1	ØEu
_25H		4E51	3211		ØF.4
26 н	Ø3.L		33.4	eø31	ØF1
<u>27н</u>	03.1	<u> </u>	34 2	FØ3H	C.F.
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. ØЧ.н	<u> </u>	50411		<u> ८</u> ७५ म	124
<i>2</i> 5н	Eul 1		i1 is	× ØH H	124
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7 <u>7</u> +		5¢7H	134	5°54 11	124
Ø84	_CH	553.1	<u>14</u> H		12 N
н	OH	509H	151	50411	104
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MB,		566 H	<u> </u>	9 0 × 11	1.24
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. DH	K	50DH		964 H	124
- SEH	_Ć ^{/I} .B	JEEN_	JAH	5°64 N	124
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.Ø	<u>del 11</u>	510.	1Cы	Signali	12 H
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_2014	(x)			Ban H	1211
H	4.1.1		2D.H	8C-1H	1211
	<u>64</u>			80/14	121
_23.11	K	52311	2FH	<u>8041 H</u>	1911
24.16	K	2544		8 CH II	1211
_95N	OHH		ЗІн	5'5'1'H	1211
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274	051:5	5274	334	50414	124
ØØH		5404	ØEH	SØSH	15 _M
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- 1611	Ø5n	556n	244	Ø5H	15.1
170	_05.11				<u>15</u> н
- 11	Ø5H	558k	<u> </u>	1.25 _H	
	-C2n	59н	27-1		15
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<u>УР.К</u>	Cibu	58'24	141	8058	18,1-
C ⁴⁷ M	Chau	587.4	1511	8661	181
C II	264	5851	16.н	N BELL	1
(A)	Cisu	5894	1714	32611	181
Ċ ::	Clon	5811	184	Roby	184
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	U'SIS_	58Dy	164	Rtibu	181
; į lu	Clark	SREH	دی ک	Rech	18.
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e,,,,		D2	264	£\$7.1	1BH
19н	<u></u>		27:1	\$\$7K	1B.,
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1.0 	Ø7.1	5M2	244	807.	1B11
<u></u>	67-1		JB.	S¢ZH	18.1
2 p 3 d - 13 mm	571	SDE	QC <u>''</u>	RØ7H	1B4
		<u></u>		SØ7H	1B H
: SI	27/1		<u>SE1</u>	<u></u> еф7п	
37.11	\$7.H	SE1.	<u> </u>	\$Ø7H	1811.
25.4	Ø711	E	<u> </u>	8074	1B 1
<u></u>	¢74	5E3 H	31	807 H	1B14
. <u> ~iu</u>	_Ø7.1	SEH H	32.1	<u></u> <i><i><i><i></i></i></i></i>	<u>.</u> <u>1</u> B ₁₁
51	07.	_5E5	3311	507 x	1BH
	_Ç.7	5E6H	341	S:67H	1Bn
	····	5E7H	354	KOTH .	4B.c
.cq.			ØD4	STØ8 H	1EN
-म	<u>_39.1</u>		ØFH	808.	LEH
-МЭ.н	1/2,1	60211	ØFH	<u>868 н</u>	1 <u>EH</u>
<u>''3</u>		603H	10 r	ROBU	1E H
11/14	0811	60411		8.03H	1EH
A5H		605н	12H	<u><u>S</u>¢8_H</u>	1Eu
-6н	<u> </u>	60611	13 H	8 BSN	JEH.
07H	<u></u>	6¢7.H	14 11	<u><u></u><u></u><u></u><u></u><u></u><u></u><u></u><u></u><u></u><u></u><u></u><u></u><u></u><u></u><u></u><u></u><u></u><u></u><u></u></u>	JEN.
. 84	<u> 491</u>	-608H	15 _H	50311	1EH
<u>ф</u> ?н	- 50.1	65911	16 N	SUBN	1 E.K.
4 4 5	38.	68A.1	17н	80811	1E.N
2B:	30,	GZE1:	1811	SOBN	1E.,
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5ri	_ØŠu	_60F1	1CH	<u>\$'-18</u> H	LIE4
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r	031		1F_H	5 6BH	1EH
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- 46.1		656711		155+	<u></u> <u></u> <u></u>
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	634	6.5811			
1911	6811	<u> </u>		8C'S_H	5111
144	09.1	65A.u		5 <u>5 ? H</u>	21.11
	<i></i>		9.7.4	<u> </u>	
_1C11	141		<u> </u>	509N	
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_1E.1	<u>n'a</u> ,			8 3'9 H	
1FH	0'9 11			BOGN	31.11
 	69.			SØ9 N	21 4
	76,1	<u> </u>		857 _N	211
2211	<u> </u>			839.4	21.1
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341	29.11	-6534		80711	
	1	<u> </u>	314	8 54 .:	21.1
gg	<u><u><u></u><u></u><u></u><u></u><u></u><u></u><u></u><u></u><u></u><u></u><u></u><u></u><u></u><u></u><u></u><u></u><u></u><u></u></u></u>	<u>665 н</u>	3211	80911	21.4
. 'SII	CO H	- E66H	331	8091	0.1.11 D111
700 H	30.4	<u>:674</u>	34 3	8094	
	OA	- ESCH	ØDH	8¢An	24.4
Ø1H	- QAIS	65111	ØEH	SOAN	21 K
- 32	_ <u></u>	é8211	ØFa	801H	<u></u>
NSN	CAIL	<u> 645</u> 4	10 H	8014	24.
- 75 <u>4</u> 14	CA !!	6.24.,		861.1	<u></u>
15n	OAH	63511	1211	801.	
М6н	C/6.4	6:61	131	821.4	<u></u>
- 7.11	L'hs		14.,	80A	<u> </u>
_08x	_2%	E : 8,1	15.1	8ø/n	
4 7.4	-74×	6.2911	16.H	BOLH	24.1
OXN.	ZAu	GAN	174	BOA.	<u> </u>
<u> 3</u> n	Ø/1-	63.Eu	184	80An	<u>â: ''</u>
Ben	OA	<u>6=-</u> 6	19.4	8.0A.	<u> </u>
<u> </u>	Chu_	68.D.I	14.4	SØA H	24:3
13	.Ø/si	GSEN_			<u></u>
	SKI	GEF.	4Cu	SØAN	24.,
	.0/11	_6ºZ.H	1D.,	ВФАн	<u>\$4</u>
·	Shi	69' 13	<u> </u>	<u><u> </u></u>	الانج
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C. Programmable Power Supplies

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FIMS-B uses two programmable high voltage power supplies. Both power supplies were constructed from the design drawings prepared by the Goddard Space Flight Center for use on the HAPI and LAPI instruments on the Dynamics Explorer Satellite.

A few design changes were made to the power supplies to adapt them to the scientific and operational requirements of the FIMS/Centaur rocket program. The two power supplies used by FIMS are referred to as the energy and mass units, corresponding to the two sections of the instruments analyzer. The energy supply is very similar to the standard D.E. design. Table VII-1 shows the relationship between the 6-bit programming code word and the high voltage output.

The mass analyzer power supply is considerably different from the basic P.P.S. design, the principal difference being that this supply has its high voltage output floated at the -2400VDC bias used for the mass analyzer float potential. To realize this objective, a considerable effort was invested in insulating the electronics within the supply from the chassis-grounded case of the power supply. The inner surfaces of the case of the power supply are lined with a fiberglass material to prevent high voltage breakdown.

The second major change was the addition of a set of six optical couplers to interface with the 6-bit parallel command interface to the CEP. In summary, the mass P.P.S. has its high voltage return connected to the minus high voltage output of the HVPS, thus floating most of the circuits within the supply at -2400V. A 50-megohm resistor was placed in series with the high voltage return line to the minus high voltage output of the bias power supply (PICO-PAK). Table VII-2 shows the relationship between the 6-bit programming code and the high-voltage output of the mass supply.

D. High Voltage Power Supply (HVPS)

The FIMS-B instrument uses a single HVPS to provide the float potential for the mass analyzer deflection plates and the bias for the microchannel plate detector. A standard PICO-PAK Model PP9N provides the bias potential. The pages following contain specification sheets for the unit used on FIMS-B. The high voltage output has been adjusted to -2400VDC for this application.

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31 32 33 34		28.867 24.998V 21.647 18.746	29,0 25,12 21,66 1886	29.0 2512 21.66 18.86	.0573 5.06 4137 7.81	,0586 4,900 4,220 3,67	31 32 33 34	
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571.P	RI	87	V1	VO	+10	+TM	-10	-TM	I + (an x) = 80.
1	127K				•	+ • • •			OVER Voltage =2
2	14/8	127K 120K	-7.50	-982,22	977	4.86	979	4901	Uter County =2
		113K	-7.08	-926,62	924	4.60	1927	4.34	-
		107K	-6.67	-874.17	365	4.30	867	4.10	
		107K	-6.30	-824.69	818	4,07	820	3,86	
		95K	-5,94	-778.01	ツフレ	3.83	772	3.65	ORIGINAL PAGE IS
		POK	-5.60	-733,97	728	3.62	695	3.48	OF POOR QUALITY
		84K	-5.29	-692,43	694	3,45	647	3.29	OF POUR QUALT
-	202K	127K	-4.99	-653,23	617	3.21	618	3,095	I
10	AVAR	120K	-4,71	-616,26		2.50	58	2.93	7
11			-4,44	-581,38	584	2.716	547	2,74	
12		1136	-4.19	-548.47	546		- 1	2,59	1 m
		1075	-3,95	-517.42	517	2,57	518		
13		101K	-3.73	-488.13	486	2,42	487	2,44	
		95K	-3.52	-460.50	440	2-285	461	2.306	
15		90K	-3.32	-434.44	438	2,18	439	2.197	> + 21sms
16		84K	-3.13	-409.85	408	2.026	409.	21241	
17	32 3 K	127K	-2,95	-346.65	382	1.898	383	1,515	
18		120K	-2,79	-364,76	361.5	1.796	362	1-81	
19 -		11 JK	-2.63	-344,12	338	1,68	339	1.696	
20		107K	-2,48	-324,64	320	1,54	321	1.51	ананананананананананананананананананан
21		101K	-2,34	-306.26	301	1,496	302	1,43	
22		95K	-2.21	-288.93	2.85	1,41	285		
23		90K	-2.08	-272.57	2,7/	1,347	272	1.36	TUIN
24		84K	-1.96	-257.14	252	1,254	253	1.26	FINAL DATA
25	514K	127K	-1.85	-242.59	241	1,198	242	1.21	
26		120K	-1.75	-228.86	228	113	229	1,143	TDATA
27		113K	-1.65	-215.90	213,4	1,06	214	1,07	
28		107K	-1.56	-203.68	202	1.00	202	1.01	
29		101K	-1.47	-192.15		5.943	190.	10952	
30		95K	-1.38	-181.28	179.6	1891	180	.900	
31		90K	-1.31	-171.02	171		1171	.857	
32		84K	-1.23	-161.33	159	.790	159		200
33	ADDK-	127K	-1,16	-152.20	150	,746	121	.754	171,4 2 171 169,80
34	820k	120K	-1.10	-143.59	142	,706	143	.713	105.0 =
35		113K	-1.03	-135.46	133	.660	133	:667	· · ·
36		107K	98	-127.79	126	, 623 -		. 631	
37		101K	92	-120.56	118	,587	119	1594	
38		95K	87	-113.73	112	1554	112	.53.5	
39		90K	82	-107.30	107	1528	107	421	
40		84K	77	-101.22	799.3	;493	99.6	,498	
41	1,306M	127K	73	-95.49	94.6		94.6	,474	
42		120K	69	-90.09	\$9.5	.416	89.8	.449	
43		113K	65	-84.99	· 83,5		84,0	,420	
44		107K	61	-80.18	79,2	,393	79.5	, 374	
45		101K	58	-75.64	74,6	. ,370	74,8	2 2 17	 A state of the sta
46		95K	54	-71.36	70.	5,349	7017	1353	
47		90K	51	-67.32	671		67.3	.3/3	
48		84X	48	-63.51	62.		62.7	1	
49	2.082M	127K	46	-59.91	591		59.7	.298	
50		120K	43	-56.52	56.		565	. 282	
51		113K	41	-53.32	52,		52.9	.264	
52		107K	38	-50, 31	49,		50.0	,250	
53		101K	36	-47.46	46.		47.0	,235	•
54		95K	34	-44.77			04,4	1222	
55		POK	-, 32	-42.24	44,			,212	
56		84K	30	-39.85	42.			.197	
57	3.318H	127K	29	-37.59	391		319.14	.186	
58		120K	27	-35.46	37.		37,2	1	TM-200 = 10
39		120K	26	-33.56	357		35,2	.176	1 11 200
60		107K	24		32,		33.0		1227(200)=24,6
61		101K	24	- 31.56	31-0		31,2	,156	2454 - 711
62		95K	23	-29.78	29,2		27,3	1466	
63		95K 90K	21	-28.09	27,6		26.		
64		90K	19	-26.50	26.7				TADIE VIT-2
			-,17	-25.00	24.4	1.120	24		TABLE VII-2
						1		e de la Bereira.	