

Juniper Networks MX240, MX480, MX960 3D Universal Edge Routers and EX9204, EX9208, EX9214 Ethernet Switches with RE-S-X6-64G/RE-S-X6-128G/EX9200-RE2 Routing Engine and MPC7E-10G/EX9200-40XS MACSec Card

Firmware: Junos OS 18.3R1-S1

Non-Proprietary FIPS 140-2 Cryptographic Module Security Policy

Document Version: 1.0 Date: December 03, 2019



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1 Introduction

This is a non-proprietary Cryptographic Module Security Policy for the Juniper Networks MX Series 3D Universal Edge Routers with the RE-S-X6-64G/Re-S-X6-128G routing engine and the MPC7E-10G MACSec Line Card and, the EX9200 series switches with the EX9200-RE2 routing engine and the EX9200-40XS MACSec Line Card. The MX series provides dedicated high-performance processing for flows and sessions and integrates advanced security capabilities that protect the network infrastructure as well as user data. The EX9200 series enables collaboration and provides simple and secure access for the delivery of mission-critical applications in the enterprise campus. In the data center, it simplifies operations to align the network with fast-changing business requirements.

This FIPS 140-2 validation includes the following MX series router models: the MX240, MX480, MX960, and the following EX series switch models: EX9204, EX9208, EX9214. The FIPS validated version of firmware is Junos OS 18.3R1-S1.

The cryptographic boundary for this MX Series and EX Series is defined as follows for the validation:

- the outer edge of the chassis includes the Routing Engine (RE), MPC7E-10Gor EX9200-40XS MACSec Line Card, Switch Control Board (SCB/SFB/SF), slot cover in the following configurations:
 - For MX240 (2 available RE slots, 2 additional slots): 1 SCB, 1 RE (RE-S-X6-64G/ RE-G-S6-128G-S-S-A) and at least 1 MPC7E-10G MACSec Line Card. All empty module bays must have a slot cover installed for proper cooling air circulation.
 - For MX480 (2 available RE slots, 6 additional slots): 1 SCB,1 RE (RE-S-X6-64G/ RE-G-S6-128G-S-S-A) and at least 1 MPC7E-10G MACSec Line Card. All empty module bays must have a slot cover installed for proper cooling air circulation.
 - For MX960 (2 available RE slots, 12 additional slots): 1 SCB, 1 RE (RE-S-X6-64G/ RE-G-S6-128G-S-S-A) and at least 1 MPC7E-10G MACSec Line Card. All empty module bays must have a slot cover installed for proper cooling air circulation.
 - For EX9204 (2 available RE slots, 2 additional slots): 1 SF module, 1 RE (EX9200-RE2) and at least 1 EX9200-40XS MACSec Line Card. All empty module bays must have a slot cover installed for proper cooling air circulation.
 - For EX9208 (2 available RE slots, 6 additional slots): 1 SF module, 1 RE (EX9200-RE2) and at least 1 EX9200-40XS MACSec Line Card. All empty module bays must have a slot cover installed for proper cooling air circulation.
 - For EX9214 (2 available RE slots, 12 additional slots): 1 SF module, 1 RE (EX9200-RE2) and at least 1 EX9200-40XS MACSec Line Card. All empty module bays must have a slot cover installed for proper cooling air circulation.
- includes the inverse three-dimensional space where non-crypto-relevant line cards fit, with the backplane port serving as the physical interface.
- excluding the power distribution module on the rear of the device.

The cryptographic modules provide for an encrypted connection, using SSH, between the management station and the module. The cryptographic modules also provide for an encrypted connection, using MACSec, between devices. All other data input or output from the modules are considered plaintext for



this FIPS 140-2 validation.

The cryptographic modules are defined as multiple-chip standalone modules that execute Junos OS 18.3R1-S1 firmware on any of the Juniper Networks MX 3D Universal Edge Routers and the Juniper Networks EX Series Switches listed in Table 1 below.

Chassis PN	Power PN	SCB PN	RE PN	MACSec Card PN
MX240	PWR-MX480-2400-DC PWR-MX480-2520-AC	SCBE2-MX	RE-S-X6-64G RE-G-S6-128G	MPC7E-10G
MX480 PWR-MX480-2400-DC PWR-MX480-2520-AC		SCBE2-MX	RE-S-X6-64G RE-G-S6-128G	MPC7E-10G
MX960	PWR-MX960-4100-DC PWR-MX960-DC PWR-MX960-4100-AC PWR-MX960-AC-S	SCBE2-MX	RE-S-X6-64G RE-G-S6-128G	MPC7E-10G
EX9204	PWR-MX480-2400-DC PWR-MX480-2520-AC	EX9200-SF2	EX9200-RE2	EX9200-40XS
EX9208	PWR-MX480-2400-DC PWR-MX480-2520-AC	EX9200-SF2	EX9200-RE2	EX9200-40XS
EX9214	PWR-MX960-4100-DC PWR-MX960-4100-AC	EX9200-SF2	EX9200-RE2	EX9200-40XS

The enhanced routing engine RE-G-S6-128G that can be used in the MX series routers (MX240/ MX480/ MX960) offers SSDs with more storage capacity (2x100G), and RAM with more memory (128G) than the other routing engines. Both the RE-S-X6-64G and RE-S-X6-128G routing engines were tested for the FIPS 140-2 validation.



The modules are designed to meet FIPS 140-2 Level 1 overall:

Area	Description	Level
1	Module Specification	1
2	Ports and Interfaces	1
3	Roles, Services, and Authentication	3
4	Finite State Model	1
5	Physical Security	1
6	Operational Environment	N/A
7	Key Management	1
8	EMI/EMC	1
9	Self-test	1
10	Design Assurance	3
11	Mitigation of Other Attacks	N/A
	Overall	1

Table 2- Security Level of Security Requirements

The modules have a limited operational environment as per the FIPS 140-2 definitions. It includes a firmware load service to support necessary updates. New firmware versions within the scope of this validation must be validated through the FIPS 140-2 CMVP. Any other firmware loaded into the modules are out of the scope of this validation and require a separate FIPS 140-2 validation.

The modules do not implement any mitigations of other attacks as defined by FIPS 140-2.



1.1 Hardware and Physical Cryptographic Boundary

The cryptographic modules' operational environment is a limited operational environment.

The images below depict the physical boundary of the modules, including the Routing Engine, the MACSec line card (the MPC7E-10G for the MX Series and the EX9200-40XS for the EX Series) and SCB. The boundary excludes the non-crypto-relevant line cards included in the figure. The modules exclude the power supplies from the requirements of FIPS 140-2. The power supplies do not contain any security relevant components and cannot affect the security of the module.



Figure 1 – Physical Cryptographic Boundary (Left to Right: MX240, MX480, MX960)



Figure 2 - Physical Cryptographic Boundary (Left to Right: EX9204, EX9208, EX9214)



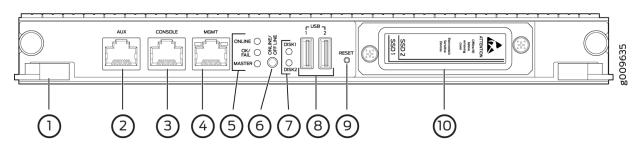
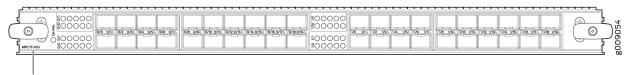


Figure 3 – Routing Engine (RE-S-X6-64G/RE-G-S6-128G/ EX9200-RE2)

1—Extractor clips	2—Auxiliary port (AUX)
3—Console port (CONSOLE)	4—Management port (MGMT)
5—LEDs—ONLINE, OK/FAIL, and MASTER	6—ONLINE/OFFLINE button
7—SSD LEDs—DISK1 and DISK2	8—USB ports—USB1 and USB2
9—RESET button	10—SSD slots—SSD 1 and SSD 2



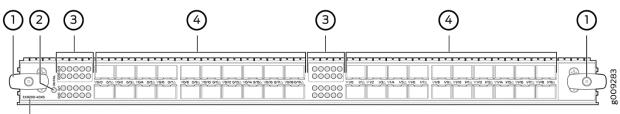
MPC7E-10G



The MPC7E-10G has 40 10-Gigabit Ethernet ports. The ports support SFP+ transceivers. The ports are labeled as (with the MPC orientation as shown in Figure 4):

- 0/0 through 0/9
- 0/10 through 0/19
- 1/0 through 1/9
- 1/10 through 1/19





EX9200-40XS

Figure 5 – MACSec EX9200-40XS Line Card

Figure 5 shows the following components of an EX9200-40XS line card:

- 1 Ejector lever 3- LEDs for the 10-Gigabit Ethernet ports
- 2- Line card LEDs 4- 10-Gigabit Ethernet ports with MACsec capability

Table 3 - Ports and Interfaces

Port	Device (# of ports)	Description	Logical Interface Type
Ethernet	MX240/MX480/MX960: Management port (1), MPC7E-10G interfaces: 10-Gigabit Ethernet SFP+ ports (40) EX9204/EX9208/EX9214: Management port (1), EX9200-40XS interfaces: 10-Gigabit Ethernet MACsec-capable SFP+ ports (40)	LAN Communications/Remote management	Control in, Data in, Data out, Status out
Serial	MX240 (1), MX480 (1), MX960 (1), EX9204 (1), EX9208(1), EX9214(1)	Console serial port	Control in, Data in, Data out Status out
USB	MX240 (2), MX480 (2), MX960 (2), EX9204 (2), EX9208(2), EX9214(2)	USB port - load Junos image	Control in, Data in
Power	MX240 (4), MX480 (4), MX960 (4), EX9204 (4), EX9208(4), EX9214(4)	Power connector, Power over Ethernet	Power
Alarm LEDs	MX240 (2), MX480 (2), MX960 (2), EX9204 (2), EX9208(2), EX9214(2)	Status indicator lighting	Status out
Online/Offline	MX240 (3), MX480 (3),	Status indicator lighting	Status out



[
Indicator	MX960 (3), EX9204 (3),			
	EX9208(3), EX9214(3)			
Master/Slave	MX240 (3), MX480 (3),			
Indicator	MX960 (3), EX9204 (3),	Status indicator lighting	Status out	
indicator	EX9208(3), EX9214(3)			
	MX240 (1), MX480 (1),			
Reset Button	MX960 (1), EX9204 (1),	Reset	Control in	
	EX9208(1), EX9214(1)			
Chassis Cluster	MX240/MX480/MX960	Disabled	N/A	
Control	EX9204/EX9208/EX9214	Disableu	N/A	
	MX240 (1), MX480 (1),			
Aux	MX960 (1), EX9204 (1),	Disabled	N/A	
	EX9208(1), EX9214(1)			
	MX240 (1), MX480 (1),	Line card backplane	Control in, Data in, Status	
Backplane	MX960 (1), EX9204 (1),	interface	out, Data out	
	EX9208(1), EX9214(1)			

1.2 Modes of Operation

The module supports three FIPS Approved modes of operation and a non-Approved mode of operation. The module must always be zeroized when switching between any FIPS Approved mode of operation and the non-Approved mode of operation and vice versa.

1.2.1 FIPS Approved Mode

The hardware versions contained in Table 1, with Junos OS 18.3R1-S1 installed, contain three FIPS-Approved modes of operation and a non-Approved mode of operation. The Junos OS 18.3R1-S1 firmware image must be installed on the device. The module is configured during initialization to operate in an approved mode or a non-approved mode.

The Crypto-Officer places the module in an Approved mode of operation by following the instructions in cryptographic officer guidance (section 6.1).

The Crypto-Officer can verify that the cryptographic module is in an Approved mode by observing the console prompt and running the "show version" command. When operating in FIPS mode, the prompt will read "<user>@<device name>:fips#" (e.g. crypto-officer@mx240:fips#). The "show version" command will allow the Crypto-Officer to verify that the validated firmware version is running on the module. The Crypto-Officer can also use the "show system fips level" command to determine if the module is operating in FIPS mode.

The module supports three Approved modes of operation. The three modes are identified as "FIPS Standard Mode", "FIPS Reduced Throughput Mode" and "FIPS Recovery Mode."



The FIPS Standard Mode is entered when the module is configured for FIPS mode and successfully passes all the power on self-tests (POST) in both the routing engine (RE) and in each of the MPC7E-10G or EX9200-40XS line cards installed in the chassis of the module (the maximum number of MPC7E-10G line cards that can be installed in the module chassis is 2 in case of the MX240, 6 in case of the MX480 and 12 in case of the MX960 modules. Similarly, the maximum number of EX9200-40XS line cards that can be installed in the chassis is 2 in case of the EX9204, 6 in case of the EX9208 and 12 in case of the EX9214 modules). The FIPS Standard Mode supports the approved and allowed algorithms, functions and protocols identified in Table 4 - 11. The services available in this mode are described in Tables 14 and 16.

The FIPS Reduced Throughput mode is automatically selected by the module at power-up when all the power on self-tests (POST) in the routing engine (RE) pass successfully, at least one MPC7E-10G or EX9200-40XS line card in the module chassis passes all of its self-tests, and at least one MPC7E-10G or EX9200-40XS line card in the module chassis fails one or more of its self-tests. In this mode, the module offers reduced throughput MACsec services. The FIPS Reduced Throughput Mode supports the approved and allowed algorithms, functions and protocols identified in Table 4 - 11. The services available in this mode are described in Tables 14 and 16.

The FIPS Recovery Mode is automatically selected when the module is configured for FIPS mode, at power-up, when one or more of the POST fail for each of the MPC7E-10G or EX9200-40XS line cards inserted into the module chassis, and all of the RE POST pass successfully. In the FIPS Recovery Mode, the module does not allow MACsec services and shuts down all data ports on each of the MPC7E-10G or EX9200-40XS line cards. The module supports the OpenSSL, LibMD and Kernel algorithms in Table 4-6; the algorithms in Table 10, and the SSH protocol in Table 11 when in the FIPS Recovery mode. The services available in the Recovery mode are described in Table 15 and Table 17.

1.2.2 Non-Approved Mode

The cryptographic module supports a non-Approved mode of operation. When operated in the non-Approved mode of operation, the module supports the algorithms identified in Section 2.2 as well as the algorithms supported in the Approved mode of operation.

The Crypto-Officer can place the module into a non-approved mode of operation by following the instructions in the cryptographic officer guidance (section 6.1).

1.3 Zeroization

The cryptographic module provides a non-Approved mode of operation in which non-Approved cryptographic algorithms are supported. When transitioning between the non-Approved mode of operation and the FIPS-Approved mode of operation, or vice-versa, the cryptographic officer shall zeroize all keys and CSPs.

Zeroization completely erases all configuration information on the Router/Switch. The Crypto Officer



initiates the zeroization process by entering the "*request vmhost zeroize no-forwarding*" (FIPS) operational command from the CLI after enabling FIPS mode. Use of this command is restricted to the Crypto Officer. (To zeroize the system *before* enabling FIPS mode, use the "*request vmhost zeroize no-forwarding* command.)

Use of the zeroize command is restricted to the Cryptographic Officer. The cryptographic officer shall perform zeroization in the following situations:

- 1. Before FIPS Operation: To prepare the device for operation as a FIPS cryptographic module by erasing all CSPs and other user-created data on a device before its operation as a FIPS cryptographic module.
- 2. Before non-FIPS Operation: To conduct erasure of all CSPs and other user-created data on a device in preparation for repurposing the device for non-FIPS operation.

CAUTION: Perform system zeroization with care. After the zeroization process is complete, no data is left on the Routing Engine. The device is returned to the factory default state, without any configured users or configuration files.

To zeroize the device:

1. From the CLI, enter

Crypto-officer@device> request vmhost zeroize no-forwarding warning: System will be rebooted and may not boot without configuration Erase all data, including configuration and log files? [yes, no] (no)

2. To initiate the zeroization process, type yes at the prompt:

Erase all data, including configuration and log files? [yes, no] (no)

yes

3. When the system finishes rebooting the system will be in a factory default state.

Note: The Cryptographic Officer must retain control of the module while zeroization is in process.



2 Cryptographic Functionality

2.1 Allowed Algorithms and Protocols

The module implements the FIPS Approved and Non-Approved but Allowed cryptographic functions listed in Tables 4, 5, 6, 7, 8, 9 and 10 below. Table 11 summarizes the high-level protocol algorithm support. There are some algorithm modes that were tested but not implemented by the module. Only the algorithms, modes, and key sizes that are implemented by the module are shown in this/these table(s).

CAVP					
Cert.	Algorithm	Standard	Mode	Description	Functions
C 484	DRBG	SP800-90A	HMAC	SHA-256	Random Bit Generation
			SHA-1	Key size: 160 bits, λ = 96	Message Authentication,
C 484	HMAC	PUB 198	SHA-256	Key size: 256 bits, λ = 128, 256	DRBG Primitive
C 484	SHS	PUB 180-4	SHA-1 SHA-256		Message Digest
C 404	5115	100 100-4	SHA-384 SHA-512		Generation

Table 4 – Kernel Approved Cryptographic Functions

Table 5 – LibMD Approved Cryptographic Functions

CAVP					
Cert.	Algorithm	Standard	Mode	Description	Functions
			SHA-1	Key size: 160 bits, λ = 96	
C 485	HMAC	PUB 198	SHA-256	Key size: 256 bits, λ = 128, 256	Message Authentication
C 485	SHS	PUB 180-4	SHA-1 SHA-256 SHA-512		Message Digest Generation

Table 6 – OpenSSL Approved Cryptographic Functions

CAVP Cert.	Algorithm	Standard	Mode	Description	Functions
C 488	AES ¹	PUB 197-38A	CBC, CTR, ECB	Key Sizes: 128, 192, 256	Encrypt, Decrypt
N/A ²	СКБ	SSH-PUB 133	Section 6.1 Section 6.2		A generated seed used in asymmetric key generation using

¹ The AES-ECB mode was used for testing the AES-CTR mode.

² Vendor Affirmed



		1		NORKS	
					an unmodified DRBG output
C 488	CVL (KAS)	SP 800-56A	$F((1)H) = P_{-3}X_{4}(SHA_{3}X_{4})$		Key Agreement Scheme
C 488	CVL	SP 800-135	SSH	SHA 1, 256, 384, 512	Key Derivation
C 488	DRBG	SP 800-90A	HMAC	SHA-256	Random Number Generation
C 488	ECDSA	PUB 186-4		P-256 (SHA 256) P-384 (SHA 384) P-521 (SHA 512)	SigGen, KeyGen, SigVer, PKV
			SHA-1	Key size: 160 bits, $\lambda = 160$	
		IMAC PUB 198	SHA-224	Key size: 224 bits, λ = 192	Message Authentication
C 488	HMAC		SHA-512	Key size: 512 bits, $\lambda = 512$	
			SHA-256	Key size: 256, bits, λ = 256	Message Authentication, DRBG Primitive
N/A	ктѕ		AES Cert. # C 4 488	188 and HMAC Cert. # C	key establishment methodology provides between 128 and 256 bits of encryption strength
			Triple-DES Cer # C 488	t. # C 488 and HMAC Cert.	key establishment methodology provides 112 bits of encryption strength
C 488	RSA	PUB 186-4	n=2048 (SHA 256, 512)		KeyGen, SigGen, SigVer ³
C 488	SHS	PUB 180-4	SHA-1 SHA-256 SHA-384 SHA-512		Message Digest Generation, KDF Primitive
			SHA-224		Message Digest Generation
C 488	Triple-DES	SP 800-67	ТСВС	Key Size: 192	Encrypt, Decrypt

³ RSA 4096 SigVer was not tested by the CAVP; however, it is Approved for use per CMVP guidance, because RSA 2048 SigVer was tested and testing for RSA 4096 SigVer is not available.



Table 7 - QuickSec Approved Cryptographic Functions

CAVP Cert.	Algorithm	Standard	Mode	Description	Functions
C 486	НМАС	PUB 198	SHA-256	Key size: 256, bits, λ = 256	
C 486	SHS	SP800- 180-4	SHA-256		Message Digest Generation, KDF Primitive
C 486	DRBG	SP800-90A	НМАС	SHA-256	Random Bit Generation

Table 8 MACSec Approved Cryptographic Functions

CAVP Cert.	Algorithm	Standard	Mode	Description	Functions
		SP800- 197- 38A	ECB, CBC	Key Sizes: 128, 256	AES CMAC
C 502 AES		SP800-38D	СМАС	Key Sizes: 128,256	Key Derivation SP 800- 108: Used to generate MACsec keys
		SP800-35F	KW	Key Size: 128	Key Wrapping for MACsec keys
C 502	KDF	SP 800-108	Counter	CMAC AES128 CMAC AES256	KDF for MACsec keys
N/A	ктѕ		AES #C 502		Key Wrapping, key establishment methodology provides 128 bits of encryption strength

Table 9 MPC7E/EX9200-40XS VSC8258 Chip

CAVP Cert.	Algorithm	Standard	Mode	Description	Functions
3969	AES	SP800-38D	GCM	Key Sizes: 128,256	Key Derivation SP 800- 108: Used to generate MACsec keys

Table 10 Allowed Cryptographic Functions

Algorithm	Caveat	Use
Elliptic Curve Diffie-	Provides between 128 and 256 bits of	key agreement; key establishment
Hellman IG D.8	encryption strength.	key agreement, key establishment



NDRNG IG 7.14 Scenario	The module generates a minimum of	Seeding the DRBG
1a	256 bits of entropy for key generation.	Seeding the DRBG

Table 11 Protocols Allowed in FIPS Mode

Protocol	Key Exchange	Auth	Cipher	Integrity
MACsec	MACsec Key Agreement	Shared secret	AES-GCM-128	HMAC-SHA-256
ΜΚΑ	MACSEC KEY Agreement	Shared Secret	AES-GCM-256	TIMAC-STIA-250
SSHv2⁴	EC Diffie-Hellman P-256, P-384, P-521		Triple-DES CBC	
		RSA 2048,4096 ECDSA P-256	AES CBC	HMAC-SHA-1
			128/192/256	HMAC-SHA-256
		ECDSA P-250	AES CTR	HMAC-SHA-512
			128/192/256	

No part of these protocols, other than the KDF, have been tested by the CAVP and CMVP. The MACsec and SSH algorithms allow independent selection of key exchange, authentication, cipher and integrity. In Table 11 above, each column of options for a given protocol is independent and may be used in any viable combination.

The modules can take on the role of Peer or Authenticator in reference to the MACsec protocol. The AES GCM IV construction is performed in compliance with IEEE 802.1AE and its amendments.

2.2 Disallowed Algorithms and Protocols

These algorithms and protocols are non-Approved algorithms and protocols that are disabled when the module is operated in an Approved mode of operation. The algorithms are available as part of the SSH connect service when the module is operated in the non-Approved mode.

Algorithms

- RSA with key size less than 2048
- ECDSA with ed25519 curve
- ECDH with ed25519 curve
- ARCFOUR
- Blowfish
- CAST
- DSA (SigGen, SigVer; non-compliant)
- HMAC-MD5
- HMAC-RIPEMD160
- UMAC

⁴ RFC 4253 governs the generation of the Triple-DES encryption key for use with the SSHv2 protocol



Protocols

- Finger
- ftp
- rlogin
- telnet
- tftp
- xnm-clear-text

2.3 Critical Security Parameters

All CSPs and public keys used by the module are described in this section. The CSPs in Table 12 are used in the FIPS Standard and FIPS Reduced Throughput Modes. The FIPS Recovery Mode uses a subset of the CSPs found in Table 12. The MACsec CSPs are not available for use in FIPS Recovery Mode of operation.

Name	Description and usage
DRBG_Seed	Seed material used to seed or reseed the DRBG
DRBG_State	Values V and Key which comprise the HMAC_DRBG state
Entropy Input	256 bits entropy (min) input used to instantiate the DRBG
ECDH Shared Secret	The Diffie-Hellman shared secret used in EC Diffie-Hellman (ECDH) exchange. Created per the EC Diffie-Hellman protocol. Provides between 128-256 bits of security.
SSH PHK	SSH Private host key. 1st time SSH is configured, the keys are generated. ECDSA P-256. RSA 2048
SSH ECDH	Ephemeral EC Diffie-Hellman private key used in SSH. ECDH P-256, P-384, or P-521
SSH-SEKs	SSH Session Keys: SSH Session Encryption Key: 3-Key Triple-DES or AES (128,192,256); SSH Session Integrity Key: HMAC.
MACsec CAK	User-configured PSK entered when MACsec using static connectivity association key (CAK) security mode is enabled (32 characters).
MACsec CKN	User-configured PSK used to identify the CAK (64 characters).
MACsec SAK	Security Association Key used to encrypt/decrypt traffic for a given session. Derived from CAK using KDF SP 800-108. (128-bit AES).
MACsec KEK	Key Encryption Key used to transmit SAK to other members of a MACSec connectivity association. Derived from CAK using KDF SP 800-108. (128-bit AES).
MACsec ICK	Integrity Check Key used to verify the integrity and authenticity of MPDUs. Derived from CAK using KDF SP 800-108. (128/256-bit CMAC).
HMAC Key	The LibMD HMAC keys: message digest for hashing password and critical function test.
User Password	Passwords used to authenticate Users to the module.
CO Password	Passwords used to authenticate COs to the module.

Table 12 Critical Security Parameters (CSPs)



Table 13 Public Keys

Name	Description and usage			
SSH-PUB	SSH Public Host Key used to identify the host. ECDSA P-256. RSA 2048, 4096.			
SSH-ECDH- PUB	Ephemeral EC Diffie-Hellman public key used in SSH key establishment. ECDH P-256, P-384, or P-521			
Auth-User Pub	User Authentication Public Keys. Used to authenticate users to the module. ECDSA P-256, P-			
Auth-Oser Pub	384, or P-521			
Auth-CO Pub	CO Authentication Public Keys. Used to authenticate CO to the module. ECDSA P-256, P-384,			
Autil-CO Pub	or P-521			
Root CA	ECDSA P-256 X.509 Certificate; Used to verify the validity of the Juniper Package CA at			
ROOLCA	software load and also at runtime for integrity.			
De cha ca CA	ECDSA P-256 X.509 Certificate; Used to verify the validity the Juniper Image at software load			
Package CA	and also at runtime for integrity.			



3 Roles, Authentication and Services

3.1 Roles and Authentication of Operators to Roles

The module supports two roles: Cryptographic Officer (CO) and User. The module supports concurrent operators but does not support a maintenance role and/or bypass capability. The module enforces the separation of roles using identity-based operator authentication.

The Cryptographic Officer role configures and monitors the module via a console or SSH connection. As root or super-user, the Cryptographic Officer has permission to view and edit secrets within the module.

The User role monitors the router via the console or SSH. The User role cannot change the configuration.

3.2 Authentication Methods

The module implements two forms of Identity-Based authentication, Username and password over the Console and SSH as well as Username and ECDSA or RSA public key over SSH.

Password authentication: The module enforces 10-character passwords (at minimum) chosen from the 96 human readable ASCII characters. The maximum password length is 20-characters. Thus, the probability of a successful random attempt is 1/96¹⁰, which is less than 1/1 million.

The module enforces a timed access mechanism as follows: For the first two failed attempts (assuming 0 time to process), no timed access is enforced. Upon the third attempt, the module enforces a 5-second delay. Each failed attempt thereafter results in an additional 5-second delay above the previous (e.g. 4th failed attempt = 10-second delay, 5th failed attempt = 15-second delay, 6th failed attempt = 20-second delay, 7th failed attempt = 25-second delay).

This leads to a maximum of 7 possible attempts in a one-minute period for each getty. The best approach for the attacker would be to disconnect after 4 failed attempts and wait for a new getty to be spawned. This would allow the attacker to perform roughly 9.6 attempts per minute (576 attempts per hour/60 mins); this would be rounded down to 9 per minute, because there is no such thing as 0.6 attempts. The probability of a success with multiple consecutive attempts in a one-minute period is 9/(96¹⁰), which is less than 1/100,000.

ECDSA signature verification: SSH public-key authentication. Processing constraints allow for a maximum of 5.6e7 ECDSA attempts per minute. The module supports ECDSA (P-256, P-384, and P-521), which has a minimum equivalent computational resistance to attack of either 2^{128} , 2^{192} or 2^{256} depending on the curve. Thus, the probability of a successful random attempt is $1/(2^{128})$, which is less than 1/1,000,000. Processing speed (partial establishment of an SSH session) limits the number of failed authentication attempts in a one-minute period to 5.6e7 attempts. The probability of a success with multiple consecutive attempts in a one-minute period is $5.6e7/(2^{128})$, which is less than 1/100,000.

RSA signature verification: SSH public-key authentication. Processing constraints allow for a maximum of 5.6e7 RSA attempts per minute. The module supports RSA (2048, 4096), which has a minimum equivalent computational resistance to attack of 2^{112} (2048). Thus, the probability of a successful random attempt is 1/ (2^{112}), which is less than 1/1,000,000. Processing speed (partial establishment of



an SSH session) limits the number of failed authentication attempts in a one-minute period to 5.6e7 attempts. The probability of a success with multiple consecutive attempts in a one-minute period is $5.6e7/(2^{112})$, which is less than 1/100,000.

3.3 Approved and Allowed Services

All services implemented by the module are listed in the tables below. Table 13 lists the access to CSPs by each service.

Service	Description	CO	User
Configure security	Security relevant configuration	x	
Configure	Non-security relevant configuration	х	
Secure Traffic	MACsec encrypted transfer of data	х	
Status	Show status	х	х
Zeroize	Destroy all CSPs	х	
SSH connect	Initiate SSH connection for SSH monitoring and control (CLI)	х	х
MACsec connect	Initiate MACsec connection	х	
Console access	Console monitoring and control (CLI)	х	х
Remote reset	Software initiated reset, performs self-tests on demand.	х	
Load Image	Verification and loading of a validated firmware image	х	

Table 14 - Authenticated Services in FIPS Standard and Reduced Throughput Modes

Table 15 - Authenticated Services in FIPS Recovery Mode

Service	Description	СО	User
Configure security		x	
	Security relevant configuration	^	
Configure	Non-security relevant configuration	х	
Status	Show status	х	х
Zeroize	Destroy all CSPs	х	
SSH connect	Initiate SSH connection for SSH monitoring and control (CLI)	x	х
Console access	Console monitoring and control (CLI)	х	х
Remote reset	Software initiated reset, performs self-tests on demand.	х	
Load Image	Verification and loading of a validated firmware image	х	

Table 16 - Unauthenticated Services in FIPS Standard and Reduced Throughput Modes

Service	Description
Local reset	Hardware reset or power cycle



Traffic	Traffic requiring no cryptographic services (e.g. OSPF, BGP)	
LED Status	Basic	

Table 17 - Unauthenticated Services in Recovery Mode

Service	Description					
Local reset	Hardware reset or power cycle					
LED Status	Basic					

Table 18 - CSP Access Rights within Services

			<u> </u>	CSPs											
Service	DRBG_Seed	DRBG_State	Entropy Input	ECDH Shared Secret	SSH PHK	SSH ECDH	SSH-SEK	MACsec SAK	MACsec CAK	MACsec CKN	MACsec KEK	MACsec ICK	НМАС Кеу	CO-PW	User-PW
Configur e security		E		GW R	GW R			GW R	W R	W R	G W	G W	G	w	w
Configur e															
Secure traffic								E			E				
Status															
Zeroize	Z	Z	Z	Z	Z	Z	Z	Z	Z	Z	Z	Z	Z	Z	Z
SSH connect		E		E	Е	GE	GE							E	E
MACsec connect		E						GE			GE	GE			
Console access														E	E
Remote reset	GE Z	GZ	GZ	Z		Z	Z	Z				Z			
Load Image															
Local reset	GE Z	GZ	GZ	Z		Z	Z	Z				Z			
Traffic															

G = Generate: The module generates the CSP

R = Read: The CSP is read from the module (e.g. the CSP is output)

E = Execute: The module executes using the CSP



W = Write: The CSP is updated or written to the module (persistent storage) Z = Zeroize: The module zeroizes the CSP.

3.4 Non-Approved Services

The following services are available in the non-Approved mode of operation. The security functions provided by the non-Approved services are identical to the Approved counterparts with the exception of SSH Connect (non-compliant). SSH Connect (non-compliant) supports the security functions identified in Section 2.2 and the SSHv2 row of Table 11.

Service	Description	CO	User
Configure security (non-compliant)	Security relevant configuration	х	
Configure	Non-security relevant configuration	х	
Secure Traffic (non-compliant)	MACsec encrypted transfer of data	х	
Status (non-compliant)	Show status	х	x
Zeroize (non-compliant)	Destroy all CSPs	х	
SSH connect (non-compliant)	Initiate SSH connection for SSH monitoring and control (CLI)	х	x
MACsec connect (non- compliant)	Initiate MACsec connection	х	
Console access (non-compliant)	Console monitoring and control (CLI)	x	x
Remote reset (non-compliant)	Software initiated reset, performs self-tests on demand	x	
Load Image (non-compliant)	Verification and loading of a validated firmware image into the switch.	х	

Table 19 – Non-Approved Authenticated Services in FIPS Standard and Reduced Throughp	out Modes

Table 20 – Non-Approved Authenticated Services in FIPS Recovery Mode

Service	Description	СО	User
Configure security (non-compliant)	Security relevant configuration	х	
Configure	Non-security relevant configuration	х	
Status (non-compliant)	Show status	х	х
Zeroize (non-compliant)	Destroy all CSPs	x	
SSH connect (non-compliant)	Initiate SSH connection for SSH monitoring and control (CLI)	x	x



Console access	Console monitoring and control (CLI)	х	х
(non-compliant)			
Remote reset (non-compliant)	Software initiated reset, performs self-tests on demand	х	
Load Image (non-compliant)	Verification and loading of a validated firmware image into the switch.	х	

Table 21 – Non-Approved Unauthenticated Services

Service	Description			
Local reset	lardware reset or power cycle			
Traffic	Traffic requiring no cryptographic services (e.g. OSPF, BGP)			
LED Status	Basic			



4 Self-tests

Each time the module is powered up it tests that the cryptographic algorithms still operate correctly, and that sensitive data have not been damaged. Power-up self-tests are available on demand by power cycling the module (Remote reset service).

On power up or reset, the module performs the self-tests described below. All KATs must be completed successfully prior to any other use of cryptography by the module in the FIPS Standard and Reduced Throughput Modes of operation. If any one of the Routing Engine KATs fails, the module enters the Error state. If all the RE KATs pass and at least one MPC7E-10G or EX9200-40XS in the module chassis passes all of its self-tests, and at least one MPC7E-10G or EX9200-40XS in the module chassis fails one or more of its self-tests, the module selects the FIPS Reduced Throughput Mode of operation and the module offers reduced throughput MACsec services. If all the RE KATs pass and each of the MPC7E-10 or EX9200-40XS line cards in the module chassis fails one or more of its KATs , the module selects the FIPS Recovery Mode of operation and all MPC7E-10G or EX9200-40XS algorithms are prevented from being used.

The module performs the following power-up self-tests:

Routing Engine (RE)

- Firmware Integrity check: using ECDSA P-256 with SHA-256
- Kernel KATs
 - SP 800-90A HMAC DRBG KAT
 - Health-tests initialize, re-seed, and generate
 - HMAC-SHA-1 KAT
 - HMAC-SHA-256 KAT
 - o SHA-384 KAT
 - SHA-512 KAT
- OpenSSL KATs
 - AES-CBC (128/192/256) Encrypt KAT
 - AES-CBC (128/192/256) Decrypt KAT
 - SP 800-90A HMAC DRBG KAT
 - Health-tests initialize, re-seed, and generate
 - o ECDSA P-256 Sign/Verify PCT
 - o ECDH P-256 KAT
 - Derivation of the expected shared secret.
 - HMAC-SHA-1 KAT
 - o HMAC-SHA-224 KAT
 - HMAC-SHA-256 KAT
 - o HMAC-SHA-512 KAT
 - KAS -ECC KAT
 - KDF-SSH KAT
 - RSA 2048 w/ SHA-256 Sign KAT
 - o RSA 2048 w/ SHA-256 Verify KAT
 - o SHA-384 KAT



- Triple-DES-CBC Encrypt KAT
- Triple-DES-CBC Decrypt KAT
- LibMD KATs
 - HMAC SHA-1
 - o HMAC SHA-256
 - o SHA-512
- QuickSec KATs
 - SP 800-90A HMAC DRBG KAT
 - Health-tests initialize, re-seed, and generate
 - o HMAC-SHA-256 KAT
- MacSec KATs
 - AES128-CMAC KAT
 - AES256-CMAC KAT
 - o AES-ECB (128/256) Encrypt KAT
 - AES-ECB (128/256) Decrypt KAT
 - AES-KEYWRAP KAT

MPC7E-10G/EX9200-40XS

- VSC8258
 - AES GCM KAT
- Critical Function Test
 - The cryptographic module performs a verification of a limited operational environment, and verification of optional non-critical packages.

The module also performs the following conditional self-tests:

- Continuous RNG Test on the SP 800-90A HMAC-DRBGs in the OpenSSL and Quicksec libraries.
- Continuous RNG test on the NDRNG.
- Pairwise consistency test when generating ECDSA, and RSA key pairs.
- Firmware Load Test (ECDSA signature verification).

5 Physical Security Policy

The modules physical embodiment is that of a multi-chip standalone device that meets Level 1 Physical Security requirements. The module is completely enclosed in a rectangular nickel or clear zinc coated, cold rolled steel, plated steel and brushed aluminum enclosure. The module enclosure is made of production grade materials. There are no ventilation holes, gaps, slits, cracks, slots, or crevices that would allow for any sort of observation of any component contained within the cryptographic boundary.



6 Security Rules and Guidance

The module design corresponds to the security rules below. The term *shall* in this context specifically refers to a requirement for correct usage of the module in the Approved mode; all other statements indicate a security rule implemented by the module.

- 1. The module clears previous authentications on power cycle.
- 2. When the module has not been placed in a valid role, the operator does not have access to any cryptographic services.
- 3. Power up self-tests do not require any operator action.
- 4. Data output is inhibited during key generation, self-tests, zeroization, and error states.
- 5. Status information does not contain CSPs or sensitive data that if misused could lead to a compromise of the module.
- 6. There are no restrictions on which keys or CSPs are zeroized by the zeroization service.
- 7. The module does not support a maintenance interface or role.
- 8. The module does not support manual key entry.
- 9. The module does not output intermediate key values.
- 10. The module requires two independent internal actions to be performed prior to outputting plaintext CSPs.
- 11. The cryptographic officer shall verify that the firmware image to be loaded on the module is a FIPS validated image. If any non-validated firmware image is loaded the module will no longer be a FIPS validated module.
- 12. The cryptographic officer shall retain control of the module while zeroization is in process.
- 13. If the module loses power and then it is restored, then a new key shall be established for use with the AES GCM encryption/decryption processes.
- 14. The operator shall ensure that the number of 64-bit blocks encrypted by the same key does not exceed 2^20 with a single Triple-DES key when Triple-DES is the encryption algorithm for SSH.
- 15. Virtual Chassis is not supported in FIPS mode and shall not be configured on the modules.
- 16. RSA key generated shall only be 2048 bits or greater.
- 17. The module shall only be used with CMVP FIPS 140-2 validation modules when supporting the MACsec protocol for providing Peer, Authenticator functionality.
- 18. The link between the Peer and Authenticator, used in the MACsec communication, shall be secure to prevent the possibility for an attacker to introduce foreign equipment into the local area network.
- 19. The module shall not be configured to use a radius server and the radius server capability shall be disabled.



6.1 Cryptographic-Officer Guidance

The cryptographic officer must check to verify the firmware image on the Router/Switch is the FIPS 140-2 validated image. If the image is the FIPS 140-2 validated image, then proceed to section 6.1.2.

6.1.1 Installing the FIPS-Approved firmware image

Download the validated firmware image from the

<u>https://www.juniper.net/support/downloads/junos.html</u>. Log in to the Juniper Networks authentication system using the username (generally your e-mail address) and password supplied by Juniper Networks representatives. Select the validated firmware image. Download the firmware image to a local host or to an internal software distribution site.

Connect to the console port on the device from your management device and log in to the Junos OS CLI. Copy the firmware package to the deviceto the /var/tmp/ directory. Install the new package on the device:

user@device> request vmhost software add /var/tmp/package.tgz.

NOTE: If you need to terminate the installation, do not reboot your device; instead, finish the installation and then issue the request system software delete *package*.tgz command, where *package*.tgz is, for example, jinstall-ppc-18.3R1.S1-4-signed.tgz. This is your last chance to stop the installation.

Reboot the device to load the installation and start the new firmware image: user@device> request vmhost reboot

After the reboot has completed, log in and use the show version command to verify that the new version of the firmware is successfully installed.

Also install the fips-mode package and jpfe-fips package needed for Routing Engine KATS and for Line card KATS. The following are the commands used for installing these packages:

user@device >request system software add optional://fips-mode.tgz

user@device >request system software add optional://jpfe-fips.tgz

6.1.2 Enabling FIPS-Approved Mode of Operation

The cryptographic officer is responsible for initializing the module in a FIPS-Approved mode of operation. The FIPS-Approved mode of operation is not automatically enabled. The cryptographic officer shall place the module in the FIPS-Approved mode by first zeroizing the device to delete all keys and CSPs. The zeroizing instructions are in section 1.4 of this document. Next, the cryptographic officer



shall follow the steps found in the *Junos OS FIPS Evaluated Configuration Guide for MX240, MX480, MX960, EX9204, EX9208, and EX9214 Devices, Release 18.3R1* document Chapters 3 & 7 to place the module into a FIPS-Approved mode of operation. The steps from the aforementioned document are repeated below:

The FIPS Modes are not automatically enabled once the firmware image is installed on the platform. These steps are for putting the module into the FIPS Standard Mode, FIPS Reduced Throughput Mode or FIPS Recovery Mode.

The FIPS Standard Mode will be selected automatically if all power-on self-tests pass successfully during the reboot after committing the module to FIPS mode. The FIPS Reduced Throughput Mode of operation is selected automatically if all the RE KATs pass and at least one MPC7E-10G or EX9200-40XS in the module chassis passes all of its self-tests, and at least one MPC7E-10G or EX9200-40XS in the module chassis fails one or more of its self-tests. The FIPS Recovery Mode is selected automatically if all the RE KATs pass and if each of the MPC7E-10 or EX9200-40XS line cards fails one or more of its KATs. To enable FIPS mode in Junos OS on the device:

- 1. Zeroize the device as explained in Section 1.3.
- Login to the device with crypto-officer credentials and enter configuration mode: crypto-officer@device> edit Entering configuration mode [edit] crypto-officer@device#
- 3. Enable FIPS mode on the device by setting the FIPS level to 1, and verify the level:

[edit] crypto-officer@device # set system fips chassis level 1

[edit] crypto-officer@device # show system fips chassis level 1;

4. Commit the configuration

[edit]
crypto-officer@device# commit
configuration check succeeds
 Generating RSA key /etc/ssh/fips_ssh_host_key
 Generating RSA2 key /etc/ssh/fips_ssh_host_rsa_key
 Generating ECDSA key /etc/ssh/fips_ssh_host_ecdsa_key
 [edit]
 'system'
 reboot is required to transition to FIPS level 1



commit complete

5. Reboot the device:

[edit] crypto-officer@device# **run request vmhost reboot** Reboot the system ? [yes,no] (no) **yes**

During the reboot, the device runs Known Answer Tests (KATS). It returns a login prompt:

crypto-officer@device:fips>

6. After the reboot has completed, log in and use the show version I command to verify the firmware version is the validated version.

crypto-officer@device:fips> show version

- 7. Configure local login authentication for FIPS users.
- 8. Configure the console port to log out automatically when you unplug the cable and

require the root password for single-user mode (Junos OS in FIPS mode automatically logs out of your user account when you disconnect because the log-out-on-disconnect configuration statement is enabled by default).

9. Configure FIPS logging to record events.

6.1.3 Placing the Module in a Non-Approved Mode of Operation

As cryptographic officer, the operator needs to disable the FIPS-Approved mode of operation on the device to return it to a non-Approved mode of operation. To disable FIPS-Approved mode on the device, the Router/Switch must be zeroized. Follow the steps found in section 1.3 to zeroize the Router/Switch.

6.2 User Guidance

The user should verify that the module is operating in the desired mode of operation (FIPS-Approved mode or non-Approved mode) by observing the command prompt when logged into the device. If the string ":fips" is present, then the switch is operating in a FIPS-Approved mode. Otherwise it is operating in a non-Approved mode.

All FIPS users, including the Crypto Officer, must observe security guidelines at all times.

All FIPS users must:



- Keep all passwords confidential.
- Store devicess and documentation in a secure area.
- Deploy devicees in secure areas.
- Check audit files periodically.
- Conform to all other FIPS 140-2 security rules.
- Follow these guidelines:
 - Users are trusted.
 - Users abide by all security guidelines.
 - Users do not deliberately compromise security.
 - Users behave responsibly at all times.



7 References and Definitions

The following standards are referred to in this Security Policy.

Table 20 - References

Abbreviation	Full Specification Name
[FIPS140-2]	Security Requirements for Cryptographic Modules, May 25, 2001
[SP800-131A]	Transitions: Recommendation for Transitioning the Use of Cryptographic Algorithms and Key Lengths, January 2011
[IG]	Implementation Guidance for FIPS PUB 140-2 and the Cryptographic Module Validation Program

Table 21 - Acronyms and Definitions

Acronym	Definition	
AES	Advanced Encryption Standard	
САК	Connectivity Association Key	
CKN	Connectivity Association Key Name	
DH	Diffie-Hellman	
DSA	Digital Signature Algorithm	
ECDH	Elliptic Curve Diffie-Hellman	
ECDSA	Elliptic Curve Digital Signature Algorithm	
EMC	Electromagnetic Compatibility	
FIPS	Federal Information Processing Standard	
HMAC	Keyed-Hash Message Authentication Code	
ICV	Integrity Check Value (i.e. Tag)	
ICK	Integrity Check Key	
KEK	Key Encrypting Key	
MACsec	Media Access Control Security	
MD5	Message Digest 5	
RE	Routing Engine	
RSA	Public-key encryption technology developed by RSA Data Security, Inc.	
SCB	Switch Control Board	
SHA	Secure Hash Algorithms	
SSH	Secure Shell	
Triple-DES	Triple - Data Encryption Standard	



Table 22 - Datasheets

Model	Title	URL
MX240	MX240, MX480, MX960 3D	https://www.juniper.net/assets/us/en/local/pdf/datashee
MX480	Universal Edge Routers	<u>ts/1000597-en.pdf</u>
MX960		
EX9204	EX9200 Ethernet Switch	https://www.juniper.net/us/en/local/pdf/datasheets/100
EX9204		<u>0432-en.pdf</u>
EX9214		