T. Suyemoto

## FE BRUARY 1975

Project No. 572D
Prepared by

Approved for public release; distribution unlimited.

THE MITRE CORPORATION
Bedford, Massachusetts

> When U.S. Government drawings, specifications, or other data are used for any purpose other than a definitely related government procurement operation, the government thereby incurs no responsibifity nor any obligation whatsoever; and the fact that the government may have formu. lated, furnished, or in any way supplied the said drawings, specifications, or other data is not to be regarded by implication or otherwise, as in any manner licensing the holder or any other person or corporation, or conveying any rights or permission to manufacture, use, or sell any patented invention that may in any way be related thereto.

```
Do not return this copy. Retain or destroy
```

REVIEW AND APPROVAL

This technical report has been reviewed and is approved for publication.


FOR THE COMMANDER


ROBERTJ. LATINA, Colonel, USAF
Directol lof ADPE Selection
Deputy for Command and Management Systems

UNCLASSIFIED
SECURITY CLASSIFICATION of This PAGE (Whon Deta Entorod)

|  | REPORT DOCUMENTATION PAGE | READ INSTRUCTIONS BEFORE COMPLETING FORM |
| :---: | :---: | :---: |
|  | REPORT NUMBER 2. GOVT ACCESSION NO. <br> ESD-TR-74-314  | 3. recipient's catalog number |
| 4. TITLE (and Subetite) <br> REMOTE-TERMINAL EMULATOR (DESIGN <br> VERIFICATION MODEL) - USER'S MANUAL |  | 5. TYPE OF REPORT A PERIOD COVERED <br> 6. PERFORMING ORG. REPORT NUMBER <br> MTR- 2677 , Vol. 10 |
|  | AUTHOR(s) T. Suyemoto | 8. CONTRACT OR GRANT NUMBER(s) F19628-75-C-0001 |
|  | PERFORMING ORGANIZATION NAME AND ADDRESS <br> The MITRE Corporation <br> Box 208 <br> Bedford, MA 01730 | 10. PROGAM ELEMENT. PROJECT. TASK ARA A WORKUETT NUMEESS Project No. 572 D |
| 11. Controlling office name and adoress <br> Deputy for Command and Management Systems <br> Electronic Systems Division, AFSC <br> Hanscom Air Force Base, Bedford, MA 01731 |  | 12. REPORT DATE February 1975 |
|  |  | 13. NUMBER OF PAGES <br> 215 |
|  | 4. Monitoring agency name a Aodress(if different from Confroffing office) | $\begin{aligned} & \text { 15. SECURITY CLAS5. (of this report) } \\ & \text { UNCLASSIFIED } \end{aligned}$ |
|  |  | 15a. declassification downgrading |
| 16 DISTRIBUTION STATEMENT (Ol this Reporf) <br> Approved for public release; distribution unlimited. |  |  |
| 17. DISTRIBUTION STATEMENT (of the abstract ontered in Bloch 20, 11 dillerent (trom Roport) |  |  |
| 18. SUPPLEMENTARY NOTES |  |  |
| 19. KEY WORDS (Continue on reverse side if necessary and idently by block number) <br> DESIGN VERIFICATION MODEL <br> REMOTE-TERMINAL EMULATOR |  |  |
| 20. ABSTRACT (Conitnue on reverse side ff necesaary and identify by block number) <br> The Remote-Terminal Emulator is a minicomputer-based system which generates message traffic for use in testing and evaluating large-scale, multi-terminal computer systems. This series of reports will describe the two Design Verification Models that were developed on Data General NOVA 800 minicomputers. This volume is a user's manual which contains the information necessary to prepare and run the software portions of the Remote-Terminal Emulator. |  |  |

## PREFACE

The Remote-Terminal Emulator is a minicomputer-based system which generates message traffic for use in testing and evaluating large-scale, on-line computer systems. In real-time testing, it emulates the actions of a collection of operators, terminals, and, depending upon configuration, modems. In 1972 and early 1973, two Design Verification Models (DVM) of the emulator were developed by The MITRE Corporation under the sponsorship of the Air Force Directorate of Automatic Data Processing Equipment Selection (MCS). The fixed-site system, which is used primarily for program and scenario development, is located at MITRE/Bedford and interfaces with the computer system under test (SUT) through the switched telephone network. The on-site system, which is used primarily for detailed emulator test and evaluation, is representative of the equipment planned for operational use in future computer procurements. This system, which is moved to each SUT site, interfaces through cables directly with the SUT's communication line adapters.

The primary hardware components of each of these systems are a Data General NOVA 800 minicomputer, a fixed-head disk, a magnetic tape unit, a control teletype, and an appropriate emulator/SUT interface unit. Both DVM's have sufficient hardware to emulate up to 16 lowspeed interactive terminals. The on-site DVM also has hardware to emulate eight additional terminals or terminal networks by the use of high-speed synchronous line adapters and associated circuitry. The primary sof tware components that have been developed for this project consist of the Macro Preprocessor, the Scenario Assembler, the RealTime Executive, the Scenario Interpreter and the Data Reduction Program.

The common denominator of remote-terminal emulation is the scenario, which is a program that controls the actions to be taken by the emulator in emulating a given device and mix of devices. The scenario defines the queries (system commands, input data, and control characters) to be sent to the SUT, how SUT responses are to be processed, and other details of the test to be conducted. The Macro Preprocessor is a general purpose support program that provides a basic macro capability to aid in scenario writing and which was also used in emulator program development. In the scenario development process, the Scenario Assembler is used to convert external (symbolic) scenarios to internal (absolute) scenarios which are tailored to a specific terminal type and to specific data communications control procedures. Both the Macro Preprocessor and the Scenario Assembler run under the Data General Disk Operating System (DOS). In real-time testing, internal scenarios are brought into core from disk and are processed by the Scenario Interpreter which runs under the Real-Time Executive. All messages sent to and received from the SUT, as well as messages describing other actions of the emulator, can be time-tagged and logged on magnetic tape. Upon completion of the test, these data are processed in various fashions by the Data Reduction program (which also runs under DOS) to produce scenario trace data and various statistics on the performance and utilization of both the emulator and the SUT.

This document is part of a series of reports which will describe the design, implementation and use of the two Design Verification Models. The titles of the reports in the series are as follows:

Volume
1

2
3
4

## Title

Introduction and Summary
Scenarios and Data Structures
Macro Preprocessor
Scenario Assembler

Volume
5
6
7
8
9
10

## Title

Scenario Interpreter
Real-Time Executive
Data Reduction Program
Hardware
Support Software
User's Manual

It is suggested that the reader become familiar with the emulator concepts and terminology presented in Volume 1 preparatory to reading other volumes in the series.

## TABLE OF CONTENTS

Page
LIST OF ILLUSTRATIONS ..... 9
LIST OF TABLES ..... 11
SECTION I INTRODUCTION ..... 12
SECTION II DOS AND SUPPORT SOFTWARE ..... 14
44 DOS ..... 14
Loading DOS ..... 14
Executing Under DOS ..... 15
SUPPORT SOFTWARE ..... 16
Utilities ..... 16
File Management ..... 18
Programming Aids ..... 19
SECTION III MACRO PROCESSOR ..... 20
INTRODUCTION ..... 20
PREPARATION AND USE OF MACROS ..... 20
Macro Names ..... 20
Macro Body ..... 21
Macro Definition ..... 21
Macro Call ..... 21
Parameter Substitution ..... 22
Label Generation ..... 23
Character Set ..... 25
Features ..... 25
Special Characters ..... 25
Quotes ..... 25
Master Macro Directory ..... 26
Notes and Restrictions ..... 27
SYSTEM FLOW ..... 28

## TABLE OF CONTENTS (Continued)

Page
SECTION III (Cont. OPERATING PROCEDURES ..... 28
SSUB ..... 28
Input File ..... 31
Output File ..... 31
MACDEF ..... 31
Input File ..... 32
Output File ..... 32
Output Messages ..... 32
SECTION IV SCENARIO ASSEMBLEKK ..... 36
INTRODUCTION ..... 36
SYSTEM FLOW ..... 36
OPERATING PROCEDURES ..... 38
Preparing Files ..... 38
External Scenario ..... 38
Program Files ..... 40
Executing Assembler ..... 40
OUTPUT ..... 45
Internal Scenario ..... 45
Optional Listings ..... 46
Output Messages ..... 48
SECTION V EQUIPMENT TABLE ..... 50
INTRODUCTION ..... 50
GENERATION ..... 50
REQUIREMENTS AND CONVENTIONS ..... 51
FUNCTION ..... 74
SECTION VI
REAL-TIME EMULATOR SYSTEM GENERATION ..... 80
INTRODUCTION ..... 80
SSUB ..... 80

## TABLE OF CONTENTS (Continued)

Page
SECTION VI (Cont.) ASM ..... 81
RLDR ..... 83
MKABS ..... 83
Disk Requirements ..... 85
SECTION VII REAL-TIME EMULATOR ..... 87
INTRODUCTION ..... 87
SYSTEM FLOW ..... 87
OPERATING INSTRUCTIONS ..... 87
Startup ..... 89
Control TTY Inputs ..... 90
Run ID ..... 90
Commands ..... 90
CANCEL Input ..... 90
BREAK Output ..... 91
Responses ..... 91
Shutdown ..... 91
ERROR MESSAGES ..... 92
DEVICE STATUS ..... 99
RING COUNTERS ..... 101
RESPONSE HANDLING AND LOGGING ..... 104
DIGITAL I/O ..... 106
STORAGE REQUIREMENTS ..... 113
MISCELLANEOUS NOTES ..... 116
PANIC CODES AND ACTIONS ..... 117
SECTION VIII DATA REDUCTION PROGRAM ..... 123
INTRODUCTION ..... 123
SYSTEM FLOW ..... 123
OPERATING PROCEDURES ..... 125
Input Message ..... 125

TABLE OF CONTENTS (Continued)
Page
SECTION VIII (Cont.) Command Interpreter ..... 126
Interactive Mode ..... 126
Switch Mode ..... 128
Summaries ..... 131
Brief Summary ..... 131
Detailed Summary ..... 135
Listings ..... 136
Octal Tape ..... 136
Actual Times ..... 137
Time Intervals ..... 137
Relative Times ..... 138
ERRORS ..... 138
SAVING TEST DATA ..... 140
Program Description ..... 140
Input Message ..... 141
Operation ..... 141
Errors ..... 143
SECTION IX EXECUTION TIMES ..... 145
REAL-TIME INSTRUCTIONS ..... 145
NON-REAL TIME PROGRAMS ..... 159
SSUB ..... 159
MACDEF ..... 160
CVT ..... 160
DATAR ..... 161
MASTR ..... 161
REFERENCES ..... 163
APPENDIX I CONVERSION CODES FOR IBM 2741 ..... 164
APPENDIX II
SAMPLE LISTINGS FROM SCENARIO ASSEMBLER ..... 168

TABLE OF CONTENTS (Concluded)Page
APPENDIX III LISTING OF EQUIP. RB ..... 174
APPENDIX IV DATAR LISTINGS ..... 188
APPENDIX V EXAMPLE OF TELETYPE LISTING FOR AN EMULATION RUN ..... 196APPENDIX VI TIMING SAMPLES FOR NON-REAL TIMEPROGRAMS198

LIST OF ILLUSTRATIONS
Figure Number Page
1 SSUB System Flow ..... 29
2 MACDEF System Flow ..... 30
3 System Flow of the Scenario Assembler ..... 37
4 External Scenario Format ..... 39
5 Equipment Table Macros ..... 526 File EQ of Equipment Table (Macros notExpanded)56State Transition Diagram8816Ring Counter Changes100
17
Digital I/O Connections102
18
Normal Interface Rack Wiring for Asynchronous Devices ..... 110
Normal Asynchronous Correspondence ..... 112
19Macro Definitions for Digital I/O112
21
HANDSHAKE Scenario ..... 112

LIST OF ILLUSTRATIONS (Conc1uded)
Figure Number ..... Page
22 Example of Panic Message ..... 122
23 General System Flow of Data Reduction Program ..... 124
24
Interactive Tree Diagram for DATAR ..... 129
25 Switch Tree Diagram for DATAR ..... 1322627
Brief Summary Output Format ..... 189
Detailed Summary Output Format ..... 190
Histogram Output Format ..... 191
Octal Tape Output Format ..... 192
Actual Time Output Format ..... 193
Time Interval Output Format ..... 194
Relative Time Output Format ..... 195
Fortran Cost Scenario with Macros not Expanded ..... 199
Scenarios for Fortran Cost Problem with Macros Expanded ..... 201
Macro Libraries for Fortran Cost
Problem ..... 212
Table Number Page
I Common Utility Programs ..... 17II
Common File Management Commands ..... 18
III Output Messages for Macro Processor ..... 33
Available Codes for Conversion ..... 42
Available Codes for SOM/EOM ..... 44
Output Messages for Scenario Assembler ..... 49
VII Input File Names for Emulator System ..... 82
VIII Inputs to Relocatable Loader ..... 84
IX Disk Requirements for Emulator System ..... 86X
Error Message Classes for Scenario Interpreter ..... 93
Error Messages for Scenario Interpreter ..... 94
Core Storage Requirements for Scenario Interpreter ..... 114
Core Storage Requirements for Real-Time Exec ..... 115
RTOS Panic Codes ..... 119

Interactive Requests and Responses for

Interactive Requests and Responses for

Interactive Requests and Responses for  DATAR  DATAR  DATAR .....  ..... 127 .....  ..... 127 .....  ..... 127
XVI
XVI
XVI Option and Suboption Switches for DATAR Option and Suboption Switches for DATAR Option and Suboption Switches for DATAR ..... 130 ..... 130 ..... 130
XVII
XVII Record Type Switches Record Type Switches Record Type Switches ..... 133 ..... 133 ..... 133
XV
XV
XVXVIIIXIXXXXXISwitch Combinations and Valid Inputs133
DATAR Error Message File (ERFILE) ..... 139
X MASTR Error Message File ..... 144
Real-Time Scenario Instruction Execution Times ..... 146
XXII Control Characters for IBM 2741 Terminal ..... 165
XXIII Conversion Code Table used for IBM 2741 Terminal ..... 166

## SECTION I

## INTRODUCTION

The Remote-Terminal Emulator consists of a combination of hardware components and software packages designed to generate message traffic for use in testing and evaluating on-line computer systems. The hardware configurations for both the fixed-site and on-site systems are discussed in Volume 8 of this series. This user's manual presents the user information necessary to prepare and run the software portions of the system. Included here are excerpts from previous volumes as we11 as additional material required for running the Remote-Terminal Emulator.

The common denominator of remote-terminal emulation is the scenario, which is a program that controls the actions to be taken by the emulator in emulating a given device or mix of devices. A scenario is formed by a series of scenario instructions which determine the queries to be sent to a SUT, how responses are to be handled, and the various control functions of a test. The command is a special instruction which exerts gross control over emulator actions, and is the only means by which the user can exert external control during an emulation run. Both instructions and commands are described in detail in Sections IV and V of Volume 2 of this series.

This paper is organized as a logical presentation of steps needed for preparation, execution, and data reduction of an emulator run. Section II describes both the NOVA Disk Operating System (DOS) as it applies to the Emulator, as well as the system support software which may be applicable in most phases of emulation. The macro processing function is described in Section III and the assembly function is presented in Section IV. These two functions prepare the scenario for the real-time run. Sections V and VI respectively deal with
preparing the Equipment Table, and following this, building an emulator system. The operating instructions and other information necessary for execution of a real-time emulation run are presented in Section VII. The final phase of an emulator run, data reduction, is discussed in Section VIII. An example of the on-line teletype output for all processing steps for a single emulation run is given in Appendix $V$. Section IX contains timing information for both the real-time and non-real time functions of the emulator.

## SECTION II

DOS AND SUPPORT SOFTWARE

DOS

All of the non-real time programs included in the Emulator system run under Revision 5 of Data General's Disk Operating System (DOS). The support software described in Volume 9 of this series also operates under control of DOS. A complete description of DOS can be found in Reference 1. Under DOS a carriage return and a line feed are echoed back when the RETURN key is depressed. In this document the symbol $\mathcal{L}$ is used to denote the depression of the RETURN key and the echo back of both the carriage return and line feed.

Loading DOS
The DOS system can be loaded into core from tape, or, if it already exists on disk, it can be loaded from there. To load from tape, the following sequence should be performed:
(1) Turn on CPU, disk, tape drive, and system teletype;
(2) Mount the system tape; press LOAD to advance tape to ready position;
(3) Set panel data switches to 100022;
(4) Raise the RESET panel switch and then raise the PROGRAM LOAD panel switch;
(5) The remainder of the process involves the following activity on the system teletype. The underlined portion is what is to be entered by the user. The non-underlined portion is the response of the system.

```
FULL(\emptyset) OR PARTIAL(1)? \emptyset
R
XFER MT\emptyset:1 SYS.SV 
CHATR SYS.SV SP
R
INSTALL SYS.SV
R
LOAD/A MT\emptyset:2
FILE ALREADY EXISTS, FILE: SYS.DR
FILE ALREADY EXISTS, FILE: MAP.DR
R
```

To load DOS from disk the following sequence should be performed:
(1) Turn on CPU, disk, and TTY;
(2) Set panel data switches to 100020 ;
(3) Raise the RESET panel switch and then raise the PROGRAM LOAD panel switch;
(4) The system will respond as follows:

DOS REV $\emptyset 5$
Press the continue panel switch and DOS responds:

- R

There is not enough disk space on the present NOVA to accommodate the complete Disk Operating System plus the emulator system. Therefore, to delete from disk all DOS files which are not essential to preparing or executing an emulator run, the following command line should be typed directly after loading DOS.
@REMAL@レ
This frees space on the disk to allow for the emulator system and scenarios, which can then be loaded.

## Executing Under DOS

Programs which operate under control of DOS are executed in response to a user input request entered at the system teletype. The
input message is called a command line and is processed by an executable program called the Command Line Interpreter (CLI). The CLI indicates to the user that it is ready to accept commands by typing the ready message, $R \mathcal{L}$. The user enters a command by typing a line and depressing the RETURN key. When execution of a program running under DOS is completed, control is returned to the CLI.

When operating under DOS, depressing CTRL and A simultaneously on the system teletype causes an immediate interrupt to the executing program, regardless of the program status. This can be useful, for instance, to discontinue a run when errors have been detected. The word INT is typed by the CLI upon recognition of the CTRL-A break, and control is returned to the CLI which then types $R$.

## SUPPORT SOFTWARE

All support sof tware programs operate under control of DOS. They are described in detail in Volume 9 of this series. A brief presentation of operating instructions for the most commonly needed functions is given here. This section does not include all available programs.

## Utilities

The utilities transfer data from one DOS file to another. Note that all peripheral devices are treated as files. Table I below shows some methods for moving data. Where appropriate, filenames for peripherals may be used for input or output files to the utility programs. These names include:
\$CDR card reader input
\$TTI teletype keyboard input
\$TTO teletype printer output
\$LPT line printer output

Table I

Comon Utility Programs


The switch /A on the XFER command causes the data to be input from the card reader ( $\$ C D R$ ) as ASCII data with a carriage return inserted at the end of the text on a card to denote an end of line. Without the switch the input is transferred sequentially without alteration. The LXFER program is MITRE generated and provides the capability to convert Hollerith data to ASCII (the code of the NOVA), including control characters and lower case. It also permits entry of any 8-bit value via card input. A description of the program is given in Volume 9.

Both the LOAD and DUMP commands have an additional option, /V, which causes the names of the files to be verified on the teletype. Also in these commands MT $\emptyset$ signifies transport $\emptyset$ of the tape drive, and $x$ designates which file on the tape is selected. The brackets indicate optional information; if no filename is specified, all nonpermanent files are moved. The PRINT program lists the designated file(s) on the line printer without either a title or line numbers,
and truncates a line after 80 characters. The PRINTL program, however, lists the file(s) with both a title and line numbers, and prints lines longer than 76 characters on successive lines without associating a new line number.

File Management
Several DOS programs may be useful in handling files containing scenarios or libraries. Table II shows some of the more common commands.

Table II
Common File Management Commands

| Operation | CLI Input Message |
| :---: | :---: |
| Delete file(s) from directory <br> and free space <br> Change filename <br> Concatenate copies of files <br> to produce a new file | DELETE filenamel ... |
| List number of disk blocks in <br> use and number available <br> List names, byte count, and <br> attributes of files in <br> directory | DISK |

The specific command DELETE*.* deletes from disk all files which are not permanent. The LIST command with no parameters causes a listing of the byte count for each file on the teletype. In the option /L is used, the listing is printed on the line printer. If the option /A is used, all permanent files are also listed. If specific files are designated, only those specified are listed.

## Programming Aids

The two programs most of ten employed by an Emulator user are the EDIT and OEDIT (octal edit) programs. The EDIT program is used to build a new source file or edit an existing one. This program is described in full in Reference 2. The octal editor is used to examine and/or modify, in octal, any location in any disk file. A complete description of this program can be found in Reference 1.

# SECTION III 

MACRO PROCESSOR

## INTRODUCTION

The basic function of a macro processor is text substitution, where a name appearing in the source code is replaced by an associated string of characters. A general purpose macro capability, including a macro library generator (MACDEF) and a macro processor (SSUB), was developed on the NOVA 800. One of the main purposes of this software is to facilitate scenario writing by (1) providing a one-to-many statement capability and (2) allowing for substitution of parameter values at the external scenario level. This permits the scenario writer to include common pieces of code in different scenarios and to change subscenario calls to in-line code, or vice-versa. Another use for the macro capability is in writing code in NOVA Assembly language, which is the means used for generating an emulator Equipment Table.

Macros may be created and saved separately in a macro library by using the MACDEF program; or they may be defined in the source file itself during execution of SSUB. Both MACDEF and SSUB are written in Extended ALGOL and operate in 24 K core under control of DOS. A description of the design and implementation of the Macro Processor can be found in Volume 3 of this series of reports.

PREPARATION AND USE OF MACROS
The discussion of macros presented here applies to all macros whether they are defined in a library, or directly in the source code. Macro Names

Macro names are identifiers consisting of ten or less alphanumeric characters.

## Macro Body

In its simplest form a macro body consists of a string of ASCII characters to replace every occurrence of the macro name in the source data. No extra spaces are inserted.

## Macro Definition

A macro definition associates an identifier (the macro name) with a string of text (the macro body). Format for a macro definition is as follows:

```
                                    MDEF macroname (number of arguments)
                                    macro body
                                    .
                                    MEND
```

The literals MDEF and MEND are left-adjusted on separate lines (or cards). The macro body consists of all characters beginning with the next line after MDEF up to, but not including, the carriage return before the MEND. If the macro has no arguments, the initial line may be terminated after the macro name.

Macro Call
A macro call is any reference to a macro name in the source file. Formats for a call are:
macroname (arg l,arg 2...) if the macro has arguments.
macroname if there are no arguments.
Arguments are separated by commas and enclosed in parentheses.

| Example 1: |  |  |
| :--- | :--- | :--- |
| Simple Substitution |  |  |
| Source Data: ALGOL Program |  |  |
| Macro Definition | Source Code | Output Code |
|  |  |  |
| MDEF DIGIT | IF DIGIT | IF ((CHAR>=60R8) AND |
| $($ (CHAR>=60R8) AND | THEN GO TO | (CHAR<=71R8)) |
| (CHAR<=71R8)) | EXIT; | THEN GO TO |
| MEND |  | EXIT; |
|  |  |  |

## Parameter Substitution

Macro bodies may contain formal parameters which will be replaced by actual parameters (arguments) in a macro call. Up to 9 formal parameters can be used in a macro definition. Each formal parameter is specified by a \$ (dollar sign) followed by a digit $n$ where $0<n<10$. When the macro name and $i t s$ arguments are encountered by SSUB in the source code, the first positional argument will be substituted for the formal parameter $\$ 1$; the second, for $\$ 2$, etc. Formal parameters may be passed as macro arguments.

| Example 2: Use of Parameters |  |  |
| :--- | :--- | :--- |
| Source Data: NOVA Assembly |  |  |
| Macro Definition | Source Code | Output Code |
| MDEF LDI (2) | LDI (3,50) | JMP .+2;MLDI (R3,50) |
| JMP.+2;MLDI (R\$1,\$2) |  | 50 |
| \$2 |  | LDA 3,.-1 |
| LDA \$1,.-1 |  |  |
| MEND |  |  |

Macro calls may be nested within arguments and within macro bodies.

| Example 3: Nested Macro Call in Macro Argument |  |  |
| :---: | :---: | :---: |
| Source Language: NOVA Assembly Language |  |  |
| Macro Definitions | Source Code | Output Code |
| ```MDEF LDI (2) JMP .+2 $2 LDA $1,.-1 MEND MDEF DEC (1) .RDX 10 $1 .RDX } MEND``` | LDI (3, DEC (50)) | $\begin{aligned} & \text { JMP . +2 } \\ & . \text { RDX } 10 \\ & 50 \\ & \text {. RDX } 8 \\ & \text { LDA } 3, .-1 \end{aligned}$ |

Label Generation (The TAIL Function)
To insure that labels appearing within macro bodies will not be multiply defined, a special function $\$ T$ is provided. Each reference to $\$ T$ is replaced by a numeric value. This value is unique for each macro call, but remains constant for all \$T references within a macro body. \$T may be passed one level as a macro argument.

| Example 4: Use of \$T Function |  |  |
| :---: | :---: | :---: |
| Source Data: | Scenario Assembly Code for Login Sequence |  |
| Macro Definitions | Source Code | Output Code |
| MDEF FINDLIT (1) L FL\$T R $\quad$ '' S FL\$T \$1 MEND | ALLOCREGS 10 <br> FINDLIT (6000) <br> QCESDM002 <br> FINDLIT (PASSWORD) <br> QXXXX <br> FINDLIT (SYSTEM?) | ALLOGREGS 10 <br> L FL3 <br> R ' ' <br> S FL3 6000 <br> QCESDM002 <br> L FL4 <br> R ' ' |


| Example 4: Use of \$T Function (Concluded) |  |  |
| :--- | :---: | :---: |
| Source Data: $\quad$ Scenario Assembly Code for Login Sequence |  |  |
| Macro Definitions | Source Code | Output Code |
|  |  | S FL4 PASSWORD |
|  |  | QXXXX |
|  |  | L FL5 |
|  |  | R '' |
|  |  | S FL5 SYSTEM? |


| Example 5: Nested Macro Calls in Macro Body |  |  |
| :---: | :---: | :---: |
| Source Data: S | embly Code |  |
| Macro Definitions | Source Code | Output Code |
| MDEF FINDLIT <br> (1) <br> L FL\$T <br> R '' <br> S FL\$T \$1 <br> MEND <br> MDEF BACKUP <br> QB <br> REDY <br> MEND <br> MDEF REDY <br> FINDLIT (READY) <br> MEND <br> MDEF LIST <br> **PRINT FILE** <br> BACKUP <br> REDY <br> QPRINT;* <br> EOF <br> MEND <br> MDEF EOF <br> FINDLIT (FILE) <br> MEND | LIST | **PRINT FILE** <br> QB <br> L FL40 <br> R ' ' <br> S FL40 READY <br> QPRINT;* <br> L FL42 <br> R '' <br> S FL42 FILE |

## Character Set

Source input to both SSUB and MACDEF normally consists of ASCII characters. The results of using non-ASCII characters are not defined, although in the current version most values are processed correctly. Two known exceptions are the eight-bit values 0 and 1 , which are used internally by SSUB and MACDEF and should never be included in source code for either program.

## Features

## Special Characters

\$ The dollar sign is used for three special
functions performed by SSUB. It is illegal
to use it otherwise in normal source data,
other than in a quote string.
\$T specifies the TAll function. \$Q
specifies the quote function. \$digit
is used to specify formal parameters.
A single quote delimits a string not
to be scanned by SSUB. The string is
passed with quotes.
() Parentheses are used to enclose arguments
in a macro call. Parentheses may appear
elsewhere in source data.
Commas are used to separate macro arguments.
They may also appear elsewhere in source

## Quotes

When a string of characters is enclosed in single quotes, it is passed on (including quotes) without being scanned.
\$Q is a special macro function which can be used to pass a string of characters including commas, leading blanks, etc., in macro arguments. \$Q is followed by a string delimited at the beginning and end by a character selected by the user. Delimiter characters may be any ASCII characters except those listed above in the special group and the space character. The expansion of $\$ Q$ is the string without delimiters. The string itself will be scanned when it is substituted for its corresponding formal parameters.

| Example 6: \$Q Function |  |  |
| :--- | :---: | :---: |
| Source Data: Scenario Assembly Code |  |  |
| Macro Definition | Source Code | Output Code |
| MDEF INSTR (1) | INSTR (\$Q*LDA 3,A*) | LDA 3,A |
| \$1 |  |  |
| MEND |  |  |

Master Macro Directory
As part of its initialization, SSUB creates a master directory which is effectively the sequential concatenation of all library directories in left-to-right order as they appear in the DOS command line. Later, if more definitions are encountered in the source file, they are added to the master directory. During an SSUB run names are never deleted, and no name duplication check is made. The directory is ordered so that if duplicate macro names occur, the text of the macro most recently added to the directory will be used.

## Notes and Restrictions

1. Single quote strings are limited to 1000 characters.
2. In an SSUB run the total of all macros in the libraries and all macros defined during the run itself cannot exceed 160.
3. Each macro library is limited to 100 macros.
4. $\$ Q$ is legal only in macro arguments.
5. The identifiers MDEF and MEND are reserved and cannot be used as macro names, or appear in any source data except in their normal use in macro definitions.
6. The file name TSUB.MB is reserved.
7. The system error message "stack overflow" usually indicates a recursion loop in macro substitution. Example:
```
MDEF OR
COM 1, 1
    AND 1, 2 ; PERFORMS LOGICAL OR
MEND
```

When the macro $O R$ is called, infinite recursion will occur because of the " $O R$ " in the comment within the macro body.
9. If an unsuccessful MACDEF run has been made, the .ML file should be deleted before MACDEF is rerun with the same name. Otherwise a new file is not created and the new information is written over the old information. If
this occurs, and if the new file is to be smaller than the old file, whatever has not been overwritten will remain at the end of the file.

## SYSTEM FLOW

Overall system flows for $\operatorname{SSUB}$ and MACDEF are shown in Figure 1 and Figure 2, respectively. Operations taking place on the NOVA are listed at the bottom of the figures with the required DOS commands.

OPERATING PROCEDURES
SSUB

SSUB is the actual macro processing program; it performs the macro substitutions. Input to SSUB consists of a source file and up to four macro libraries. SSUB copies the source file into an output file. While copying, it scans the source data for macro definitions and references to macro names (macro calls). When a macro name is detected, the text of the specified macro is copied into the output file replacing the macro name. Macros may have arguments which modify the text of the macro as it is copied. For SSUB, modification consists simply of replacing formal parameter references contained in the macro body by actual parameters supplied as arguments.

To use the SSUB program the following steps should be performed:

1. Load the SSUB save file.
2. Create or load the source file.
3. Load any macro library files to be used.
4. Ready the line printer.
5. Enter the following command at the teletype:

SSUB input-file output-file library-names)


1. LOAD LIBRARY FILE
LOAD MTO: X lib1.ML lib2.ML)
2. LOAD PROGRAM FILE
LOAD MTO: X SSUB.SV,
3. LOAD INPUT FILE
XFER/A SCDR INPUTFILE
4. EXECUTE SSUB
SSUB INPUTFILE OUTPUTFILE libi lib2,

Figure 1. SSUB SYSTEM FLOW


Figure 2. MACDEF SYSTEM FLOW

Do not include the .ML after library names. Up to four names may be specified. All libraries must have been processed previously by MACDEF. Error codes will be printed on the line printer. An R L typed out by the CLI indicates that the program is completed.

## Input File

The input file contains source data containing macro calls and optionally macro definitions. It should be a normal ASCII file with a legal DOS name. Read-protect attribute must be off.

Output File
File must be new, with a legal DOS file name.

## MACDEF

MACDEF is a separate program used to generate macro libraries for later use in $S S U B$ runs. Input to the program is a file containing definitions of commonly used macros. MACDEF produces a file consisting of a library directory and the texts of all macro bodies in the library. This library file is generally saved on magnetic tape by the user for later use with the macro preprocessor program.

To use the MACDEF program the following steps should be performed:

1. Load the MACDEF save file.
2. Create a new file containing the definitions for all macros to be included in the library. The name given to this file is used to form the macro library name.
3. Ready the line printer.
4. Enter the following command at the teletype: MACDEF library-name $L$

The names of all defined macros and any error message codes will be printed on the line printer. An $R \mathcal{L}$ typed out by the CLI indicates that the program is finished.
5. To save the library on tape, dump the file created by MACDEF. This file is named "library-name.ML".
6. If any errors are detected by MACDEF, the original file should be corrected, the .ML file deleted, and the program rerun.

## Input File

The input file consists of up to 100 macro definitions. Extra cards should not be placed between macro definitions. The file should be a normal ASCII file with a legal DOS name. Read protect attribute should be off.

Output File
The output file is created on disk by MACDEF. The name of this file is the same as the input file with a .ML extension appended.

Output Message
Error messages from $\operatorname{SSUB}$ and MACDEF are output to the printer. Error messages have the following format:
"LINE line-number ERROR NO. number" where "line-number" identifies a line in the input file and "number" identifies the type of error. In Table III errors related to macro definitions are listed under MACDEF although they may also occur in any SSUB run.

Error messages appearing on the teletype are DOS system messages and are described in the DOS User's Manual.

Table III
Output Messages
For Macro Processor

| Number | Problems | Program Action |
| :---: | :---: | :---: |
| 6 | Input file not specified or not a legal DOS file. | Exit from program. |
| 7 | Disk read error. | Processing continues. |
| 8 | Output file already exists. | Exit from program. |
| 9 | a. Disk write error. <br> b. Disk space exhausted. | Processing continues. |
| 10 | End of source data while processing quote string. Source data may be the input file, a macro parameter value, or a macro body. | String is terminated. If source is input file, exit from program. Otherwise processing continues. |
| 11 | Quote string greater than 1000 characters. | String terminated. Processing continues. |
| 12 | Illegal use of \$ in source data. | Processing continues. |
| 13 | Illegal number of arguments in macro call. | Macro call is ignored Processing continues. |
| 14 | Illegal delimiter character following \$Q. | Processing continues. \$Q ignored. |
| 15 | Preprocessor storage area exceeded. | No more argument values are accepted. Processing continues but other errors will likely occur. |

# Table III (Continued) <br> Output Messages <br> For Macro Processor 

| SSUB Errors |  |  |
| :---: | :---: | :---: |
| Number | Problems | Program Action |
| 16 |  |  |
|  | Error in macro call argument | Macro call is ignored. |
|  | a. No left parenthesis | Processing continues. |
|  | when arguments expected. |  |
|  | b. End of input source |  |
|  | before all argument |  |
|  | values obtained. |  |
| 17 | Too many macros. Limit is 160. | Program is terminated. |
| 19 | Library file could not be opened. | Program terminates. |
| MACDEF Errors |  |  |
| Number | Problems | Program Action |
| 7 | Disk read error. | Processing continues. |
| 9 | a. Disk write error. | Processing continues. |
|  | b. Disk space exhausted. |  |
| 30 | Number of arguments on MDEF line | Macro is not defined. |
|  | not a digit. | Scan to next MDEF line. |
| 31 | Illegal or missing macro on MDEF | Macro is not defined. |
|  | line. | Scan to next MDEF line. |
| 32 | "MDEF" not found where expected. | Continues scan for |
|  |  | "MDEF". |
| 33 | Unexpected end of input file |  |
|  | a. While reading macro body. | Macro is terminated as if MEND found. |
|  | final MEND line. | Termination of program. |

# Table III (Concluded) <br> Output Messages <br> For Macro Processor 

## MACDEF Errors

Number Problems Program Action
34 Input file cannot be opened. Termination of program.
$35 \begin{array}{ll}\text { Attempt to put more than } 100 & \text { Program terminates } \\ \text { macros in a 1ibrary. } & \text { as if end of file read. }\end{array}$
MACDEF Informational Message
"MACRO name DEFINED"

## SECTION IV

SCENARIO ASSEMBLER

## INTRODUCTION

The Scenario Assembler program (CVT) converts external (symbolic) scenarios into internal (absolute) scenarios which are tailored to a specific terminal type and data communications control procedure. This reduces the real-time work of the Scenario Interpreter in the area of scenario processing. To further ease the burden of the Scenario Interpreter, the Scenario Assembler performs character conversions where appropriate and adds start-of-message/end-of-message (SOM/EOM) sequences to queries to be sent to a system under test (SUT). CVT runs under Data General's Disk Operating System (DOS) and its operation must follow the conventions established by DOS. A complete description of the design and implementation of the Scenario Assembler can be found in Volume 4 of this series.

## SYSTEM FLOW

The system flow of the assembly process is shown in Figure 3. The external scenarios may be input to the system from a card deck, from magnetic tape, or from the control teletype. The Scenario Assembler program (CVT.SV) and its associated tables, DEVTAB and SUTTAB, must be input from magnetic tape. The external scenarios, the Assembler, and the tables must reside on disk before execution is initiated. The symbol table is a temporary file written to disk during execution of the Assembler and then deleted at the end of the assembly. The listing on the line printer is also a temporary file and can be relisted only by re-executing the Assembler. The internal scenario is written to disk and can remain there or be written on magnetic tape for further use.


Figure 3 SYSTEM FLOW OF THE SCENARIO ASSEMBLER

## OPERATING PROCEDURES

The Scenario Assembler operates with disk files only, and therefore all input files and the program save file itself must reside on disk before execution can begin.

## Preparing Files

External Scenario
An external scenario (ES) is a stream of characters containing the scenario instructions to be assembled. The format of the ES is shown in Figure 4. The Assembler processes the ES one instruction at a time, interpreting a carriage return as the end of the instruction. This means that a scenario instruction is not restricted in its length, but must use a carriage return only as an instruction termination character.

The first field of an instruction is the op-code field, which is a single character defining the instruction type. The op-code must always appear as the first character of an instruction with no preceding blanks. If the first character of an instruction is a blank, the instruction is treated as a commend by the Assembler. Following the op-code are 0 to 3 fields, depending upon the requirements of the particular instruction type. These fields are separated by one or more blanks except that a blank between the first field (opcode) and the second field is optional. A detailed list of instruction types and their descriptions may be found in Volume 2 of this series.

Scenarios which are to be assembled may be loaded to disk in several ways, using the Command Line Interpreter (CLI) of the Disk Operating System.

| Length <br> in Bytes | Description* |
| :---: | :--- |
| $4-6$ | Allocate instruction to cause a set of Registers <br> to be allocated in core. |
| 1 | Instruction type or op code field. <br> $0-j$ <br> $0-\mathrm{krom} 0$ to 3 fields (depending on instruction <br> the internal scenario. |
| 1 | Either $\emptyset$ or l variable length character string <br> field (depending on instruction type). May <br> include control characters. <br> Carriage return character which signals end of <br> a scenario instruction. |
|  | Above 4 fields are repeated for each instruction <br> in the scenario. |

*A11 character data

Figure 4. External Scenario Format

1. Load from tape to disk LOAD MTO:x scen
2. Transfer from card reader to disk XFER/A \$CDR scen or LXFER \$CDR scen
3. Created through the DOS Editor
4. Created as an output file of the Macro Preprocessor SSUB $x$ scen (lib)

The various DOS commands and programs are fully described in the Data General Software Manuals (References 1 and 2). The Macro Preprocessor is described in Volume 3 of this series.

## Program Files

The Assembler program and its associated conversion tables reside on tape as files, and they also must be read to disk. This can be accomplished with the DOS command

LOAD MTØ: $x$ CVT.SV DEVTAB SUTTAB
This loads the Assembler program save file (CVT.SV) as well as the conversion table (DEVTAB) and start/end-of-message table (SUTTAB), from file $x$ of a magnetic tape mounted on the system tape drive selected as transport $\varnothing$.

## Executing Assembler

The Assembler can be operated in either conversational or nonconversational mode from the control teletype (TTY). In non-conversational mode, all input parameters are included in the initial call. In conversational mode, the Assembler requests the input parameters one at a time. To execute in non-conversational mode, type:

CVT $\left[\begin{array}{c}/ \mathrm{p} \\ / \mathrm{N}\end{array}\right]$ scen codel code2
where:
CVT Is the name of the Assembler program
P
is the optional partial print switch which provides a printout of the ES only

N
is the optional no-print switch
scen is the name of the external scenario to be assembled
codel indicates the conversion method and conversion subtable from DEVTAB to be used for string conversions (see Table IV)
code2 Indicates the SOM/EOM sequence subtable from SUTTAB to be used (see Table V)

In both conversational and non-conversational modes, the Assembler types the message:

TO CANCEL RUN, TYPE CONTROL-A
which indicates that the assembly process has begun. The Assembler can be interrupted at any time during assembly by depressing the Control and A characters simultaneously.

For conversational mode enter:

$$
\operatorname{CVT}\left[\begin{array}{l}
/ \mathrm{P} \\
/ \mathrm{N}
\end{array}\right]
$$

and the Assembler responds with:
ENTER EXTERNAL SCENARIO NAME
When a valid external scenario name is entered, followed by a carriage return, the program types:

Table IV
Available Codes for Conversion

| Code | Comment |
| :---: | :---: |
| 1 | A one-to-one conversion to 8-bit zero-parity ASCII where the leftmost bit is the parity bit and is always set to zero. |
| 2 | A one-to-one conversion to 8-bit even-parity ASCII where the leftmost bit is the parity bit and is set to one only if it is necessary to make the total number of bits in the byte even. |
| 3 | A one-to-several conversion to 7-bit 2741 EBCDIC where the parity bit (odd parity) is the rightmost bit, and a zero bit is added at the left to fill the byte. (See Appendix I) |
| 4 | A one-to-one conversion to 8-bit one-parity ASCII where the parity bit is the leftmost bit and is always set to 1 . |
| 5 | A one-to-several conversion to 7-bit 2741 EBCDIC where the seven bits are in the reverse order of those in use for code3 and a zero bit is added at the left to fill the byte. |
| 6 | A one-to-several conversion to 7-bit 2741 Correspondence Code reversed for use on the field test system. The parity bit is right most bit and a zero bit is added at the left to fill the byte. |

Table IV (Concluded)
Available Codes for Conversion

| Code | Comment |
| :---: | :--- |
| 7 | A one-to-several conversion to $7-b i t 2741$ Correspondence <br> Code for use in the fixed-site system. The parity bit <br> is the rightmost and a zero bit is added at the left to <br> fill the byte. |
| A one-to-one conversion to $8-b i t ~ o d d ~ p a r i t y ~ A S C I I ~ w h e r e ~$ <br> the leftmost bit is the parity bit. |  |

Table V
Available Codes for SOM/EOM

| Code | EOM | SOM |
| :---: | :---: | :---: |
| 1 | ${ }^{15} 8=C R$ | none |
| 2 | ${ }^{223} 8$ | none |
| 3 | $1768=\sim$ | none |
| 4 | $133_{8}{ }^{37} 7_{8}=C R$ (C) | $26_{8}=$ D |
| 5 | $215_{8}=C R$ | none |
| 6 | ${ }^{15} 8_{8}{ }^{12} 8=C R L F$ | none |
| 7 | none | none |
| 8 |  | $64_{8}=$ D |
| 9 | none | ${ }^{26} 8{ }^{26} 8{ }^{26} 8{ }_{8}{ }_{8}$ |
|  |  | $2688=$ SYN SYN |
|  |  | SYN STX SYN |
| 10 | $04=$ EOT | none |

An integer, from Table IV, should be entered, followed by a carriage return. The Assembler then asks:

ENTER CODE FOR END-OF-MESSAGE SEQUENCE
and a value from Table $V$ should be entered. This completes the conversational mode of input.

If an assembly error occurs, the number of the line which caused it and the error message are printed on the teletype. This happens regardless of the print option selected. At the end of the run, or if Control-A is used, control is returned to the NOVA disk operating system (DOS) and an " $R$ " is typed.

OUTPUT

Output of the Assembler is an internal scenario written to disk with the same name as the external scenario but with the extension. IS appended. If an internal scenario already exists for a particular scenario, the old one is automatically deleted and a new one is created for the new Assembly run. Other output of the Assembler includes optional printed listings on the line printer and messages printed to the teletype.

## Internal Scenario

The internal scenario consists of 3 initial bytes of information, followed by processed scenario instructions, and ended by a 2-byte null word. The first information byte is an 8-bit error indicator, each bit being set only if a specific error occurred during assembly. The Scenario Interpreter will accept an internal scenario only if its first byte is zero, i.e., no errors have occurred.

The second byte of the internal scenario identifies the equipment type for which the scenario was assembled. It contains the conversion parameters used to assemble the scenario and make it specific to a
given SUT and terminal. The first four bits are the conversion code (first input parameter) and the second four bits are the SOM/EOM code (second input parameter). If the internal scenario is completely independent of any conversion parameters (i.e., no queries are sent to or received from the SUT), the scenario is called universal, the equipment type is set to zero, and the Scenario Interpreter will accept it to run on any device because it has not been tailored for a particular SUT or terminal.

The third byte indicates the number of registers to be allocated for each use of this scenario. This number may vary from 3 to 127. The Assembler determines this number, not from input parameters as with byte two, but from an Assembler Directive instruction included within the scenario itself, preferably the first instruction. This instruction (op-code $=a$ ) should appear only once per scenario; if the instruction is missing, byte three contains the default value of 8 .

The scenario instructions themselves follow these three initial bytes. Each instruction begins with a 2-byte length field, giving the length in bytes of the instruction, including the length field. The l-byte op-code field is next. Depending upon the particular instruction requirements, there may follow 0 to 3 fixed length fields, 0 or 1 variable-length-string field, or no additional fields. The instructions immediately follow one another, with no intervening delimiters. The end of the internal scenario is signalled by a $2-$ byte null word.

## Optional Listings

When running the Scenario Assembler, three print options are available for printing on a line printer.

```
1. full printing
2. partial printing
3. no printing
```

A sample output listing is given in Appendix II. Full printing is selected when invoking the Assembler by typing CVT without either the $P$ or $N$ options in either the conversational or non-conversational mode. This produces first a listing of the external scenario. Each line contains the external line number, the starting byte address of the corresponding instruction in the internal scenario, and then up to 58 more characters of the instruction. If the instruction is longer than 59 characters, it is truncated. Interspersed in this listing are error messages listed beneath the instructions which caused them.

The listing of the internal scenario appears after the external scenario. This begins with the printing of the error indicator, equipment type, and the Register allocation bytes. Each instruction of the ES is printed, followed by the corresponding internal scenario instruction if one exists (assembler directives are never written in the internal scenario). The internal scenario instruction is printed, 2 bytes on a line, preceded by the byte address, in decimal, of the first of the two bytes. Following the two bytes is the ASCII translation of the bytes with control characters printed as blanks. Two bytes are always printed. Therefore, if the instruction has an odd number of bytes, the first byte of the next instruction is printed and is also repeated as the first byte of the next IS instruction.

The symbol table is printed after the internal scenario. Each entry of the symbol table is represented by a line of print which gives the length of the label, the label, the internal scenario byte address associated with the label, and the line number of the external scenario instruction which first referenced the label. Also printed is the number of entries in the table. An example of the full printout is given in Appendix II.

The partial print option is selected by typing CVT/P in either the conversational or non-conversational mode. This option produces the listing of the external scenario as described above plus a printout of the name, indicator byte, equipment byte, and Register allocation byte of the internal scenario. The rest of the listing of the internal scenario and the listing of the symbol table are omitted.

The no-print option produces no listing to the line printer. As in the case of the other two options, if any errors occur, the error messages are printed on the teletype. Output Messages

Messages are printed to the teletype for two reasons:

1. to request an input in conversational mode; and
2. to report an error.

Both types are self-explanatory. To correct errors in input parameters, input corrections must be typed in. For other messages, no immediate action is needed, except when it may be desirable to interrupt the assembly with a Control-A command. If assembly errors occur, they need to be corrected in the external scenario, and the external scenario needs to be reassembled. Otherwise, the error indicator byte will not be zero, and the internal scenario will not be accepted by the Scenario Interpreter. Table VI includes all possible output messages. The error message designates the number of the line which caused it, except for the LABEL UNDEFINED message which indicates the line number of the first reference to the label.

Table VI

Output Messages
For Scenario Assembler

## Messages Requiring Responses

TO CANCEL RUN, USE CONTROL-A.
ENTER EXTERNAL SCENARIO NAME.
SCENARIO NAME NOT FOUND, RE-ENTER OR CANCEL RUN. ENTER CODE FOR CONVERSION.

CONVERSION CODE NOT IN TABLE. ENTER NEW CODE OR CANCEL RUN .

ENTER CODE FOR END-OF-MESSAGE SEQUENCE.
END-OF-MESSAGE CODE NOT IN TABLE. ENTER NEW CODE OR CANCEL RUN.

TABLE NOT FOUND. CANCEL RUN.

## Messages Requiring No Responses

TOO MANY FIELDS.
LABEL --- IS UNDEFINED.
ALLOCATE IS TOO SMALL.
UNDEFINED OP CODE = ---.
LITERAL MISSING.
OUT-OF-RANGE NUMBER.
WARNING, SHOULD DOUBLE QUOTE BE TWO SINGLE QUOTES
LABEL MULTIPLY DEFINED.
FIELD MISSING.
ILLEGAL FIELD.

## SECTION V

## EQUIPMENT TABLE

## INTRODUCTION

The Equipment Table (ET) is not considered part of the Scenario Interpreter, but is a separate entity to be created by the user to reflect the characteristics of the equipment to be emulated. The Equipment Table consists of a set of ET entries which describe the SUT remote-terminal equipment to be emulated (as well as the control TTY), and relate it to the emulator I/O ports. Each entry ( $255_{8}$ words long) describes one equipment component of the SUT. The format of an ET entry is given in Table $V$ of Volume 2 of this series.

## GENERATION

The Equipment Table must be generated by the user to depict the particular equipment configuration to be emulated. A source file (EQUIP) of the ET is normally created and then assembled with the NOVA assembler. The assembled file (EQUIP.RB) must be included when generating an emulator system, as described in Section VI.
(The EQUIP file contains several items in addition to the ET. The ET history record (ETREC), which is the second record written on the log tape during a run, is a proper subset of EQUIP. The ET itself is a proper subset of ETREC. The requirements and conventions of EQUIP, ETREC, and ET will be clarified in the next subsection.)

The ET source file, EQUIP, is normally written in NOVA assembly language, with each entry correctly formatted. This can be accomplished by creating the file line by line as needed, or by using macros and the macro processor to ease the burden of repetition. Most of ten macros will be used. The macros used to create an Equipment Table
for the present field-test system (including digital I/O facilities) are described in Figure 5.

An ET entry is generated by a sequence of four ordered macro calls: either ETENTRY1, ETENTRY5, ETENTRY3, ETENTRY6 or ETENTRY1, ETENTRY2, ETENTRY3, ETENTRY6. The only difference between the two sequences is that the former (ETENTRY5) allows ETEOM to be specified as a parameter whereas the latter (ETENTRY2) generates an ETEOM value of EOM1. For ease of reading the assembly listing, the ETENTRYI card should start in column 1 and the others in column 10.*

An input file, $E Q$, for an Equipment Table with macros not yet expanded is shown in Figure 6. The six macro definitions used to create the EQUIP file from the EQ file appear at the beginning of the EQ file. A seventh macro definition occurs later in lines 89-91 but is is not essential to the proper formatting of the file. Figure 7 shows a portion of the EQUIP file after execution of the macro processor. In this form, the file is acceptable to the NOVA assembler. Figure 8 shows a portion of the ET after it has been assembled. Appendix III contains a complete listing of EQUIP.RB, the assembled Equipment Table file.

REQUIREMENTS AND CONVENTIONS
The following mandatory requirements must be met by EQUIP, ETREC, and the ET. Line numbers referenced below are those of Figure 6.

1. The following (defined below) must be declared as entry points (external/global variables) as shown at lines 44 to 48: E $\varnothing \rho \rho \emptyset, E \emptyset, E 1, E T R E C, E T E N D, E T E N T, E 2, E T L E N$.
[^0]| $\begin{aligned} & \text { MACRO } \\ & \text { NAME } \end{aligned}$ | PURPOSE | PARAMETERS |
| :---: | :---: | :---: |
| ETENTRY1 | ```Generates words 0-5 of ET entry.``` | \$1 = NOVA assembler label for ET Entry <br> $\$ 2=$ ETRO. Should be initialized to zero. <br> \$3 = first ASCII character of ETYPE. <br> \$4 = second ASCII character of ETYPE. <br> \$5 = ETID in decimal. <br> \$6 = CHILD. The NOVA assembler label for some other ET entry or zero. <br> \$7 = LINK. The NOVA assembler label for some other ET entry or zero. <br> \$8 = PARNT. The NOVA assembler label for some other ET entry or zero. |
| ETENTRY2 | ```Generates words 6-178}\mathrm{ of ET entry with help of ETENTRY4``` | ```$1 = ETRAT in octal.* $2 = TERMT in octal. $3 = STATI. Enter I or U. $4 = PORTO in octal. $5 = PORTI in octal. $6 = SPRTO in octal. $7 = SPRTI in octal.``` |
| *To enter a decimal value, follow it with a decimal point. |  |  |

Figure 5. Equipment Table Macros

| MACRO NAME | PURPOSE | PARAMETERS |
| :---: | :---: | :---: |
| ETENTRY 3 | Generates words $2 \emptyset_{8}-21_{8}$ of ET entry | ```$1 = SUTAD in octal. $2 = ETIND in octal. Bits 1, 2, and 3 should be initialized to zero, the others as desired. Bit }\emptyset\mathrm{ must be set to l for the control TTY. $3 = BYTEL in octal. $4 = PARTY in octal.``` |
| ETENTRY6 | Generates words $2_{8}-24_{8}$ of ET entry | ```$1 = CCC+1 in ETDID. Number of digital inputs in decimal. $2 = BSSSS in ETDID. First input in octal. $3 = DDDDDD in ETDID. Device number in octal. $4 = CCC+1 in ETDOD. Number of digital outputs in decimal. $5 = BSSSS in ETDOD. First output in octal. $6 = DDDDDD in ETDOD. Device number in octal. $7 = ETDOA.``` |

Figure 5. Equipment Table Macros (Continued)

| MACRO <br> NAME | PURPOSE | PARAMETERS |
| :---: | :--- | :--- |

Figure 5. Equipment Table Macros (Concluded)

The following labels must be used for particular ET entries (although the user may also assign labels of his own choice to the same entries):
2. The label $E \emptyset \emptyset \emptyset$ must be used for the first ET entry which must be the control TTY (see line 83).
3. The label $\mathrm{E} \emptyset$ must be used for the ET entry for the control TTY. Therefore, E $\emptyset$ is equivalent to EØППП. See line $84 ;$ the first parameter of the macro ETENTRY1 is the label E $\varnothing$.
4. The label El must be used for the ET entry for the single asynchronous device in the lab system (see line 89).
5. The label E2 must be used for the ET entry for the first asynchronous device in the field-test system and for the first DCM device in the 1 ab system. The Exec assumes that the ET entries for the asynchronous devices in the 64-1ine field-test system are ordered as shown in Figure 9 and that those for the DCM in the lab system are ordered as shown in Figure 10. (Figure 11 shows the ordering and device numbers used for the 16 -line field-test system which are those of Figure 6, lines 96-159.)

ETREC must be defined so that:
6. It includes the entire ET, preceded by four words as shown in Table XII of Volume 2 of this series (see lines 79-191).

EQUIP must include the following definitions:
7. ETEND must contain the length of an ET entry (see line 78).
8. ETLEN is equivalent to ETEND (see line 195).
9. ETENT must contain the number of ET entries (see line 194).
10. One or more EOM lists must be established as in lines 196225. An EOM list is of variable length, terminated by -1


Figure 6. File EQ of Equipment Table (Macros not Expanded)

```
    cCuE PT2:N
    .[.6m こDULINE=3*16.*4
    .OUS= 1H+2848:3*16.+4
    -1;\s* dHM2260=3*15.*4
    .CuSr 18M1053=5
    -Mj* u2080.0
    .Du54 1842741:1
    .CLE= 12741=3*13.*4
    .NuSr 2ASC1=1*15.+1
    -LuS* 24SC6=1*15.+6
    .nuS* EASC2:2*16.+2
    -unSr EASCEz2*15.*5
    .TXTM =
    . 2NEL
    -NatEL
```



```
ETHEC: <U゙?+"E USEOO TO WRITE ET ON TAPE
    =yyGy-ヒス\180+a
    "m
    .4
E0%.9:
```



```
    ETEATFYS(1d|., LASCO,I,dd,1v,|,\Delta,EOMZ)
    ETENTEY3(0,1,9,., L)
    ETcNjEYS(1,0,0,1,0,0,n)
EwEvC:
ETENTMY1(EI,%,L,S,14,0,E2,0)
    ETEFifY5(011|., EASC2,I,51,5*,1,1,EOM3)
    é=^,wr3(15.,1,9..E)
```



```
MLEP TTY3S
12741
MENJ
```



```
    ETEP.TKY5(RT1,TTY33,1,43,42,1,1,EOM4)
    ETETTEY3(30.,3,3L1,PT1)
    ETEATGY6(2,00.,71,4,00,,66,00604)
ETENT&YI(EJ,0,T,V,2,Z,E4, , )
    ETENTKY5(RT1,TPY33,1,43,42,2,2,EUM5)
    ETENIFYJ(3!., 8, 甘LI,PT!)
    cTE:T&Y5(2,02,,71,4,04.,66,00604)
EIENTMYI(EA,&,T,Y,3,0,EA4,O)
    ETENTPY2(RT1,TTY33,I,43,42,3,3)
    ETEATMY3(32,,8,8L!,PT!)
    ETFMTRYE(2,84.,7!,4,08.,06,DO664)
ETENTKYL(E&A,C,1,Y,4,0,E!3,0)
    ETENTRY2(RT1,TTY33,I,43,42,4,4)
    ETENTRY3(29.,0,BL!,PTI)
    ETENTEY6(2,86.,71,4,12.,66,00664)
ETENTMY{(E13,*)T,Y,5,0,E14,0)
    ETENTRY2(RT1,TIY33,I,43,42,5,5)
    ETENIEY3(33.,0,81.1,PT1)
    ETENIFY6(2,88.,71,4,16.,66,00668)
ETENTWY1(E\A,G,T,Y,6,B,E!5,0)
    ETEATGY2(RT1,TTY33,1,43,42,6,6)
    ETENTFY3(34.,0,9Ld,PT1)
    ETENTFY6(2,10.,71,4,20.,66,006003)
ETENTGY1(E15,*,T,Y,7,0,E!6,0)
    ETENTKY2(RT1,TTY33,I,43,42,7,7)
    ETE゙NTFY3(35.,0,BL!,PT1)
    ETETMFY5(2,12,,71,4,24,,66,D066B)
ETENTKY1(E1t, K,T,Y,8,Q,E\7,Oj
    ETENTPY2(RT1,TTY33,1,43,42,8.,8.)
```

Figure 6．File EQ of Equipment Table（Macros not Expanded） （continued）

```
126 ETEN,IFY3(30.,8,9Ld,PT1)
```



```
    cTEN|GY2(RT1,TTYM3,I,45,44,1,1)
    ETE!TF\3(37..0,BL1,PT&)
```



```
ETENTKYI(EIL,U,T,Y,1D,Z,EI9,O)
    ETEPTGV?(RT1,TTY33,I,45,44,2,2)
    cTEPTEY3(38., (,BLI,PT1)
    ETヒNTFIF(2,18,171,4,(4.,67,00674)
ETERTHYI(EIb,N,I,Y,1l,て,E20,0)
    LTEI.TkY?(RT1,TTY33,1,45,44,3,3)
    ETET.lF`3(35.,र,861,PT1)
    ETc!TFYE(2,20.,71,4,88.,07,0U674)
EIENTMY1(Ć2:, %,1,Y,12,Z,E21, R)
    とTどif,2(RT1,T\Y33,I,45,44,4,4)
```



```
    ET&FPF, ( (2,22.,71,4,12.,07,00674)
```



```
    ETEN,F\2(RT1,T1Y$3,I,42,44,5,5)
    CTEN|G\3(41., D,361,PT1)
    ETLTTKY6(2,24.,71,4,16,,67,DU67B)
FTENTKY1PE8́&,%,T,Y,14,Q,E23,P)
    ETとNTAM2(RT1,TTY33,I,4 2,44,5,5)
    LTLNIFY3(42.,Q,ELI,PT1)
    ETFNTRYE(2,20.,71,4,20.,67,0U67%)
ETENTHYI (ECS,*,T,Y,15, ), E24,Q)
    ETERTKY2(RT1,TTY33,1,45,44,7,7)
    ETENINT3(43., D,RLI,PT1)
    とTENTKYE(2,20., 71,4,24.,67,0ル078)
```



```
    ETENTHY2(KT1,TTY33,I,45,44,8., %,)
    =TESTFYS(44.,的EL&,PTI)
    ETENIRY6(2,30.,71,4,28.,09,0067B)
    ETENTMYI(EJ,F,L,N,5,E6,0,Q)
    ETENTRY2(24Q#.,DUDLINE,I,32,31,9,0)
    ETENTRY3(43.,B, 5L2,PT2)
    ETEPTRYE(1,0,0,1,0,8,8)
ETENTMYI(EN,V,C,N,6,EE,ET,ES)
    ETESTHYR(24RO, IBM2848,I,32,31,0,D)
    ETENTFY3(116,0,SL2,PT2)
    ETLNTKYG(1,0,0,1,0,0,0)
    ETENTKYI(ET,&,C,N,7,E\I,O,ES)
    ETEATKY?(24&も.,IEM2848,U,32,31, (0,0)
    ETEP1FY3(250,0,BL2,PT2)
    ETENTEYO(1,0,0,1,0,0,0)
    ETENTKY!(E&,O,D,S,8,0,E9,E6)
    ETEATRY2(2480, IBM2200,I,32,31,0,0)
    ETEATFY3(24Q, &,8L2,PT2)
    ETEATRYS(1,0,0,1,0,0,0)
    ETENTKYI(ES,N,C,S,9,R,EIO,EO)
    ETENTKYZ(24CO.,IBM2260,I,32,31,0,0)
    ETEんTHY3(241,0,BL2,PT2)
    ETENTRYG(1,0,0,1,0, 2,0)
    ETENTKY1(EIK,O,V,T,10,0,0,E6)
    ETENTRY2(152,,IBM1053,U,32,\otimes,0,0)
    ETENIPY3(242,0,BL2,PT2)
    ETENTRYS(1,8,8,1,0,0,8)
    ETENTKYI(EI&,O,L,S,I1,0,EI2,E7)
    ETENTRY2(24AO..IBM2260,U,32,31,0,0)
    ETEMTRY3(244,0,3L2,PT2)
    ETENTEYG(1,0,0,1,0,0,0)
\87 ETENTKY6(1, &,0,1,0,0,0
```

Figure 6．File EQ of Equipment Table（Macros not Expanded） （continued）

| 109 |  |  |
| :---: | :---: | :---: |
| 104 |  |  |
| 191 |  |  |
| 192 | Egugys |  |
| 153 | 6ヒ人 |  |
| 194 | ETfパ： |  |
| 145 | rflen： | －E：－ |
| 180 | LOM1： | 37 |
| 197 |  | －1 |
| lye |  | －1 |
| 159 |  | －1 |
| 2 nd |  | －1 |
| 201 |  | －1 |
| 2.22 | Eu＇s： | 12 |
| 203 |  | ， |
| 2.84 |  | s： |
| 206 |  | －1 |
| 200 |  | －1 |
| 2 V |  | －－ |
| $2 \triangle A$ | Ev－u： | －1 |
| 289 |  | －1 |
| 210 |  | －1 |
| 211 |  | －1 |
| 212 |  | －1 |
| 213 |  | －1 |
| 214 | とum4： | ． 9 |
| 21.5 |  | －1 |
| 210 |  | －1 |
| 217 |  | －1 |
| 218 |  | －1 |
| 219 |  | －1 |
| 22is | £じいら： | 37 |
| 221 |  | 43 |
| 222 |  | －1 |
| 223 |  | －1 |
| 224 |  | －1 |
| 225 |  | －1 |
| 226 | 206048 | $\%$ |
| 227 | 00608： | 0 |
| 228 | 0U671： | $\checkmark$ |
| 229 | UC678： | 3 |
| 230 |  | ．Evi |

Figure 6．File EQ of Equipment Table（Macros not Expanded） （concluded）

```
EQUIP
l
2
3
5
7
10
10
12
14
15
17
18
19
22
23
25
20
2%
29
33
33
35
36
```



```
y
Enfunus
ED:
    - TITl ERuIP
    .ENT EMDUQ,in,t!, ETREC
    -FnT ETFND
    .t.oi kIlNy
    .ENT E2
    .cat Elgen
    -9|\ar. A=10!
    .0usr' I5\11
    .0use b=12s
    .ulom fel24
    .'u\N u-I2b
    .busR n=127
    .UUSN E=105
    .lusar zel3%
    -visir v=116
    -uusk vel17
    .ण|sk सTI=135.
    ."usw %1.1:7.
    .uusk alcob.
    -rujor rti=0
    omsk HTemN
    .Juom ululINE=3*16.*4
    .DuSK INm284d=3*10.+4
```



```
    - nuse Inmaujz=o
    00uSr u2000=0
    -Dusk ICM27al=1
    .CuSN 12741:3*1R.+4
    -DuSN LASC1=1*10.+1
    .UUSM ZASCB=1*16.+6
    .Du:N EASC2-2*10.+2
    .JUSF EASC5:2*10.*5
    .TXYM s
    .2qEL
    -MNEL
ETEND: EAENL-EMOQQ
```



```
    "H
    .+1
    "!\mp@code{OESR.+"T OETRD}
    "C*たSく.*"T IETYPF
    O ICHILD
    El MLINK
    11d. JPAKNT JETHAT
    n IETOHP
    EJM2 JETEOM
    IETRSP
    r IETPAD
    |RRING,PRING
    **<30.*37 IETLGA, ETLGN
    ZASCO-256.+I JTERMT, STATI
    11-256.+10 IPURTO, POKTI
    C*250.+& ISPRTO, SPRTI
    0*256.+188 ISUTAD, ETIND
    6.*206.+z JHYTEL, PARTY
    1.-1*132*087+n IETOID
    1.-1*1н2*0日7+0 IETUOU
```

Figure 7．Portion of File EQUIP of Equipment Table
（Macros Expanded）


Figure 7. Portion of File EQUIP of Equipment Table (Continued)

```
126
127
128
129
136
131 E48
132
133
134
135
136
137
138
139
140
141
142
143
144
145
140
147
148
148
150
151
:52
153 E4A
154
155
156
157
158
159
100
101
162
163
164
165
166
167
108
169
170
171
172
173
174
175 E13
17%
177
178
179
180
181
162
163
184
185
186
$87
880
```

Figure 7. Portion of File EQUIP of Equipment Table (Continued)

| 567 |  | － 42 －250．＊PT2 | 18YEL，PARYY IETOID |  |
| :---: | :---: | :---: | :---: | :---: |
| 568 |  | 1．－1＊192＋0日7＊0 |  |  |
| 569 |  | 1．－1－1－12＋0日7＋2 | IETOD |  |
| 578 |  | 0 | IETOOA |  |
| 571 | E11 |  |  |  |
| 572 |  | $\checkmark$ | jEtro |  |
| 573 |  | ＂0．255．＊＂ | IETYPE |  |
| 674 |  | 11. | IETID |  |
| 575 |  | 0 | ICMILD |  |
| 576 |  | E12 | ILINK |  |
| 597 |  | $E 7$ | iparnt jeyrat |  |
| 578 |  | 24Ju． | jetrat |  |
| 579 |  | n | IETOBP |  |
| 580 |  | E0：11 | IETEDM |  |
| 381 |  | n | IETRSP |  |
| 582 |  | $\omega$ | IETPAD |  |
| 583 |  | H | IRRING，PRING |  |
| 584 |  | －4＊50．0．37 | IETLGA，ETLGN |  |
| 585 |  | 1HM2？5才＊256．＊！ | ITERMT，STATI |  |
| 588 |  | 32＊256．＋31 | IPORTO，PORTI |  |
| 587 |  | H＊253．＊＊ | ISPRTO，SPRTI |  |
| 588 |  | 244＊250．＊088 | isutad，etind |  |
| 589 |  | － $2+250$. ＋P 9 | IGYTEL，PARTY |  |
| 590 |  | 1．－1＊10？＋0日7＊ | IETOIO |  |
| 591 |  | 1．－1＊1－2＋087＊日 | IETODD |  |
| 392 |  | $\Delta$ |  |  |
| 593 | E121 |  |  |  |
| y 4 |  | 0 | IETRO |  |
| 595 |  | ＂）＊255．＊＂S | IETYPE |  |
| 596 |  | 12. | IETID |  |
| 599 |  | 0 | ICHILO |  |
| 598 |  | 0 | ILINK |  |
| 598 |  | E） | iparnt |  |
| 000 |  | 2400. | ietrat |  |
| 601 |  | 1 | IETOBP |  |
| 002 |  | E0M1 | IETEOM |  |
| 603 |  | 0 | IETRSP |  |
| 604 |  | $\Delta$ | JETPAD |  |
| 605 |  | 0 | ｜RRING，PRING |  |
| 646 |  | 0＊250．437 | IETLGA，ETLGN |  |
| 609 |  | 18M2280＊256．＊U | ITERMT，STATI IPORTO，PDRTI |  |
| 608 |  | 32－256．431 |  |  |
| 009 |  | 4＊256．＊0 | ISPRTO，SPRTI |  |
| 610 |  | 245＊256．4088 |  | ETIND |
| 611 |  | 3L2＊256．＊P4 | IBYTEL，PARTY |  |
| 612 |  | 1．－1＊182＊087＊0 | IETDID |  |
| 613 |  | 1．－1＊$-82+087+0$ | $\begin{aligned} & \text { IETODD } \\ & \text { IETDOA } \end{aligned}$ |  |
| 614 |  | 0 |  |  |
| 016 | E99991 |  |  |  |
| 616 | LEN | －EAEND－E0000 |  |  |
| 617 | ETENTI | E9990－EDU00／LEN |  |  |
| 615 | ETLEN： | LEN |  |  |
| 619 | EOM1： | 37 |  |  |
| 620 |  | －1 |  |  |
| 621 |  | －1 |  |  |
| 622 |  | －1 |  |  |
| 623 |  | －1 |  |  |
| 824 |  | －1 |  |  |
| 626 | EOM2 ${ }^{\text {I }}$ | 12 |  |  |
| 826 |  | 5 |  |  |
| 627 |  | 30 |  |  |
| 628 |  | $\begin{array}{r} -1 \\ -1 \end{array}$ |  |  |
| 629 |  |  |  |  |  |  |

Figure 7．Portion of File EQUIP of Equipment Table （Continued）

| 630 |  | -1 |
| :---: | :---: | :---: |
| 631 | EOM3: | -1 |
| 632 |  | -1 |
| 633 |  | -1 |
| 634 |  | -1 |
| 635 |  | -1 |
| 636 |  | -1 |
| 637 | EOM41 | 37 |
| 638 |  | -1 |
| 630 |  | -1 |
| 640 |  | -1 |
| 641 |  | -1 |
| 642 |  | -1 |
| 643 | EOMD: | 37 |
| 644 |  | 43 |
| 645 |  | -1 |
| 646 |  | -1 |
| 647 |  | -1 |
| 648 |  | -1 |
| 649 | 00664: | $\checkmark$ |
| 650 | 00663: | 0 |
| 651 | D067A: | 0 |
| 652 | 006781 | 1 |
| 653 |  | - ENO |

Figure 7. Portion of File EQUIP of Equipment Table (Concluded)


Figure 8. Portion of File EQUIP.RB, Assembled Equipment Table

| OUO2 EJUIP |  |  |  |
| :---: | :---: | :---: | :---: |
| 40426＇604132 |  | 8． 25 56．4 | IGYTEL，PARTY |
| 030271896098 |  | 1．－1－182＋087＋ 0 | PETOLO |
| $40030^{\prime} 000000$ |  |  | IETUOO |
| －0031＇8时时 |  | $\downarrow$ | IETDOA |
|  |  |  |  |
|  |  | $\theta$ | IETRO |
| 02033＇と42123 |  | ＂0．256．＊＂ | IETYPE |
|  |  | 14. | IETIU |
|  |  | $\theta$ | ICHILO |
| －0430＇ciccos ${ }^{\circ}$ |  | E2 | ILINK |
|  |  | 0 | IPAHNT |
| －90401cevis5 |  | 0110. | IETRAT |
| $0004150082 \%$ |  | $y$ | PETQAP |
|  |  | EOM． 3 | IETEUM |
| 20043＇0日U日里 |  | $\Delta$ | IETRSP |
| SOUA4＇varcan |  | 0 | IETPMO |
| Su045＇＾pugea |  | 0 | IRKING，PRING |
| OUC4010めuC37 |  | 0－256．＊37 | IETLGA，ETLGN |
| 0U0471021111 |  | EASC2＊250．＊I | ITERMT，STATI |
| $00 c 501824450$ |  | 51＊256．458 | PPURTO，PORTI |
| 090511004461 |  | 1＊256．＊1 | ISPRTO，SPHTI |
| （0）05 ${ }^{\prime} 0074$ an |  | 15．＊250．404y | ISUTAO，ETINO |
| －DD531日84165 |  | 8． 25 26．+E | B BYTEL，PARTY |
|  |  | 1．－1＊182＋087＋4 | IETULO |
|  |  |  | IETOOO |
| 00056104000n |  | 0 | IETOOA |
|  | F． 2 ： |  |  |
| －0657100ッ0日ッ |  | $\Delta$ | IETRA |
| y0064＇n52131 |  | ＂T＊256．4＂Y | JETYPE |
|  |  | 1. | IETIO |
| －2362＇00000＊ |  | 0 | 1CHILD |
| 0005310001841 |  | E 3 | ILINK |
| 200641800008 |  | 0 | PPARNT |
| 40065＇000207 |  | RTI | IETHAT |
| 000661000080 |  | 0 | IETOBP |
| 1006710010731 |  | EOMA | IETEUM |
| 000781008000 |  | 0 | IETRSP |
| 000711000000 |  | 0 | IETPAO |
| 280721000080 |  | 0 | IRRING，PRING |
| 日のロ73＇600037 |  | 0＋236．437 | ，ETLGA，ETLGN |
| 000741032111 |  | 12741＊256．＊I | ITERMT，STATI |
| ט6075＇021442 |  | 43－256．＊42 | PPORTO，PORTI |
| 800761000401 |  | 1－256．＊1 | ISPRTO，SPRTI |
| 000771017809 |  | 30．＊256．＊088 | ISUTAD，ETINO |
| $88100^{1} 403517$ |  | HLI－256．＊PTI | IBYTEL，PAHTY |
| 001011020071 |  | 2．$-1+182+00.87+71$ | IETOIO |
| 401021060066 |  | 4．－1＊182＋00． $87+66$ | IETOOO |
| ט010310011071 |  | 0006A | IETDOA |
| E31 |  |  |  |
| 40104＇000080 |  | 0 | IETAO |
| 001051052131 |  | ＂$T+256 .+17$ | PETYPE |
| 001001000002 |  | 2. | IETIO |
| 001071000080 |  | 0 | ICHILU |
| 401181000131＇ |  | Ed | ILINK |
| 001111800040 |  | 0 | IPAFNT |
| 401121080207 |  | RTI | J ETRAT |
| $\triangle 1131002000$ |  | 0 | IETOBP |
| －D14＇001101＇ |  | EOM5 | IETEOM |

Figure 8．Portion of File EQUIP．RB，Assembled Equipment Table（Continued）
010221080000 010231842123 010241000014 01025160080 01026.000829 $01027^{10060511}$ 010321004540 01031.800008 $018321081031^{\prime}$ 010331000020 $01034^{1406000}$ $0103510090 \times 8$ 010361000037 010371032125 0！c40181503！ 01041100400 a 01042122480 018431004116 $01044^{\prime 2} 00040$ 010451601808 e1840＇menker

| $1 .-1 * 182+087+8$ | IETOOU |
| :---: | :---: |
| $\checkmark$ | IETOOA |
| $\bullet$ | ietre |
| ＂0＊236．＊＂ | IETYPE |
| 12. | IETIO |
| e | ICHILU |
| $\Delta$ | ILINK |
| E 7 | IPAR |

240日．IETOHP
EOM！IETEOM
－IETRSP
－IETPAD
0＊256．437
IRRING，PRING
iETLGA，ETLGN
itermt，stapi
18M2264＊256．＊U
IPURTO，MORTI
$6.256 .+0$
ISPRTO，SPRTI
ISUTAD，ETIND
＋
BYTEL，PARTY
OL2＊256．＊FT2
IETOIO
1，1＋1
$1 .-1+162+887+0$
e
jetrat
999：
－EDENO－EADOR
U16471000832 ETENT：
E9990－EDBCO／LEN Cl0301＊DOZ2S ETLEN：
LEN
01051＇800037 EUM11
Q10521177777－
01053177777 －1
012541177777 －1
010551179777 －I
010561177777 －1
018571400E12 EOM2： 12 $01060^{1} \mathrm{K000} 5 \mathrm{~S}$
01061100003838
01052177777 －1
016031177777－－
010641177777 010651177クフ7 EOM3：－1 010861177777 －1 010671177777－1 010701177777 －1 010711177777 －1 012721177777 －1 010731000037 Eum48 37 01074117クリフク－－ 010751177777 －1 018761177797 －1 01077177777 －－ 011001177777－1 01101＇00n037 Eums： 37 $0116212 \mathrm{Das3} 43$ 011031177777 －1 011841177777－！ 011051177779 －！ d11061177777－1 0110718000000066 A8
Figure 8．Portion of File EQUIP．RB，Assembled Equipment Table（Continued）

```
0日&2 EQUIP
01818'0008%0 00668:
01111'0088&8 0067A1
01112'000028 0067B2
.END
```

Figure 8. Portion of File EQUIP.RB, Assembled Equipment Table (Concluded)

| INTERFACE ADAPTER | ASYNCHRONOUS LINE ADAPTERS |  |  |  |
| :---: | :---: | :---: | :---: | :---: |
|  | Port |  | Subport |  |
|  | Output | Input | Output | Input |
|  | 24 | 24 | 1 | 1 |
|  | 24 | 24 | 2 | 2 |
|  | 24 | 24 | 3 | 3 |
|  | 24 | 24 | 4 | 4 |
|  | 24 | 24 | 5 | 5 |
|  | 24 | 24 | 6 | 6 |
|  | 24 | 24 | 7 | 7 |
|  | 24 | 24 | 8. | 8. |
|  | 24 | 24 | 9. | 9. |
|  | 24 | 24 | 10. | 10. |
|  | 24 | 24 | 11. | 11. |
|  | 24 | 24 | 12. | 12. |
|  | 24 | 24 | 13. | 13. |
|  | 24 | 24 | 14. | 14. |
|  | 24 | 24 | 15. | 15. |
|  | 24 | 24 | 16. | 16. |

Figure 9. ET Entries for DCM Devices for Lab System

| INTERFACE ADAPTER | ASYNCHRONOUS LINE ADAPTERS |  |  |  | DIGITAL I/0 |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  |  |  |  |  | Inputs <br> (ETDID) |  | Outputs (ETDOD) |  |
|  | Port |  | Subport |  | First <br> Input <br> (BSSSS) | Device <br> (DDDDDD) | First Output (BSSSS) | Device (DDDDDD) |
|  | Output | Input | Output | Input |  |  |  |  |
|  | 41 | 40 | 0 | 0 | 0 | 73 | 0 | 62 |
|  | 41 | 40 | 1 | 1 | 2 | 73 | 4 | 62 |
|  | 41 | 40 | 2 | 2 | 4 | 73 | 8. | 62 |
|  | 41 | 40 | 3 | 3 | 6 | 73 | 12. | 62 |
|  | 41 | 40 | 4 | 4 | 8. | 73 | 16. | 62 |
|  | 41 | 40 | 5 | 5 | 10. | 73 | 20. | 62 |
|  | 41 | 40 | 6 | 6 | 12. | 73 | 24. | 62 |
|  | 41 | 40 | 7 | 7 | 14. | 73 | 28. | 62 |
|  | 43 | 42 | 0 | 0 | 16. | 73 | 0 | 63 |
|  | 43 | 42 | 1 | 1 | 18. | 73 | 4 | 63 |
|  | 43 | 42 | 2 | 2 | 20. | 73 | 8. | 63 |
|  | 43 | 42 | 3 | 3 | 22. | 73 | 12. | 63 |
|  | 43 | 42 | 4 | 4 | 24. | 73 | 16. | 63 |
|  | 43 | 42 | 5 | 5 | 26. | 73 | 20. | 63 |
|  | 43 | 42 | 6 | 6 | 28. | 73 | 24. | 63 |
|  | 43 | 42 | 7 | 7 | 30. | 73 | 28. | 63 |
|  | 45 | 44 | 0-7 | 0-7 | 0-14. | 74 | 0-28. | 64 |
|  | 47 | 46 | 0-7 | 0-7 | 16.-30. | 74 | 0-28. | 65 |
|  | 51 | 50 | 0-7 | 0-7 | 0-14. | 75 | 0-28. | 66 |
|  | 53 | 52 | 0-7 | 0-7 | 16.-30. | 75 | 0-28. | 67 |
|  | 55 | 54 | 0-7 | 0-7 | 0-14. | 76 | 0-28. | 70 |
|  | 57 | 56 | 0-7 | 0-7 | 16.-30. | 76 | 0-28. | 71 |

Figure 10. ET Entries for Asynchronous Devices for 64-Line Field-Test System

| INTERFACE ADAPTER | ASYNCHRONOUS LINE ADAPTERS |  |  |  | DIGITAL I/O |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  |  |  |  |  | Inputs <br> (ETDID) |  | Outputs (ETDOD) |  |
|  | Port |  | Subport |  | First <br> Input <br> (BSSSS) | $\begin{aligned} & \text { Device } \\ & \text { (DDDDDD) } \end{aligned}$ | First Output (BSSSS) | $\begin{aligned} & \text { Device } \\ & \text { (DDDDDD) } \end{aligned}$ |
|  | Output | Input | Output | Input |  |  |  |  |
|  | 43 | 42 | 1 | 1 | 0 | 71 | 0 | 66 |
|  | 43 | 42 | 2 | 2 | 2 | 71 | 4 | 66 |
|  | 43 | 42 | 3 | 3 | 4 | 71 | 8. | 66 |
|  | 43 | 42 | 4 | 4 | 6 | 71 | 12. | 66 |
|  | 43 | 42 | 5 | 5 | 8. | 71 | 16. | 66 |
|  | 43 | 42 | 6 | 6 | 10. | 71 | 20. | 66 |
|  | 43 | 42 | 7 | 7 | 12. | 71 | 24. | 66 |
|  | 43 | 42 | 8 | 8 | 14. | 71 | 28. | 66 |
|  | 45 | 44 | 1 | 1 | 16. | 71 | 0 | 67 |
|  | 45 | 44 | 2 | 2 | 18. | 71 | 4 | 67 |
|  | 45 | 44 | 3 | 3 | 20. | 71 | 8. | 67 |
|  | 45 | 44 | 4 | 4 | 22. | 71 | 12. | 67 |
|  | 45 | 44 | 5 | 5 | 24. | 71 | 16. | 67 |
|  | 45 | 44 | 6 | 6 | 26. | 71 | 20. | 67 |
|  | 45 | 44 | 7 | 7 | 28. | 71 | 24. | 67 |
|  | 45 | 44 | 8 | 8 | 30. | 71 | 28. | 67 |

Figure 11. ET Entries for Asynchronous Devices for 16-Line Field-Test System
(177777 octal). The lists are pointed to by ETEOM in each ET entry. If no EOM checking is to be done, ETEOM must point to a location containing -1. Figure 6 presently contains duplicate lists (EOM1 and EOM4). The lists are longer than needed so that additional EOM character codes can be added octally if needed. The 30 words in lines 196-225 are equivalent to the following seven words (except that the order of list EOM5 is changed):

EOM5: 43
EOM1:
EOM4: 37
EOM3: -1
EOM2: 12
5
30
$-1$
11. One word of storage must be provided for each group of 16 contiguous digital outputs which are to be used in the test, as shown in lines 226-229 as D066A, D066B, D067A, and D067B. The words are pointed to by ETDOA in each ET entry which uses digital outputs. The storage must be initialized to zero.

A number of conventions were observed in generating the file in Figure 6. The Macro Processor was used to perform certain substitutions and the NOVA assembler pseudo-op .DUSR (see lines 49-74) was used to perform others. The Macro Processor performs its substitutions prior to the assembly. The differences can be seen between the file EQ and the EQUIP (symbolic) portion of the assembly listing. The macro TTY 33 defined at lines 93-95 of Figure 6 changes TTY33 in line 97, for instance, to 12741 . The pseudo-op .DUSR causes the substitution to be made internally by the assembler. Therefore, the symbolic portion
of the assembly listing gives the symbol and the assembled code shows the substituted value. For instance, on line 8 of page 1 of Figure 8, the name $I$ is assigned the value $111_{8}$. On line 56 of the same page, the $I$ is shown in the symbolic code and the 111 is the rightmost portion of the assembled value of $131118_{8}$.

The labels E3, E4, etc., (as well as E $\varnothing$, E1, and E2) for each ET entry are needed to provide values for the cross-reference fields CHILD, LINK, and PARNT. A better tactic than using the arbitrary labels, however, would be to use the device names for labels, to use TY2 as a label rather than E3 at line 100 of Figure 6. The field ETYPE should be used to group like devices and to distinguish unlike devices, for instance: TT for TTY's, TY for IBM 2741's, CT for the control TTY, DS for displays, LN for communications lines, CN for multiplexor device-controllers, PT for printers, etc. Several combinations should be used to distinguish displays with different characteristics, for instance.

The label EØEND (line 88) is used to define the end of entry E $\emptyset$ and in defining ETEND (line 78). The label E9999 (line 192) is used to define the end of the last ET entry and in defining ETENT (line 194) and the length of ETREC (line 80). The symbol LEN (line 193) has the value of the length of an ET entry and is used in defining ETENT and ETLEN.

The equivalences for $A$ through $W$ at lines $49-54$ are provided for use in giving values to the field STATI although only $I$ and $U$ should normally be used for initial values. The equivalences for $W$ through 0 at lines 54-58 are for use in defining parity type (PARTY). The meanings are:

```
W = one (parity bit set to a constant 1)
E = even parity
Z = zero (parity bit set to a constant \emptyset)
```

$$
\begin{aligned}
& \mathrm{N}=\text { no parity bit } \\
& \mathrm{O}=\text { odd parity }
\end{aligned}
$$

Only the values $E$ and 0 are used by emulator programs.
The equivalences at lines 64-74 are used to define terminal type (TERMT). Those at lines 67-69 are of the earlier, arbitrary type which have not been updated.

The equivalences at lines $59-63$ are used so that the fields ETRAT, BYTEL and PARTY in the ET entries may be given symbolic values rather than absolute values. Only the equivalence statement has to be changed to assign a new value rather than changing each ET entry.

## FUNCTION

Each Equipment Table entry defines one equipment component of the SUT. In the simplest case, one ET entry is used to describe a point-to-point communications channel, possibly a pair of modems, and the single device attached to the channel. In a more complicated case, one entry describes the channel (and possibly modems), one is used to describe each controller or terminal (in a multipoint configuration), and one is used to describe each device at each terminal.

In the latter case, cross references (CHILD, LINK, and PARNT) are used to describe the hierarchical structure. As an example, the hierarchical ET structure described in Figures 6 through 8 is shown in Figure 12. Since each ET entry can reflect only one of each relationship, the arrows and labels indicate which relationship is expressed in the ET. Using this method of cross-referencing most configurations of equipment can be easily described. The number of levels and the number of entries at each level are limited only by core memory.


Figure 12. Equipment Table Hierarchy

Information in the Equipment Table is used by the Scenario Interpreter and by the Exec and is available to a scenario by means of certain scenario instruction types. The scenario may examine information, or in limited cases, change information in an ET entry. A scenario may access its own ET entry, or, through the relationships described above, access the ET entry of a relative, a relative's relative, and so on (in the direction of the arrows only). This capability of a scenario becomes increasingly useful as the equipment being emulated becomes increasingly complex.

The set of Registers of the current scenario associated with a particular device is pointed to by the first word (ETR $\varnothing$ ) in the ET entry for that device. The first word of an ET entry is pointed to by the relationship pointers described above. Using instruction types $h$ and then $g$ and $p$ (as defined in Volume 2 Table XVII), $a$ scenario A running on device LN5 (as shown in Figure 12) can access the ET entry and Registers of scenario $B$ running on device CN6, and then gain access to the ET entry and Registers of scenario $C$ running on DS8, and so on. An example of this method of communication among devices is shown in the scenario segments in Figure 13.* In this case LN5 running with scenario A establishes the linkage to DS8 running with scenario C. Scenario A checks Register 9 of scenario C to determine when DS8 is ready to send a query. When scenario A senses that $R 9=1$, it then performs a specified function (function 1 ) and resets R 9 to zero. This zero indicator is put into R 9 of scenario C , which senses the indicator and proceeds to send the query. Meanwhile CN6 running with scenario $B$ is engaged in performing function 2 , which may or may not be involved in communication with LN 5 or DS8.

[^1]Also, using instruction type $h$, and then instruction types $Y$ or $n$, scenario A can examine the bit indicators (ETIND) of the ET entry of device CN6 and then DS8, etc. There are other scenario instructions which access the Equipment Table contents, and can be used in numerous ways to enhance scenario abilities and efficiency. A complete presentation of scenario instructions is given in Volume 2, Table XVII.

The technique of utilizing the Equipment Table to examine or pass information among devices can be useful, for example, when emulating a polled network. Assume, for instance, that CN6 was a controller and DS8 and DS9 were polled terminals. Then by making use of the cross references in the Equipment Table, the scenario for CN6 could poll the scenarios for DS8 and DS9 by examining indicator bytes or Registers to determine which devices were active, ready to send, or ready to receive. The individual terminal scenarios could send their queries and examine responses when indicated by the controller scenario.

| $\begin{gathered} \text { LN5 } \\ \text { SCENARIO A } \\ \text { (SCA) } \end{gathered}$ | $\begin{gathered} \text { CN6 } \\ \text { SCENARIO B } \\ \text { (SCB) } \end{gathered}$ | $\begin{gathered} \text { DS8 } \\ \text { SCENARIO } \\ \text { (SCC) } \end{gathered}$ |
| :---: | :---: | :---: |
| ALLOCREGS 15 <br> C[START CN6 SCB <br> ETOREG $\emptyset \emptyset \mathrm{Rl} \emptyset$ <br> R1ø CONTAINS ADDRESS <br> TO ET ENTRY OF SCA <br> ETOREG R1ø 3 Rll <br> Rll CONTAINS CHILD POINTER (WORD 3) OF R1ø WHICH IS ADDRESS OF ET ENTRY OF CN6 ETOREG R11 3 Rl2 <br> R12 CONTAINS CHILD POINTER OF R11 WHICH IS ADDRESS OF ET ENTRY OF DS8 <br> L LAB1 <br> GTR 9 R12 R9 <br> THE CONTENTS OF R9 <br> OF SCC IS PUT INTO <br> R9 OF THIS SCENARIO <br> B CONT 1 R9 <br> IF R9=1 THEN GO TO CONT <br> D 1 <br> J LAB1 <br> OTHERWISE, DELAY 1 SEC. AND JUMP TO LAB1 | C [START DS8 SCC function 2 : | ALLOCREGS 15 <br> A 12 <br> ALLOCATE 12 BYTE QUERY <br> BUFFER <br> 5 BUILD QUERY <br> $+\emptyset 13$ R11 <br> PUT ASCII CR INTO R11 <br> ᄀ R11 <br> ADD CONTENTS OF Rll <br> TO QUERY BUFFER <br> $+\emptyset 1$ R9 <br> R9 SET TO 1 INDICATES <br> that query Is ready <br> L LAB1 <br> B CONT $\emptyset$ R 9 <br> IF R9=ø THEN GO TO CONT <br> D 1 <br> J LAB1 <br> OTHERWISE DELAY 1 SEC <br> AND JUMP TO LABI <br> L CONT <br> JUMP HERE WHEN R9 RESET <br> TO ZERO BY SCA <br> 0 <br> SEND THE QUERY <br> : |

Figure 13. Example of Device Communication Through Scenarios

| $\begin{gathered} \text { LN5 } \\ \text { SCENARIO } \\ \text { (SCA) } \end{gathered}$ | $\begin{gathered} \text { CN6 } \\ \text { SCENARIO } \\ \text { (SCB) } \end{gathered}$ | $\begin{gathered} \text { DS8 } \\ \text { SCENARIO } \\ \text { (SCC) } \end{gathered}$ |
| :---: | :---: | :---: |
| L CONT <br> PUT CONTENTS OF CURRENT R9 INTO R9 OF SET OF REGISTERS POINTED TO BY R12 (DS8) |  |  |

Figure 13. Example of Device Communication Through Scenarios (Concluded)

## SECTION VI

REAL-TIME EMULATOR SYSTEM GENERATION

INTRODUCTION

The generation of the real-time emulator system is a four-step process which can be represented as follows:


The four steps are execution of the Macro Processor (SSUB), execution of the NOVA assembler (ASM), execution of the NOVA relocatable loader (RLDR), and execution of the DOS command MKABS. The first two steps must be performed separately for each assembly module which is to be changed (including the Equipment $T$ able which is not considered a part of the Scenario Interpreter). The last two steps must be performed once each whenever one or more assembly modules (including those of the Exec) have been changed. In creating the Executive from the various source files, there is some flexibility available in defining buffer sizes, storage requirements, and parity checking on SUT terminals. These options are described in detail in Volume 6 of this series in the User Information Section.

SSUB
For purposes of this discussion the general form of the command to execute the Macro Processor is assumed to be:

SSUB input-file source-file macro-libraries

The input-file names, source-file names, and the macro libraries needed for the Scenario Interpreter are given in Table VII. The implementation uses of the Macro Processor are also discussed in Section III.

To execute the Macro Processor, type on the control TTY;
SSUB II ININT RTOSLIB LIB LIB1,
or
SSUB EQ EQUIP,
where II and EQ are the input files; ININT and EQUIP are the output files; RTOSLIB, LIB and LIBI are libraries; and $\downarrow$ represents the carriage-return key. The macro libraries must be in the form of the output files produced by the macro library generator (MACDEF), the file LIB.ML, for instance. Unlike the last three steps, the output file (ININT or EQUIP, above) must be absent from the DOS file directory before executing SSUB.

If one of the three macro libraries must be changed, it must be read into the NOVA using LIB, for instance, as the input file name. Typing

## MACDEF LIB)

on the control TTY will execute the macro library generator which will generate the macro library LIB.ML.

ASM
An output file from the Macro Processor (Source File) must next be processed by the Data General assembler by typing, for instance:

$$
\text { ASM/L/X } \quad \text { \$LPT/L } \quad \text { ININT }
$$

The output file produced is a relocatable, binary file, ININT.RB in this case. Because the switches /L/X and the line printer \$LPT are specified, an assembly listing including the source file and cross reference list will be produced on the line printer.

TABLE VII

Input File Names for Emulator System

| Input | Source | Macro |
| :---: | :---: | :---: |
| File | File | Library |
| Name | Name | Names |
| EQ | EQUIP | -- |
| SI | SCINT | RTOSLIB, LIB, LIB1 |
| CI | CMINT | RTOSLIB, LIB, LIB1 |
| II | ININT | RTOSLIB, LIB, LIB1 |
| FC | FETCH | RTOSLIB, LIB, LIB |
| TP | TESTP | RTOSLIB, LIB, LIB 1 |
| S 1 | SUBR1 | RTOSLIB, LIB, LIB1 |
| S 2 | SUBR2 | RTOSLIB, LIB, LIB1 |
| AF | ALF | RTOSLIB, LIB, LIB 1 |
| ERROR | ERMSG | RTOSLIB, LIB, LIB1 |
| FTC | FTCHG | RTOSLIB, LIB, LIB1 |
| DW | DUMPW | RTOSLIB, LIB, LIB1 |
| DH | DUMPH | RTOSLIB, LIB, LIB1 |
| IS | ISCEN | -- |

## $\underline{\text { RLDR }}$

Table VIII lists the assembly modules needed by the Data General relocatable loader to generate the real-time software for each of the two versions of the emulator. The files used by RLDR are those with the . RB suffixes. A list of the module names (excluding the suffix) must be given to RLDR. These can be typed from the list in Table VIII, if desired; however, the system tapes for each of the emulator versions contain a file called LOADLIST which is a list of the file names needed for each version. To execute RLDR, type on the control TTY :

RLDR/Z MAP/L @LOADLIST@ $\downarrow$
The output file produced by RLDR is in a form suitable for execution under control of DOS. Although the real-time emulator cannot be executed under DOS, the step is a necessary preliminary to producing the required file. The output file is named RTOS.SV since RTOS is the first file in the list in LOADLIST. Since MAP/L is specified the core map produced by RLDR will be placed in a DOS disk file called MAP. It can be listed by typing:

PRINT MAP $\mathcal{L}_{\downarrow}$ or PRINTL MAP $\downarrow$
The MAP file should be saved on tape with the other files for future reference. The file RTOS.SV should also be saved since octal patches, if needed, can be made to it, with the MAP file for guidance. The four th step must then be performed with a new or patched RTOS.SV.

## MKABS

The DOS command MKABS produces a file which can be executed independently of DOS. The command is executed by typing:

$$
\text { MKABS } / Z \quad \text { RTOS SCINT.BN } \quad \text { INIT/S } \alpha
$$

The octal equivalent of INIT (obtained from the MAP file) is the

TABLE VIII
Inputs to Relocatable Loader

|  | Assembly Module Name | Lab System | Field-Test System |
| :---: | :---: | :---: | :---: |
| $\begin{aligned} & \text { U } \\ & \text { u } \\ & \text { x } \\ & \text { n } \end{aligned}$ | *RTOS | X | X |
|  | *RTIN | X | X |
|  | LPT | X | X |
|  | MTA | X | X |
|  | TTY 1 | X |  |
|  | DCM | X |  |
|  | DCMT | X |  |
|  | ASYNC |  | X |
|  | SCMGT | X | X |
|  | PAGE | X | X |
|  | DSK | X | X |
|  | DMP | X | X |
| ET | *EQUIP | X | X |
|  | SCINT | X | X |
|  | CMINT | X | X |
|  | ININT | X | X |
|  | FETCH | X | X |
|  | TESTP | X | X |
|  | SUBR1 | X | X |
|  | SUBR2 | X | X |
|  | ALF | X | X |
|  | ERMSG | X | X |
|  | *FTCHG | X | X |
|  | DUMPW | X | X |
|  | DUMPH | X | X |
|  | IS CEN | X | X |

[^2]value to be used in the command. MKABS uses RTOS.SV as the input file and produces $\operatorname{SCINT.BN}$ as the output file. SCINT.BN is the real-time emulator program, containing the Exec, the Equipment Table, and the Scenario Interpreter. It may be executed, by means of the DOS program EXEC, by typing:

## EXEC SCINT/

A more convenient method of executing SCINT.BN, however, is discussed under Operating Instructions for the Scenario Interpreter.

Disk Requirements
After a system is generated, it is not necessary to maintain all the binary and source program files on disk. These files should be saved on tape, and disk space freed to allow space for additional macro libraries and scenarios. Table IX indicates the disk requirements of the files which should be retained on disk during emulator operation.

Table IX
Disk Requirements for Emulator System

| File | Size <br> Bytes/Pages | Comments |
| :---: | :---: | :---: |
| DOS, etc | 101221/210 | Includes basic support software after @REMAL@ has been executed. Includes SYS.DR, MAP.DR, EDIT.SV, XFER.SV,SYS.LB,RLDR.SV,OEDIT.SV PRINTL.SV,REMAL, BLDR.SV,EXEC.SV,ASM.SV |
| MACDEF.SV | 14976/30 | Macro Processor. See MTR 2677 Volume 3. |
| SSUB.SV | 20736/41 | Macro Processor. See MTR 2677 Volume 3. |
| SCENLIB.ML | 242/1 | Lower-case scenario instruction op-codes. See MTR 2677, Volume 2, Table XIV and related text. |
| CVT.SV | 31488/62 | Scenario Assembler. See MTR 2677, Volume 4. |
| SUTTAB | 384/1 | Scenario Assembler. See MTR 2677, Volume 4. |
| devtab | 1792/4 | Scenario Assemb1er. See MTR 2677, Volume 4. |
| RTOS.SV | 32512/64 | Real-Time Emulator. See MTR 2677, Volumes 5 and 6. |
| SCINT.BN | 33514/66 | Real-Time Emulator. See MTR 2677, Volumes 5 and 6. |
| P | 30/1 | Real-Time Emulator. See MTR 2677, Volumes 5 and 6. |
| C | 3/1 | Real-Time Emulator. See MTR 2677, Volumes 5 and 6. |
| LOADLIST* | 130/1 | Real-Time Emulator. See MTR 2677, Volumes 5 and 6. |
| DATAR.SV | 29056/57 | Data Reduction Program. See MTR 2677, Volume 7. |
| SUMRY.SV | 27904/55 | Data Reduction Program. See MTR 2677, Volume 7. |
| TLIST.SV | 27264/54 | Data Reduction Program. See MTR 2677, Volume 7. |
| CTABS | 1664/4 | Data Reduction Program. See MTR 2677, Volume 7. |
| ERFILE | 420/1 | Data Reduction Program. See MTR 2677, Volume 7. |
| TREL.SV | 26240/52 | Data Reduction Program. See MTR 2677, Volume 7. |
| MASTR.SV | 17024/34 | Data Reduction Program. See MTR 2677, Volume 7. |
| MAP | 3752/8 | Core map of RTOS.SV and, thus, of SCINT. BN |
| NOTES | 1926/4 | Text description of system. Should be updated when changes made in public or private copy. |
| FILECH. BN | 4806/10 | Verifies file validity on disk. See Reference 3. |
| MTLIST. BN | 3606/8 | Physical tape dump for MT1. See Reference 4. |
| Total | 380690/758 |  |

* When used, also need EQUIP.RB and . RB files for Scenario Interpreter and Exec.


## SECTION VII

## REAL-TIME EMULATOR

## INTRODUCTION

The Scenario Interpreter is the real-time, emulator application program which operates in conjunction with the Real-Time Exec, a multitasking, application-oriented executive program. The Scenario Interpreter executes commands used to exert gross control over the run, executes scenarios which describe the actions to be taken in emulating terminal and operator functions, and records real-time events on a log tape. The Scenario Interpreter and the Real-Time Exec perform all the functions of the real-time emulator run.

## SYSTEM FLOW

As shown in Figure 14 the real-time emulator system as well as the internal scenarios to be used must reside on disk before a run can be initiated. The Scenario Interpreter program (running under the Real-Time Executive) is then started by input from the control teletype. Once the emulation has begun, the teletype may be used for both output messages and input commands for the run. The events of the emulation are recorded on the log tape during the run, and this tape is used at the completion of the run for analytical purposes. If any dumps of the emulator system are requested during the real-time run, they will be printed on the line printer during the run.

## OPERATING INSTRUCTIONS

External control over a real-time emulator run is exerted primarily through the control TTY. The run is started under DOS conventions. Once started, emulator conventions apply. In existing Equipment Tables, the control TTY is defined as device CTO. Device CTO is made to look


Figure 14. System Flow for Real-Time Emulator
as much like other (emulated) devices as possible. CTO can be used as an emulated device if desired although responses must be supplied by the user, of course. Unlike other devices, CTO is operated in echo-plex mode so that keystrokes will cause printing on the TTY. Unlike DOS, the Exec does not echo back a carriage-return and a line feed when the carriage return key is depressed. Therefore, the symbol $\ell$ is used to denote depression of the carriage-return key and echo back of both carriage-return and line-feed under DOS. Under Exec control, both keys must be depressed and they are represented below by CR/LF. It is assumed that the list of EOM characters pointed to by ETEOM in the ET entry for CTO includes LF (12 ${ }_{8}$ ) CANCEL ( $30_{8}$ ), and break (5).

## Startup

If not already mounted, a scratch tape is needed on tape drive 0. The real-time run is most conveniently started by typing on the control TTY:
@P@
This input causes DOS to treat the file $P$ as a list of DOS commands. The file P contains:

RELEASE MTO;
TYPE C;
EXEC SCINT;
This set of commands causes the contents of file $C$ (containing WAIT $\downarrow$ ) to be typed on the control TTY. The log tape is then rewound if it was left other than at the load point by a previous real-time run or by an aborted Data Reduction run. While the tape is rewinding, the real-time emulator program (SCINT.BN) is called and initialization is begun. All further control TTY inputs must follow Exec conventions.

## Control TTY Inputs

Run ID

After the word 'WAIT' is typed on CTO, the user must wait for the message 'ENTER RUN ID' to be typed on CTO before taking any further action. The user must then enter a character string, terminated by CR/LF, which will be written on the log tape as the run identification. The run identification consists of all characters typed up to but not including the first control character (those with octal values less than 40 ) or the first $20{ }_{10}$ non-control characters. If an error is made in entering the run $I D$, simultaneous depression of the control and $X$ keys (Control-X) will cancel the input and the user can start again. Almost immediately after entry of the run ID, the emulator will write the run $I D$ and the other two history records on the log tape in one burst and then type "READY" on CTO. The emulator is now ready to accept commands so as to start emulation.

## Commands

The emulator will remain in the idle state until a command is entered from CTO or from another emulator module or until an (unsolicited) response is received from the SUT, from CTO, or from another emulator module. Even then, the emulator will return to the idle state until one or more START commands are executed by the emulator. Commands are described in Volume 2 of this series. Commands from CTO (or another emulator module) must be preceded by an ASCII left-bracket character (Control-K). With a single START command, the user can execute a control scenario, if he desires, which can automatically START other devices and execute other commands (by means of the type-C scenario instruction) and any of the scenario instructions defined in Volume 2.

## CANCEL Input

Any CTO input can be cancelled by depressing Control-X. The input will not be logged, and CR/LF will be typed as an acknowledgement.

## BREAK Output

If the first (or any odd) character of a CTO input message is a BREAK character (input by depressing Control-E), the input is considered a BREAK input whose purpose is to BREAK or stop output on CTO of error messages (see ERROR command) and the monitor output of queries and responses (see MONITOR command). Error messages, queries, and responses already queued for typing, will be typed, but no more will be queued until another ERROR or MONITOR command causes them to be queued again.

## Responses

A CTO input not in any of the above classes is considered a response. If no scenario is operating for CTO, they will be treated as unsolicited. If a scenario is operating and is waiting for a solicited response, the response will be processed immediately. Otherwise, the response will be queued until the scenario requests it or until the scenario terminates.

Shutdown
The real-time run is terminated by execution of a QUIT command from CTO, another emulator module, or a scenario. If the run does not terminate immediately, the emulator is so busy that the QUIT command (which is purposely given the lowest possible priority) is never executed because of a continuing string of higher priority tasks. One or more devices must be STOPped for the QUIT command to be executed. When the QUIT command is executed, two lines of emulator statistics are typed on CTO and the NOVA halts. By depressing Continue on the panel, DOS will be brought back in core and executed. DOS will type 'DOS REV XX.' and it will halt. Depressing Continue again will cause ' $R$ ' to be typed, and DOS is again in control. If desired, the Data Reduction program can be executed for the run just completed or any DOS function can be performed.

## ERROR MESSAGES

The high-order digit of the printed error message number has been used to classify the error messages generated by the Scenario Interpreter as to seriousness. The most serious errors correspond to the highest digit. The ten error message classes are given in Table X. General comments are also included as to the kinds of errors associated with each class and the system action following detection of the error.

Table XI lists and explains all the error messages generated by the Scenario Interpreter. Each three-digit number shown is a part of the message. The message itself represents the only use in the Scenario Interpreter of the three-digit numbers. Elsewhere, error messages are referenced only by the two low-order digits, and the table is in order based on these digits. The convention (6) 40 has been used to indicate the internal and external message numbers. The table gives the meaning and cause of each error message as well as the subroutines and modules which generate the message.

Table X

Error Message Classes for Scenario Interpreter

Class

Error encountered in attempt to free a block of allocable core memory. Probably a system error although improper use of a type-F scenario instruction or previous improper action with Registers could cause it. System attempts to continue with emulation of device. Unable to execute command. Erroneous command operator or operand. Action continues as for class 5.

Usually an indication of an action taken although an error may be present also.

Not an error. Indication of action taken.

Table XI

Error Messages for Scenario Interpreter

| Message | Meaning |
| :---: | :---: |
| 800 STACK OVERFLOW | ```System error. Attempt to PUSH a value into stack portion of RS when stack full. (Sub- routine POSH\emptyset, POSH1, POSH2, or POSH3).``` |
| 801 STACK UNDERFLOW | ```System error. Attempt to POP a value from stack portion of RS when stack empty (Sub- routine PUP\emptyset, PUP1, PUP2, or PUP3).``` |
| 502 NO RS TO FREE | System error. Attempt to free RS when STACK $=\varnothing$. (Subroutine FRRS). |
| 503 ILLEGAL FREE ADDRESS | Probably a system error. Attempt to free RS or buffer whose address not in allocable core. (Subroutine FRRS or FRBF). |
| 504 NO BUFFER TO FREE | ```Probably a system error. Attempt to free a non-existent buffer, i.e., pointer = \emptyset (Subroutine FRBF).``` |
| 406 TOO FEW REGS FOR SUBSCENARIO CALL | Register RGCAL not allocated in current set so that execution of a SUB command is ruled to be invalid. (Subroutines CMINT or ALRG). |
| 507 NO REGS TO FREE | The set of Registers pointed to may have been freed previously or the contents of the Register may have been altered erroneously by a scenario. Otherwise, a system error. (Subroutine FRRG). |
| 210 COMMAND NOT IMPLEMENTED | Specified command (MOD or TRANSFER) has not been implemented. (Subroutine CMINT). |
| 211 INCORRECT COMMAND OPERATOR | Erroneous command operator. (Subroutine CMINT). |
| 312 EQUIPMENT UNAVAILABLE | Attempt to START a device whose status is other than 'I' or 'S'. (Subroutine CMINT). |

Table XI (Continued)
Error Messages for Scenario Interpreter

|  | Message | Meaning |
| :---: | :---: | :---: |
|  | OUT-OF-RANGE <br> REG 非 | ```Attempt to access Register not allocated in current set (module FETCH) or in another set (module ININT - type g or p scenario instruction).``` |
| 114 | DEVICE STOPPED | End of top-level scenario reached by normal operation or simulated due to serious error. (Module FETCH). |
| 215 | VALUE NEEDED FOR COMMAND | Numeric (decimal) value missing from SCALE command or numeric portion of equipment name missing from MONITOR, RESTART, START, STATUS, or STOP command. (Subroutine CMINT or FNENT). |
| 216 | UNKNOWN DEVICE NAME IN COMMAND | Unable to find equipment name specified in MONITOR, RESTART, START, STATUS, or STOP command in Equipment Table. (Subroutine FNENT). |
| 217 | INCORRECT <br> SCENARIO NAME | Unable to find scenario name specified in START or SUB command in Scenario Directory (Subroutine CMINT). |
| 020 | ACTION TAKEN | Indicates successful execution of DUMP, ERROR, MONITOR, RESTART, SCALE, START, STOP, or SUB command (Subroutine CMINT). |
|  | SUB COMMAND LEGAL ONLY FROM SCENARIO | No rational way to execute a SUB command from one device for another since they operate asynchronously (Subroutine CMINT). |
| 422 | INVALID SUBSCENARIO COMMAND REFERENCE | Attempt to execute a SUB command with no scenario specified when no uncompleted subscenario exists for device (RGCAL $=\emptyset$ ) or when Register RGCAL does not point to a valid set of Registers (C(RGR $\emptyset) \neq R G R \emptyset)$. (Subroutine CMINT). |
|  | $\begin{aligned} & \text { ONLY "ON" OR "OFF" } \\ & \text { LEGAL } \end{aligned}$ | First operand of LOG command specifies 'ALL' and second operand specifies neither 'ON' nor 'OFF' (Subroutine CMINT). |

Table XI (Continued)
Error Messages for Scenario Interpreter

| Message | Meaning |
| :---: | :---: |
| 224 ONLY "A", "N", OR | First operand of LOG command specifies 'THIS' OR equipment name and second operand specifies none of 'A', 'N', or 'U'. (Subroutine CMINT). |
| 125 LOG ACTION COMPLETE | LOG command has processed as much as it can of the third operand. Each component of this operand is processed separately and program has reached illegal component or end of command Rather than attempting in an iterative program to separate the cases of missing third operand, error in nth component but first $n-1$ of them were processed, or all components were correct, a combination message is used which is intended to cause the user to verify that there was no error in the third operand. Note that for this type of SUB command, the SUBSCENARIO form is invalid and no character (such as a blank) may follow 'SUB' in the command instruction or the program will assume a scenario is specified. |
| 826 STATI INCORRECT | System error. Instruction Interpreter attempting to emulate device whose status (STATI) is neither ' $A$ ' nor ' $T$ '. (Module FETCH.) |
| 327 DEVICE INACTIVE OR STOPPED | Attempt to STOP a device whose status (STATI) is 'I', 'T', 'S', or 'U'. (Subroutine CMINT.) |
| 330 DEVICE NOT STOPPED | Attempt to RESTART a device whose STATUS (STATI) is neither ' $T$ ' nor 'S'. (Subroutine CMINT.) |
| 631 QUERY BUFFER OVERFILL | Attempt to fill query buffer beyond end by scenario instruction of type 5, |
| , or @. Note that if an error message intervenes after generation of query buffer,but before filling it, the error message buffer will displace the query buffer and the error message buffer will then be filled by the instruction. (Module ININT.) |  |

Table XI (Continued)
Error Messages for Scenario Interpreter

| Message | Meaning |
| :---: | :---: |
| 732 NO QUERY BUFFER TO FILL | This message will only appear if there is no query buffer (or error message buffer) associated with the device and a scenario instruction of type 5, |
| , or @ is executed. This condition will only occur prior to generation of the first buffer or following execution of a type-E scenario instruction and before generation of next query buffer or of next error message buffer which is not the result of a type-E instruction. |  |
| 333 DEVICE STOPPED BY TYPE-7 INSTR | A RESTART command is not legal for the device since it was STOPped by a type-7 scenario instruction rather than by a STOP command so that there is no current task which can be RESTARTed. See Miscellaneous Notes section. (Subroutine CMINT). |
| 634 OTHER REG SET DOES NOT EXIST | Attempt to execute a scenario instruction of type $g$ or $p$ when the other set of Registers does not exist (pointer $=\emptyset$ ). (Module ININT). |
| 035 TTY OUTPUT SUPPRESSED | A BREAK input was recognized and executed. (Module SCINT.) |
| 336 ASSEMBLY ERROR IN SCEN | First byte of internal scenario is non-zero. Scenario needs to be reassembled after correction of errors before it will be acceptable for use with START or SUB command. (Subroutine CMINT). |
| 337 EQUIPMENT TYPE MISMATCH | Scenario may not be used with specified device (START command) or with current device (SUB command) because scenario is not a universal scenario and the second byte of the internal scenario fails to match TERMT in the ET entry for the device. (Subroutine CMINT). |

Table XI (Concluded)
Error Messages for Scenario Interpreter

| Message | Meaning |
| :---: | :--- |
| 640 BEHIND SCHEDULE | A type-W scenario instruction was executed <br> after the specified time had passed. The <br> amount of time by which the task is behind <br> schedule, in milliseconds, is contained in <br> the start transmission time fields of the <br> buffer. Processing continues for device. <br> (Module ININT.) |
| 641 WAIT INSTR |  |
| IGNORED |  |$\quad$| Type-W scenario instruction may not specify |
| :--- |
| a time in excess of approximately 4.62 hours |
| because of conversion problems. Instruction |
| ignored and processing continues for the |
| device. (Module ININT.) |

DEVICE STATUS
Figure 15 shows all the valid state (STATI) transitions which can occur for a device. These transitions occur as the following functions are performed:
$I \rightarrow A$ occurs when a START command is successfully executed for the device.
$A \rightarrow I$ occurs when the end of the top-level scenario (RGRET $=0$ for the current set of Registers) is reached for the device.
$A \rightarrow T$ occurs when a STOP command is successfully executed for the device.
$A \rightarrow W$ occurs when a time delay type of scenario instruction (type D, W, or d) is executed.
$A \rightarrow S$ occurs for the current device when a type-7 scenario instruction transfers control of the task to another device.
$\mathrm{W} \rightarrow \mathrm{A}$ occurs upon the expiration of a time delay caused by execution of a scenario instruction of type $D, W$, or $d$.
$\mathrm{W} \rightarrow \mathrm{T}$ occurs when a device is STOPped while executing a scenario instruction of type $D, W$, or $d$.
$\mathrm{S} \rightarrow \mathrm{A}$ occurs when a STOPped device is STARTed or RESTARTed after the transition from $T$ to $S$ has taken place or for the new device during execution of a type-7 scenario instruction.
$T \rightarrow S$ occurs for a STOPped device after completion of execution of the current scenario instruction or upon receipt of a response following execution of a scenario instruction of type R or I
$T \rightarrow A$ occurs when a RESTART command is executed for a STOPped device before the T to S transition has taken place.


Figure 15. State Transition Diagram
device is unavailable and status cannot be changed by the emulator (can only be changed in non-real-time by reassembly of the $E T$ or with the octal editor).

RING COUNTERS
There is a pair of ring counters in each ET entry (for each emulated device). They are used to sequence number tasks of types 6 (unsolicited responses), 7 (solicited responses), and 8a (newly STARTed devices), so that only one task of these types at a time (per device) can proceed past a certain point in the Input Processor (types 6 and 7) or the Instruction Interpreter (type 8a) so as to preserve reentrancy. The ring counter RRING (the response ring counter) is used to count and sequence number such tasks. The subroutine CHEKR is used to maintain RRING. CHEKR fetches RRING and uses it to sequence number the task (by setting RSEQU), steps RRING, and stores the updated value. CHEKR then compares RSEQU with PRING (the processing ring counter). If they are equal, the task is allowed to proceed. Otherwise, the task remains in CHEKR until PRING equals RSEQU. Thus, a queue of such tasks is maintained for each device, when necessary, and the tasks are released one at a time in the order in which they reached CHEKR.

The processing ring counter (PRING) is maintained by the subroutine STEPR. STEPR is called when task types 6 (unsolicited responses), $7 b$ (type $R$ or $I$ scenario instruction executed), or 7c (end top-level scenario) terminate and when certain tasks of type 8a are generated (when a STOPped device is STARTed, the STOPped task must first be terminated). The only function performed by STEPR is to step PRING so that the next sequence numbered task may proceed.

These steps are shown in Figure 16 which is a modification of the state diagram in Figure 15. In Figure 16, when a device is STARTed, its status (STATI) changes from I to A. RRING is also

(R) RRING used and stepped by CHEKR
(P) PRING stepped by STEPR

Figure 16. Ring Counter Changes
stepped and the new task may be queued. When the end of a top-level scenario is reached for a device, its status changes from A to I and PRING is stepped.

If a STOPped device is STARTed (not RESTARTed), its status changes from 'S to A. When the STOPped task is terminated, PRING is stepped for the old task. RRING is then stepped for the new task (which may be queued.)

The loop around the I status indicates no change in status but the fact that if the device is inactive, receipt of an unsolicited response first causes RRING to be stepped and then PRING. Unsolicited responses are queued since a change in device status while the response is queued may cause a change in the type of response. The final determination as to the type of response is made when the response leaves the queue.

Similarly the loop around the A status indicates no change in status but the execution of a scenario instruction of type $R$ or $I$ which causes PRING to be stepped followed by a new task which steps RRING. Had one or more responses already been queued for the device, the stepping of PRING would allow the first of these to advance.

The discussion also indicates possible problems regarding use of the type-7 scenario instruction. For a type-7 instruction to be valid, the device to which control of the task is transferred must be STOPped. Thus, for this new device there already exists a suspended Scenario Interpreter task. If a task which has been generated for one device is allowed to terminate for a second device, PRING will not get stepped at the end of the task for the old device but for the new device. Thus, since the ring counters provide for ${ }^{256}{ }_{10}$ sequence numbers, the old device would have to accumulate a total of 255 queued responses (which would tie up 255 Exec clock blocks) before any further activity could occur for the old device. The new
device should be able to resume activity when a new task is generated for it, but the original STOPped task would be destroyed without its allocable core being freed when the task which executed the type- 7 instruction terminated. The first problem is the more serious one, of course, but the latter ties up system resources for the duration of the run. Therefore, a task which is started for one device should be terminated for the same device to avoid these problems.

RESPONSE HANDLING AND LOGGING
The determination of whether logging is enabled or not for a particular device and a particular buffer type is made at the time the buffer is allocated. Changing the setting of the logging indicators, with the LOG command, has no affect on logging of buffers which have already been allocated. In the present implementation, if logging is enabled in a given case, a long buffer (one with a long header) is allocated and all long buffers are logged. For all long buffers, the log processing bit in BFIND is set at time of allocation. For either long or short buffers, one of the other five processing bits is set (based on buffer type) at time of allocation. When a task is done with a buffer or when it needs the buffer pointer space in the $R S$ for a new buffer to be allocated, it resets the appropriate processing bit and attempts to free the buffer. If all six processing bits are reset, the free attempt is successful.

Unlike the other four types of buffers, response buffers are not automatically logged in all cases. Every long response buffer must be logged by one means or another or it will not be freed and the space will not be available for reallocation during the rest of the run. A separate response queue is maintained for each emulated device so that only one main task can be active at a time to process a single response. When a response and its associated task leave the queue, the determination is made as to whether the response is
solicited (or unsolicited) depending essentially on whether the device is active (or inactive). If the device is inactive when the response leaves the queue, the response will be logged automatically as unsolicited, if logging is enabled, and the task is terminated.

If the device is active at the time the response leaves the queue, the response will be logged automatically as solicited if Response Indicator 2 in ETIND is set and logging is enabled. The indicator must be set by executing a scenario instruction of the form $=2$ prior to the time the response leaves the queue. A long response buffer can also be logged by executing a type-8 scenario instruction, which specifies whether the response is solicited or unsolicited. The use of both techniques will cause the buffer to be logged two or more times, once automatically and once for each type-8 instruction executed. Since there is no apparent advantage in logging a response more than once and the solicited response indicator (bit 0 in BFIND) is initially reset, execution of a type-8 instruction to $\log$ a response as unsolicited does not reset the solicited response indicator. Therefore, once the indicator has been set by either means, any further type- 8 instructions will cause logging as solicited regardless of the value of the first operand.

When a device is active, all responses received will be queued until one is requested by the scenario by means of executing a scenario instruction of type $R$ or $I$. When such an instruction is executed, the main task for the device is terminated at the end of execution of that Instruction. Further execution of the scenario is done by the task associated with the next queued response which starts execution with the scenario instruction following the $R$ or $I$ instruction. If any responses are queued for a device when the end of the top-level scenario is reached or when a STOPped device is STARTed (not RESTARTed), the responses will be logged automatically as unsolicited. In addition, when either event occurs, all indicators in ETIND are reset except
for the Command Indicator and the Monitor Indicator. Therefore, if responses are to be logged automatically as solicited, each scenario STARTed (not RESTARTed or executed by a SUB command) must set Response Indicator 2.

DIGITAL I/O
Digital I/O devices are installed on the field-test system but not on the lab system. With the field-test system connected directly to a SUT (without use of modems), the emulator must emulate the actions of modems as well as devices and operators. For each device, the SUT must believe it is communicating with the modem at its end of a communications channel. To provide more direct and complete control over the modem control lines (those not used for data transfer) than that provided by most line adapters, the emulator uses digital input devices to read the control signals set and reset by the SUT and digital output devices to set and reset the control signals read by the SUT.

The field-test system to be discussed is that containing 16 asynchronous communications channels and 8 synchronous channels. The discussion is largely concerned with emulation of asynchronous devices, with comments as to the extensions for synchronous devices.

The digital I/O design was done by Data General. The intended software design had to be modified to interface with the hardware as delivered.

A digital output device contains the capability of setting ${ }^{32}{ }_{10}$ digital outputs. Since a single NOVA instruction can set only ${ }^{16}{ }_{10}$ outputs, the outputs associated with one device address are separated into $A$ and $B$ groups. Since the outputs must be continuous rather than momentary, a register is associated with each of the two groups of an output device. Thus a NOVA digital output instruction loads either the $A$ or the $B$ register and the SUT reads (senses) the bits in those
registers. Loading a register corresponds to the simultaneous setting of some outputs to 1 and resetting of others to 0 . Since Data General provided no means of reading an output register, the emulator software has to maintain a record of the status of each set of 16 outputs, in the word pointed to by ETDOA. (Each such word contains the current settings of outputs associated with 2 to 16 emulated devices, as should be clear later.) When one or more digital outputs must be set or reset for an emulated device, the software has to fetch the word pointed to by ETDOA and either reset the appropriate bits by masking or set them by ORing. The updated word then has to be stored back in memory and loaded into the appropriate register.

The system contains four digital output devices with (octal) addresses of $64,65,66$, and 67 . The outputs for a single digital output device are numbered from 0 to 31 decimal ( 0 to 15 in the $A$ register, 16 to 31 in the $B$ register). The system contains 128 digital outputs. Devices 64 and 65 are reserved for synchronous emulation, and 66 and 67 are used for asynchronous emulation.

The digital input hardware is similar to that for digital output but simpler. A digital input device allows reading (sensing) 3210 inputs. The inputs are grouped in $A$ and $B$ groups although a group is simply a group of lines in the emulator hardware since, in this case, the inputs read are in registers in the SUT. When one or more digital inputs must be read and tested for an emulated device, the appropriate digital input device and group (containing inputs associated with 2 to 16 emulated devices) must be read, and the appropriate inputs tested.

The system contains two digital input devices with (octal) addresses of 70 and 71. The inputs for a single digital input device are numbered from 0 to 31 decimal ( 0 to 15 in the A group, 16 to 31 in the B group). The system contains 64 digital inputs. Device 70 is reserved for synchronous emulation, and 71 is used for asynchronous emulation.

Figure 17 shows the types of connections between the NOVA rack and the SUT, by way of the interface rack. On the left are the connection points, and on the right is shown the type of path connecting each pair of adjacent points. The jumpers between the $A$ and $B$ barrier strips are intended to be the primary means of changing configurations. For asynchronous devices, there are 16 A barrier strips and 16 B strips, one of each per device. Up to 10 separate connections can be made from an $A$ barrier strip to 10 or less of the 24 connection points on a B barrier strip.

The relationships within the interface rack should be clarified by Figure 18. A single cable carries all 32 inputs or outputs (both $A$ and $B$ groups) of a single digital $I / O$ device between the NOVA rack and the interface rack. A single section of the interface rack accommodates 16 emulated asynchronous devices. The normal wiring needed for emulating Bell 103A modems is shown in the figure. Only the digital $I / 0$ wiring is shown. For each emulated device, two digital inputs and four digital outputs are shown although only one of the inputs is used. Digital input device 71 is adequate for the needs of all 16 emulated devices. Digital output devices 66 and 67 are needed to provide four outputs per emulated device. In the diagram, the outputs are labeled from 0 through 3 and the inputs from 0 through 1. These are the addresses to be used by scenarios.

The purpose of the ETDID and ETDOD fields in an ET entry is to describe the relationship between the fixed digital I/O addresses used by scenarios (the same for all emulated devices) and the hardware addresses which are different for each emulated device. ETDID and ETDOD as well as the four types of digital I/O scenario instructions allow up to eight digital inputs and eight digital outputs to be associated with each emulated device. Since only one NOVA instruction is used to read digital inputs or to set and reset digital outputs and to conserve space in the ET, all the inputs (or outputs) for an


Figure 17. Digital I/O Connections


Figure 18 NORMAL INTERFACE RACK WIRING FOR ASYNCHRONOUS DEVICES
emulated device must have the same digital I/O device address, be in the same group ( A or B ), and be adjacent to one another.

ETDID and ETDOD have the same format (CCCBSSSSOODDDDDD in binary) and specify the digital I/O device address (DDDDDD), the number of the left-most input or output (BSSSS, where the value of the high-order (B) bit separates the A group from the B group), and the number of consecutive inputs or outputs minus one (CCC). If ETDID (or ETDOD) is zero, there are no inputs (or outputs) associated with the emulated device. From Figure 18 it can be seen that:
for device TY:

```
ETDID: }\quad\mathrm{ CCC = 1, BSSSS = 0, DDDDDD = 71
ETDOD: }\quad\mathrm{ CCC = 3, BSSSS = 0, DDDDDD = 66
```

for device TY2:
ETDID: $\quad C C C=1, \operatorname{BSSSS}=2, \operatorname{DDDDDD}=71$
ETDOD: $\quad C C C=3, \operatorname{BSSSS}=4, \operatorname{DDDDDD}=66$
for device TY 16:
ETDID: $\quad C C C=1, B S S S S=30 ., \operatorname{DDDDDD}=71$
ETDOD: $\quad C C C=3, B S S S S=28 ., \operatorname{DDDDDD}=67$
where a decimal point following a number indicates a decimal number, otherwise octal.

In Figure 18, the six digital input and output connections on an A barrier strip are connected to six points on a B barrier strip which in turn are connected to six pins on a front connector which is cabled to the SUT. These correspondences are shown in Figure 19. The codes are standard pin or signal codes. Figure 20 contains synonyms for the five scenario instruction op-codes used for digital I/O as well as correspondences between the digital I/O addresses used by a scenario and the two-letter signal codes. These equivalences can be made by use of the Macro Processor.

| Scenario <br> I/O Address | Pin <br> Number | Code | Function |
| :--- | :---: | :---: | :--- |
|  | DO-0 | 6 | CC | Data Set Ready (DSR)

Figure 19. Normal Asynchronous Correspondence

DON = ;
DOF = :
$\mathrm{BDN}=9$
$B D F=q$
$A D Y=d$
$C C=0$
$C F=1$
$C E=2$
$C B=3$
$\mathrm{CA}=0$
$C D=1$

Figure 20. Macro Definitions for Digital I/O

DON CE
L CDLOOP
BDN CDON CD
ADY 250
J CDLOOP
L CDON
ADY 500
DON CC
DOF CE
ADY 4500
DON CB CF

Figure 21. HANDSHAKE Scenario

Figure 21 contains the HANDSHAKE scenario which causes the emulator to exchange the modem control signals necessary prior to data transmission. The scenario first turns on (sets) the Ring Indicator (CE). At the label CDLOOP, a branch is taken to the label CDON if Data.Terminal Ready (CD) is on. Otherwise, a 250 -ms delay is taken followed by a branch to CDLOOP to test CD again. When CD has been turned on by the SUT (at CDON), a 500 -ms delay is taken, Data Set Ready (CC) is turned on, and Ring Indicator is turned back off. A $4 \frac{1}{2}$-second delay is then taken and Clear to Send (CB) and Carrier Detect (CF) are both turned on.

In Figure 18, connection points 1, 2, 9, and 10 are not used for digital I/O. Points 1 and 2 are received and transmitted data, and 9 and 10 are for clock signals for synchronous emulation. If more than two digital inputs or four digital outputs are needed for an emulated device or if secondary data transmission paths are needed, two A barrier strips must be connected to the same B barrier strip. This technique is necessary for synchronous emulation. From the standpoint of digital I/O, two adjacent A barrier strips will have to be used so that the digital inputs and digital outputs for the emulated device form consecutive sets. ETDID can then be changed to describe up to four inputs, and ETDOD can be changed to describe up to eight outputs.

## STORAGE REQUIREMENTS .

The core storage requirements for both the Scenario Interpreter and the Real-Time Exec are presented in Tables XII and XIII respectively. The data for the Real-Time Exec are based on the 64-line field test system, while the information for the Scenario Interpreter applies to both lab and field test systems.

Core Storage Requirements for Scenario Interpreter

| Assembly Module | Program, Words | Major Tables, Words | Total, Words |
| :---: | :---: | :---: | :---: |
| SCINT | 417 | - | 417 |
| CMINT | 668 | - | 668 |
| ININT | 996 | 64 | 1060 |
| FETCH | 464 | 64 | 528 |
| TESTP | 162 | - | 162 |
| SUBR1 | 310 | - | 310 |
| SUBR2 | 292 | - | 292 |
| ALF | 317 | 45 | 362 |
| ERMSG | 195 | 491 | 686 |
| FTCHG | 192* | - | 192* |
| DUMPW | 171 | - | 171 |
| DUMPH | 185 | - | 185 |
| ISCEN | - | 7 | 7 |
|  | 4369 | 671 | 5040 |

* For field test system

Table XIII
Core Storage Requirements for Real-Time Exec

| Name | Words |
| :--- | :---: |
| RTOS | 2686 |
| RTIN | 672 |
| MTA | 758 |
| LPT | 98 |
| SCMGT | 442 |
| PAGE | 385 |
| DSK | 64 |
| DMP | 164 |
| ASYNC | 2916 |
|  | TOTAL |
|  | 8185 |

## MISCELLANEOUS NOTES

(1) Assume devices A and B are both STARTed and then device B is STOPped by a STOP command. Further assume that the scenario for device A executes a type-7 scenario instruction to transfer control to device $B$ at time $T$ and that the scenario for device $B$ transfers control back to $A$ at time $T^{\prime}$. An attempt to RESTART device A between times $T$ and $T^{\prime}$ is not legal since device $A$ has no task associated with it (its original task is associated with device B) even though its status (STATI) is 'S'. Error message 非33 is generated in this case. Device $B$ may not be RESTARTed during the interval since its status is not 'S', although it may be RESTARTed after the STOP command and prior to $T$, and after $T^{\prime}$.
(2) The Scenario Directory is ordered the same as the DOS file directory. (LIST/L *.IS $\downarrow$ will produce a list on the printer of internal scenarios and their order in the DOS file directory.) By design, the Scenario Interpreter will find the first entry in the Scenario Directory whose $n$-character name matches the first $n$-characters of a scenario name in a command. Thus, if TEST precedes TESTA in the directory, a command specifying TESTA will find TEST in the directory, Similarily, M can prevent access to Ml, MATCH, etc. Implementation was done in this manner since there is no guarantee as to which of many characters may follow the last character of a scenario name. In particular, a user may declare any ASCII character as an EOM character, which would follow a scenario name.

To avoid problems of selection of an unintended scenario because of such subset names, various techniques are available. No subsetting will occur if all scenario names contain the same number of characters. In particular, if all scenario names are ten characters or more in length, no problems will occur because the DOS file directory contains only the first ten characters of a file name. Another solution is to end each scenario name with a character which is used nowhere else in
a scenario name (the ASCII \$ sign appears a likely candidate). If subset names occur, they will cause no problems if the longer names precede the shorter ones in the DOS file directory. The final solution, of course, is not to form scenario names by appending one or more characters to previous scenario names.
(3) Commands entered at the control TTY must be preceded by a left bracket (control-K):
[START DS14 Y
Command instructions punched in cards should be in the form:
Ç̧START DS 14 Y
The cents sign is the keypunch equivalent of the left bracket. (In the case of the scenario instruction, the cents sign is not needed for identification, but the first character in the literal is skipped over.)
(4) Partial core dumps on the printer will result from:
a. use of the DUMP command
b. use of the Structure Dump (?) instruction

The dump routines used to implement these functions are not reentrant since interleaved usage by several tasks of the same printer seems unuseful. The continuity of the dump is necessary to identify the device (and scenario) causing it. The dump functions are for diagnostic purposes and should be used with care to avoid reentrancy violations.

## PANIC CODES AND ACTIONS

If during the normal operation of the emulator, certain abnormal conditions occur, the Real-Time Exec will abort the run. Before aborting the run, however, the system saves the contents of accumulators ACO-AC3 in locations 12, 13, 14, and 15, respectively, disables
interrupts, prints out a panic code on the control teletype, and halts. The panic codes are described in Table XIV.

The user can obtain a full core dump of the system at this point by depressing the "CONTINUE" switch on the NOVA console. If only a partial dump is desired, the word count and starting address of the desired area can be entered into accumulators 0 and 1 , respectively, before depressing the "CONTINUE" switch. When the dump is completed, the system will automatically try to write the magnetic tape buffers to tape, write an end-of-file on the tape and then try to make a normal emulator exit, printing out the run statistics. An example of a panic message and termination is given in Figure 22.

The run statistics that are printed on the control teletype at the end of an emulator run are: the maximum number of task control blocks that were in use at any one time (TCB MAX XXX), the maximum number of tasks that existed on the task pending queue at any one time, the number of available core blocks that exist at exit time, and the total number of core words available at exit.

Table XIV

RTOS Panic Codes

| Error Code | Meaning |
| :---: | :---: |
| 1 | System error. Two tasks are illegally trying to remove core space from the free chain at the same time. |
| 2 | System error. Two tasks are illegally trying to return core space to the free chain at the same time. |
| 3 | System error. A task issuing a . FREE supervisor call has illegally given a block size of zero length. Usually means the core chain or Scenario Interpreter data structures are in error. |
| 5 | System error. A task issuing a . FREE supervisor call has illegally tried to free a block with a starting address the same as a block already in the free chain. Usually means Scenario Interpreter data structures are in error. |
| 6 | System error. A task issuing a . FREE supervisor call has illegally tried to free a block which overlaps the front part of a block already in the free chain. Usually means core chain or Scenario Interpreter data structures are in error. |
| 7 | System error. A task issuing a . FREE supervisor call has illegally tried to free a block which overlaps the end part of a block already in the free chain. Usually means core chain or Scenario Interpreter data structures are in error. |
| 8 | System error. A task exiting from either a .ALOC or . FREE supervisor call has found the core chain busy indicator illegally set. |
| 9 | System error. A task exiting from either a .ALOC or . FREE supervisor call has found that the link word of its TCB is illegally set. Usually means that the queue stack is in error. |
| 10 | System error. A task issuing a . FORK supervisor call has illegally given a value of zero for the new task's stack address. Usually Scenario Interpreter error. |
| 11 | System error. A task issuing any supervisor call other than . ALOC or . FREE has a zero value for its stack address. Usually Scenario Interpreter error. |

Table XIV (Continued)
RTOS Panic Codes

## Error Code

System error. The number of clock blocks reserved at system generation have been used up by tasks issuing .WAIT supervisor calls. User is either trying to emulate too many lines with space for clock blocks or is running in loopback mode at a high baud rate.

Hardware error. An undefined device has caused an interrupt. Location 14 (accumulator 2) contains the device number of the offending device.

System error. A response having an odd number of characters has been terminated without padding out the right byte of the last word. Usually indicates response handling logic is in error when adding a new device to system.

System error. The word count in a query buffer is greater than 32,768 , which is outside the address space of the NOVA 800. Usually means the Scenario Interpreter data structures are in error.

System error. The interrupt dismissal routine was called with an illegal interrupt data block address. Usually means an executive error.

System error. The interrupt data block address was equal to zero for a device that was trying to perform an end of operation at the non-interrupt level because the queue for the device was not available at time of interrupt.

System error. The initial word count for the text portion of a query buffer is equal to zero. Usually means scenario is in error or Scenario Interpreter data structures are in error.

System error. Lab system only. On exiting from the DCM handler the bit time indicator had been reset illegally. This panic condition was part of original Data General software.

System error. Lab system only. The system was unable to service all DCM lines in 5 bit times. Usually means core chain became too long. Part of original Data General software.

Hardware error. The magnetic tape controller indicated an error when a status instruction was executed upon a

Table XIV (Concluded)
RTOS Panic Codes

Error Code

Meaning
tape interrupt. Location 12 (accumulator $\emptyset$ ) contains the status of the tape drive. The explanation of the status is given in Reference 5.

System error. The magnetic tape handler received a non-error interrupt and did not have a record of having written a tape buffer. Usually means the tape device unit control block has been destroyed.

Hardware error. In reading the magnetic tape status before writing, either bit $1,2,3$, or 5 has been set indicating some type of tape unit trouble. From experience panic code 25 usually occurs before this condition.

System error. A task issuing a . FTCH supervisor call has passed a scenario program counter which is larger than the scenario itself. Usually means that the internal scenario on disk has been destroyed or the scenario management routine has an error.

Hardware error. The disk controller indicated an error when a status instruction was executed upon a disk interrupt. Location 12 (accumulator $\emptyset$ ) contains the status of the disk controller. The explanation of the disk status is given in Reference 5.

Note: The above panic conditions were inserted during the debugging and development phase of the emulator software. From experience the only ones that a user may usually encounter are 12, 13, 21, and 25. Any of the others occurring usually means a new problem uncovered and should be reported to the system programmers.

```
ePO
WAIT
ENTER RUN ID
I
READY
PANIC: ERR0R CODE=2.1
HIT CONTINUE FOR FULL CORE DLMF
TCE MAX 000003 TPQ MAX 000003
CORE LINKS 000002 CORE AVAIL 027363
DOS REV 05.
R
```

Figure 22. Example of Panic Message

## SECTION VIII

## DATA REDUCTION PROGRAM

## INTRODUCTION

The Data Reduction program (DATAR) processes log tape data gathered during an emulator test run. The program produces scenario trace data and various statistics on the performance and utilization of both the emulator and the SUT. A complete description of the design and implementation of the program can be found in Volume 7 of this series. DATAR runs under Data General Corporation's standard Disk Operating System (DOS), Revision 5.

DATAR may be used to produce several kinds of summary and detailed listings from the log tape, and thus it allows the user to obtain a quick summary of activity during the run on an individual basis or as an entire system. DATAR also gives detailed information in the form of record-by-record listings that include information such as readable real-time clock (RRTC) times, various timing calculations, and the text message.

After the tape file is processed by DATAR, the user may save the test data on master $10 g$ tapes (to consolidate tapes or to put similar runs on one tape). The master (or original) log tape may be used for later analysis on the NOVA 800 or on a larger machine with more sophisticated data reduction and analysis capabilities.

## SYSTEM FLOW

Figure 23 depicts the system flow of DATAR programs. The log tape, with data gathered from a single emulation run or a series of runs, is mounted and readied on the system tape drive, transport $\emptyset$, prior to any user input requests. The log tape provides the input to DATAR.


14 -41,733

DATAR is called by entering an input message on the system teletype. There are two forms of input messages which result in two modes of operation, interactive (conversational) or switch. The interactive mode requires the user to specify input arguments by responding to a series of interactive requests output by DATAR. The switch mode, where a switch is the character / (slash) followed immediately by an alphabetic character, uses switches to modify input groups and specify input arguments.

On entry, the Command Interpreter (CI) residing in DATAR.SV is loaded from disk and uses the input arguments to determine the type of output to be produced. The user may obtain a brief summary, a detailed summary, an octal tape listing, or a listing with actual RRTC times, with time differences (intervals), or with relative times. The output device, (line printer or teletype) is also determined from the input message. DATAR output is printed at the specified device, and error messages are output to the teletype and, if in use, the line printer.

DATAR requires the conversion tables (CTABS) and the error message file (ERFILE) to be disk resident for all types of output. If an octal listing is desired, the CI begins printout on the output device. However, if a summary or another type of listing is desired, the CI saves some information on disk in two temporary files, PAGEZERO and HBUF, and calls one of the save files (SUMRY.SV, TLIST.SV, or TREL.SV) into execution to do the processing. Error messages are directed to the teletype and the output device. Note that a CONTROL-A interrupt stops all programs and returns to DOS without deleting the temporary disk files, PAGEZERO and HBUF.

## OPERATING PROCEDURES

## Input Message

DATAR is called by entry of a user input request starting with
the program name DATAR. The two valid messages are:

$$
\begin{aligned}
& \text { 1. DATAR [bout-device] } \\
& \text { 2. DATAR/ }\left(\begin{array}{l}
\text { B } \\
D \\
L
\end{array}\right) \text { [/sub-options] [bid] [GRECORDS/types] } \\
& \text { [bout-device] }
\end{aligned}
$$

where $b$ indicates a space.
Both messages result in the disk iperating system (DOS) loading the save file DATAR.SV and passing control to the $C I$ portion of DATAR. The ordering of the input groups is important and should be adhered to as illustrated above.

## Command Interpreter

The CI operates in two modes, interactive (conversational) and switch. The interactive mode is invoked by message type 1 above. The switch mode requires a more complex input message (type-2) but minimal user interaction. Also, the switch mode is easier to enter and is processed by the CI in less time.

## Interactive Mode

The interactive mode operates in the following manner. DATAR types an interactive request that includes all valid responses as shown in Table $X V$. The user must reply with either the full word response or the corresponding integer. Based upon the user response, DATAR either types another request or determines that the required input parameters have been obtained and passes control to processing. A user reply of COMBINATION (or 7) to request number 5 or of COMBINATION (or 8) to request 7 causes the $C I$ to type requests 6 or 8 , respectively. In either case, a 1 to 5 or 6 digit integer must be entered using the specified digits from the preceding request. Also, a user reply of LIST to request 9 causes the CI to type a list of the numbers and names of all devices defined in the Equipment Table. Following the list, the $C I$ reissues request 9. The user may respond with numbers or names, but repetitions are ignored. A list of requested devices

Table XV

Interactive Requests and Responses for DATAR

| Request Number | Text |
| :---: | :---: |
| 1 | ENTER SUT RUN NAME. |
| 2 | ENTER OPTION: BRIEF (1), DETAILED (2), OR LIST (3). |
| 3 | ENTER YES (1), OR NO ( $\emptyset$ ) FOR PLOT. |
| 4 | ENTER SUB-OPTION: INTERVAL (1), SPECIFIC (2), ORDERED (3). |
| 5 | ENTER SUB-OPTION: INTERVAL(1), SPECIFIC(2), ORDERED(3), ACTUAL(4), OCTAL(5), RELATIVE(6), OR COMBINATION(7). |
| 6 | ENTER COMBINATION AS 1 TO 5 DIGIT INTEGER USING 2 TO 6 ABOVE. |
| 7 | ENTER RECORD KEY: ALL(1), HISTORY(2), SCENARIO(3), QUERY (4), RESPONSE (5), COMMAND (6), ERROR (7), OR COMBINATION (8). |
| 8 | ENTER COMBINATION AS 1. TO 6 DIGIT INTEGER USING 2 TO 7 ABOVE. |
| 9 | ENTER DESIRED DEVICE NUMBERS OR NAMES SEPARATED BY BLANKS OR LIST. |
| 10 | ENTER YES(1), OR NO ( $\varnothing$ ), FOR START, STOP SPECIFICATION. |
| 11 | TO TERMINATE, ENTER END. <br> ENTER LOGICAL OR PHYSICAL RECORD START, STOP PRECEDED BY L OR P. |

is printed in the order defined by the Equipment Table. Figure 24 illustrates the various interactive paths to obtain the desired output.

The output device to be used must be specified in the original message. The optional input group, Out-device, has a value of \$TTO for the system teletype or \$LPT for the system line printer (the default output device).

## Switch Mode

The message which invokes the switch mode is given in general form by message type 2 above. One of the three switches (/B, /D, or /L) must accompany the program name DATAR, otherwise the interactive mode is entered. All switch letters were chosen to relate to the function performed and to simplify mnemonic identification.

B
The input group DATAR/D [suboption(s)] allows various combinations of option and suboption switches. One of the option switches B, D or L is required; if more than one is given, precedence is given first to B, then D. The option switches, listed in Table XVI, determine the type of output to be generated: brief summary, detailed summary, or listing.

The suboption switches are also listed in Table XVI. The suboption switches are meaningless for the $B$ option. For option D, only $0, S$, and $P$ are meaningful. . For the $L$ option, all are meaningful except $P$. The suboption switches specify the type of data to be included in the output option. They also determine if the data are to be given sequentially or on an individual device basis. If the data are to be given by device, the suboption switches tell DATAR whether all or user specified devices are to be examined.

The optional input group id specifies the run identification. It is the first $n(1 \leq n \leq 20)$ characters of the run identification given at the start of a real-time emulator run. If id is not given, DATAR uses data from the first run on the tape.
$14-41.734$


Table XVI
Option and Suboption Switches for DATAR

| Switch | Function |
| :---: | :---: |
| Options |  |
| /B | Brief Summary |
| /D | Detailed Summary |
| /L | Listing |
| Suboptions |  |
| /s | Examine only user specified devices |
| 10 | Examined all devices in order of E.T. |
| /P | Histograms of Response Distributions |
| /N | Name records for octal dump |
| /I | Print time intervals rather than actual times |
| /R | Print Relative times rather than actual times |
| /T | Octal format tape dump |

The optional input group RECORDS/type(s) specifies the type(s) of logical records to be included in the output. Valid switches are given in Table XVII. Any combination of values is allowed. Omission of this group implies all types. The B and D options ignore this group.

The optional input group Out-device is defined above under interactive mode.

The option and suboption switches may be combined as shown in Table XVIII and Figure 25. Table XVIII presents all meaningful input requests with a brief description of the output. (The optional input groups are not listed.) Figure 25 also illustrates the meaningful switch combinations.

## Summaries

There are two types of summaries, brief and detailed. The brief summary examines all records for all devices, listing error messages and gathering general statistics. The detailed summary gives similar statistics but does so by device. Note that the input group RECORDS is meaningless since both summaries examine all types of records.

## Brief Summary

The brief summary ignores suboption selections. The format of the brief summary output is illustrated in Appendix IV, Figure 26. The summary data in this figure is taken from the file with the run identification of "RUN FT7". A list of all error messages with associated device names precedes the summary data.

Various RRTC times are given in the following units: elapsed time is expressed in seconds to the nearest 100,000 th, response times in seconds to the nearest hundredth, total emulator CPU time in tens of microseconds, and percent emulator CPU to the nearest hundredth.

Figure 25 SWITCH TREE DIAGRAM FOR DATAR

Table XVII
Record Type Switches

| Logical Record | Symbol | Switch |
| :--- | :---: | :---: |
| HISTORY | H | $/ \mathrm{H}$ |
| RESPONSE | R | $/ \mathrm{R}$ |
| QUERY | Q | $/ \mathrm{Q}$ |
| SCENARIO INSTRUCTION | S | $/ \mathrm{S}$ |
| COMMAND | C | $/ \mathrm{C}$ |
| ERROR | E | $/ \mathrm{E}$ |

Table XVIII
Switch Combinations and Valid Inputs

| Input Message | Action Taken |
| :---: | :--- |
| 1. DATAR/B | BRIEF summary of all data preceded by a list <br> of error messages. |
| 2. DATAR/D [/P] or |  |
| DATAR/D/0 [/P] | DETAILED summary for each active device in the <br> order established by the Equipment Table. A <br> plot of response times is available as an <br> option (/P). <br> Same as above except only those devices <br> specified by the user (upon request) are <br> examined. |
| 4. DATAR/L/I | LIST all records in sequence written. Include <br> transmission time intervals, processing (task) <br> time intervals, and response times. |
| 5. DATAR/L/I/O | Same as above except list separately for each <br> active device. |
| Same as above except devices must be specified |  |$|$| by user. |
| :--- |

Table XVIII (Concluded)
Switch Combinations and Valid Inputs

| Input Message | Action Taken |
| :---: | :---: |
| 7. DATAR/L | ```LIST all records in sequence written. Include internal scenario address and actual clock times for start transmission and start/end task.``` |
| 8. DATAR/L/O | Same as above except list separately for each active device. |
| 9. DATAR/L/S | Same as above except devices must be specified by user. |
| 10. DATAR/L/R/O | LIST separately for each active device all records in sequence written. Include internal scenario address and start/end transmission times relative to LOGON and test start time. |
| 11. DATAR/L/R/S or DATAR/L/R | Same as above except devices must be specified by user. |
| 12. DATAR/L/T [/N] | ```LIST all records in sequence written in octal tape dump format. Naming of starting and stopping logical (or physical) record numbers is available as an option (/N).``` |
| 13. DATAR/L/T/O [/N] | Same as above except list separately for each active device. |
| 14. DATAR/L/T/S [/N] | Same as above except devices must be specified by user. |

The logical and physical record counts are given by the counts following the headings MESSAGES and RECORDS, respectively. The headings UN-R and UNSOLICITED specify unsolicited responses. The TERMINAL-MAX heading is used to name the terminal associated with the maximum response. The asterisk (*) following a scenario instruction type denotes a lower case character or a non-printable special character.

Detailed Summary
The detailed summary allows a device specification suboption as well as a special histogram output. The format of the detailed summary is illustrated in Appendix IV Figure 27. A 1ist of all requested devices to be examined is printed prior to summary data, and consists of either all devices defined in the Equipment Table or only those devices specified by the user.

The name of the file used in Figure 27 is "RUN FT7". A detailed summary is given for each active, requested device, and the name of the device is given as a terminal identification. Unsolicited responses are counted as record types. Also, the average and maximum RRTC response times are given in seconds, to the nearest hundredth. As in the brief summary, an asterisk (*) is used to identify nonprintable lower case and special characters which are used as scenario instruction types.

If requested, a histogram of response distribution is printed for each active device following the summary data. Figure 28, Appendix IV illustrates the format of the histogram. As can be seen, the name of the device is given at the top of each page and is followed, on the first page, by a list of all quarter-second response intervals which have a positive count and percentage. The count gives the actual number of responses which fall within the specified interval. The percentage is calculated by dividing the count by the total number of responses. All responses less than $\emptyset .25$ seconds are
included in the first interval, while all responses greater than $15 . \phi \emptyset$ seconds are shown in the $15 . \emptyset \emptyset$ second interval. If there are no intervals with a positive count, then a histogram is not generated.

Following the summary data (and histogram if requested) for the last active device, the program lists all requested devices which were found to be inactive during the run.

## Listings

There are basically four types of listings: octal tape, actual times, time intervals, and relative times. All these suboptions allow record selection based on device and/or record type. If the user decides to obtain the listing by device, then all devices defined in the Equipment Table must be requested or the desired device names and/or numbers must be specified in response to the interactive request number 9. A list of all requested devices will precede any data and a list of requested but inactive devices will terminate the listing.

The types of logical records to be listed may be selected by using the RECORDS input group. In the switch mode, all records are listed if the RECORDS group is omitted. The heading MESSAGE on each listing page refers to the logical record number of the first nonhistory record on the page.

## Octal Tape

The octal tape dump listing is used to print the contents of each logical record in octal byte format. The user may name the starting and stopping logical (or physical) record number by using the /N option. If starting and stopping numbers are given, the program skips all logical (or physical) records up to the start. It produces its octal output in logical record format and stops at the given logical (or physical) record number. Figure 29 in Appendix IV illustrates an octal tape listing output format. The user requested
that all devices in the Equipment Table.be examined and named the starting and stopping logical record numbers as 101 to 110.

As can be seen, the output for each active device gives the range limits and device name prior to the data. After a range is completed, the user may specify another range of limits or continue to the next active device. Note that there may not be records within the range associated with the given device (CTø in Figure 29). The character $P$ represents the physical record boundary.

Actual Times
The actual time listing contains the actual RRTC start of transmission and the start and end of task processing times. The values are taken directly from the record and listed in tens of microseconds. The actual time listing is the default suboption in the switch mode. Figure 30 in Appendix IV illustrates the format of the actual time listing.

As shown by the example in Figure 30, the user requests an actual time listing of Query and Response records ordered sequentially and output on the system teletype. The name of the run is "6-14 4:30 PM." For each record, the type of record is given followed by transmission start, task start, and task end times. The heading SCEN ADDR gives the location of the start of the scenario instruction relative to the beginning of the scenario, if any.

## Time Intervals

The time interval listing contains differences between the RRTC times. This listing also calculates response times as the difference between the start of transmission for a solicited Response and the end of transmission from the preceding query associated with the same device. Figure 31 in Appendix IV presents the format of the time interval listing.

The example in Figure 31 shows a time interval listing of "RUN2" in which the user chooses to specify the devices to be examined. For each active device, the terminal identification is given, followed by all the data associated with the particular device. For each record, the record type is given as well as the difference between the end and start of transmission time, the difference between the end and start of task processing time, and the cumulative emulator CPU time, all in tens of microseconds. The response times are given in seconds to the nearest hundredth.

## Relative Times

The relative time listings are by device with user specification of devices being the default case. Figure 32 in Appendix IV illustrates the format of a relative time listing.

In the example shown in Figure 32, the user requests that all

- devices defined in the Equipment Table be examined. Both the runstart time and the user start time (UST) are given in tens of microseconds. The run-start time is the start of transmission of the first non-history record in the file. The UST is the start of transmission of the first Query or solicited Response associated with the device. A value of BELOW is given for UST if a Query or solicited Response is not the first record type in the file for the particular device. For each record, the record type is given in addition to the start and end of transmission minus the UST, the start and end of transmission minus the end of transmission time of the previous Query, and the location of the scenario instruction (as SCEN ADDR) relative to the beginning of the scenario, if any.


## ERRORS

There are several error conditions recognized by the various programs. Table XIX lists all error conditions and messages that may

Table XIX
DATAR Error Message File (ERFILE)

| Number | Message | Cause or Corrective Action |
| :---: | :---: | :---: |
| 1 | Invalid option | Submit valid option. |
| 2 | Invalid termination option | Submit valid option. |
| 3 | Invalid sub-option or key (record) | Submit valid option. |
| 4 | Invalid device specification | Submit valid device name or bad device address logged. |
| 5 | Disk file accessing error (read/write) | Error from DOS, disk file may be missing. |
| 6 | End-of-file (on tape) | End of run. |
| 7 | Invalid tape identification | Log tape file incorrectly logged. |
| 8 | Unrecognizable message type | Bad record type logged. |
| 9 | Zero length record found | Two successive records with zero word length. |
| 10 | Illegal program call | Overlay problem, maybe disk file is missing. |
| 11 | $\begin{aligned} & \text { Command instruction } \\ & \text { missing } \end{aligned}$ | C-type record with null text. |
| 12 | DISK SPACE exhausted | Not enough disk for temporary files or overlay. |
| 13 | Invalid device table format | Equipment Table not second record in file. |
| 14 | Tape read error | Tape drive problems, may not be mounted properly. |

occur, during execution of DATAR.
The general format of the error message is:
RECORD m, WORD $n$ : error message text
where $m$ specifies the physical record that contains the erroneous logical record and $n$ specifies the first word of the logical record relative to the start of the in-core buffer containing the record. Many of the conditions allow the user to start over or submit another choice. However, some (such as tape and disk errors) are unrecoverable. The cause of error condition and/or corrective action for each error is also given in Table XIX.

SAVING TEST DATA
After analyzing the test data with DATAR, the user may wish to save the data for future analysis on the NOVA 800 or some larger computer. A program (MASTR) has been written to transfer data from a $\log$ tape to a master $\log$ tape (to consolidate tapes or to get comparable runs on one tape). The master tape (or original tape) may then be used as input to DATAR to analyze the run again or compare a series of runs manually. In addition, more sophisticated statistical methods may be employed to produce more meaningful statistics for comparing and evaluating an SUT.

## Program Description

In general, the MASTR program (written for a one tape drive system) reads the data from the input log tape, temporarily stores it in a file on disk, waits for the output (master) tape to be mounted, writes the data from disk onto the tape as the last sequential file, and terminates the file with two end-of-file (EOF) marks. If disk storage is insufficient to complete the transfer in one pass, the program continues through as many passes as necessary, each time notifying the user that an additional pass is required. Obviously, a
multiple pass transfer requires input and output tapes to be mounted and dismounted several times.

## Input Message

MASTR requires two user supplied input parameters: a run identification (used to locate the test run) and the amount of available disk space (used as temporary storage). The two commands that activate the tape transfer program are:

1. MASTR ${ }_{\Omega}$
2. MASTR id ds d
(Although message 1 appears more concise, note that requests to supply values for the input groups id and ds will be issued by the program.) The first input group, id, specifies the first $n(1 \leq n \leq 20)$ characters of the run identification as found in the Identification-History record, the first logical record logged. The run identification, which was entered at the start of emulation, is required to allow access to different runs on multiple run tapes.

The second group, ds, is the number of unused disk blocks available for temporary storage. The program uses ds-2 blocks to protect the used portions of disk. The number of unused blocks is given by the DOS command DISK. This number can be increased by deleting disk files no longer in use. A good approximation for the number of blocks required for a single pass transfer is the number of physical records used for the run (obtained from the record count in a brief (/B) summary) plus five (two for the unused blocks and three for disk file linkage words). This number must be multiplied by the ratio of physical record size to disk block size, which presently is 1.

## Operation

The MASTR program is called by one of the input messages described above. It obtains the run identification and disk size from the input message (\#2), or as responses to the program commands ENTER RUN

IDENTIFICATION and ENTER AMOUNT DISK LEFT. The program then issues the command:

MOUNT INPUT TAPE, STRIKE CARRIAGE RETURN
and waits for a carriage return. Upon receipt of the carriage return, MASTR locates the first file (on the input tape) that contains the specified run identification as the first $n$ characters in the HistoryIdentification record.

The program uses the disk size and physical record size to calculate the number of tape records that can be written in the temporary disk file, MITCHTEMP. MASTR reads the tape until disk space is exhausted or an EOF mark is encountered. If disk space is insufficient the message:

NOT ENOUGH DISK.
REMOUNT INPUT TAPE AFTER OUTPUT TAPE IS WRITTEN.
notifies the user that one or more additional passes are necessary to complete the transfer. This implies remounting the input tape after the first segment is transferred to the master tape.

After the disk file is written, MASTR issues the command:
MOUNT OUTPUT TAPE, STRIKE CARRIAGE RETURN
and waits for the carriage return. Upon receipt, the program locates the double EOF mark on the master and writes all the data from the disk file onto the output tape, overwriting the second EOF of the preceding run. If an additional pass is necessary, the program requests that the input tape be mounted and continues the loop until the transfer is completed. Upon completion, the message:

LOG TAPE TRANSFER COMPLETE
is output and two EOF marks are written. The first EOF terminates the file while the second indicates that the file is the last one on the
tape. Note that a tape intended to be a master must be initialized by the DOS command INIT/F MTD prior to the transfer operation. The command writes two EOF marks at the beginning of the tape.

The program does not check the run identification of each file on the output file. Therefore, files may be written with duplicate file names. However, only the first file with a duplicate file name is accessible.

## Errors

The MASTR program checks for various error conditions. If an error exists, a message is output and the transfer terminates by returning to DOS. Table XX lists the error conditions, messages, and suggested corrective action. Remember that files on a master tape are only as unique as the run identification given at the start of the emulation test.

Table XX

MASTR Error Message File

| ERROR MESSAGE | ERROR CONDITION | CORRECTIVE ACTION |
| :---: | :---: | :---: |
| 1. NOT ENOUGH DISK | Space too small for one physical tape record. | Delete some files and specify larger number. |
| 2. ERROR LOCATING INPUT FILE | Invalid run id, illegal format, tape read error. | Check id, format, read errors by using DATAR/B with and without run id. |
| 3. DISK ERROR | Trouble writing/reading file MITCHTEMP. | Ensure disk accessibility. Try again. |
| 4. INPUT TAPE READ ERROR | Tape equipment or parity problem. | Check channel number unit ready, etc. Otherwise, fatal parity error. |
| 5. OUTPUT TAPE WRITE ERROR | Tape equipment or parity problem. | No double EOF or check channel number unit ready, write lockout, etc. Other wise, fatal parity error. |
| 6. EOF WRITE ERROR | Tape equipment or parity problem. | No double EOF or check channel number unit ready, write lockout, etc. Other wise, fatal parity error. |
| 7. ERROR LOCATING OUTPUT FILE | No second EOF, tape equipment or parity problem. | Check equipment, initialize tape if never done before. |

## SECTION IX

EXECUTION TIMES

## REAL-TIME INSTRUCTIONS

Because of the variety of scenario instructions available to the user, it may be possible in some instances to accomplish the same task using more than one method, or combination of scenario instructions. In these cases, execution timing for scenario instructions may be a consideration in determining maximum scenario efficiency.

Table XXI gives the current best estimates of real-time emulator execution times. The times given represent the total cost (Scenario Interpreter as well as Real-Time Exec execution time) in microseconds of emulator CPU time for executing each function once. The functions timed include two miscellaneous functions (logging and the receipt of an unsolicited response) followed by the scenario instruction types given in the same order as Table XVI of Volume 2 of this series followed by the command types in alphabetical order.

The data were obtained by making a very large number of short runs on the field-test emulator. In most cases, a run consisted of executing a single scenario for a single device. After performing its task, the scenario executed a QUIT command. The data reduction brief summary operation was used to obtain the CPU time.

The general technique used was to execute the desired function 1000 times in a loop, as in the case below of one of the scenarios used to test the add instruction:

| 1 | 3 | A 12 |
| :--- | ---: | :--- |
| 2 | 3 | C [ LOG ALL OFF ALL |
| 3 | 22 | 1 1000 R9 |
| 4 | 28 | L LOOP |

Table XXI
Execution Times
Real-Time Scenario Instruction
(in microseconds)

| Function | Execution Time | Footnotes |
| :---: | :---: | :---: |
| Miscellaneous Functions |  |  |
| Logging | $1778+6.6 \mathrm{~b}$ |  |
| Receipt of Unsolicited Response | $2325+231 \mathrm{r}$ | 10, 26 |
| Control Instructions |  |  |
| R | $4397+477 \mathrm{i}$ | 10, 26, 27 |
| $\mathrm{R}^{\prime \prime}$ | $3312+220 \mathrm{r}$ | 10, 26, 27 |
| Q | $1721+255 q$ | 10, 26, 27 |
| I | $3845+440 \mathrm{i}$ | 10, 26, 27 |
| O | $953+213 q$ | 26, 27 |
| ; | 679 | 27 |
| : | 679 | 13, 27 |
| C | - | 10, 14, 27 |
| E | $3137+54 \mathrm{e}$ | 10, 15, 27 |
| E | 1333 | 10, 16, 27 |
| D | 1305 | 1, 3 |
| W | 1458 | 1, 3, 13 |
| d | 1236 | 1, 3 |
| e | 1136 | 5, 27 |
| X | 7490 | 7, 27 |
| 7 | 761 | 3 |
| 8 | 2703 | 1, 17, 27 |

Table XXI (Continued)
Real-Time Scenario Instruction Execution Times

| Function | $\begin{gathered} \text { Execution } \\ \text { Time } \end{gathered}$ | Footnotes |
| :---: | :---: | :---: |
| Arithmetic and Logical Instructions |  |  |
| + | 691 | 1, 27 |
| - | 691 | 1, 13, 27 |
| * | 768 | 1, 27 |
| 1 | 788 | 1, 6, 27 |
| \& | 708 | 3 |
| Assembler Directive Instructions |  |  |
| L | - | 18 |
| a | - | 18 |
| blank | - | 18 |
| t | - | 18 |
| i | - | 18 |
| Branch and Comparison Instructions |  |  |
| J | 631 | 3 |
| B | 689 | 1, 2, 3, 13 |
| U | 689 | 1, 2, 3, 13 |
| $>$ | 689 | 1, 2, 3, 13 |
| $<$ | 689 | 1, 2, 3 |
| G | 689 | 1, 2, 3 |
| H | 689 | 1, 2, 3 |
| M | $657+39 \mathrm{~m}$ | 3, 19 |
| S | $677+39 m+48 n$ | 1,9, 27 |
| Y | 719 | 2, 3 |

Table XXI (Continued)
Real-Time Scenario Instruction Execution Times

| Function | Execution Time | Footnotes |
| :---: | :---: | :---: |
| Branch and Comparison Instructions (continued) |  |  |
| n | 719 | 2, 3, 13 |
| 9 | 721 | 2, 3 |
| q | 722 | 2, 3, 13 |
| K | 682 | 3, 20 |
| 3 | 682 | 3, 4, 20 |
| P | $1016+43.5 p$ | 3 |
| Core Memory Allocation Instructions |  |  |
| A | 1361 | 3, 10 |
| F | 7858 | 10, 21, 27 |
| Move Instructions |  |  |
| 1 | 668 | 3 |
| g | 687 | 27 |
| p | 687 | 1, 27 |
| 5 | $621+43.6 \mathrm{t}$ | 27 |
| T | 640 | 27 |
| = | 717 | 27 |
| Z | 715 | 13, 27 |
| V | $663+21.6 c$ | 13, 27 |
| 6 | $647+21.6 c$ | 27 |
| 1 | 708 | 27 |
| @ | 748 | 27 |
| r | 680 | 27 |
| c | 666 | 4, 27 |
| h | 684 | 27 |

Table XXI (Continued)
Real-Time Scenario Instruction Execution Times

| Function | $\begin{gathered} \text { Execution } \\ \text { Time } \end{gathered}$ | Footnotes |
| :---: | :---: | :---: |
| Diagnostic Instruction |  |  |
| ? | - |  |
| Commands |  |  |
| DUMP | - |  |
| ERROR | 3950 |  |
| LOG | 5355 | 12, 22 |
| MONITOR | 7204 | 12 |
| QUIT | 2060 | 23 |
| RESTART | 12,571 | 12, 24 |
| SCALE | 5046 | 25 |
| START | 14,540 | 8, 10, 11, 12 |
| STATUS | 4681 | 12 |
| STOP | 12,571 | 12, 24 |
| SUB | 7490 | 7, 10, 11 |
| SUB | 7858 | 10, 11, 21 |

## Table XXI (Continued) <br> Real-Time Scenario Instruction Execution Times

## Nomenclature

$b=$ length of MESBF (variable or text) portion of buffer to be logged, in bytes
$c=$ number of bytes for which longitudinal redundancy check (LRC) byte is calculated
$e=$ length of error message in type-E scenario instruction, in bytes
$i=$ length of query and length of response, in bytes
$m=$ number of bytes successfully matched
$\mathrm{n}=$ number of bytes unsuccessfully matched
$\mathrm{p}=$ number of bytes parity checked
$q=$ length of query transmitted, in bytes (those up to, but not including, the first NULL (zero) byte in a query buffer)
$r=$ length of response received, in bytes
$t=$ number of bytes transferred from (contained in) a type-5 scenario instruction to a query buffer

Table XXI (Continued)
Real-Time Scenario Instruction Execution Times

## Footnotes

Add 6.4 microseconds for each field of type 10 or 11 which contains a Register number.

Add 6.4 microseconds if the branch is not taken.
(3) Add 14 microseconds if instruction starts at an even byte.
(4) Add 26.8 microseconds if initial value of RGRPT points to an odd byte.
(5) The time includes the time for the type-e instruction plus the additional time for the following (executed) instruction over what it would be if executed normally rather than by the execute instruction. The normal execution time of the following instruction is excluded. Increased time over most other instructions is spent in scenario management code in the Exec. The type-e instruction causes two changes in the scenario associated with the device. The time given includes the time to free each core page when control passes to the other but no time to read pages from disk since the core pages were not overlaid. If one or both core pages were in use by other devices, the freeing time would be less, but if all core pages were in use, the type-e instruction could require disk reads to be done.
(6) Execution time varies by 11.4 microseconds from minimum to maximum, depending upon values used.

Includes time to execute the SUB command with scenario specified and the type-X scenario instruction. Includes time to allocate set of Registers but not the time to free them.

Includes the time to start the scenario for the named device and to terminate that scenario by execution of end-of-scenario.

Add 13 microseconds if branch not taken. If substrings of the instruction string occur in the response, the number of comparisons may be relatively large. For instance, if the response ABCABACABABCABABACABABABC is searched for the string ABABABA, then $m=27$ and $n=19$ and the execution time is 2642 microseconds.

## Table XXI (Continued) <br> Real-Time Scenario Instruction Execution Times

## Footnotes

(10) Execution time will vary depending upon the number of blocks in the free chain which have to be examined to find a large enough block to allocate and/or to find the proper place in the chain to place a freed block.
(12) Execution time will vary depending upon the number of Equipment Table entries which have to be examined before the named one is found, and this number may be different depending upon whether hierarchical equipment names are used in the command or not.
(13) Time estimated based on measured time for a similar instruction.
(15) Time with error-message logging enabled. Time includes logging time.

Time with error-message logging disabled.
Includes time to log the response. If response logging is disabled, the instruction is equivalent to a NOP.

Assembler directives are not executed in real-time and are not even included in the internal scenario.

Add 24 microseconds if the branch is taken. If an m-character compare is made, the first four characters match, but the fifth one does not, the execution time should be $657+4(39)+24=837$ microseconds.

Add 10 microseconds if branch not taken.
Includes time to execute the SUB command with scenario specified and the type-F scenario instruction.

Table XXI (Concluded)
Real-Time Scenario Instruction Execution Times
(22) Time for LOG ALL OFF ALL
(23) Time through the time the record is logged. Certain termination activities are performed after logging.
(24) Includes time to execute RESTART of a named device and time to execute STOP THIS for the named device.
(25) Time will vary depending upon number of digits in scale factor to be converted. Conversion time is 31 microseconds per decimal digit.
(26) Using an asynchronous line adapter at 10 characters per second.
(27) Add 12.8 microseconds if instruction starts at an odd byte.

| 5 | 28 | + R9 34 Rl1 |
| :--- | :--- | :--- |
| 6 | 34 | + R8 1 R8 |
| 7 | 40 | U LOOP R9 R8 |
| 8 | 47 | C [ LOG ALL ON C |
| 9 | 63 | $C[$ QUIT |

Instruction 5 is the one being timed. Instructions 6 and 7 are for loop control and instruction 3 controls the iteration count. Instructions 2 and 8 turn logging off and then back on to capture the final CPU time value. Such scenarios were run two or more times each to check the degree of reproducibility.

A second, base scenario was then prepared, identical to the above except that instruction 5 was eliminated. The second scenario was then run two or more times, and the most representative CPU time value was chosen for each of the two scenarios. The difference between these values divided by the iteration count gives the function execution time.

The contents of the two scenarios were varied depending upon the function being timed. In the case of several of the commands, more than one scenario had to be run concurrently. The iteration count was reduced to 100 for the miscellaneous functions, for some of the commands, and for the query instructions. In any case, an appropriate base scenario was always constructed and run so that the difference in CPU times would isolate the function or functions being timed (a few of the functions cannot be executed multiple times independently of other functions).

The measured results were given general reasonableness checks and were also evaluated by comparing differences between measured results for different functions (primarily the scenario instructions) and differences obtained from NOVA instruction counts for the same functions. No attempt was made to verify the absolute values given
in Table XXI because of the complexity of the emulator system. The relative comparisons checked reasonably well, although certain differences have not yet been explained. The data in Table XXI cannot be regarded as precise. The presence of a zero in the units position cannot be regarded as indicating low precision nor can the presence of a decimal place be regarded as indicating high precision in all cases. In the latter case, the increments given in the table proper for those functions whose execution times vary with string length, the increments given were obtained by computations on the measured results, although these increments checked rather well in those cases in which instruction counts were made. The increments given in the footnotes are generally more precise since most of them are based on instruction counts (assuming that the CPU clock is accurate).

The relative comparisons made for approximately 15 scenario instruction types indicate precision varying from 0 to 35 microseconds. No formal comparisons were made for the commands although it appears possible that much larger discrepancies may be present. In particular, from scanning the code for the LOG command and the MONITOR command, it does not seem reasonable that the latter should require nearly 2 milliseconds more than the former. It should also be noted that a typical command generally provides many more options than a typical instruction and, therefore, will result in a much greater range of execution times. It was not possible to time and report each option of each function. In addition, as the footnotes show, a number of run-dependent factors can significantly affect the timing results.

Several factors are present which would make it very costly to attempt to resolve the discrepancies noted above. At least 700 runs were made to obtain the current data. Most of these lasted several seconds in real-time, but some lasted a minute or two. The results had to be listed, recorded, and analyzed. Most of the scenarios were run two or more times each since the results frequently showed some
variation in total emulator CPU time. It was felt that replicated runs should agree within possibly 10 to 30 microseconds based on early experience with the simpler instructions. In the case of some of the commands and query instructions, the total variation was sometimes 200 or 300 microseconds. In the case of a common base scenario run a number of times over a two-month period, the total variation was 3200 microseconds (for a $1 \frac{1}{2}$ second run - $0.2 \%$ ). It seems likely that these variations are the result of clock frequency variations, possibly the result of temperature differences. The clocks involved were the CPU clock, the Readable Real-Time Clock used for timing measurements, the "Real-Time Clock" used for response timeouts and which places a continuous overhead on the DVM, and the line-adapter clocks in the case of query instructions. In addition, it is known that the timing characteristics of the magnetic tape drive have a rather coarse control, and the tape drive had to be used in all runs to record at least the first and last event of the run.

A cause of greater variation in execution times is the fact that the NOVA computer has only very superficial byte-manipulation ability. The Exec uses 12.8 microseconds more to fetch the two-byte scenario instruction length field (for any instruction) from two adjacent words (when the instruction starts at an odd byte) than when it starts at an even byte. The Scenario Interpreter uses 26.8 microseconds more to fetch a two-byte operand (contained in certain instructions) when the instruction starts at an even byte than when it starts at an odd byte. The effect of these differences is that to achieve the best results one needs to examine the starting byte of each instruction in a scenario (or at least those within the loop) and make adjustments in case of differences between a base scenario and a timing scenario. One may also need to modify both scenarios by adding one or more instructions or changing their positions to cause cancellation of the even-odd effects. The nature and magnitude of this problem were only realized after a number of runs were made, instruction counts were made for
portions of certain instructions, and relative comparisons were made. Making such even-odd corrections for a large number of scenarios would be quite time consuming.

In the case of the START and SUB commands, the present implementation reads at least the first two bytes of the Scenario name from the command for each Scenario Directory (SD) entry encountered. If 30 entries have to be compared and the scenario name starts at an odd byte in the command, the execution time for the command is 800 microseconds more than if the scenario name started at an even byte. (To force the even byte case, an odd number of blanks must occur after "START" and before the scenario name if the device name contains one digit.) To control this situation, the length and content of the SD as well as the location of the scenario name within a command must be controlled.

In the case of commands which contain device names, a further variation can arise. A total of 26.8 to 80.4 microseconds more will be used if the device name or "THIS" starts at an odd byte in a command. A total of 31 microseconds is used to convert each digit (after the two initial characters) in a device name. The execution time will further vary depending upon the number of Equipment Table (ET) entries which have to be searched. The number of entries searched will depend upon the ordering and linking of the ET entries and whether or not hierarchical equipment names are used in commands.

If the above factors are handled properly, one may be able to obtain relatively accurate results for the tests run. Certain additional factors need to be considered before applying the results. Of necessity, the tests were run under conditions whereby there was little competition for resources within the emulator. As the number of active, emulated devices increases, allocable core memory becomes splintered and those functions which must allocate and/or free core memory will use up more emulator CPU time. When a block is to be
freed, each link in the free chain which must be examined, uses up 7 microseconds of CPU time, and approximately the same amount of time is needed during allocation. If an average of 25 links needs to be examined, the cost is 175 microseconds for each allocate or free operation. Every command executed requires the allocation of a command buffer, freeing of the command buffer, and allocation of an error-message buffer for the response to the command and may also require the freeing of a previous error-message or query buffer. In addition, 6 or 7 instructions (see footnote 10) and one of the miscellaneous functions allocate and/or free core memory. There is no dynamic measure of the length of the free chain, but the timing tests probably only caused a free chain of five or ten links. Very little logging was done (from 2 to 6 records per run), but each record logged requires one allocation and one free operation (for a Register Stack).

Scenario management can also have a significant effect on individual execution times. If an instruction spans a scenario page boundary, it must be buffered and a new scenario page becomes the active page for the device. The cost of the latter operation is in the vicinity of 200 microseconds. If the new page must be read from disk, the cost is greater. When the number of active scenario pages in core approaches the number of core pages allocated, a disk read may be required for each scenario instruction fetched from a new page. The emulator is designed to cope with this situation to handle peak loading problems. If an emulator module operates in this mode more than a relatively small fraction of the time, it is overloaded and its load should be reduced.

The data given in Table XXI ignores the effect of any error conditions. The only error messages allowed for are the normal responses to commands.

The "Real-Time Clock", used for response timeouts, provides a continuous overhead estimated at between $0.2 \%$ ( 2000 microseconds per
second of elapsed time) and $0.3 \%$. The effect of this overhead has been ignored in Table XXI in those cases in which emulator \%CPU time was near $100 \%$ since the effect on a 700 -microsecond instruction is only 1 or 2 microseconds. In those cases in which the \% CPU time was lower (primarily some of the commands, the query instructions, the delay and wait instructions, and the miscellaneous functions), the emulator CPU times for the base scenario and the timing scenario were corrected for this overhead based on elapsed time, generally using a conservative $0.2 \%$ factor. This overhead is present throughout the elapsed time of a run, regardless of the amount of emulator activity.

NON-REAL TIME PROGRAMS
It is difficult to give anything but intelligent estimates as to the running times of the non-real-time programs. This is because of the many variables involved which determine execution times for each of the programs. Presented here is a sample problem for each program, with key characteristics defined, and approximate running times given. The times are based on an average derived from several runs of each program, and may vary within a 5 second range.

SSUB
The example shown in Appendix VI, Figure 33 shows a scenario called 34FORTN with macro calls not yet expanded. Figure 34 in Appendix VI shows the same scenario, now called FORTN, with macros expanded. The libraries which contain the macro definitions are given in Figure 35. The table below summarizes the key characteristics pertinent to the macro processing of this example. In this case, the macro processor takes about 20 seconds to complete execution.

| number of libraries | 2 |
| :--- | ---: |
| number of macros in libraries | 16 |
| length of file without macros <br> expanded | 1172 |

```
length of file with macrosexpanded
number of macro substitutions 185
```


## MACDEF

The program used to generate macro libraries is MACDEF. The execution time of this program depends on characteristics summarized in the table below for the example shown in Appendix VI, Figure 35, the KAPLIB library.

| number of definitions | 3 |
| :--- | ---: |
| length of input file | 205 |
| length of output file (.ML) | 203 |

Execution time to create KAPLIB.ML from KAPLIB is 4 seconds. CVT

The scenario assembler program may convert the FORTN scenario (Figure 34) into an internal scenario by using any of its three printing options. Average times for execution are 35 seconds for assembly with no listings (CVT/N option), 55 seconds for assembly with partial listings (CVT/P option), and 3 minutes 10 seconds for assembly with complete listings (CVT option). These times, of course, reflect to some degree, the speed of the printer. The table below summarizes the key characteristics pertinent to the Assembly of the example.

| label definitions | 22 |  |
| :---: | :---: | :---: |
| other label references | 24 |  |
| queries | 25 |  |
| arithmetic instructions | 133 |  |
| search instructions | 22 |  |
| commands | 3 |  |
| assembler directives | 3 |  |
| other instructions | 159 |  |
| Total instructions | 391 |  |
| length, in bytes, of interna | scenario | 2435 |

DATAR
The data reduction program processes the log tape written during an emulation run and can produce many combinations of listings and summaries. Execution times for all combinations are too cumbersome to be presented here. The table below describes the key characteristics pertinent to the data reduction of a log tape from a sample emulation of the Fortran Cost scenarios presented in Figures 33-35.

| number of physical records | 43 |
| :--- | ---: |
| number of logical records | 376 |
| number of internal scenarios in | 70 |
| directory | 26 |
| number of devices in ET | 2 |
| number of active devices | 123 |
| number of queries | 226 |
| number of responses | 1 |
| number of scenario instructions |  |
| number of lines of output for |  |
| relative-time listing | 660 |

Using such a log tape, the data reduction program produces a brief summary in 20 seconds and a relative-time listing for a single emulated device in an average of 4 minutes. These times are for processing of a file which is the first file on a log tape. If more than one emulation file is on a tape (perhaps a tape created by the MASTR program) the DATAR program rewinds to the beginning of tape and re-searches for the correct run every time it begins a new device listing for the run. This, of course, may consume considerably more time.

## MASTR

The execution time of the MASTR program depends on several factors, as described in the table below:

```
number of physical records in run
disk space available
file number of MASTR tape
```

Also included in the complete execution time is the length of time it takes the user to dismount the original log tape and mount the MASTR tape, for as many times as is needed to complete the transfer. Therefore it is unrealistic to give any meaningful timing estimates.

## REFERENCES

1. Data General Corporation, Disk Operating System User's Manual, 093-000048-03, Southboro, Massachusetts, 1971.
2. Data General Corporation, NOVA Editing Routines, 093-000018-02, Southboro, Massachusetts, 1971.
3. Data General Corporation, File Check Program, 093-000071-00, Southboro, Massachusetts, 1971.
4. Data General Corporation, Tape Dump Program, 093-000059-01, Southboro, Massachusetts, 1971.
5. Data General Corporation, How to Use the NOVA Computers, Southboro, Massachusetts, 1971.

## APPENDIX I

Conversion Codes for IBM 2741

Because some of the 2741 control characters do not have a direct counterpart in the ASCII character set, an exact mapping was not possible. Table XXII is a list of the 2741 control characters, and their position in the ASCII table. This same mapping was used in the 2741 conversion code tables used for the on-site model of the emulator.

Table XXIII represents the conversion codes used by the Scenario Assembler for 2741 EBCDIC odd parity code, with the parity bit as the right-most bit. The "lab" conversion is used on the fixed-site model of the Emulator when emulating an IBM 2741 terminal using Data General's software driven data communcations multiplexor. The "field" conversion reverses the order of the bits, and is used on the on-site model of the Emulator when emulating an IBM 2741 terminal using Digital Computer Controls asynchronous line adapters.

Table XXII
Control Characters for IBM 2741 Terminal

| 2741 |  | ASCII |  |
| :---: | :---: | :---: | :---: |
| Octal | Character | Octal | Character |
| 037 | EOT $=$ control D | Ф04 | EOT $=$ end-of-transmission |
| 135 | $\mathrm{BS}=$ backspace | 010 | BS = backspace |
| 172 | HT $=$ horizontal tab | 011 | HT $=$ horizontal tab |
| 073 | LF = line feed | 012 | LF $=$ line feed |
| 130 | RES = restore | ¢14 | $\mathrm{FF}=$ form feed |
| 133 | $\mathrm{NL}=$ new line | 015 | $C R=$ carriage return |
| 034 | $\mathrm{UC}=$ upper case | 016 | $\mathrm{SO}=$ shift out |
| 174 | LC = lower case | 017 | SI $=$ shift in |
| 031 | PN = punch on | $\square 22$ | DC2 = device control 2 |
| 032 | $\mathrm{RS}=$ reader stop | 023 | DC3 = device control 3 |
| 171 | PF = punch off | 024 | DC4 = device control 4 |
| 136 | $\mathrm{IL}=\mathrm{idle}$ | 026 | SYN = synchronous idle |
| 075 | $E O B=$ end-of-block | 027 | ETB $=$ end-of-block |
| 076 | PRE $=$ prefix | $\emptyset 33$ | ESC = escape |

Table XXIII
Conversion Code Table used for IBM 2741 Terminal

| ASCII <br> CHARACTER | $\begin{gathered} \text { ASCII } \\ \text { CODE } \end{gathered}$ | $\begin{aligned} & 2741^{*} \\ & \text { LAB } \\ & \text { CODE } \end{aligned}$ | $\begin{aligned} & 2741 \\ & \text { FIELD } \\ & \text { CODE } \end{aligned}$ | $\begin{gathered} \text { ASCII } \\ \text { CHARACTER } \end{gathered}$ | $\begin{aligned} & \text { ASCII } \\ & \text { CODE } \end{aligned}$ | $2741^{*}$ <br> LAB CODE | $\begin{aligned} & 2741 \\ & \text { FIELD } \\ & \text { CODE } \end{aligned}$ |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| NuL | 000 |  |  | SP | 040 | U 001 | 100 |
| SOH | 001 |  |  | ! | 041 | (1) 127 | 165 |
| STX | 002 |  |  | " | 042 | (1) 026 | 064 |
| ETX | 003 |  |  | \# | 043 | I. 026 | 064 |
| EOT | 004 | C 037 | 174 | \$ | 044 | L. 127 | 165 |
| ENQ | 005 |  |  | \% | 045 | (1) 013 | 150 |
| ACK | 006 |  |  | \& | 046 | L 141 | 103 |
| bel | 007 |  |  | 1 | 047 | U 015 | 130 |
| BS | 010 | C 135 | 135 | ( | 050 | U 023 | 144 |
| HT | 011 | C 172 | 057 | ) | 051 | U 025 | 124 |
| LF | 012 | C 073 | 156 | * | 052 | - 020 | 004 |
| vT | 013 |  |  | + | 053 | U 141 | 103 |
| FF | 014 | C 130 | 015 | , | 054 | L 067 | 166 |
| CR | 015 | C 133 | 155 | - | 055 | L 100 | 001 |
| So | 016 | C 034 | 034 | - | 056 | L. 166 | 067 |
| SI | 017 | C 174 | 037 | 1 | 057 | L 043 | 142 |
| DLE | 020 |  |  | $\emptyset$ | 060 | L 025 | 124 |
| DC1 | 021 |  |  | 1 | 061 | L 002 | 040 |
| DC2 | 022 | C 031 | 114 | 2 | 062 | L 004 | 020 |
| DC3 | 023 | C 032 | 054 | 3 | 063 | L 007 | 160 |
| DC4 | 024 | C 171 | 117 | 4 | 064 | L 010 | 010 |
| NAK | 025 |  |  | 5 | 065 | L 013 | 150 |
| SYN | 026 | C 136 | 075 | 6 | 066 | L 015 | 130 |
| ETB | 027 | C 075 | 136 | 7 | 067 | L 016 | 070 |
| CAN | 030 |  |  | 8 | 070 | L 020 | 004 |
| EM | 031 |  |  | 9 | 071 | L 023 | 144 |
| SUB | 032 |  |  | : | 072 | บ 010 | 010 |
| ESC | 033 | C 076 | 076 | ; | 073 | 〕 007 | 160 |
| FS | 034 |  |  | $<$ | 074 | U 004 | 020 |
| GS | 035 |  |  | $=$ | 075 | U 002 | 040 |
| RS | 036 |  |  | > | 076 | U 016 | 070 |
| vs | 037 |  |  | ? | 077 | U 043 | 142 |
|  |  |  |  | a | 100 | L 040 | 002 |

```
* C = control
    U = upper case
    L = lower case
```

Table XXIII
Conversion Code Table used for IBM 2741 Terminal (Concluded)

| $\begin{gathered} \text { ASCII } \\ \text { CIARACTER } \\ \hline \end{gathered}$ | $\begin{aligned} & \text { ASCII } \\ & \text { CODE } \\ & \hline \end{aligned}$ | 2741 <br> LAB <br> CODE | $\begin{aligned} & 2741 \\ & \text { FIELD } \\ & \text { CODE } \\ & \hline \end{aligned}$ | ASCII CHARACTER | $\begin{aligned} & \text { ASCTt } \\ & \text { CODE } \\ & \hline \end{aligned}$ | $\begin{aligned} & 2741 \\ & \text { WA } \\ & \text { CODE } \end{aligned}$ | $\begin{aligned} & 2741 \\ & \text { FTEID } \\ & \text { CODE } \\ & \hline \end{aligned}$ |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| A | 101 | (1) 142 | 043 | a | 141 | I. 142 | 043 |
| F | 102 | (1) 144 | 023 | b | 142 | I. 144 | 023 |
| c | 103 | (1) 147 | 163 | c | 143 | I 147 | 163 |
| D | 104 | (1) 150 | 013 | d | 144 | I. 150 | 013 |
| E | 105 | (1) 153 | 153 | e | 145 | 1. 153 | 153 |
| F | 106 | U 155 | 133 | f | 146 | 1. 155 | 133 |
| G | 107 | U 156 | 073 | $g$ | 147 | I. 156 | 073 |
| H | 110 | (1) 160 | 007 | h | 150 | I. 160 | 007 |
| I | 111 | (1) 163 | 147 | 1 | 151 | I. 163 | 147 |
| J | 112 | 1) 103 | 141 | j | 1,2 | L. 103 | 141 |
| K | 113 | U 105 | 121 | k | 1;3 | I. 105 | 121 |
| L. | 114 | U 106 | 061 | 1 | 154 | L. 106 | 061 |
| M | 115 | U 111 | 111 | m | 155 | 1. 111 | 111 |
| N | 116 | U 112 | 051 | n | 156 | L 112 | 051 |
| 0 | 117 | U 114 | 031 | 0 | 157 | T. 114 | 031 |
| P | 120 | U 117 | 171 | $p$ | 160 | L. 117 | 171 |
| Q | 121 | U 121 | 105 | $q$ | 161 | L. 121 | 105 |
| R | 122 | U 122 | 045 | r | 162 | L. 122 | 045 |
| S | 123 | U 045 | 122 | $s$ | 163 | T. 045 | 122 |
| T | 124 | U 046 | 062 | t | 164 | I. 046 | 062 |
| U | 125 | U 051 | 112 | u | 165 | L 051 | 112 |
| v | 126 | U 052 | 052 | $v$ | 166 | 1. 052 | 052 |
| w | 127 | U 054 | 032 | w | 167 | L. 054 | 032 |
| X | 130 | U 057 | 172 | * | 170 | I. 057 | 172 |
| Y | 131 | U 061 | 106 | y | 171 | L 061 | 106 |
| 2 | 132 | U 062 | 046 | $z$ | 172 | L 062 | 046 |
| [ | 133 | U 040 | 002 | \{ | 173 |  |  |
| $\backslash$ | 134 | U 166 | 067 | 1 | 174 |  |  |
| ] | 135 |  |  | \} | 175 |  |  |
| , | 136 | U 067 | 166 | $\sim$ | 176 |  |  |
| - | 137 | U 100 | 001 | DEL | 177 | L 177 | 177 |
| - | 140 |  |  |  |  |  |  |

## APPENDIX II

Sample Listings from Scenario Assembler

## TEST

CONVERSION CTOE : 1
ENU-OF=MESSAGE COOE : 1


```
TEST.IS
    INDICATOR y
                            CONVERSIDN CODE : 1
                                    END-OF-MESSAGE CODE, &
```

                                    Allocate 9
    1 1 9
21 FL3
3 R 11

| 3 | 1 | 0 |
| ---: | ---: | ---: |
| 5 | 1 | 122 |

                                    R
    4 s flu canoe

| 6 | 1 | 0 | 12 |
| ---: | ---: | ---: | ---: |
| 8 | 1 | 123 | 6 |
| 10 | 1 | 3 | 103 |
| 12 | 1 | 101 | 110 |
| 14 | 1 | 104 | 105 |

                                    \(S\)
    $C$
$A N$
$U E$
5 u mitre/emulate

| 10 | 0 | 21 |  |
| ---: | ---: | ---: | ---: |
| 18 | 121 | 115 | $O M$ |
| 20 | 111 | 124 | $I T$ |
| 22 | 122 | 125 | $R E$ |
| 24 | 157 | 125 | EE |
| 26 | 115 | 125 | $M U$ |
| 28 | 114 | 101 | $L A$ |
| 32 | 124 | 1.15 | $T E$ |
| 32 | 10 | 0 |  |

6 LFL 4
7 R 11

- 33103
R
- 8 FL4 LOGGED

| 36 | 0 | 13 |  |
| ---: | ---: | ---: | :--- |
| 38 | 123 | 0 |  |
| 40 | 41 | 114 |  |
| 42 | 117 | 107 | 06 |
| 44 | 107 | 105 | $0 G$ |
| 40 | 104 | 0 | $G E$ |
|  |  |  | 0 |

016

| 47 | 1 | 0 | 5 |
| ---: | ---: | ---: | ---: |
| 49 | 1 | 141 | 0 |
| 51 | 1 | 6 | 0 |
| + | . | 13 | 0 | 88



1871850
22 LF F



APPENDIX III
Listing of EQUIP.RB



|  | 0 | IETRSP |  |
| :---: | :---: | :---: | :---: |
| 001101000000 | 0 | IETPAD |  |
| 901171000006 | 0 | \|RRING, PRING |  |
| 001201000037 | 8.256.*37 | IETLGA, ETLGN |  |
| 091211032111 | 12741*256.*1 | ITERMT, STATI |  |
| 001221021442 | 43-256.+42 | IPORTD, PORTI |  |
| 001231001002 | 2*256.+2 | ISPRTO, SPRTI |  |
| 001241017440 | 31.*256.+088 | isutad. | ETIND |
| 001251003517 | ALI*256.*PTl | gBYTEL, PARTY |  |
| 001261021071 | 2, - 1* $182+82, B 7+71$ | IETOIO |  |
| 001271062066 | 4, -1+182+04, $87+06$ | IETDOO |  |
| 0613010011071 | 0066a | IETOOA |  |
| E41 |  |  |  |
| 001311000004 | $\theta$ | IETRE |  |
| 001321052131 | "T*256.*"Y | IETYPE |  |
| 941331000003 | 3. | IETIU |  |
| 041341000904 | $\bigcirc$ | ICHILO |  |
| 0413510008561 | E4A | ILINK |  |
| 041361040080 | ${ }^{\text {b }}$ | IPARNT |  |
| 048371000247 | RT1 | ietrat |  |
| 0014010000uy | 0 | IETABP |  |
| 001417001051' | EOM1 | IETEDM |  |
| 001421004000 | - | I ETRSP |  |
| 00 dssioungu | 0 | IETPAO |  |
| 08844100080 | $\bullet$ | IKRING, PRING |  |
| 041451400637 | 0*256.+37 | IETLGA, ETLGN |  |
| 001461432111 | 12741*256.-1 | ITERMT, STATI |  |
| 048471021442 | 43-256.*42 | PPDRTO, PORTI |  |
| 045501081403 | 3.256.* ${ }^{\text {a }}$ | ISPRTO, SPRTI |  |
| 001511420004 | 32, $256 .+488$ | ISUTAD, | ETIND |
| 041521403517 | BLI*256.+PTI | IBYTEL, PARTY |  |
| 041531422071 | 2, $-1 * 182+44,87+71$ | IETOIO |  |
| 001541064066 | 4.-1*182+48.87+66 | IETOOO |  |
| 045510411071 | OD86A | IETOOA |  |
| E4A: |  |  |  |
| 001561040084 | 0 | jEtRe |  |
| 001571052131 | "T*256.4Y | IETYPE |  |
| 00160100004 | 4. | IETIO |  |
| 901611000008 | $\theta$ | ICHILO |  |
| 0016210002031 | E13 | ILINK |  |
| 001631000000 | 0 | IPARNT |  |
| 901641008207 | RT1 | ietrat |  |
| 001651000000 | 0 | IETOBP |  |
| 0016610010511 | EOM1 | IETEOM |  |
| 001671000000 | - | IETRSP |  |
| 001701400400 | 0 | IETPAO |  |
| 001711000000 | 0 | \|RRING,PRING |  |
| 001721080037 | 0*256.+37 | IETGGA, ETLGN |  |
| 001731032111 | 12741-256.*I | ITERMT, STATI |  |
| 001741021442 | 43*256.+42 | IPORTO, PORTI |  |
| 001751002004 | 4+256.44 | ISPRTO, SPRTI |  |
| 001761016409 | 20.*256.+888 | ISUTAD, | ETINO |
| 041771003517 | B61*256.+PTd | IBYTEL, PARTY |  |
| 042081023071 | 2.-1* $182+06.87+71$ | IETDID |  |
| 042011066066 | 4. $-1+182+12.87+60$ | IETOOD |  |
| 092021日g8887 | D066A | IETDOA |  |
| E13: |  |  |  |
| 002031000000 | 0 | jetro |  |
| 002041052131 | "T*256.*"Y | IETYPE |  |


| ODOA EQUIP |  |  |  |
| :---: | :---: | :---: | :---: |
| 002051000005 | 5. | IETIO |  |
| 002061000000 | 0 | JCHILO |  |
| 0020710002301 | E14 | ILINK |  |
| 002801000000 | $v$ | IPARNT |  |
| 002811000207 | RT1 | JETRAT |  |
| 042821040000 | $\theta$ | IETQ日P |  |
| 002831040511 | EOM1 | IETEOM |  |
| 002141040000 | 0 | 1ETRSP |  |
| 082851000000 | 0 | IETPAO |  |
| 048161008040 | 0 | IRRING, PRING |  |
| 042171000437 | 0+258. +37 | IETLGA, ETLON |  |
| 002241032111 | 12741*256. +1 | ITERMT, STATI |  |
| 402211021442 | $43.256 .+42$ | IPORTO, PORTI |  |
| 002221412405 | 5*256.+5 | ISPRTO, SPRTI |  |
| 002231020400 | 33.*256.*日B8 | ISUTAD, | ETINO |
| 042241043587 |  | IBYTEL, PARTY |  |
| 402251024078 | $2,-1+182+48,87+71$ | JETDIO |  |
| 002261070066 | $4,-1-182+16,87+66$ | IETOOO |  |
| 0022710011101 | 00868 | IETOOA |  |
| E14: |  |  |  |
| 042301000000 | 0 | IETR |  |
| 002311052131 | "Y*256.*"Y | IETYPE |  |
| 002321040006 | 0. | IETIO |  |
| 002331000000 | 0 | ICHILD |  |
| 0023410002551 | E 15 | ILINK |  |
| 042351040004 | 0 | IPARNT |  |
| 002361800207 | RT1 | jetrat |  |
| 002371000000 | 0 | IETOBP |  |
| 0024610080581 | EOM 1 | IETEOM |  |
| 00241000400 | 0 | IETRSP |  |
| 402421000000 | 0 | IETPAD |  |
| 062431000000 | 0 | IRRING, PRING |  |
| 002441000037 | $0+250 .+37$ | IETLGA, ETLGN |  |
| 002451032111 | 12741*256.+1 | ITERMT, STATI |  |
| 042461021442 | $43+256.142$ | IPORTO, PORTI |  |
| 092471003006 | $6+256 .+6$ | ISPRTO, SPRTI |  |
| 00250102100 \# |  | ISUTAO, | ETINO |
| 042511003517 | BLI ${ }^{\text {2 }}$ 56, +PT1 | IBYTEL, PARTY |  |
| 002821028071 | $2 .-1 * 182+10.87+71$ | IETOID |  |
| 002531072086 | $4,-1+1 B 2+20, B 7+66$ | IETOOD |  |
| 0025410011101 | D066B | IETDOA |  |
| E15: |  |  |  |
| 002551000000 | 0 | IETRG |  |
| 102561053131 | "T*256. +14 | JETYPE |  |
| 002571000007 | 7. | IEFIO |  |
| 002601000080 | 0 | ICHILO |  |
| 0026810003021 | E16 | ILINK |  |
| 002621000000 | 0 | IPARN ${ }^{\text {P }}$ |  |
| 002631000207 | RT1 | IETRAT |  |
| 002641000000 | 0 | IETQBP |  |
| 0026510014511 | EOM 1 | IETEOM |  |
| 002661000400 | 0 | IETRSP |  |
| 002671000004 | 0 | IETPAD |  |
| 002701000004 | 0 | IRRING,PRING |  |
| 002711040037 | $0+256 .+37$ | IETLGA, ETLGN |  |
| 042721032111 | 12741*256, +1 | ITERMT, STATI |  |
| 092731921442 | $43+256 .+42$ | IPORTO, PORTI |  |
| 002741803407 | 7-256.*7 | ISPRTO, SPRTI |  |
| 002751021400 | 35, 256 , +088 | ISUTAO, | ETIND |


| Q005 EQUIP PRYTEL PARTY |  |  |  |
| :---: | :---: | :---: | :---: |
| 002761843517 | BLI*256, +PT1 | IBYTEL, PARTY |  |
| 002771626071 | $2,-1 * 182+12,87+71$ | IETDIO |  |
| 003001674066 | 4,-1*182+24, $87+66$ | IETOUD |  |
| 0030110011101 | 00668 | IETOOA |  |
| E16: |  |  |  |
| 003021000000 | 0 | IETKO |  |
| 003631052131 | " $9+250 .+$ MY | IETYPE |  |
| 0030410080 | 8. | IETID |  |
| 003051900000 | 0 | ICHILU |  |
| 0030610003271 | E17 | ILINK |  |
| 003071000000 | 0 | IPARNT |  |
| 003101000207 | RT1 | IETRAT |  |
| 063111002000 | $\Delta$ | IETOUP |  |
| 0031210010311 | EDM | IETEDM |  |
| 0u3131000900 | 0 | IETRSP |  |
| 003141008004 | $b$ | IETPAD |  |
| 003151000000 | $\Delta$ | IRRING, PRING |  |
| 003161000037 | $0+256 .+37$ | IETLGA, ETLGN |  |
| 003171832111 | 12741*256. +1 | ITERMT, STATI |  |
| 003201028442 | $43 * 256$ * 42 | IPORTO, PDRTI |  |
| 003211004010 |  | ISPRTO, SPRTI |  |
| 003221022000 |  | ISUTAD, | ETIND |
| 003231003517 | BLI*256.*PTI | IBYTEL, PARTY |  |
| 003241027071 | $2,-1+182+14, B 7+71$ | IETDID |  |
| 003251076466 | 4, $-1+182+28,87+66$ | IETDOD |  |
| 003201001110' | D066B | IETDOA |  |
| E17: |  |  |  |
| 003271000000 | 0 | IETRD |  |
| 003301052131 | "7*256.4Y | IETYPE |  |
| 003311000011 | 9. | IETID |  |
| 003321000000 | 0 | ICHILD |  |
| 0033310003541 | E18 | ILINK |  |
| 003341000000 | 0 | IPARNT |  |
| 003351000207 | RT1 | IETRAT |  |
| 003361000000 | 0 | IEPOBP |  |
| $003371001051^{\prime}$ | EOM1 | IETEOM |  |
| 003401800009 | 0 | IETRSP |  |
| 003411000000 | 0 | IEPPAD |  |
| 003421000800 | 0 | IRRING,PAING |  |
| 003431000037 | 0 -256.437 | IETLGA, ETLGN |  |
| 003441032118 | 12741-256. ${ }^{\text {2 }}$ I | ITERMT, STATI |  |
| -03451022444 | 45*256.t44 | IPDRTO, PORTI |  |
| 003461008481 | 1+256. +1 | ISPRTD, SPRTI |  |
| 003471022400 | 37.*256.* +18 BB | ISUPAO, | ETIND |
| 003501003517 | BLI*256. + P71 | IBYTEL, PARTY |  |
| 003511030071 | $2,-1+182+16,87+71$ | IETDIO |  |
| 003521060067 | 4, $+1+182+80, B 7+67$ | IETODO |  |
| $00353100111^{\prime}$ | D067A | IETODA |  |
| E181 |  |  |  |
| 003541080000 | 0 | IETRO |  |
| 083551052131 | " 7 -256.*"Y | IETYPE |  |
| 043961000012 | 10. | 1ETID |  |
| 003571000004 | 0 | ICHILD |  |
| 0436019004011 | E19 | ILINK |  |
| 08341108009 | 0 | IPARNT |  |
| 003621000267 | RT1 | IETRAT |  |
| 043631000000 | 0 | JETQBP |  |
| -03641901051' | EOM 8 | IETEDM |  |
| 003651000000 | 0 | JETRSP |  |


| Uovo EQUIP |  |  |  |
| :---: | :---: | :---: | :---: |
| 083001000000 | 0 | IETPAD |  |
|  | 0 | IRRING，PRING |  |
| 003701000037 | 0＊256．${ }^{\text {－}} 37$ | IETLGA，ETLGN |  |
| 003711032111 | 12741＊250．+1 | ITERMT，STATI |  |
| 003721022444 | 45＊256．＊44 | IPORTO，PORTI |  |
| 003731041802 | 2＊256．＋2 | ISPRTO，SPRTI |  |
| 003741023006 |  | ISUTAO， | ETIND |
| 003751003517 | 日LI＊256．＊PTI | I日YTEL，PARTY |  |
| 003761031471 | 2，－1＊182＋18，87＊71 | IETOID |  |
| 003771062067 | 4．$-1+182+44,87+67$ | IE9000 |  |
|  | 00674 | IETODA |  |
| E198 |  |  |  |
| 00401） | 4 | JETRU |  |
| 004621052131 | ＂Y＊256．4＂Y | IETYPE |  |
| リu4031000013 | 11． | IETID |  |
| 00404100000 | 1 | ICHILD |  |
| 昛40510064261 | E20 | ILINK |  |
| 10400） 000080 | $\theta$ | IPARNT |  |
| 0日407） 000207 | RTI | IETRAT |  |
| 004101008000 | 0 | PETOBP |  |
|  | EDM | IETEOM |  |
| 004121000000 | 0 | IETRSP |  |
|  | $\theta$ | IETPAO |  |
| 004141000000 | $\theta$ | PRRING，PRING |  |
| 004151000037 | 6．256．+37 | IETLGA，ETLGN |  |
| 004161032111 | 12741－256．1 | ITERMT，STATI |  |
| 004171422444 | 45＊256．444 | IPDRTO，PORTI |  |
| 044201001403 | 3－250．+3 | ISPRTD，SPRTI |  |
| 004211023404 | 39． $256 .+488$ | ISUTAD， | ETIND |
| $04422^{1003517}$ | BL． $256 .+P T 1$ | IBYTEL，ARTY |  |
| 004231032871 | 2，$-1 * 182+24, B 7+71$ | IETOIO |  |
| 004241064067 | 4．$-1 * 182+48,87 * 67$ | IETOOO |  |
| 0042510411111 | 00674 | IETOUA |  |
| E20： |  |  |  |
| 004261080400 | $\theta$ | IETRE |  |
| 004271052131 | MT＊256．＊＂Y | IETYPE |  |
| 004301000014 | 12. | IETIO |  |
| 404311004000 | $\theta$ | ICHILO |  |
| ט043210004531 | E21 | PLINK |  |
| 04433100000 | 0 | IPARNT |  |
| 004341008207 | RTI | JETRAT |  |
| 004351080400 | 0 | IETOBP |  |
| 0043610010511 | EDM1 | IETEOM |  |
| 004371000004 | 0 | PETRSP |  |
| リด44010日月の日0 | 0 | IETPAO |  |
| 00441080000 | $\theta$ | ｜RRING，PRING |  |
| 004421000037 | 8－256．437 | IETLGA，ETLEN |  |
| 094431032118 | 12741＊256．4 | PTERMT，STATJ |  |
| 004441022444 | 45＊256．＋44 | IPORTO，PORTI |  |
| 004451082004 | 4＊256． 4 $^{4}$ | ISPRTO．SPRTI |  |
| 004461024000 | $40.4256 .+488$ | I SUTAO． | ETIND |
| 044471003517 |  | IBYPEL，PARTY |  |
| 084501033471 | 2，$-1+182+22,87+71$ | IETOID |  |
| 044511066067 | 4，$-1 * 182+12,87+67$ | JETOOO |  |
| 0045210018111 | DD67A | IETOUA |  |
| E211 |  |  |  |
| 094531000000 | 0 | IETRO |  |
| 004541052131 | ＂T＊256．${ }^{\text {H Y }}$ | IETYPE |  |
| 0945510 －0815 | 13. | JETID |  |


| 0007 EQUIP |  |  |  |
| :---: | :---: | :---: | :---: |
| 044561000004 | 0 | ICHILO |  |
| 0045718485001 | E22 | ILINK |  |
| 0446 Itu0000 | $\theta$ | IPARNT |  |
| 004611000207 | RTI | IETRAT |  |
| 04621080080 | $B$ | I ETOBP |  |
| 0440310010511 | EDM | IETEOM |  |
| 081641000080 | 0 | IETRSP |  |
| 044051808000 | $B$ | IETPAD |  |
| 004681000006 | 0 | IRRING,PRING |  |
| 404671080437 | - 256 - 37 | IETLGA, ETLGN |  |
| 004781032111 | 12741*256.*1 | ITERMT, STATI |  |
| 004711022444 | 45*256.*44 | PPORTO, PORTI |  |
| 004721002485 | 5.256.*5 | ISPRTO, SPRTI |  |
| 004731024481 | 41.*256.*488 | ISUTAO, | ETINO |
| 044741003517 |  | IBYTEL, PARTY |  |
| 004751034071 | 2.-1*182+24, $87+71$ | IETDID |  |
| 094761070067 | $4 .-1+182+16.87+67$ | IETODD |  |
| 0447710011121 | 00678 | IETOOA |  |
|  |  |  |  |
| 085001040800 | 8 | IETR ${ }^{\text {P }}$ |  |
| 005011052131 | "9.256.*"Y | IETYPE |  |
| 085021080816 | 14. | IETID |  |
| 045031000080 | $B$ | ICHILD |  |
| -054410005251 | E23 | ILINK |  |
| 085u51880840 | 0 | IPARNT |  |
| 005061000207 | RT1 | IETRAT |  |
| 085071080080 | 0 | IETOBP |  |
| 4051818010511 | EOMI | IETEOM |  |
| 085111000000 | B | IETRSP |  |
| 005121000000 | 0 | IETPAO |  |
| 005131800000 | 0 | IRRING,PRING |  |
| 005141800037 | 0.256. +37 | IETLGA, ETLGN |  |
| 005151032111 | 12741*256.*1 | ITERMT, STATI |  |
| 045161022444 | 45*256.*44 | IPORTD, PORTI |  |
| 005171003006 | 6*256.+6 | ISPRTD, SPRTI |  |
| 005201025000 |  | ISUTAD, | ETIND |
| 005211003517 |  | IBYTEL, PARTY |  |
| 005221035071 | 2, -1* $182+26, B 7+71$ | IETOID |  |
| 065231072867 | $4,-1 * 1 B 2+20, B 7+67$ | IETDOD |  |
| 0052410011121 | D0678 | IETODA |  |
|  |  |  |  |
| 005251040808 | 0 | IETHE |  |
| 805261052131 |  | IETYPE |  |
| 005271800017 | 15. | IETID |  |
| 005301000000 | 0 | ICHILD |  |
| 0053110405521 | E24 | ILINK |  |
| 005321000000 | 0 | \|PARNT |  |
| 005331000207 | RT1 | IETRAT |  |
| 005341000000 | 0 | IETQ日P |  |
| 0953510010511 | EOM 1 | IETEOM |  |
| 008361000000 | - | IETRSP |  |
| 405371000000 | 0 | IETPAD |  |
| 005401000000 | 8 | IRRING, PRING |  |
| 005411000037 | 0.256.+37 | IETLGA, ETLGN |  |
| 000421832111 | 12741*256. 1 | ITERMT, STATI |  |
| 005431022444 | 45-256.*44 | IPORTD, PORTI |  |
| 005441043407 | 7*256.*7 | ISPRTD, SPRTI |  |
| 005451025400 | $43.256 .+088$ $81.256+P 71$ | IBYTEL, SUTAO, | ETIND |
| 005461003517 | 861*256. + PT | IBYTEL, PARTY |  |


| 6088 EQUIP |  |  |
| :---: | :---: | :---: |
| 645471036078 | $2,-1 * 182+28,87+71$ | IETDID |
| 405501074067 | $4 .-1$－1 $82+24,87+67$ | IETDUD |
| 60551昛1／121 | D0678 | IETDOA |
| E24： |  |  |
| 405521000800 | 0 | IETRO |
| 005531052131 | ＂Y＊256．${ }^{\text {¢ }}$ Y | IETYPE |
| 005541000620 | 16. | IETID |
| 085551800000 | 0 | ICHILD |
| 0055610005771 | E5 | ILINK |
| 005571000000 | 0 | IPARNT |
| 0056810 10207 | RT1 | IETRAT |
| 005011800000 | $\Delta$ | JETOBP |
| 0656210410511 | EDM1 | IEPEOM |
| 40563184600日 | $B$ | IETRSP |
| U05041800006 | $b$ | IETPAD |
| 005051800468 | 6 | IRRING，PRING |
| 005661800037 | $4 * 256.437$ | IETLGA，ETLGN |
| 045671832111 | I2741＊256．＋I | ITERMT，STATI |
| 005701022444 | 45＊256．+44 | IPDRTO，PORTI |
| 085711084010 |  | ISPRPD，SPRTI |
| 005721026400 | 44．＊256．＊4日8 | ISUTAD，ETIND |
| 005731083517 |  | ISYTEG，PARTY |
| 405741637071 |  | IEPUID |
| 005751676067 | 4．$-1+182+28,87+67$ | IETDOD |
| 4057610011121 | 00678 | IEPDDA |
| E5： |  |  |
| 005771000408 | $\Delta$ | IETRO |
| 006001046816 | ＂L－256．＊＂N | IEPYPE |
| 006011008085 | 5. | IETID |
| 0060210008241 | E6 | ICHILD |
| 4963） 0 00000 | 0 | ILINK |
| 006041000006 | 0 | IPARNT |
| 086051004546 | 2484. | JEPRAT |
| 466061008080 | 0 | IETUBP |
| 6060710485511 | EOM 1 | IETEDM |
| 006101800008 | 0 | IETRSP |
| 086111086486 | 0 | IETPAD |
| 006121000000 | 0 | IRRING，PRING |
| 006131000437 | 6－256． 37 | IETLGA，EPLGN |
| 006141032111 | DDDLINE＊256．${ }^{\text {¢ }}$ | ITERMT，STATI |
| 006151815031 | 32－256．+31 | IPORTD，PDRTI |
| 066861008080 | $4+256+8$ | ISPRTD，SPRTI |
| 006171025400 | $43 .+256 .+488$ | ISUPAD，ETIND |
| 006201004116 | 8L2－256＋PT2 | IGYTEL PARTY |
| 466211800606 | 1．$-1+182+887+0$ | IETDID |
| 466221040000 | $1 .-1$－ $182+487+8$ | IETDOD |
| 006231000008 | 0 | IETODA |
| E6： |  |  |
| 006241000006 | $b$ | IETRG |
| 006251041516 | ＂C． $256 .+$ N | IETYPE |
| 006261008006 | 6. | IETID |
| 0062710086761 | E8 | ICHILD |
| 0063610006511 | E7 | ILINK |
| 0063110005771 | E5 | IPARN |
| 086321004544 | 2400. | JETRAT |
| 006331000400 | 0 | IETOBP |
| $066341081051^{\prime}$ | EDM 1 | IETEOM |
| 006351000800 | 0 | IETRSP |
| 006361068400 | 0 | IEPPAD |


| 0009 EQUIP |  |  |  |
| :---: | :---: | :---: | :---: |
| 046371000004 |  | $\bullet$ | IRRING, PRING |
| 040401000037 |  | 4.250.*37 | IETLGA, ETLGN |
| 006411032118 |  | IBM2848*256*1 | ITERMT, STATI |
| U06421015038 |  | 32*256.*31 | PPORTO, PORTI |
| 008431040008 |  | - $2256 .+0$ | SPPRTO, SPRTI |
| 006441047004 |  | 116.256.*888 | ISUTAO, ETIND |
| 000451004116 |  | BL2*256. + P T 2 | IBYTEL, PARTY |
| 0ucativueut |  | $1 .-1+182+487+0$ | IETOIO |
| 006471000000 |  | $1 .-1 * 182+487+\theta$ | IETODO |
| 90650108uede |  | $\downarrow$ | IETDOA |
|  | E7: |  |  |
| 006511000000 |  | 0 | IETRO |
| 006521041516 |  | "C*256.*"N | IETYPE |
| 006531000007 |  | 7. | IETID |
| 0005410007751 |  | E11 | ICHILU |
| 006551000008 |  | 0 | ILINK |
| 0065610005771 |  | E5 | ipannt |
| 046571004540 |  | 2400. | ietrat |
| 006681000000 |  | 0 | IETOAP |
| 00001'001051' |  | EDM | IETEOM |
| 006621000000 |  | $\Delta$ | IETRSP |
| 006631000000 |  | 0 | IETPAO |
| 048641000080 |  | 0 | IRRING,PRING |
| 006651000037 |  | 0.256.037 | IETLGA, ETLGN |
| 046661032125 |  | 18M2848*256.4 | ITERMT, STATI |
| 006671085031 |  | 32-250.+31 | IPDRTO, PORT 1 |
| 000741000008 |  | 4*256.** | ISPRTO, SPRTI |
| 00071120404 |  | 254*256.+488 | ISUTAO, ETIND |
| 00672184118 |  | BL2+256.*PT2 | IBYTEL, PARTY |
| 006731000000 |  | $1,-1 * 182+087+0$ | IETDID |
| 006741000000 |  | 1. -1 - 182+087*0 | IETDOO |
| 086751000004 |  | $\downarrow$ - | IETDDA |
|  | E8: |  |  |
| 006761040000 |  | 0 | IETRU |
| 006771042123 |  | "0*256,*"S | IETYPE |
| 00700100010 |  | 8. | IETID |
| 007011040000 |  | 0 | ICHILD |
| 0070210807231 |  | E 9 | ILINK |
| 0070310000241 |  | E 6 | IPARNT |
| 007041004540 |  | 2400. | ietrat |
| 007051000000 |  | 0 | IETOBP |
| 0070810010011 |  | EDM | IETEOM |
| 00707100000 |  | 0 | IETRSP |
| 007801000000 |  | 0 | IETPAD |
| 007181000004 |  | 0 | IRRING,PRING |
| 007121000037 |  | 0.250.*37 | IETLGA, ETLGN |
| 007131032181 |  | 1842260*256.*I | ITERMT, STATI |
| 007141015038 |  | 32*256, 31 | PPORTO, PORTI |
| 047151000000 |  | $0 \cdot 256.4$ | ISPRTO, SPRTI |
| 007161820000 |  | 240*256.4088 | ISUTAD, ETIND |
| 087171004186 |  | BL2*256.*PT2 | IBYTEL, PARTY |
| 007201000004 |  | 1.-1*182+087+0 | IETOID |
| 107211000000 |  | 1. $+1.1822+087+0$ | IETDOD |
| 407221000084 |  | 0 | IETOOA |
|  | E9: |  |  |
| 007231000004 |  | $\bullet$ | IETRO |
| 007241042123 |  | "0*256.+"S | IETYPE |
| 007251000011 |  | 9 , | IETIO |
| 007261080000 |  | 0 | ICHILD |




00：2 EOUIP
01116100400日 D0068
011111804008 D067A
01112104日昭 D0678：\＆
－End

| 0613 | EQUIP |
| :---: | :---: |
| 0060 ${ }^{\text {a }}$ | 6011071 |
| 00688 |  |
| D067A | （0）1」11 |
| 0067日 | 46dil2＇ |
| E 0 |  |
| Eound | uxube51 |
| E日END |  |
| E1 | 00u0321 |
| E10 | טub7501 |
| E11 | 0067751 |
| E12 | 0610221 |
| E13 | 0042031 |
| E14 | 0462301 |
| E15 | 0ub2551 |
| E16 | 0043021 |
| E17 | bub3271 |
| E18 | 0603541 |
| E19 | $000401^{\prime}$ |
| E2 | 0000571 |
| 120 | 0004261 |
| E21 | 0004531 |
| E22 | 0045001 |
| E23 | 0005251 |
| E24 | 0005521 |
| E3 | 0001041 |
| E4 | 唯1311 |
| EAA | ט日＊ 1561 |
| E5 | 4065771 |
| E6 | 0u06241 |
| E 7 | प846511 |
| E 8 | 0406761 |
| E 9 | טu67231 |
| E9999 | 0088471 |
| EOMI | 0010511 |
| EOM2 | 0010571 |
| EOMS | 0010651 |
| EOM4 | 0680731 |
| EOMS | 9011811 |
| ETENO | 00000＇ |
| ETENT | 0010471 |
| ETLEN | 0010501 |
| etrec | $000081^{\prime}$ |
| LEN | 000025 |

APPENDIX IV

DATAR Listings

```
        USER INPUT: DATAR/B)
BRIEF SUIMAKY BF MUN FTT PAGE I
UEV EKHUN MESSSNIEI
---\infty---------*--------
```



```
TY15 w2n ACT!心年 Pakev
Tr\O 1/4 UEVILE STUNDEO
TrIs v2& ACT!0% TAGEN
CTG प20 ACTICO TaAEV
TYIO د2: ACTION PAREN
TY!O U2& ACYIUN TAAEN
Trin OAN GEMJNO SGAEOULE
```



```
TENMINALS: J
\begin{tabular}{|c|c|c|c|c|c|c|c|}
\hline Chahactens: & Tutal: & 172 & R1 & 86 & 01 & 86 & UN=R: \\
\hline
\end{tabular}
\begin{tabular}{|c|c|c|c|c|c|c|c|c|}
\hline RECUKD TYPES: & HI & 3 & 31 & 180 & 01 & 5 & & \\
\hline & R 1 & 5 & Cl & 7 & El & \(\theta\) & UNSOLICITEDI & - \\
\hline
\end{tabular}
TIMES: AVGRESPI 6,06 MAXRESPI 30,62 TERMINALWMAXI TYIG
    PEKCENT CHU& d.89 TOTAL CPUI 56569
SCEINAHIU INSTHUCIIONS USED:
\begin{tabular}{|c|c|c|c|c|c|c|c|c|c|c|c|}
\hline + & 1 & 4 & \({ }^{*}\) & 1 & 2 & 98 & 23 & 11 & 3 & 1 1 & 4 \\
\hline - & 1 & 1 & \(C\) & 1 & 4 & 0 : & 22 & J 1 & 21 & 0 - & 2 \\
\hline ? & 1 & 6 & + & : & 1 & De: & 8 & L-1 & 2 & \(0 \cdot 1\) & 3 \\
\hline & -1 & 4 & & & & & & & & & \\
\hline
\end{tabular}
COMmANUS ISSUEU:
OUIT 1 : START 1 3 8UB 3
ENU-OF-FILE (
```

Figure 26. Brief Summary Output Format


Figure 27. Detailed Summary Output Format


Figure 28. Histogram Output Format

# USER INPLT: DATAR/L/T/O/N 

OEvEES MEUUEStio ANE
Page 1
$\begin{array}{ll}1 & \text { CTE } \\ 2 & \text { DS16 }\end{array}$
${ }_{3}{ }^{3} 515$
cogical neccais 1 mi io 11 r
permanat cim
(Nane for ©TJ)

TEAmemali tyis
Ins




 10s mep te4 $102 \cdot 11$






 112 世4s iss ijs u3) ocm




LOGIGAL WESUNHS 1*1 TO 1:14
TERMIMAL Trito
181



102

 140

 112 uis its lisa 737 exi4
110



FOLLOMING UEVICEy ARE INASTIVEI
PAGE 2
20814
ENO-UFOFILE

Figure 29. Octal Tape Output Format

USER INRUT: DATAR/L RECORDS/Q/R STTO)


FNO-OF-FII.E
?

Figure 30. Actual Time Output Format


Figure 31. Time Interval Output Format


Figure 32. Relative Time Output Format

## APPENDIX V

Example of teletype on-line listing for preparation of a single scenario, a real-time emulation, and a single data reduction 1isting.

```
XFER/A SCDR KAP
LOAD SCDS, SIFI`E ANY :KEY.
F
SSUG KAP TYPE SCENLIA
R
CVT TYPE 3 4
TO CANCEL RUN, USE COVIICOL-A
R
9P3
:%AIT
ENTER RUN! ID
TYPE
READY
CSTART TABR TYPE
0 2 0 ~ A C T I O N ~ T A K E N
```



```
COFE LIN<S MOTn.34 CON゙E AVAIL n27S54
DOS REV 75.
F
DATAF/L
END-OF-FTLE
DATAK termINATED
F
```


## APPENDIX VI

Timing Samples for Non-Real Time Programs

In Figure 34 where macros are expanded, lower case op-codes and some special characters do not print. These instructions can be referenced from Figure 33 in conjunction with the SCENLIB library macro substitutions.

```
34FJHIN
    allucratis is
```



```
    ETUKEG KY 5 RIV
        ** Ha゙ IS TKANSMISSIUN MATE
    LUN 3 KY
        ** Ky IS TYPING RATE
    -KIS RU KII
    -RIG Ky kiJ
    TYRE( 8 )
    (GEIITUN
    FIND(..)
    TYPE(4)
    LuFUKTAT, F
    FIND(..)
    Crsu's Cust
    trpe(13)
    WCatate 1010
    LLAB1
    \(\kappa^{\prime \prime}\)
    SLABL =
    EXECJTV
    -Ry R11 R12
    K12 K10 K12
    LON 100 a R9
    - RG R12 M12
    AOY R12
    EXECUT:
    JLADI
    llabz
    SLABI...
    TYPE (5)
    GRUN,F
    FINU(..)
    TYiPe(1:y)
    REAEEID CONTINUE
    TYアヒ(24)
    HS3:
    TYPE(62)
```



```
    TYPE(10)
    ULIST 2A日
    PIND(..)
    TYPE (10)
    はLIST 3S 0
    FIND(..)
    TYPE(:0)
    OLIST 42d
    FIND(..)
    TYPE (13)
    usavé,test,o
    FIVO(..)
    cisus info
    TYPE(15)
    UCKEATE luo \(1: 5\)
    llanj
    K!
    SLAS4 =
    ExECUTE
    -Ry 11! H!?
    K12 K1. K12
    Lid liond Kig
    **y 11く K1く
```

Figure 33．Fortran Cost Scenario with Macros not Expanded

```
O3 ADY 1/2
04 tx+cjit
OS Jlajs
uu L-....
67 SLAH3.
of TYHE(1%)
69 usargejutu,NOSEO
70 FIU(..)
7! true(lv)
7 2 \text { GELIT,tLSY}
73 Flivi(..)
74 Trpe(口)
7 5 \text { LWIJv,F}
70 FIHD(..)
7% TYPE(1%)
78 USAVE,TEST,O
70 FI%O(..)
8a Prot(iz)
81 WESIT,IFFG,S
8 2 ~ F I N O ( . . ) ~
83 「yr上(Sts)
84 RIN:EOLE%S PTH WITH CONTROLLER
85 TYre(?j)
86 K49E゙=n`JUAS PROGRAMMING
87 TYPE(%)
8y ULIST.A
89 fINO(..)
94 TYPE(14)
91 USAVE,IM=C.O.N
92 FIND(..)
93 TYPE(!ん)
94 ULOIT,TEST
95 FIMi(..)
90. TyPe(0)
9 7 \text { ORUN,F}
98 FINO(..)
99 TYPE(ङ)
1OD W&YE,GYE
101 FINO(CGMMAND)
102 TYPE(d)
los GLUGJUT.
104 FINO(AT)
135 ClSUa OUFT1111.
```

Figure 33．Fortran Cost Scenario with Macros not Expanded（Concluded）

```
    FORTN
    ! < \3 N KO
    Ky O KIv
    ** K!V lS TRANSMISSION RATE
        < <s
        ** NG IS TYPING RATE
-NI啋kyNII
*lin ky klv
    RU W R9
    R9 O HIG
    3119
-K1A KO K11
*NG Klki l?1.1
    d R9
*Rg lill N:%1
R11 H!U KIV
    1:\lle Ko
*Ry Kl& kitu
    H1H
@EDITUK.
L LLI2
K'1
S LLI2*
    HUNHN
    KY6 N1U
    3 HO
-K18 K9 H11
-K9 Kl! N1J
    9 Ky
*HY R11 R11
<KI| klU klG
    loun my
*Ky P10 Klb
    N1b
OFOHIG&T,F
L LL?*
N'1
S LL20..
ClSU! CUST
    HU & Hy
    Kg b RIJ
    3 K9
-H!| ky k!l
*Ng RILFRIい
    13 K9
*Ry R1& R11
/H\1 N1O H1O
    1己いO K9
*R9 KIW RIU
        Kl:3
WCRE゙\TE |U |И
LLAB!
K'I
SLAG? =
*NY H11 H12
/K12 KIGKI2
    1OUO HG
2059 *RY R12 R12
    H1:
JLAUl
```

Figure 34．Scenarios for Fortran Cost Problem with Macros Expanded

```
LLAB:
    SLABI ..
        KG 6. A%
        wO Fi+1:
        3 Ry
    -HIO RY HII
    *H9 NIU RI|
        O H9
    *KY 18!! 4I!
    KK11 Kl!A N10
        10:%)R9
    *R9 R14 RIO
        H1!
    QRUN,F
    L LLS:
    R'I
    S LLJY..
        KV O KO
        N9 6 H1w
        3 119
    -KIU NY KII
    *RG RIG RIG
        19 kY
    *R9 N11 R11
    1K11 KIGR1V
        1Hun Ry
    *K9 RIG RIA
    K1H
R24g=15 CONTINUE
    RU O RY
        k96 NIG
        3 R9
    -R!tN R9 RII
    *RY RIU RIU
    24 RY
    *RY KlI RII
    R11 RIH RIO
    まぜいU Ky
    *R9 RIGKIG
        KIH
    H33G= WRITE (6,1H%)
        RU 4 MQ
        R96 R1U
        3 H9
    -RIO RU RII
    *R9 NIN R1U
        02 Ry
    *Ky RII RII
    ARII RIAR RIN
        10N| Ry
    *Rg R10 N10
        RIW
    H39&J!U4 FOKMAT (IHH,3X, EQQUIPMENT COSTS*/7X,*SUBSYSTEM &*.
        K(S WG
        R9 O KIG
        3 RY
    -RI| RY RII
    *KY H1ロ KI:
    1% k9
    *KY 1211 H1:
    /R11 R14 र1%
    124
Figure 34．Scenarios for Fortran Cost Problem with Macros Expanded （Continued）
```

```
    126 N16
    127 OLIST 24U
    12% L LLIMH
    124 N11
    134 S LLU8 ..
    131 RU S R'G
    132 KY O RIO
    133 S R9
    134-R16 K9 K11/
    135 +R9 KIS RIS
    136 1H Ky
    137 HKY K11 <1&
    138 /R\1 K\ん K゙d氏
    139 14:10 H9
    146 HS RIU RIN
    141 R10
    142 GLD5T 33W
    l&S L LL/O
    144 K11
    145 S LL76 ..
    146 RO U 1R9
    147 R9 6 R14
    148 S K9
    149 -KIV KY RII
    150 *NY RIGRI*
        1% R9
    *K9 R11 R11
    K\l KlG K!O
        10nU R9
    *RY RIJ Rld
        RIIg
    OLISI 420
    L LLMA
    R!I
    S LLH4
        RO रि
        R90-R10
        R RY
    -HIV RG RII
    -H9 R1U N&&
        13 RY
        *RY Kl! R\l
        KKII KIN KIO
        10W0 RY
    *R9 Rl0 Nよठ
        R!w
    QSAVE,TEST,O
    L LL:22
    R!I
    S LLg2 ..
    CISUH INFO
        KU H9
        K9 6 R1O
        3 R9
    -NIU H9 KIl
    *H9 RIU RIU
        15R9
    18S *H9 K11 R11
    184 /HIL H1O Kld
    185 lUIS KY
&゙180 *H9 N10 K1O
        Kl:3
    1HN HCKtAIE゙ 1よい1も
```

Figure 34．Scenarios for Fortran Cost Problem with Macros Expanded （Continued）

```
    189 LLA甘3
    194 K!!
    191 SLAB4.
    1%<
    193 *KY R1I R12
    194 /RI2 K14 K12
    195 1K,NHK
    190 KY KIL KI2
    197 Kl?
    198
    199 JLA!J
    20S LLASd
    20! SLdう3..
    N\N U Kप
    203 iry 0 klu
    234 J Yy
    205-H10 K9 R11
    200 *KY R1u K13
    207 17 k9
    200 *KY Rl! R1!
    209 /RII RIU Kl!
    21U lu'fu Kg
    211 * K'skIUN1力
    21く R1B
    21S USAVE゙,INFU,INUJEQ
    214LLL!111
    215 R11
    210 S LL111..
    217 KG G R9
    210 KGO K10
    21Y S RY
    22d-R1U R3 R111
    221 *R゙G KID N1D
    222 1# K9
    22J *KG 人11 Kll
    224 /K\I KINRIG
    220 1030 Ry
    220 KY RÍNRIJ
    227 <1)
    228 GEUIT,TEST
    229 L LL119
    23v N'1
    231 S LLIl9..
    232 K\Delta U R9
    233 K96R1U
    234 I RY
    235-Klい K9 K11
    230 *Y RII RIS
    237 O R9
    230 K\ R11 R11
    239 /K11 KI.HKJ!
    24i IN\% K9
    241 *K9 210 RIG
    242 K1J
    243 UKUN,F
    244 L LL127
    245 K11
    240 S LL127..
    247 KU N K9
    24y NY O RIU
    249 J K9
    25W-NIH RY R11
85-\\1 *Ky <1v Kl.1
```

Figure 34．Scenarios for Fortran Cost Problem with Macros Expanded （Continued）

```
    252 12 k9
    253 *KY R!1 R!!
    254 /R11 RIU RIG
    255 1U00 KY
    250 R K9 N16 H1%
    257 KIQ
    254 OSAVẼ,TEST,O
    259 L LL!35
    264 K'I
    261 S LLI35 *
    262 K6 0 Kg
    263 R9 O KIU
    264 3 R9
    265 -RIO R9 RII
    260 *Ky RIU RI|
    267 12 K9
    268 *K9 RI! R!!
    269 /K11 R10 Y1%
    27月 1000 K9
    271 *K9 श1| R!ル
    272 KiG
    273 GEDIT,INFO,S
    274 L LLI43
    275 KI'
    270 S LL143..
    277 KO RO
    278 R9 6RIO
    279 3 R9
    280-K10 R9 RI!
    281 *R9 210 R10
    282 36 R9
    2&3 *R9 RII RI!
    284 /RI! RIO RIU
    285 1UOO KY
    280*RGRIU RJU
    287 R1H
    288 RI4U=|UIUHIS WITH CONTROLLFR
    289 KO OR9
    294 R9 6 H1O
    291 3 29
    292 -R!0 R9 R!1
    N293 *R9 RIU RId
    294 28 R9
    295 *R9 RII RI!
    296 /HI! RIU KgQ
    297 1030 49
    29y HR9 R14 R10
    299 RI%
    300 R49U=|ロI00O PKOGRAMMING
    301 KO U K9
    302 R9 0 RID
    303 3 R9
    SG4 -NIU R9 RII
    305 +K9 2IU RIV
    306 7 R9
    307 HKY PII RII
    3u8 /HII HIU R10
    309 1U00 R9
    310 *RY RIU RIO
    31! H14
    312 OLIST.A
    313 L LL166
    S14R11
Figure 34．Scenarios for Fortran Cost Problem with Macros Expanded （Continued）
```

```
        315 5 LL160...
        317 Ny O K1|
    31% 3 र9
    319 -K16 N9 kl1
    326 *H9 RIU RIU
    321 14 ky
    322 *R9 R!1 R1!
    323 /K11 RIUR1U
    34 DOOU KY
    325 *K9 RlU Klv
    320 Kl%
    327 USAVE,INFO,O,N
    328 L LL174
    329 R!"
    3365 LLI74...
    331 K:8 6 k9
    332 N9 6 Kly
    33 - < R9
    334 -R14 R9 R11
    336 *R9 子1& R1U
        14 R9
    *Ry RIl R!1
    R11 RIU R1E
    1006 K9
    *R9 R10 RI|
        R19
    WEOIT,TEST
    343 L LL182
    344 R'I
    345 S LLI82 ..
    346 KU O R9
    347 R9 O R14
    348 3 २9
    349 -R10 R9 R11
    350 KY R16 RIU
    351 - K9
    352 *R` 211 R11
    353 /R11 R14 H16
    354 1000 Ry
    35S RY RIG RIA
    350 KlJ
    357 QRUN,F
    358 L 'LL19U
    359 K1'
    360 S LLI90..
    361 Q4 & K9
    302 k9 6 Rl|
    363 3 R9
    364 -K10 R9 R11
    365 *RY RIO RIN
    360 y <%
    357 *Ry 211 R11
    350/R11 R10 RIU
    36y 14:44 R9
37| *RY R1U R1&
31! N1J
372 2HYE,BYE
373 L GL19y
374 к'"
37J $ LLIg8 ComMand
37S KU & KY
3> ig o Kln
Figure 34. Scenarios for Fortran Cost Problem with Macros Expanded (Continued)
```

```
370 3 99
379-R14 R9 RII
38!4 FKN N!ONW!4
381 8 R9
382 *RO M11 M11
383/R11R10RI6
3H4 land K9
385 RY MIU RIO
386 र10
387 aliguut.
388 L I.L200
389 R11
300 S LLRUG AT
301 CISUY DUFT111!
```

Figure 34. Scenarios for Fortran Cost Problem with Macros Expanded (Continued)

```
    COST
l x =: i Vm
OIPKUGRAM CUST(UUTPUT, INFO,TAPES=INFO,TAPEO=DUTPUT)
        43 y kb
    UC ****PROGKsIA TO CNHPUTE COST ESTIMATES ****
        jy y Ho
    OTDI'IE|:GIUN IM(2&),IN(av),IP(2*i),IO(2i)
        16 % NU
    0,0U75:COUNTa1,2
        3 3 2t
    0)ISJM11=U
        1% y kn
    0;KEAl) (U,1) HuMM
        14 y Mo
    Q1ItiJmMiN(10)
        15 3 Na
    OiDO 5 i=1,NUMM
        1% y fo
    OIKEAU (5,1) IM(1)
        19 y kb
    G:ISIJM|I=IM(I)+1SUNM
        114 % Ro
        Gち;じいNな\ないま
        0 9 Kn
    0;1SUMN=6
        17 9 FKt
OIREAU (5,1) MUINN
        16 g kt
    Q:UU &w! =1, NUMN
        18 9 KB
    O/REAU (D,1) IN(I)
        19 9 &f;
    O;ISUMH:IN(I)*IELRMN
        12 3 %%
    OIOICUHTINGE
        2n y Ho
    GiIETSUM=IEUMN+ISUMM
        | y ro
    GiLSUMP=\
        17 % Ko
    W;HE゙\U (ち,I) NUMF
        10$FE
    OIUU 15 I=1,NUMP
        18 y fio
OIREAU (5,1) IP(I)
        :Y 9 h'n
    0:ISUMPEIP(I)+ISUMP
        11 9 KO
    Q1DICUNINUL
        y 9 PG
    UIISUMGEU
        17 9 KU
    (H:RLAO (5,1) NUIIC
        10 y rio
    OIUO <U I=1,NUMU
        10 y no
    UIKEAD (5,1) IO(I)
        19 y Ho
    UIISUNは汻(I)-ISUMO
        1% Y F'O
    UCVICUNTLNUE
        if y mo
Figure 34．Scenarios for Fortran Cost Problem with Macros Expanded （Continued）
```

```
63 O:ITOTALzIGGSUM+ISUMH+ISUUMO
    2y y wo
```



```
    149 Kb
O|WN&TC J,1(UИ)
    64%NO
U&GG;rUMMAF (IHQ//OX, WWELIMINARY COST ESTIMATE*//IX,*SYSTEM A*)
    1c y ko
0IGOTO SS
    17 9 HO
Q50:NNITE (S.dG%)
    32 1) \tilde{O}
0ISZ:FUNHAT (IND//IX,*SY:STEM 日*)
    NO = RO
WS5;VINITL (0,IUA) ISUMH,ISUMN,IECOSUM
    55 9 re
Q&u4;FURMAT (1N3,3x, EEO.IPMENT CDSTS*/#X,*SUBSYSTEM &*,
    55 % Ko
0 * 5X,I8/7X, SUESY,STEM 2*,5X,I8/19X,*TOTAL*,2X,I1U)
    349 Kö
UIWKITE (O,IHN) ISUMP,ISUMG,ITOTAL
    3) N Ro
UINO;FUKMAT (IHM, 3X,*OEVELOPMENT COSTS*,5X,II*/4X,*O& M*
    44 9 HG
Q * COSTS*,1&X,I1|//119X, FTOTAL*,2X,I1*)
    12 (Ru
UTSICUNTINUE
    6 y R6
0/5TJP
    5 % ko
OIENO
    2 % K6
O=
```

Figure 34. Scenarios for Fortran Cost Problem with Macros Expanded (Continued)

```
INFO
    x
        29 g Ro
        **)
    0405000
        36 9 RS
    0日USAMA
        37 9 Ro
    0001048
        32 9 Ko
    0001000
        32g Ro
    WH1%000
        38 9 RO
    040!コS%
        39 3 Rn
    0401ulb
        40 9 Ro
    OHOLuNO
        29 9 H0
    040000%2
        22 % K6
    0041040
        23 y R6
0月010^D
        29 9 K6
ucununs
        23 9 K5
004100*
        23 9 R6
0001060
        24 % R6
0001000
        20 9 Rö
QuesDNO
        18 9 R6
0601030
        29 9 R6
Q4000%4
    33 2 R6
00010408
        29 9 N6
0401390
        34 9 RS
    0u01004
        28 9 RO
    Qu41090
        27 9 R6
    0000006
        24 9 RG
    00010u4
        32 9 k6
    0001008
        32 y RG
    0601000
        3n 9 Ro
    0001000
        4 2 ~ 9 ~ k 6
    0301.4JJ
        40 y R\sigma
    UNOIDHO
        <゙ 9 &o
            \thereforeU|\mp@code{* I: LJS! M}
            CPU WITH 24K MEMM
            FH UISE WITH CONTROLLER
            mag tare with controller
        AIth Controlleg
            TTY WITH CONTROLLER
            LINE PRINTER & CONTROLLER
            10 ASYNCHRONGUS LINE ADAPT
            1 HISPEEO ASYNCHRONUUS LINE ADAPT
            NUMBER IN LIST N
            16 MODEMS
            MODEM KACK
            Number IN LISt P
            ELEC ENGIN
            MECH ENGIN
            PROGRAMMING
            DOCUMENTATION
            T * E
            NUMBER IN LIST Q
                    UPERATIUNS PERSONNEL
                    SERVICE CONTRACT
                    TELEPHONE & DAA LEASE
                    telephune usage
                    NUM IN LIST M*
                    CPU WITH 8K MEM
        WITH CONTROLLER
            TTY WITH CONTROLLER
            O LJ-LINE OIG I/O
            1 6 \text { ASYNCHYONOUS LINE AOAHTERS}
                            y synchronous liNE AUAPTERS
```

Figure 34．Scenarios for Fortran Cost Problem with Macros Expanded （Continued）

```
\begin{tabular}{|c|c|c|}
\hline 63 & 000日鸟5 & NUM IN LIST \(N\)＊ \\
\hline 64 & 299 Ho & \\
\hline 65 & पüdubu & 2 Hdsret M Mutilu \\
\hline 00 & 24 y Ro & \\
\hline 67 & Qusinut & MODEM CLOCK \\
\hline 68 & 179 K 6 & \\
\hline 69 & 0401000 & RACK \\
\hline 70 & 339 Ho & \\
\hline 71 & 0001\％ & PANEL \％SPECIAL CKTS \\
\hline 72 & 459 k 6 & \\
\hline 73 & 0001800 & hispeeo synchronous line aoaptek \\
\hline 74 & 209 R6 & \\
\hline 75 & 0000065 & NUM IN LIST P＊ \\
\hline 76 & \(2 J 966\) & \\
\hline 77 & 0081008 & ELEC ENGIN \\
\hline 78 & 2J 9 K6 & \\
\hline 78 & Whesuby & MĖCH tNGIN \\
\hline 80 & 249 k 6 & \\
\hline 81 & 0001060 & PROGRAMMING \\
\hline 82 & 209 R6 & \\
\hline 83 & 0601000 & OUCUMENTATION \\
\hline 84 & 169 रo & \\
\hline 85 & 08410no & T \＆E \\
\hline 86 & 259 k & \\
\hline 87 & Quyubua & NUM IN LIST O＊ \\
\hline 88 & 339 R6 & \\
\hline 89 & 0001060 & OPERATIONS PERSONNEL \\
\hline 98 & 299 R6 & \\
\hline 91 & 0001308 & SERVICE CONTRACT \\
\hline 92 & J5 9 Rô & \\
\hline 93 & Quv1000 & telephone 8 oata lease \\
\hline 94 & 289 R5 & \\
\hline 95 & Qe日tus） & TELEPHONE USAGE \\
\hline \(90^{\circ}\) & 29 R6 & \\
\hline 97 & 0： & \\
\hline
\end{tabular}
```

[^3]| Nare | value |
| :---: | :---: |
| allocregs | a |
| besptoreg | $c$ |
| ADY | d |
| execote | e |
| prepbopr | $f$ |
| GTR | 9 |
| Etoreg | b |
| inputparam | i |
| LDR | 1 |
| BROFP | n |
| PTR | F |
| BDP | 9 |
| random | r |
| typeout | t |

Figure 35. Macro Libraries for Fortran Cost Problem

```
kAPLIB
1 MUEF FINO(1)
2 L LLST
Kll
4 S LLST $1
5 MENO
6 MOEF NDEV
7
* MEND
9 MOEF TYPE (1)
10 ETOPEG RS IS RY
11 ETURFG Rg 6 RIE
12 LOR 3 kg
13 -RIO R9 RII
14 *KON MIG RIL
15 LOK SI R9
15 *Ky Kll Kl!
17 RII RIN RIN
18 LOR IHUG Mg
19 Ry RIU RIG
20 AOY RIG
2! MENO
```

Figure 35. Macro Libraries for Fortran Cost Problem (Concluded)


[^0]:    *For the lab system (one with no digital I/O), ETENTRY6 can be eliminated and ETENTRY3 modified to generate zero values for words 228 248

[^1]:    * The scenario library SCENLIB, shown in Figure 35 in Appendix VI, establishes the macros used in this example.

[^2]:    * Different versions needed

[^3]:    Figure 34．Scenarios for Fortran Cost Problem with Macros Expanded （Concluded）

