

IDENTIFICATION  
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PRODUCT CODE: AC-S039A-MC  
PRODUCT NAME: CXDPVA0 DPV-11 MODULE  
PRODUCT DATE: JUNE 1980  
MAINTAINER: DEC/X11 SUPPORT GROUP

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1. ABSTRACT  
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DPV IS AN IOMOD THAT EXERCISES UP TO SIX CONSECUTIVELY ADDRESSED SYNCHRONOUS LINE INTERFACES (DPV11) BY TRANSMITTING A STANDARD BINARY COUNT PATTERN IN EITHER INTERNAL OR EXTERNAL LOOPBACK. THE USER MAY ALSO SELECT EITHER BIT ORIENTED PROTOCOL (BOP) OR BYTE ORIENTED PROTOCOL (BCP). THE RECEIVED DATA IS COMPARED WITH THE TRANSMITTED DATA AND ANY ERRORS ARE REPORTED VIA THE CONSOLE TTY. ALL AVAILABLE INTERFACES (UP TO 6) ARE ACTIVATED AND RUNNING SIMULTANEOUSLY.

(NOTE: THIS MODULE IS A MODIFICATION OF CXDPBB WHICH IS THE DEC/X11 MODULE FOR THE DUP11).

2. REQUIREMENTS  
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HARDWARE: DPV11 SYNCHRONOUS INTERFACE  
STORAGE: THE DPV REQUIRES:  
1. DECIMAL WORDS: 604  
2. OCTAL WORDS: 1134  
3. OCTAL BYTES: 2270

3. PASS DEFINITION  
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ONE PASS OF THE DPV MODULE CONSISTS OF TRANSMITTING AND RECEIVING 14,400 8-BIT CHARACTERS (TOTAL) PER ACTIVE DEVICE.

4. EXECUTION TIME  
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ONE DPV RUNNING ALONE ON A LSI 11/23 PROCESSOR TAKES APPROXIMATELY 1 MINUTE TO COMPLETE ONE PASS. THIS TIME INCREASES SLIGHTLY AS MORE DEVICES ARE ADDED.

5. CONFIGURATION PARAMETERS  
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DEFAULT PARAMETERS:

DEVADR: 000001, VECTOR:001, BR1:5, BR2:5, DEVCNT:1  
THE DEVICE IS TESTED IN SDLC MODE AS A SECONDARY STATION, WITH A HARDWARE BCC CHECK USING THE CRC/CCITT POLYNOMIAL.

REQUIRED PARAMETERS: THE CSR AND VECTOR MUST BE SET UP BY THE USER.

6. DEVICE/OPTION SETUP  
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SRI  
BIT 0 = 0 BIT ORIENTED PROTOCOL  
BIT 0 = 1 BYTE ORIENTED PROTOCOL

BIT 1 = 0 INTERNAL LOOPBACK (MAINTENANCE MODE)  
BIT 1 = 1 EXTERNAL LOOPBACK (ONBOARD CONNECTOR - H3260 R5423)

7. MODULE OPERATION  
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TEST SEQUENCE:

- A. TEST UP TO 6 POSSIBLE DEVICES FOR SELECTION
- B. STORE THE NO. OF DEVICES TO BE TESTED AND SET UP THE VECTORS AND PRIORITIES FOR THESE DEVICES
- C. LOAD ALL REGISTERS--SET UP IN USER DEFINED MODE,TURN RECEIVER AND TRANSMITTER ON, AND INTERRUPT ENABLES FOR ALL ACTIVE DEVICES. ENABLE SELECTED DEVICES.
- D. TRANSMITTER INTERRUPT SERVICE:
  - 1.) TEST FOR FALSE INTERRUPT (READY (0)); REPORT ERRORS
  - 2.) OUTPUT NEXT CHARACTER TO THE DEVICE
  - 3.) RETURN TO MONITOR TO WAIT FOR RECEIVER INTERRUPT.
- E. RECEIVER INTERRUPT SERVICE:
  - 1.) TEST FOR FALSE INTERRUPT (DONE (0)); REPORT ERRORS
  - 2.) CHECK FOR DATA ERROR; REPORT ERRORS
  - 3.) IF LAST CHARACTER, CHECK FOR BCC ERROR.
  - 4.) RETURN TO MONITOR TO WAIT FOR TRANSMITTER INTERRUPT
- F. REPEAT D AND E UNTIL ALL DEVICES HAVE BEEN PROCESSED.
- G. TURN OFF ALL ACTIVE DEVICES AND DECREMENT ITERATION COUNT.
- H. IF NOT 0,RESTART AT B.
- H. SIGNAL END PASS.

8. OPERATION OPTIONS  
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- A. LOCATION DVID1 (DPV 14) MAY BE CHANGED TO SELECT ANY COMBINATION OF DEVICES BIT0=DEV0, BIT1=DEV1 .....BIT5=DEV5.  
NOTE: IF DVID1 IS INITIALLY 0 DPV #WILL BE DROPPED FROM TEST.

9. NON STANDARD PRINTOUTS  
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NONE: ALL PRINTOUTS HAVE STANDARD FORMATS AS DESCRIBED IN THE DEC/X11 DOCUMENT.



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3
4      100000      ;BIT DEFINITIONS
5      040000      BIT15= 100000
6      020000      BIT14= 40000
7      010000      BIT13= 20000
8      004000      BIT12= 10000
9      002000      BIT11= 4000
10     001000      BIT10= 2000
11     000400      BIT9= 1000
12     000200      BIT8= 400
13     000100      BIT7= 200
14     000040      BIT6= 100
15     000020      BIT5= 40
16     000010      BIT4= 20
17     000004      BIT3= 10
18     000002      BIT2= 4
19     000001      BIT1= 2
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28     000000      RXCSR= 0      ;RXCSR = RECEIVER CONTROL AND STATUS (16XXX0)
29     000002      RDSR= 2      ;RDSR = RECEIVER DATA AND STATUS (16XXX2) READ ONLY
30     000002      PCSAR= 2     ;PCSAR = PARAMETER CONTROL SYNC/ADDRESS (16XXX2) WRITE ONLY
31     000004      TXCSR= 4     ;TXCSR = TRANSMITTER CONTROL AND STATUS (16XXX4)
32     000006      TDSR= 6     ;TDSR = TRANSMITTER DATA AND STATUS (16XXX6)
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58     000004      TBE= BIT2      ;TRANSMIT BUFFER EMPTY
59     000002      TXACT= BIT1     ;TRANSMITTER ACTIVE
60     000001      RESET= BIT0     ;DPV RESET
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4 000244          ;BEGIN TESTING FOR THE DPV-11
5 000244 032767 000001 177544  START:
6 000252 001003          BIT    #BIT0,SRI    ;WHAT PROTOCOL?
7 000254 005067 177760  CLR    1$          ;IF SET, BCP
8 000260 000403          BR     2$          ;FLAG THAT BOP MODE REQUESTED.
9 000262          1$:
10 000262 012767 000001 177750  MOV    #1,MODE    ;FLAG THAT BCP MODE REQUESTED.
11 000270          2$:
12 000270 032767 000002 177520  BIT    #BIT1,SRI    ;WHAT LOOPBACK
13 000276 001003          BNE   3$          ;IF SET, EXTERNAL LOOPBACK.
14 000300 005067 177736  CLR    LOOP       ;FLAG THAT MAINTENANCE MODE (INTERNAL
15                          ;LOOPBACK) REQUESTED.
16 000304 000403          BR     4$
17 000306          3$:
18 000306 012767 000001 177726  MOV    #1,LOOP    ;FLAG THAT EXTERNAL LOOPBACK REQUESTED.
19 000314          4$:
20 000314 016767 177474 177714  MOV    DVID1,SELECT ;GET ACTIVE DEVICES
21 000322 001002          BNE   RESTRT     ;BR IF ANY ARE SELECTED
22 000324          DROP:
23 000324 104410 000000'  ENDS,BEGIN      ;;INCORRECT NUMBER OF DPV11'S
24
25          ;SETUP VECTORS FOR ACTIVE DEVICES
26 000330          RESTRT:
27 000330 032767 177700 177700  BIT    #<C77>,SELECT ;MAKE SURE NO MORE THAN 6 SELECTED.
28 000336 001372          BNE   DROP       ;INVALID-NO MORE THAN 6 DEVICES!!
29 000340 016701 177672  MOV    SELECT,R1   ;GET IMAGE OF RUNNING DEVICES
30 000344 001767          BEQ   DROP       ;BR IF ALL DPV'S DROPPED
31 000346 005067 177652  CLR    N,DPVS     ;CLEAR THE # OF DPV'S (SOFTWARE)
32 000352 016702 177432  MOV    VECTOR,R2  ;GET INITIAL VECTOR
33 000356 016700 177424  MOV    ADDR,R0    ;GET INITIAL ADDRESS
34 000362 012703 001752'  MOV    #LNKTAB,R3 ;SET ISR POINTER
35 000366 012767 002172' 177636  MOV    #RXBFO,RXB ;SET UP BUFFER POINTERS
36 000374          1$:
37 000374 006201          ASR    R1          ;ACTIVE?
38 000376 103410          BCS   3$          ;BR IF YES
39 000400 001437          BEQ   SETUP1     ;BR IF DONE
40 000402 062703 000024  ADD    #24,R3     ;POP ISR POINTER
41 000406          2$:
42 000406 062702 000010  ADD    #10,R2    ;NEXT DEVICE VECTOR
43 000412 062700 000010  ADD    #10,R0    ;NEXT DEVICE CSR
44 000416 000766          BR     1$        ;CONTINUE
45 000420          3$:
46 000420 005267 177600  INC    N,DPVS     ;UPDATE THE # TO RUN
47 000424 010312  MOV    R3,(R2)    ;LOAD ISR POINTER (RECEIVER)
48 000426 116762 177360 000002  MOVB  BR1,2(R2)   ;LOAD PRIORITY
49 000434 010063 000004  MOV    R0,4(R3)   ;LOAD CSR POINTER
50 000440 005063 000010  CLR    10(R3)    ;CLR REC BYTE COUNT
51 000444 062703 000012  ADD    #12,R3    ;UPDATE POINTER
52 000450 010362 000004  MOV    R3,4(R2)  ;LOAD TRANSMITTER ISR
53 000454 116762 177333 000006  MOVB  BR2,6(R2)  ;LOAD PRIORITY
54 000462 010063 000004  MOV    R0,4(R3)  ;LOAD CSR POINTER
55 000466 005063 000010  CLR    10(R3)   ;CLEAR TX BYTE COUNT
56 000472 062703 000012  ADD    #12,R3   ;NEXT ISR POINTER
57 000476 000743          BR     2$        ;CONTINUE
    
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58
59          ;SET UP BUFFERS
60 000500          SETUP1:
61 000500 016767 177520 177412  MOV    N,DPVS,INTR ;SET # OF INTERRUPTS
62 000506 016767 177512 177400  MOV    N,DPVS,WDTU ;SET # OF WORDS TO MEM
63 000514 016767 177504 177374  MOV    N,DPVS,WDFR ;SET # OF WORDS FROM MEM
64 000522 006367 177372  INTR    ;DOUBLE INTERRUPTS
65 000526 012700 000006  MOV    #6,R0     ;LOAD BUFFER SIZE
66 000532 012703 002156'  MOV    #TXBFO,R3 ;LOAD BUFFER START
67 000536          1$:
68 000536 012723 000135  MOV    #135,(R3)+ ;FIRST CHARACTER TO TRANSMIT.
69 000542 005300          DEC    R0         ;LOAD UNTIL DONE.
70 000544 001374          BNE   1$        ;BR IF MORE TO GO
71 000546 012700 000006  MOV    #6,R0     ;LOAD BUFFER SIZE
72 000552 012703 002172'  MOV    #RXBFO,R3 ;LOAD START ADDRESS
73 000556          2$:
74 000556 012723 000135  MOV    #135,(R3)+ ;FIRST CHARACTER TO RECEIVE.
75 000562 005300          DEC    R0         ;LOAD BUFFER UNTIL DONE.
76 000564 001374          BNE   2$        ;BR IF MORE TO GO
77
78          ;PRELIMINARY DEVICE SETUP
79 000566          SETUP2:
80 000566 016700 177214  MOV    ADDR,R0    ;LOAD FIRST CSR
81 000572 016701 177440  MOV    SELECT,R1 ;SET UP TO GET ACTIVE DEVICES
82 000576          1$:
83 000576 006201          ASR    R1          ;GET AN ACTIVE
84 000600 103404          BCS   2$          ;NONE
85 000602 001410          BEQ   ACTIV     ;BR IF DONE
86 000604 062700 000010  ADD    #10,R0    ;UPDATE FOR THE NEXT ONE
87 000610 000772          BR     1$        ;CONTINUE
88 000612          2$:
89 000612 004767 001024  JSR    PC,PRELIM ;GO DO THE DEVICE SETUP
90 000616 062700 000010  ADD    #10,R0    ;UPDATE FOR THE NEXT ONE
91 000622 000765          BR     1$        ;CONTINUE
92 000624          ACTIV:
93 000624 016701 177406  MOV    SELECT,R1 ;GET THE ACTIVE DPV'S
94 000630 016767 177370 177370  MOV    N,DPVS,TOTAL ;SET UP FOR DATA CHECK
95 000636 016700 177144  MOV    ADDR,R0   ;GET FIRST CSR
96 000642          1$:
97 000642 006201          ASR    R1          ;GET AN ACTIVE ONE
98 000644 103404          BCS   3$          ;BR IF ACTIVE
99 000646 001406          BEQ   WAIT     ;BR IF DONE
100 000650          2$:
101 000650 062700 000010  ADD    #10,R0   ;UPDATE CSR
102 000654 000772          BR     1$        ;CONTINUE
103 000656          3$:
104 000656 004767 001052  JSR    PC,READY  ;TURN ON DEVICE
105 000662 000772          BR     2$        ;CONTINUE
106
107          ;DELAY AND SCAN FOR FINISH ROUTINE
108 000664          WAIT:
109 000664 012705 020000  MOV    #20000,R5 ;SET UP FOR A LONG DELAY
110
111          SCAN:
112 000670          1$:
113 000670          BREAK,BEGIN ;APPROX 30 SECONDS ON A LSI 11/23
114 000670 104407 000000'  ;APPROX 60 SECONDS ON A LSI 11/2
    
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000674 104407 000000'
115 000700 005767 177322
116 000704 001004
117
118
119 000706 104413 000000'
120 000712
121 000712 000167 177412
122 000716
123 000716 005305
124 000720 001363
125 000722 104403 000000' 000734'
126 000730 104410 000000'
127 000734
128 000734 002222'
129 000736 177777

BREAK$,BEGIN ;THEN CONTINUE AT NEXT INSTRUCTION.
TST TOTAL ;GET THE # OF ACTIVE DPVS
BNE 36 ;BR IF MORE TO GO
;-----
;-----
ENDIT$,BEGIN ;SIGNAL END OF ITERATION.
;MONITOR SHALL TEST END OF PASS

2$: JMP RESIRT ;RESTART PROGRAM

3$: DEC R5 ;STALL FOR ALL DPV'S TO FINISH
BNE 16 ;BR IF DELAY NOT ZERU
MSGN$,BEGIN,HUNG ;ASCII MESSAGE CALL WITH COMMON HEADER
END$,BEGIN ;DROP THE MODULE

HUNG: MHUNG ;POINTER TO ASCII MESSAGE
-1 ;TERMINATOR
    
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1
2 000740
3 000740 010046
4 000742 010146
5 000744 012500
6 000746 012501
7 000750 032760 100000 000006
8 000756 001430
9 000760 010067 177114
10 000764 011067 177112
11 000770 016067 000006 177106
12 000776 012760 000001 000004
13 001004 012601
14 001006 012600
15 001010 012605
16
17 001012 000004 000000' 001020'
18
19 001020
20 001020 012767 000041 177060
21 001026 104406 000000' 000000
22
23 001034 000167 177630
24 001040
25 001040 005215
26 001042 022715 000001
27 001046 001004
28 001050 052760 000400 000006
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35 001072 001006
36 001074 052760 001000 000006
37 001102 042760 000100 000004
38 001110
39 001110 012601
40 001112 012600
41 001114 012605
42 001116 000002

TXISR: ;TRANSMITTER INTERRUPT SERVICE ROUTINE
MOV R0,-(SP) ;SAVE R0 ON THE STACK
MOV R1,-(SP) ;SAVE R1 ON THE STACK
MOV (R5),R0 ;GET DEVICE CSR AND POP OFFSET TO R5
MOV (R5)+,R1 ;GET DATA ADDRESS AND LEAVE R5=COUNT ADDRESS
BIT #TERR,TDSR(R0) ;DATA LATE ERROR?
BEQ 25 ;BR IF NO
MOV R0,CSRA ;ADDRESS OF RXCSR FOR TYPEOUT
MOV (R0),ACSR ;CONTENTS OF RXCSR FOR TYPEOUT
MOV TDSR(R0),ASTAT ;CONTENTS OF TDSR FOR TYPEOUT
MOV #RESET,TXCSR(R0) ;TURN OFF THE DEVICE
MOV (SP)+,R1 ;POP STACK TO R1
MOV (SP)+,R0 ;POP STACK TO R0
MOV (SP)+,R5 ;POP STACK TO R5
;-----
PIRQ$,BEGIN,16 ; QUEUE UP TO CONTINUE AT 16 AND R11
;-----

1$: MOV #41,ERRTYP ;XMITTER DATA LATE
;*****
SOFER$,BEGIN,NULL ;TRANSMIT DATA LATE ERROR
;*****
JMP SCAN ;CONTINUE SCANNING FOR END

2$: INC (R5) ;UPDATE THE COUNTER
CMP #1,(R5) ;IS THIS THE FIRST ONE?
BNE 36 ;IF NOT, SEND DATA
BIS #TSOM,TDSR(R0) ;IF YES, DON'T TRANSMIT DATA
;SEND OUT A SECOND FLAG (OR SYNCH)
;NOTE: IN BOP MODE ONLY 1 FLAG IS
;NECESSARY. IN BCP MODE, 2 SYNCHS
;ARE NEEDED. IN BOTH CASE WE SEND OUT
;2 JUST FOR EASE OF CODING.

3$: BR 46 ;

MOV (R1),TDSR(R0) ;PUSH OUT DATA.
INCH (R1) ;CHANGE DATA FOR NEXT CHARACTER.
CMP #1201.,(R5) ;CHECK FOR FINISH. (THE TRANSMIT COUNTER IS
;1201. BECAUSE WE COMPENSATE FOR THE EXTRA
;FLAG OR SYNCH).
BNE 46 ;BR IF MORE TO GO
BIS #TEDM,TDSR(R0) ;END OF MESSAGE.
BIC #TXIE,TXCSR(R0) ;TURN OFF INTERRUPTS

46: MOV (SP)+,R1 ;POP STACK TO R1
MOV (SP)+,R0 ;POP STACK TO R0
MOV (SP)+,R5 ;POP STACK TO R5
RTI ;RETURN FROM INTERRUPT
    
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1
2 001120 RXISR: ;RECEIVER INTERRUPT SERVICE ROUTINE
3 001120 010046 MOV R0,-(SP) ;SAVE R0 ON THE STACK
   001122 010146 MOV R1,-(SP) ;SAVE R1 ON THE STACK
4 001124 012500 MOV (R5)+,R0 ;GET CSR
5 001126 012501 MOV (R5)+,R1 ;GET BUFFER ADDR AND LEAVE R5=BYTE COUNT
6 001130 105710 TSTB (R0) ;IS RECEIVER DATA AVAILABLE? (BIT 7 OF RXCSR)
7 001132 100430 BMI 2$ ;UK IF SET - BR
8
9 001134 010067 176740 MOV R0,CSRA ;ADDRESS OF RXCSR FOR HARD ERROR MESSAGE.
10 001140 011067 176736 MOV (R0),ACSR ;CONTENTS OF RXCSR FOR HARD ERROR MESSAGE.
11 001144 016067 000002 176732 MOV RDSR(R0),ASTAT ;CONTENTS OF RDSR FOR HARD ERROR MESSAGE.
12 001152 012760 000001 000004 MOV #RESET,TXCSR(R0) ;TURN OFF THE DEVICE
13
14 001160 012601 MOV (SP)+,R1 ;POP STACK TO R1
   001162 012600 MOV (SP)+,R0 ;POP STACK TO R0
   001164 012605 MOV (SP)+,R5 ;POP STACK TO R5
15
   001166 000004 000000' 001174' ;-----
   PIRQS,BEGIN,1$ ; QUEUE UP TO CONTINUE AT 1$ AND RTI
   ;-----
16 001174 16$:
17 001174 012767 000011 176704 MOV #11,ERRTYP ;ILLEGAL INTERRUPT
18 001202 104405 000000' 000000 *****
   HRDERS,BEGIN,NULL ;FALSE INTERRUPT
   *****
19 001210 000167 177454 JMP SCAN ;CONTINUE SCANNING FOR END
20 001214 28$:
21 001214 016067 000002 177012 MOV RDSR(R0),SAVBF ;SAVE THE CHARACTER IN THE BUFFER
22 ;WAS THERE AN OVERRUN OR ABORT?
23 001222 032767 006000 177004 BIT #ROVERIRABORT,SAVBF
24 001230 001012 BNE 5$ ;BRANCH TO ERROR IF AN OVERRUN OR ABORT REC.
25 001232 005215 INC (R5) ;UPDATE THE CHARACTER COUNT.
26 001234 022715 002260 CMP #1200,(R5) ;DONE?
27 001240 001467 BEQ 13$ ;BR IF DATA DONE
28 001242 121167 176766 CMPEB (R1),SAVBF ;IS THE DATA OK?
29 001246 001033 BNE 10$ ;IF NOT, PROCEED.
30 001250 105211 INCB (R1) ;UPDATE DATA
31 001252 000167 000354 JMP 20$
32
33 001256 58$:
34 001256 010067 176616 MOV R0,CSRA ;ADDRESS OF RXCSR FOR HARD ERROR MESSAGE.
35 001262 011067 176614 MOV (R0),ACSR ;CONTENTS OF RXCSR FOR HARD ERROR MESSAGE.
36 001266 016067 000002 176610 MOV RDSR(R0),ASTAT ;CONTENTS OF RDSR FOR HARD ERROR MESSAGE.
37 001274 012760 000001 000004 MOV #RESET,TXCSR(R0) ;TURN OFF THE DEVICE
38
39 001302 012601 MOV (SP)+,R1 ;POP STACK TO R1
   001304 012600 MOV (SP)+,R0 ;POP STACK TO R0
   001306 012605 MOV (SP)+,R5 ;POP STACK TO R5
40
   001310 000004 000000' 001316' ;-----
   PIRQS,BEGIN,6$ ; QUEUE UP TO CONTINUE AT 6$ AND RTI
   ;-----
41 001316 68$:
42 001316 012767 000017 176562 MOV #17,ERRTYP ;OVERRUN OR ABORT.
43 001324 104405 000000' 000000 *****
   HRDERS,BEGIN,NULL ;HARDWARE DATA ERROR
   *****
44 001332 000167 177332 JMP SCAN ;CONTINUE SCANNING FOR END
    
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45
46
47 001336 10$:
48 001336 010167 176540 MOV R1,SBADR ;LOAD GOOD DATA ADRS
49 001342 010067 176532 MOV R0,CSRA ;LOAD CSR
50 001346 012767 000234' 176530 MOV #SAVBF,WASADR ;LOAD BAD DATA ADRS
51 001354 111167 176526 MOV (R1),ASB ;LOAD GOOD DATA
52 001360 116767 176650 176522 MOV #SAVBF,AWAS ;LOAD BAD DATA
53 001366 012760 000001 000004 MOV #RESET,TXCSR(R0) ;TURN OFF THE DEVICE
54 001374 012601 MOV (SP)+,R1 ;POP STACK TO R1
55 001376 012600 MOV (SP)+,R0 ;POP STACK TO R0
   001400 012605 MOV (SP)+,R5 ;POP STACK TO R5
   ;-----
   PIRQS,BEGIN,11$ ; QUEUE UP TO CONTINUE AT 11$ AND RTI
   ;-----
56 001410 116$:
57 001410 104404 000000' *****
   DATERS,BEGIN ;DATA ERROR!!!
   *****
58 001414 000167 177250 JMP SCAN ;CONTINUE SCANNING FOR END
59
60
61 001420 138$:
62 001420 005767 176614 TST MODE ;PROTOCOL?
63 001424 001405 BEQ 14$ ;BR IF BOP MODE
64 001426 032767 100000 176600 BIT #ERR,SAVBF ;CHECK FOR CRC ERROR
65 001434 001071 BNE 19$ ;BR IF NO ERROR
66 001436 000440 BR 17$ ;CRC ERROR
67 001440 146$:
68 001440 032767 100000 176566 BIT #ERR,SAVBF ;CHECK FOR CRC ERROR
69 001446 001034 BNE 17$ ;BR IF ERROR
70 001450 032767 001000 176556 BIT #REOM,SAVBF ;WAS RECEIVE END OF MESSAGE RECEIVED?
71 001456 001060 BNE 19$ ;IF YES, UK.
72
73 001460 010067 176414 MOV R0,CSRA ;ADDRESS OF RXCSR FOR HARD ERROR MESSAGE.
74 001464 011067 176412 MOV (R0),ACSR ;CONTENTS OF RXCSR FOR HARD ERROR MESSAGE.
75 001470 016767 176540 176406 MOV SAVBF,ASTAT ;CONTENTS OF RDSR FOR HARD ERROR MESSAGE.
76 001476 012760 000001 000004 MOV #RESET,TXCSR(R0) ;TURN OFF THE DEVICE
77
78 001504 012601 MOV (SP)+,R1 ;POP STACK TO R1
   001506 012600 MOV (SP)+,R0 ;POP STACK TO R0
   001510 012605 MOV (SP)+,R5 ;POP STACK TO R5
79
   001512 000004 000000' 001520' ;-----
   PIRQS,BEGIN,16$ ; QUEUE UP TO CONTINUE AT 16$ AND RTI
   ;-----
80 001520 16$:
81 001520 012767 000017 176360 MOV #17,ERRTYP ;RECEIVE END OF MESSAGE NOT RECEIVED
82 001526 104405 000000' 000000 *****
   HRDERS,BEGIN,NULL ;RECEIVER ERROR
   *****
83 001534 000167 177130 JMP SCAN ;CONTINUE SCANNING FOR END
84
85 001540 176$:
86
87 001540 010067 176334 MOV R0,CSRA ;ADDRESS OF RXCSR FOR HARD ERROR MESSAGE.
88 001544 011067 176332 MOV (R0),ACSR ;CONTENTS OF RXCSR FOR HARD ERROR MESSAGE.
89 001550 016067 000002 176326 MOV RDSR(R0),ASTAT ;CONTENTS OF RDSR FOR HARD ERROR MESSAGE.
    
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90 001556 012760 000001 000004      MOV      #RESET, TXCSR(R0) ;TURN OFF THE DEVICE
91
92 001564 012601      MOV      (SP)+, R1      ;POP STACK TO R1
   001566 012600      MOV      (SP)+, R0      ;POP STACK TO R0
   001570 012605      MOV      (SP)+, R5      ;POP STACK TO R5
93
   001572 000004 000000' 001600'    ;-----
   PIRDS, BEGIN, 18$      ; QUEUE UP TO CONTINUE AT 18$ AND RTI
   ;-----
94 001600      18$:
95 001600 012767 000043 176300      MOV      #43, ERRTP      ;CRC ERROR
96
   001606 104405 000000' 000000    ;*****
   HDRS, BEGIN, NULL      ;HARDWARE DETECTED CRC ERROR
   ;*****
97 001614 000167 177050      JMP      SCAN      ;CONTINUE SCANNING FOR END
98 001620
99 001620 012760 000001 000004      19$:
100 001626 005367 176374      MOV      #RESET, TXCSR(R0) ;TURN OFF THE DEVICE
101 001632      DEC      TOTAL      ;DECREMENT DEVICE COUNTER
102 001632 012601      20$:
   001634 012600      MOV      (SP)+, R1      ;POP STACK TO R1
   001636 012605      MOV      (SP)+, R0      ;POP STACK TO R0
103 001640 000002      MOV      (SP)+, R5      ;POP STACK TO R5
   RTI      ;RETURN FROM INTERRUPT
    
```

```

1
2
3      ;SUBROUTINES
4
5      ;PFELIM SUBROUTINE SETS UP THE DPV
6      ; R0 = CSR ADDRESS
7      PRELIM:
8 001642 052760 000001 000004      BIS      #RESET, TXCSR(R0);RESET THE DPV11
9 001650 005767 176364      TST      MODE      ;WHAT PROTOCOL?
10 001654 001004      BNE      18$      ;BRANCH IF BCP.
11
12 001656 012760 010135 000002      ;BOP MODE - CRC-CCITT AND SECONDARY ADDRESS
13 001664 000403      MOV      #SECADR:135, PCSAR(R0)
14 001666      BR      28$
15
16 001666 012760 041626 000002      18$:
17 001674      MOV      #BCP:161226, PCSAR(R0)
18 001674 005767 176342      28$:
19 001700 001004      TST      LOOP      ;WHAT LOOPBACK?
20 001702 052760 000010 000004      BNE      36$      ;BRANCH IF EXTERNAL LOOPBACK.
21 001710 000403      BIS      #MAINT, TXCSR(R0);TURN ON MAINTENANCE MODE (INTERNAL LOOP)
22 001712      BR      48$
23 001712 052760 000004 000000      36$:
24 001720      BIS      #RTS, RXCSR(R0) ;SET RTS IN ORDER TO USE TURNAROUND.
25 001720 052710 000120      48$:
26 001724 052760 000020 000004      BIS      #RXEN:RXIE, (R0) ;TURN ON RECEIVER AND INT. ENABLE
27 001732 000207      BIS      #TXEN, TXCSR(R0) ;ENABLE THE TRANSMITTER
   RTS      PC      ;RETURN
28
29      ;READY SUBROUTINE TURNS ON THE TRANSMITTER
30      ; R0 = CSR ADDRESS
31 001734      READY:
32 001734 052760 000400 000006      BIS      #TSOM, IDSR(R0) ;TURN ON TRANSMITTER
33 001742 052760 000100 000004      BIS      #TXIE, TXCSR(R0) ;TURN ON TRANSMITTER INT. ENABLE
34 001750 000207      RTS      PC      ;RETURN
35
36
    
```



DEC/X11-1 SYSTEM EXERCISER MACRO DEFINITION MODULE

```

1      ;SERVICE CODE FOR LINKING A PARTICULAR DEVICE
2      ;TO A COMMON TRANSMIT OR RECEIVE INTERRUPT SERVICE ROUTINE.
3
4 001752      LNKTAB:
5              .MACRO  $JS,$Q
6              JSR     R5,RXISR      ;ANSWER FOR DEVICE '$Q REC ISR
7              0              ;CSR FOR DEVICE '$Q REC ISR
8              RXBF'$Q         ;BUFFER LOC FOR DEVICE '$Q
9              BCNT'$Q         ;BYTE COUNT ADDRESS FOR RXBF'$Q
10
11             JSR     R5,TXISR      ;ANSWER FOR DEVICE '$Q TX ISR
12             0              ;CSR FOR DEVICE '$Q TX ISR
13             TXBF'$Q         ;DATA ADDRESS FOR DEVICE '$Q
14             OFSET'$Q        ;BYTE COUNT ADDRESS FOR DEVICE '$Q
15
16             .ENDM  $JS
17
18             $JS  $Q
19             .ENDM
20
001752 004567 177142      JSR     R5,RXISR      ;ANSWER FOR DEVICE 0 REC ISR
001756 000000              0              ;CSR FOR DEVICE 0 REC ISR
001760 002172'            RXBF0         ;BUFFER LOC FOR DEVICE 0
001762 002206'            BCNT0         ;BYTE COUNT ADDRESS FOR RXBF0
001764 004567 176750      JSR     R5,TXISR      ;ANSWER FOR DEVICE 0 TX ISR
001770 000000              0              ;CSR FOR DEVICE 0 TX ISR
001772 002156'            TXBF0         ;DATA ADDRESS FOR DEVICE 0
001774 002142'            OFSET0        ;BYTE COUNT ADDRESS FOR DEVICE 0
001776 004567 177116      JSR     R5,RXISR      ;ANSWER FOR DEVICE 1 REC ISR
002002 000000              0              ;CSR FOR DEVICE 1 REC ISR
002004 002174'            RXBF1         ;BUFFER LOC FOR DEVICE 1
002006 002210'            BCNT1         ;BYTE COUNT ADDRESS FOR RXBF1
002010 004567 176724      JSR     R5,TXISR      ;ANSWER FOR DEVICE 1 TX ISR
002014 000000              0              ;CSR FOR DEVICE 1 TX ISR
002016 002160'            TXBF1         ;DATA ADDRESS FOR DEVICE 1
002020 002144'            OFSET1        ;BYTE COUNT ADDRESS FOR DEVICE 1
002022 004567 177072      JSR     R5,RXISR      ;ANSWER FOR DEVICE 2 REC ISR
002026 000000              0              ;CSR FOR DEVICE 2 REC ISR
002030 002176'            RXBF2         ;BUFFER LOC FOR DEVICE 2
002032 002212'            BCNT2         ;BYTE COUNT ADDRESS FOR RXBF2
002034 004567 176700      JSR     R5,TXISR      ;ANSWER FOR DEVICE 2 TX ISR
002040 000000              0              ;CSR FOR DEVICE 2 TX ISR
002042 002162'            TXBF2         ;DATA ADDRESS FOR DEVICE 2
002044 002146'            OFSET2        ;BYTE COUNT ADDRESS FOR DEVICE 2
002046 004567 177046      JSR     R5,RXISR      ;ANSWER FOR DEVICE 3 REC ISR
002052 000000              0              ;CSR FOR DEVICE 3 REC ISR
002054 002200'            RXBF3         ;BUFFER LOC FOR DEVICE 3
002056 002214'            BCNT3         ;BYTE COUNT ADDRESS FOR RXBF3
002060 004567 176654      JSR     R5,TXISR      ;ANSWER FOR DEVICE 3 TX ISR
002064 000000              0              ;CSR FOR DEVICE 3 TX ISR
002066 002164'            TXBF3         ;DATA ADDRESS FOR DEVICE 3

```

DEC/X11-1 SYSTEM EXERCISER MACRO DEFINITION MODULE

```

002070 002150'            OFSET3        ;BYTE COUNT ADDRESS FOR DEVICE 3
002072 004567 177022      JSR     R5,RXISR      ;ANSWER FOR DEVICE 4 REC ISR
002076 000000              0              ;CSR FOR DEVICE 4 REC ISR
002100 002202'            RXBF4         ;BUFFER LOC FOR DEVICE 4
002102 002216'            BCNT4         ;BYTE COUNT ADDRESS FOR RXBF4
002104 004567 176630      JSR     R5,TXISR      ;ANSWER FOR DEVICE 4 TX ISR
002110 000000              0              ;CSR FOR DEVICE 4 TX ISR
002112 002166'            TXBF4         ;DATA ADDRESS FOR DEVICE 4
002114 002152'            OFSET4        ;BYTE COUNT ADDRESS FOR DEVICE 4
002116 004567 176776      JSR     R5,RXISR      ;ANSWER FOR DEVICE 5 REC ISR
002122 000000              0              ;CSR FOR DEVICE 5 REC ISR
002124 002204'            RXBF5         ;BUFFER LOC FOR DEVICE 5
002126 002220'            BCNT5         ;BYTE COUNT ADDRESS FOR RXBF5
002130 004567 176604      JSR     R5,TXISR      ;ANSWER FOR DEVICE 5 TX ISR
002134 000000              0              ;CSR FOR DEVICE 5 TX ISR
002136 002170'            TXBF5         ;DATA ADDRESS FOR DEVICE 5
002140 002154'            OFSET5        ;BYTE COUNT ADDRESS FOR DEVICE 5

```

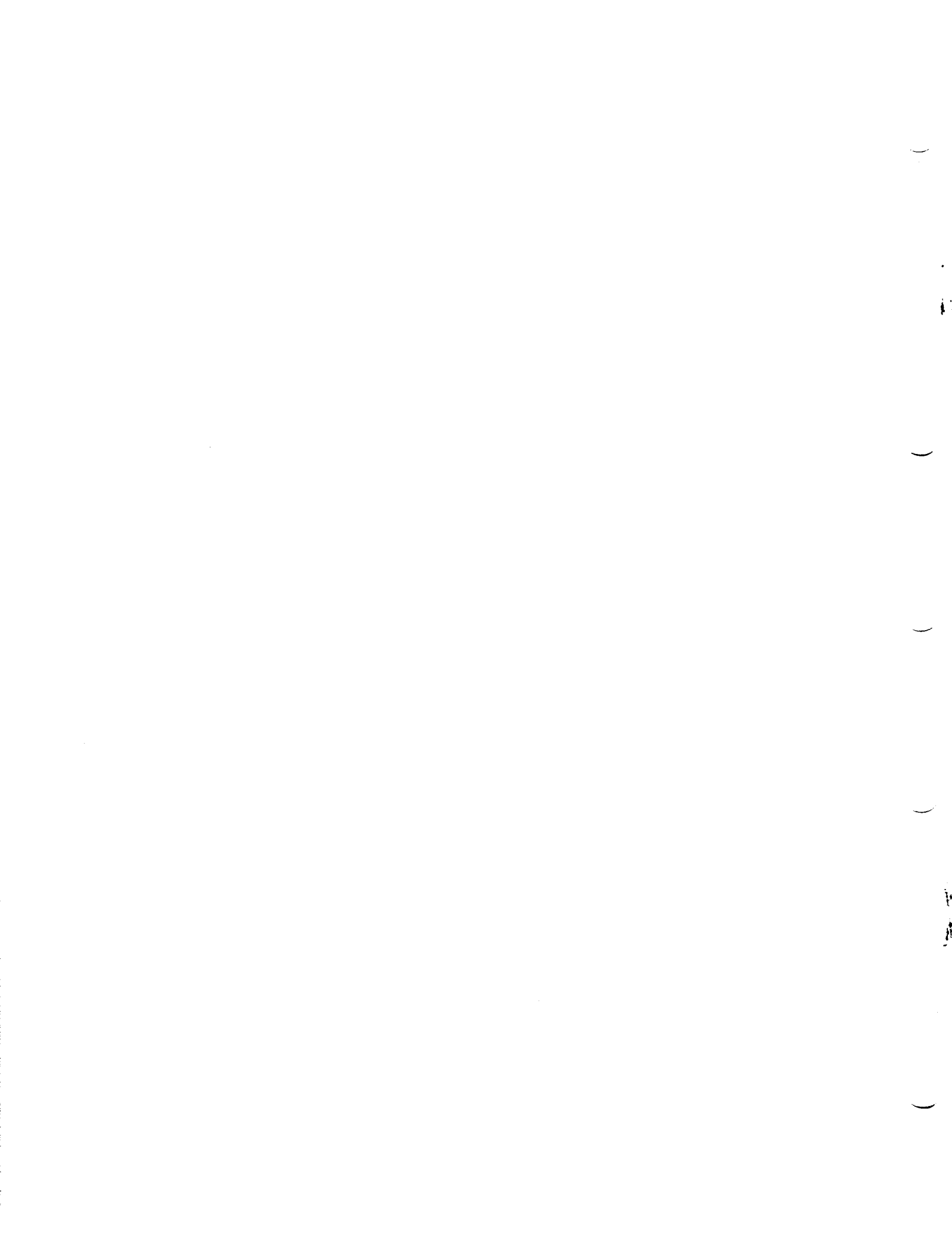
```
1          ;BUFFER AREAS
2
3          .MACRO $OF,$Q
4  OFFSET'$Q: .WORD 0          ;TRANSMITTER BYTE COUNT POINTER FOR DEVICE '$Q
5  .ENDM
6  $OF $Q
7  .ENDM
8  002142 000000  OFFSET0: .WORD 0          ;TRANSMITTER BYTE COUNT POINTER FOR DEVICE 0
9  002144 000000  OFFSET1: .WORD 0          ;TRANSMITTER BYTE COUNT POINTER FOR DEVICE 1
10 002146 000000  OFFSET2: .WORD 0          ;TRANSMITTER BYTE COUNT POINTER FOR DEVICE 2
11 002150 000000  OFFSET3: .WORD 0          ;TRANSMITTER BYTE COUNT POINTER FOR DEVICE 3
12 002152 000000  OFFSET4: .WORD 0          ;TRANSMITTER BYTE COUNT POINTER FOR DEVICE 4
13 002154 000000  OFFSET5: .WORD 0          ;TRANSMITTER BYTE COUNT POINTER FOR DEVICE 5
14
15          .MACRO $TB,$Q
16  TXBF'$Q: .WORD 0          ;TRANSMITTER BUFFER FOR DEVICE '$Q
17  .ENDM
18  $TB $Q
19  .ENDM
20 002156 000000  TXBF0: .WORD 0          ;TRANSMITTER BUFFER FOR DEVICE 0
21 002160 000000  TXBF1: .WORD 0          ;TRANSMITTER BUFFER FOR DEVICE 1
22 002162 000000  TXBF2: .WORD 0          ;TRANSMITTER BUFFER FOR DEVICE 2
23 002164 000000  TXBF3: .WORD 0          ;TRANSMITTER BUFFER FOR DEVICE 3
24 002166 000000  TXBF4: .WORD 0          ;TRANSMITTER BUFFER FOR DEVICE 4
25 002170 000000  TXBF5: .WORD 0          ;TRANSMITTER BUFFER FOR DEVICE 5
26
27          .MACRO $RB,$Q
28  RXBF'$Q: .WORD 0          ;RECEIVER BUFFER FOR DEVICE '$Q
29  .ENDM
30  $RB $Q
31  .ENDM
32 002172 000000  RXBF0: .WORD 0          ;RECEIVER BUFFER FOR DEVICE #0
33 002174 000000  RXBF1: .WORD 0          ;RECEIVER BUFFER FOR DEVICE #1
34 002176 000000  RXBF2: .WORD 0          ;RECEIVER BUFFER FOR DEVICE #2
35 002200 000000  RXBF3: .WORD 0          ;RECEIVER BUFFER FOR DEVICE #3
36 002202 000000  RXBF4: .WORD 0          ;RECEIVER BUFFER FOR DEVICE #4
37 002204 000000  RXBF5: .WORD 0          ;RECEIVER BUFFER FOR DEVICE #5
38
39          .MACRO $BC,$Q
40  BCNT'$Q: .WORD 0          ;RECEIVER BYTE COUNT POINTER FOR DEVICE '$Q
41  .ENDM
42  $BC $Q
43  .ENDM
44 002206 000000  BCNT0: .WORD 0          ;RECEIVER BYTE COUNT POINTER FOR DEVICE 0
45 002210 000000  BCNT1: .WORD 0          ;RECEIVER BYTE COUNT POINTER FOR DEVICE 1
46 002212 000000  BCNT2: .WORD 0          ;RECEIVER BYTE COUNT POINTER FOR DEVICE 2
47 002214 000000  BCNT3: .WORD 0          ;RECEIVER BYTE COUNT POINTER FOR DEVICE 3
48 002216 000000  BCNT4: .WORD 0          ;RECEIVER BYTE COUNT POINTER FOR DEVICE 4
49 002220 000000  BCNT5: .WORD 0          ;RECEIVER BYTE COUNT POINTER FOR DEVICE 5
50
51          ;ASCII MESSAGES
52 40 002222 045 104 120 MHUNG: .ASCIIZ '%DPV11 MODULE IS HUNG - SEE LISTING%'
```

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44          .EVEN
45          .END
```









368 000000

.REPT 0

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IDENTIFICATION  
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PRODUCT CODE: AC-S054A-MC

PRODUCT NAME: CXVTCA0 VTV30-K CSS DEC/X MOD

DATE CREATED: 20 MAR 80

AUTHOR: PAUL TAYLOR

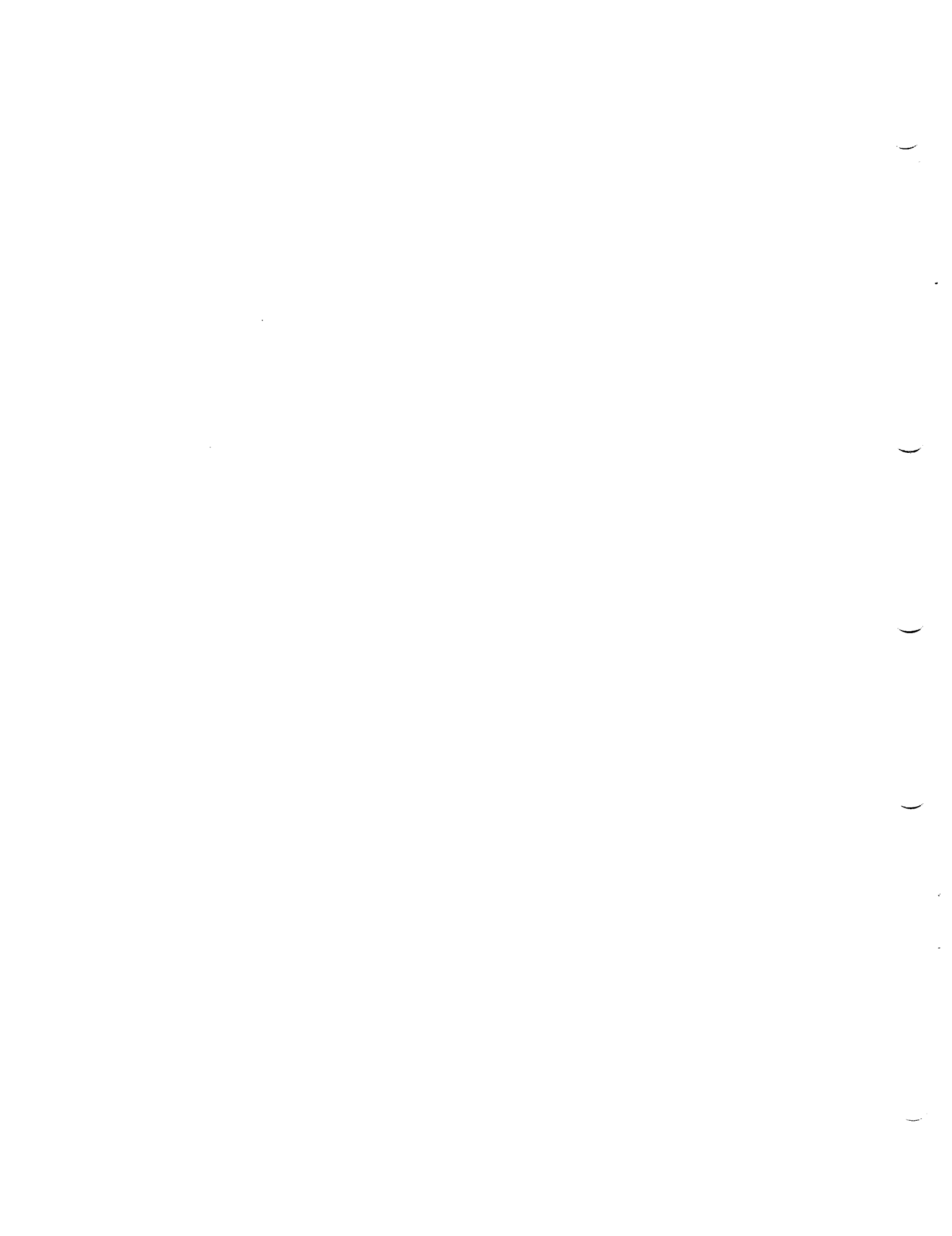
MAINTAINER: COMPUTER SPECIAL SYSTEMS,  
DIGITAL EQUIPMENT CO.  
LTD.,  
READING, BERKS, U.K.

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PROGRAM HISTORY:

VD.	AUTHOR	DATE	PURPOSE
01	P. TAYLOR	MAR-80	ORIGINAL

1. ABSTRACT

VTCA IS A BKMOD THAT EXERCISES THE VTV30-K COLOUR GRAPHICS CONTROLLER BY DISPLAYING MOVING DIAGONAL LINES OF CHARACTERS ACROSS THE DISPLAY. COLOUR INFORMATION IS DISPLAYED IN VERTICAL STRIPES.

2. REQUIREMENTS

2.1 HARDWARE

EACH MODULE WILL EXERCISE ONE VTV30-K DISPLAY CONTROLLER.

2.2 STORAGE

VTCA REQUIRES 1286 (DECIMAL) WORDS.

3. PASS DEFINITION

ONE PASS OF VTCA CONSISTS OF 43 CHANGES OF PICTURE.

4. EXECUTION TIME

ONE PASS OF VTCA TAKES TWENTY SECONDS WHEN RUNNING STAND ALONE ON AN LSI-11.

5. CONFIGURATION REQUIREMENTS

5.1 DEFAULT PARAMETERS

DVADR: 174000

5.2 REQUIRED PARAMETERS

SR1 MUST BE SET UP TO INDICATE THE TYPE OF CONTROLLER BEING TESTED:-

M7368	SR1 = 2
M7369	SR1 = 3

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M7370 SR1 = 0  
M7371 SR1 = 1  
M7372 SR1 = 2

IN ADDITION THE VALUE 4 MUST BE ADDED TO SR1  
IF A T.V. ENCODER IS USED.

6. DEVICE/OPTION SETUP

THE DEVICE MUST BE ABLE TO CONNECT TO A COLOUR T.V.  
MONITOR.

7. MODULE OPERATION

- A. SET UP THE DEVICE CSR AND DBUF ADDRESSES.
- B. CHECK FOR 525/625 LINE OPERATION AND SET UP THE  
MAXIMUM X AND Y CO-ORDINATES.
- C. IF THE DEVICE HAS A RAM CHARACTER SET, LOAD THE  
CAHRACTER SET FROM PROGRAM MEMORY.
- D. STARTING AT THE TOP LEFT HAND CORNER OF THE DISPLAY,  
PUT OUT INCREMENTING CHARACTER CODES BEGINNING ON  
ROW 0. THIS WILL APPEAR AS DIAGONAL LINES OF  
IDENTICAL CHARACTERS RUNNING FROM TOP RIGHT TO  
BOTTOM LEFT, THE CHARACTER CODES INCREMENTING FROM  
LEFT TO RIGHT.
- E. THE COLOUR CODES ARE DISPLAYED AS VERTICAL LINES.  
STARTING WITH RED BACKGROUND, THE FOREGROUND COLOURS  
OF RED, GREEN, THEN BLUE ARE DISPLAYED. THE  
BACKGROUND COLOUR IS THEN INCREMENTED AND THE  
FOREGROUND COLOUR SEQUENCE IS REPEATED.
- F. ONCE THE WHOLE DISPLAY HAS BEEN FILLED, INCREMENT  
THE STARTING CHARACTER CODE, AND CHECK FOR END OF  
PASS. THUS, UNTIL END OF PASS OCCURS, THE DISPLAY  
WILL BE UPDATED STARTING WITH THE NEXT CHARACTER  
CODE. THIS WILL APPEAR AS IF THE DIAGONAL LINES ARE  
MOVING FROM RIGHT TO LEFT.
- G. IF NO END OF PASS OCCURS, RETURN TO D. OTHERWISE,  
RETURN TO A.

8. OPERATION OPTIONS

THE SR1 SETTINGS MUST BE SET UP TO MATCH THE HARDWARE  
SETTINGS.

9. NON STANDARD PRINTOUT

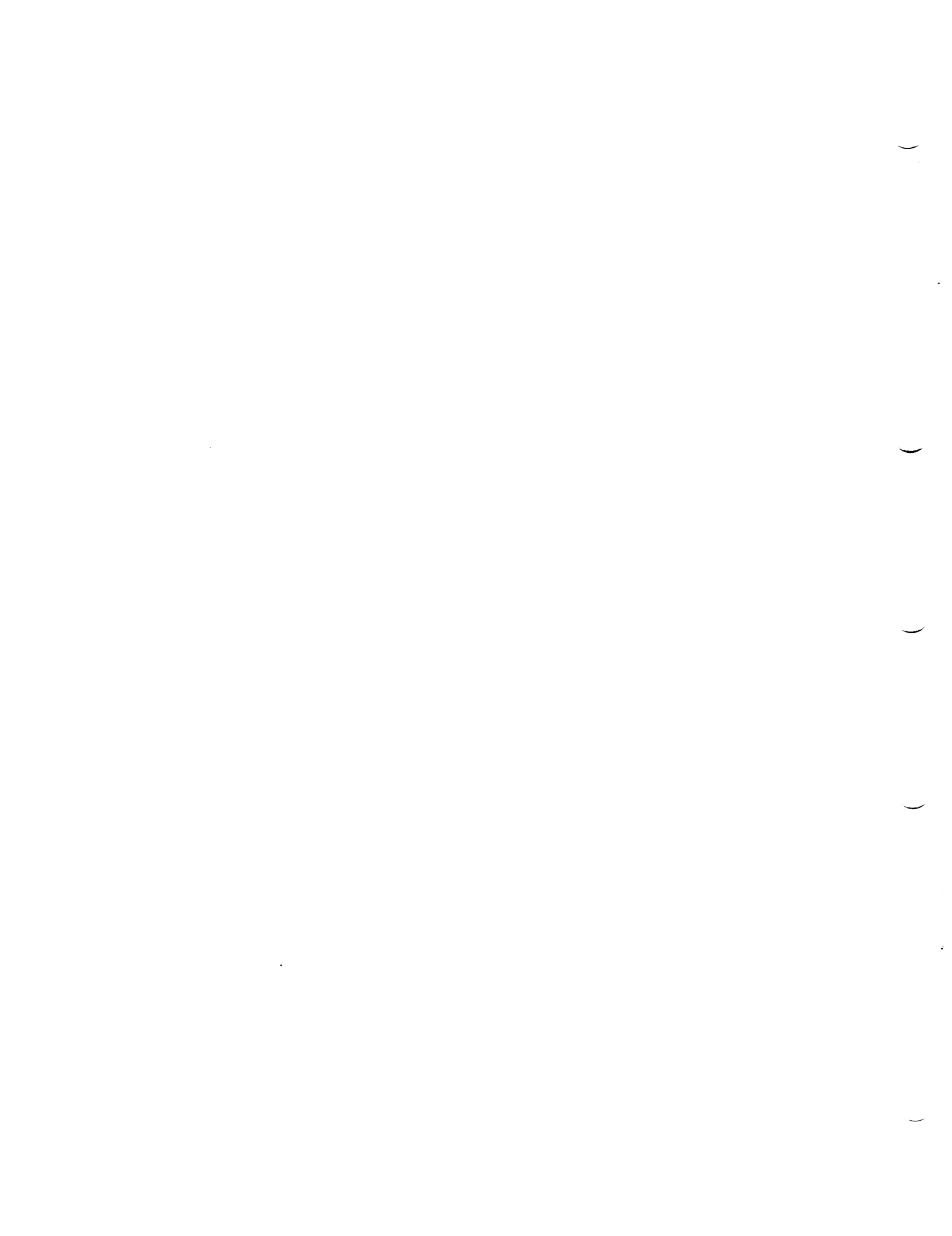
(  
VTCA DEC/X11 SYSTEM EXERCISER MODULE  
XVTCAO.P11 21-JAN-81 14:47

(  
MACY11 30A(1052) 21-JAN-81 14:48 PAGE 3-2

520  
521

(  
NONE.  
.ENDR

(  
SEQ 0004



```
523 000000'          HKMUD <VTCA>,174000,,,,43.,
(1) 000000'          MODULE 40020,VTCA,174000,,,,43.,
(2)                  .TITLE VTCA DEC/X11 SYSTEM EXERCISER MODULE
(2)                  ; DDXCDM VERSION 6 23-MAY-78
(2)                  .LIST BIN
(2)                  ;*****
(2) 000000'          REGIN:
(2) 000000' 052126 040503 040 MODNAM: .ASCII /VTCA / :MODULE NAME.
(2) 000005' 000 XFLAG: .BYTE UPEN ;USED TO KEEP TRACK OF WBUFF USAGE
(2) 000006' 174000 ADDR: 174000+0 ;1ST DEVICE ADDR.
(2) 000010' 000000 VECTOR: +0 ;1ST DEVICE VECTOR.
(2) 000012' 000 BR1: .BYTE PRTY+0 ;1ST BR LEVEL.
(2) 000013' 000 BR2: .BYTE PRTY+0 ;2ND BR LEVEL.
(2) 000014' 000001 DVID1: +1 ;DEVICE INDICATOR 1.
(2) 000016' 000000 SR1: OPEN ;SWITCH REGISTER 1
(2) 000020' 000000 SR2: OPEN ;SWITCH REGISTER 2
(2) 000022' 000000 SR3: OPEN ;SWITCH REGISTER 3
(2) 000024' 000000 SR4: OPEN ;SWITCH REGISTER 4
(2)                  ;*****
(2) 000026' 040020 STAT: 40020 ;STATUS WORD.
(2) 000030' 000222' INIT: START ;MODULE START ADDR.
(2) 000032' 000222' SPOINT: MODSP ;MODULE STACK POINTER.
(2) 000034' 000000 PASCNT: 0 ;PASS COUNTER.
(2) 000036' 000053 ICONT: 43. ;# OF ITERATIONS PER PASS=43.
(2) 000040' 000000 ILCOUNT: 0 ;LOC TO COUNT ITERATIONS
(2) 000042' 000000 SOFCNT: 0 ;LOC TO SAVE TOTAL SOFT ERRORS
(2) 000044' 000000 HRDCNT: 0 ;LOC TO SAVE TOTAL HARD ERRORS
(2) 000046' 000000 SOFPAS: 0 ;LOC TO SAVE SOFT ERRORS PER PASS
(2) 000050' 000000 HRDPAS: 0 ;LOC TO SAVE HARD ERRORS PER PASS
(2) 000052' 000000 SYSCNT: 0 ;# OF SYS ERRORS ACCUMULATED
(2) 000054' 000000 RANNUM: 0 ;HOLDS RANDOM # WHEN RAND MACRO IS CALLED
(2) 000056' 000000 CONFIG: ;RESERVED FOR MONITOR USE
(2) 000056' 000000 RES1: 0 ;RESERVED FOR MONITOR USE
(2) 000060' 000000 RES2: 0 ;RESERVED FOR MONITOR USE
(2) 000062' 000000 SVR0: OPEN ;LOC TO SAVE R0.
(2) 000064' 000000 SVR1: OPEN ;LOC TO SAVE R1.
(2) 000066' 000000 SVR2: OPEN ;LOC TO SAVE R2.
(2) 000070' 000000 SVR3: UPEN ;LOC TO SAVE R3.
(2) 000072' 000000 SVR4: UPEN ;LOC TO SAVE R4.
(2) 000074' 000000 SVR5: UPEN ;LOC TO SAVE R5.
(2) 000076' 000000 SVR6: UPEN ;LOC TO SAVE R6.
(2) 000100' 000000 CSMA: OPEN ;ADDR OF CURRENT CSR.
(2) 000102' SBADR: ;ADDR OF GOOD DATA, OR
(2) 000102' 000000 ACSR: UPEN ;CONTENTS OF CSR.
(2) 000104' WASADR: ;ADDR OF BAD DATA, OR
(2) 000104' 000000 ASTAT: OPEN ;STATUS REG CONTENTS.
(2) 000106' ERRRTYP: ;TYPE OF ERROR
(2) 000106' 000000 ASB: UPEN ;EXPECTED DATA.
(2) 000110' 000000 AWAS: UPEN ;ACTUAL DATA.
(2) 000112' 000222' RSTRT: RESTRT ;RESTART ADDRESS AFTER END OF PASS
(2) 000114' 000000 WDT0: UPEN ;WORDS TO MEMORY PER ITERATION
(2) 000116' 000000 WDFR: UPEN ;WORDS FROM MEMORY PER ITERATION
(2) 000120' 000000 INTR: UPEN ;# OF INTERRUPTS PER ITERATION
(2) 000122' IDNUM: ;MODULE IDENTIFICATION NUMBER=
(2) 000040 .REPT SPSIZ ;MODULE STACK STARTS HERE.
(2) .NLIST
```

```
(2)                  .WORD 0
(2)                  .LIST
(3)                  .ENDR
(2) 000222'          MODSP:
(2)                  ;*****
```

```

525 000222'
526 000222' 016700 177560
527 000226' 010067 000522
528 000232' 005720
529 000234' 010067 000516
530 000240' 005710
531 000242' 012767 000060 000530
532 000250' 012767 000037 000524
533 000256' 032767 000001 177532
534 000264' 001403
535 000266' 012767 000033 000506
536 000274' 032767 000002 177514
537 000302' 001045
538
539
540
;
; RAM CHARACTER SET NEEDS LOADING
;
541 000304' 012700 001014'
542 000310' 032767 000004 177500
543 000316' 001402
544 000320' 012700 003014'
545
546 000324' 005067 000442
547 000330' 005001
548 000332' 016702 000434
549 000336' 006302
550 000340' 006302
551 000342' 006302
552 000344' 050102
553 000346' 052702 100000
554 000352' 010277 000376
555 000356' 112003
556 000360' 000303
557 000362' 105003
558 000364' 000303
559 000366' 010377 000364
560 000372' 005201
561 000374' 020127 000007
562 000400' 003754
563 000402' 005267 000364
564 000406' 026727 000360 000177
565 000414' 003745
566
567
568
;
; NOW START PUTTING UP PICTURE
;
569 000416'
570 000416' 012767 040000 000340
571 000424' 005067 000346
572 000430' 016767 000352 000346
573 000436' 016767 000342 000326
574 000444' 005067 000324
575 000450' 005067 000312
576 000454' 005067 000310
577 000460' 012767 000001 000274
578 000466' 012767 000001 000264
579 000474' 016767 000274 000306
580 000502' 016701 000270

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581 000506' 000301
582 000510' 105001
583 000512' 006201
584 000514' 006201
585 000516' 042701 174000
586 000522' 056701 000236
587 000526' 050167 000256
588 000532' 016777 000252 000214
589 000540' 016767 000214 000244
590 000546' 016701 000210
591 000552' 006301
592 000554' 006301
593 000556' 006301
594 000560' 050167 000226
595 000564' 056767 000200 000220
596 000572' 056767 000170 000212
597 000600' 016701 000166
598 000604' 000301
599 000606' 105001
600 000610' 050167 000176
601 000614' 016777 000172 000134
602 000622' 005267 000144
603 000626' 042767 177600 000136
604 000634' 005267 000134
605 000640' 026767 000130 000132
606 000646' 003424
607 000650' 005267 000130
608 000654' 042767 177600 000122
609 000662' 005267 000110
610 000666' 026767 000104 000106
611 000674' 003660
612 000676' 005267 000104
613 000702' 042767 177600 000076
614
615
616
;
; SIGNAL END OF PASS
;
618 000710' 104413 000000'
619 000714' 000167 177504
620
;
621 000720' 006367 000034
622 000724' 026727 000030 000004
623 000732' 003660
624 000734' 005267 000022
625 000740' 026727 000016 000007
626 000746' 003647
627 000750' 000167 177474

```

```
629 .SHTTL DATA TABLES
630 ;
631 000754' 160010 DEVCSR: 160010 ; CSR ADDRESS
632 000756' 160012 DEVBUF: 160012 ; BUFFER ADDRESS
633 000760' 000000 FORGND: 0 ; FOREGROUND
634 000762' 000000 BAKGND: 0 ; BACK GROUND
635 000764' 000000 VIDEON: 0 ; VIDEO ON
636 000766' 000000 FLASH: 0 ; FLASH
637 000770' 000000 HLINK: 0 ; HLINK
638 000772' 000000 CHAR: 0 ; CHARACTER CODE TO DISPLAY
639 000774' 000000 CARX: 0 ; CURRENT X COORD
640 000776' 000000 CARY: 0 ; CURRENT Y COORD
641 001000' 000000 XMAX: 0 ; MAX X COORD
642 001002' 000000 YMAX: 0 ; MAX Y COORD
643 001004' 000000 BASE: 0 ; LINE START CODE
644 001006' 000000 BASE1: 0 ; PICTURE START CHAR
645 001010' 000000 SARDAT: 0 ; TEMP CSR DATA
646 001012' 000000 SDRDAT: 0 ; TEMP DBUF DATA
```

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648 .SHTTL STANDARD CHARACTER SET
649 .NLIST BEX
650 001014' POZCHR:
651 001014' 360 360 360 .BYTE 360,360,360,360,360,360,360,360
652 001024' 010 010 010 .BYTE 010,010,010,010,010,010,010,010
653 001034' 030 030 030 .BYTE 030,030,030,030,030,030,030,030
654 001044' 074 074 074 .BYTE 074,074,074,074,074,074,074,074
655 001054' 000 000 000 .BYTE 000,000,000,000,377,000,000,000
656 001064' 000 000 000 .BYTE 000,000,000,000,377,377,000,000
657 001074' 000 000 377 .BYTE 000,000,377,377,377,377,000,000
658 001104' 010 010 010 .BYTE 010,010,010,010,377,010,010,010
659 001114' 030 030 030 .BYTE 030,030,030,377,377,030,030,030
660 001124' 074 074 377 .BYTE 074,074,377,377,377,377,074,074 ;10
661 001134' 001 002 004 .BYTE 001,002,004,010,020,040,100,200
662 001144' 200 100 040 .BYTE 200,100,040,020,010,004,002,001
663 001154' 000 014 022 .BYTE 000,014,022,040,170,040,100,176
664 001164' 201 102 044 .BYTE 201,102,044,030,030,044,102,201
665 001174' 074 176 377 .BYTE 074,176,377,377,377,377,176,074
666 001204' 030 074 176 .BYTE 030,074,176,377,377,176,074,034
667 001214' 010 010 024 .BYTE 010,010,024,042,301,042,024,010
668 001224' 010 010 034 .BYTE 010,010,034,076,377,076,034,010
669 001234' 252 252 252 .BYTE 252,252,252,252,252,252,252,252
670 001244' 314 314 314 .BYTE 314,314,314,314,314,314,314,314 ;20
671 001254' 377 000 377 .BYTE 377,000,377,000,377,000,377,000
672 001264' 377 377 000 .BYTE 377,377,000,000,377,377,000,000
673 001274' 377 203 205 .BYTE 377,203,205,211,221,241,301,377
674 001304' 377 201 201 .BYTE 377,201,201,201,201,201,201,377
675 001314' 377 376 374 .BYTE 377,376,374,370,360,340,300,200
676 001324' 377 177 077 .BYTE 377,177,077,037,017,007,003,001
677 001334' 200 300 340 .BYTE 200,300,340,360,370,374,376,377
678 001344' 001 003 007 .BYTE 001,003,007,017,037,077,177,377
679 001354' 003 007 016 .BYTE 003,007,016,034,070,160,340,300
680 001364' 300 340 260 .BYTE 300,340,260,070,034,016,007,003 ;30
681 001374' 303 347 176 .BYTE 303,347,176,074,074,176,347,303
682 001404' 000 160 120 .BYTE 000,160,120,160,000,000,000,000
683 001414' 000 000 000 .BYTE 000,000,000,000,000,000,000,000
684 001424' 010 010 010 .BYTE 010,010,010,010,010,000,010,000
685 001434' 024 024 024 .BYTE 024,024,024,000,000,000,000,000
686 001444' 024 024 066 .BYTE 024,024,066,000,066,024,024,000
687 001454' 010 036 040 .BYTE 010,036,040,034,002,074,010,000
688 001464' 000 062 062 .BYTE 0, 62, 62, 4, 10, 20, 46, 46
689 001474' 000 020 050 .BYTE 0, 20, 50, 50, 20, 52, 44, 32
690 001504' 000 030 030 .BYTE 0, 30, 30, 30, 0, 0, 0, 0 ;40(')
691 001514' 000 010 020 .BYTE 0, 10, 20, 40, 40, 40, 20, 10
692 001524' 000 010 004 .BYTE 0, 10, 4, 2, 2, 2, 4, 10
693 001534' 000 052 034 .BYTE 0, 52, 34, 76, 34, 52, 0, 0
694 001544' 000 000 010 .BYTE 0, 0, 10, 10, 76, 10, 10, 0
695 001554' 000 000 000 .BYTE 0, 0, 0, 0, 30, 30, 10, 20
696 001564' 000 000 000 .BYTE 0, 0, 0, 0, 76, 0, 0, 0
697 001574' 000 000 000 .BYTE 0, 0, 0, 0, 0, 0, 30, 30
698 001604' 000 002 002 .BYTE 0, 2, 2, 4, 10, 20, 40, 40
699 001614' 000 034 042 .BYTE 000,034,042,046,052,062,042,034
700 001624' 000 010 030 .BYTE 000,010,030,010,010,010,010,034 ;50(')
701 001634' 000 034 042 .BYTE 000,034,042,002,034,040,040,076
702 001644' 000 034 042 .BYTE 000,034,042,002,014,002,042,034
703 001654' 000 004 014 .BYTE 000,004,014,024,044,074,004,004
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704	001664'	000	076	040	.BYTE	0, 76, 40, 74, 2, 2, 42, 34	
705	001674'	000	014	020	.BYTE	0, 14, 20, 40, 74, 42, 42, 34	
706	001704'	000	076	002	.BYTE	0, 76, 2, 4, 10, 20, 20, 20	
707	001714'	000	034	042	.BYTE	0, 34, 42, 42, 34, 42, 42, 34	
708	001724'	000	034	042	.BYTE	0, 34, 42, 42, 36, 2, 4, 30	
709	001734'	000	000	030	.BYTE	0, 0, 30, 30, 0, 30, 30, 0	
710	001744'	000	030	030	.BYTE	0, 30, 30, 0, 30, 30, 10, 20	:60(I)
711	001754'	000	004	010	.BYTE	0, 4, 10, 20, 40, 20, 10, 4	
712	001764'	000	000	000	.BYTE	0, 0, 0, 76, 0, 76, 0, 0	
713	001774'	000	020	010	.BYTE	0, 20, 10, 4, 2, 4, 10, 20	
714	002004'	000	030	044	.BYTE	0, 30, 44, 4, 10, 10, 0, 10	
715	002014'	000	074	102	.BYTE	0, 74, 102, 132, 132, 104, 40, 0	
716	002024'	000	010	024	.BYTE	0, 10, 24, 42, 42, 76, 42, 42	
717	002034'	000	074	022	.BYTE	0, 74, 22, 22, 34, 22, 22, 74	
718	002044'	000	034	042	.BYTE	0, 34, 42, 40, 40, 40, 42, 34	
719	002054'	000	074	022	.BYTE	0, 74, 22, 22, 22, 22, 22, 74	
720	002064'	000	076	040	.BYTE	0, 76, 40, 40, 70, 40, 40, 76	:70(E)
721	002074'	000	076	040	.BYTE	0, 76, 40, 40, 70, 40, 40, 40	
722	002104'	000	036	040	.BYTE	0, 36, 40, 40, 46, 42, 42, 36	
723	002114'	000	042	042	.BYTE	0, 42, 42, 42, 76, 42, 42, 42	
724	002124'	000	034	010	.BYTE	0, 34, 10, 10, 10, 10, 10, 34	
725	002134'	000	002	002	.BYTE	0, 2, 2, 2, 2, 2, 42, 34	
726	002144'	000	042	044	.BYTE	0, 42, 44, 50, 60, 50, 44, 42	
727	002154'	000	040	040	.BYTE	0, 40, 40, 40, 40, 40, 40, 76	
728	002164'	000	042	066	.BYTE	0, 42, 66, 52, 42, 42, 42, 42	
729	002174'	000	042	062	.BYTE	0, 42, 62, 52, 46, 42, 42, 42	
730	002204'	000	034	042	.BYTE	0, 34, 42, 42, 42, 42, 42, 34	:80(O)
731	002214'	000	074	042	.BYTE	0, 74, 42, 42, 74, 40, 40, 40	
732	002224'	000	034	042	.BYTE	0, 34, 42, 42, 42, 52, 44, 32	
733	002234'	000	074	042	.BYTE	0, 74, 42, 42, 74, 50, 44, 42	
734	002244'	000	036	040	.BYTE	0, 36, 40, 40, 34, 2, 2, 74	
735	002254'	000	076	010	.BYTE	0, 76, 10, 10, 10, 10, 10, 10	
736	002264'	000	042	042	.BYTE	0, 42, 42, 42, 42, 42, 42, 34	
737	002274'	000	042	042	.BYTE	0, 42, 42, 42, 24, 24, 10, 10	
738	002304'	000	042	042	.BYTE	0, 42, 42, 42, 42, 52, 66, 42	
739	002314'	000	042	042	.BYTE	0, 42, 42, 24, 10, 24, 42, 42	
740	002324'	000	042	042	.BYTE	0, 42, 42, 24, 10, 10, 10, 10	:90(Y)
741	002334'	000	076	002	.BYTE	0, 76, 2, 4, 10, 20, 40, 76	
742	002344'	000	070	040	.BYTE	000,070,040,040,040,040,040,070	
743	002354'	000	040	040	.BYTE	000,040,040,020,010,004,002,002	
744	002364'	000	016	002	.BYTE	000,016,002,002,002,002,002,016	
745	002374'	030	074	176	.BYTE	030,074,176,377,030,030,030,030	
746	002404'	010	014	016	.BYTE	010,014,016,377,377,016,014,010	
747	002414'	030	030	030	.BYTE	030,030,030,030,377,176,074,030	
748	002424'	020	060	160	.BYTE	020,060,160,377,377,160,060,020	
749	002434'	000	377	377	.BYTE	000,377,377,377,377,377,377,377	:100
750	002444'	000	000	377	.BYTE	000,000,377,377,377,377,377,377	
751	002454'	000	000	000	.BYTE	000,000,000,377,377,377,377,377	
752	002464'	000	000	000	.BYTE	000,000,000,000,377,377,377,377	
753	002474'	000	000	000	.BYTE	000,000,000,000,000,377,377,377	
754	002504'	000	000	000	.BYTE	000,000,000,000,000,000,377,377	
755	002514'	000	000	000	.BYTE	000,000,000,000,000,000,000,377	
756	002524'	376	376	376	.BYTE	376,376,376,376,376,376,376,376	
757	002534'	374	374	374	.BYTE	374,374,374,374,374,374,374,374	
758	002544'	370	370	370	.BYTE	370,370,370,370,370,370,370,370	
759	002554'	360	360	360	.BYTE	360,360,360,360,360,360,360,360	:110

760	002564'	340	340	340	.BYTE	340,340,340,340,340,340,340,340	
761	002574'	300	300	300	.BYTE	300,300,300,300,300,300,300,300	
762	002604'	200	200	200	.BYTE	200,200,200,200,200,200,200,200	
763	002614'	200	200	200	.BYTE	200,200,200,200,200,200,200,377	
764	002624'	377	001	001	.BYTE	377,001,001,001,001,001,001,001	
765	002634'	001	001	001	.BYTE	001,001,001,001,001,001,001,377	
766	002644'	200	200	200	.BYTE	200,200,200,200,200,200,200,377	
767	002654'	000	000	000	.BYTE	000,000,000,000,000,000,000,200	
768	002664'	200	000	000	.BYTE	200,000,000,000,000,000,000,000	
769	002674'	001	000	000	.BYTE	001,000,000,000,000,000,000,000	:120
770	002704'	000	000	000	.BYTE	000,000,000,000,000,000,000,001	
771	002714'	000	010	024	.BYTE	000,010,024,042,343,000,000,000	
772	002724'	010	010	030	.BYTE	010,010,030,040,100,040,030,010	
773	002734'	000	074	146	.BYTE	000,074,146,303,201,000,000,000	
774	002744'	030	060	140	.BYTE	030,060,140,100,100,140,060,030	
775	002754'	000	000	000	.BYTE	000,000,000,000,017,010,010,010	
776	002764'	010	010	010	.BYTE	010,010,010,010,370,000,000,000	
777	002774'	010	010	010	.BYTE	010,010,010,010,013,000,000,000	
778	003004'	000	000	000	.BYTE	000,000,000,000,370,010,010,010	





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892 004574' 300 300 300 .BYTE 300,300,300,300,300,300,300,300 ; 110
893 004604' 300 300 300 .BYTE 300,300,300,300,300,300,300,300
894 004614' 300 300 300 .BYTE 300,300,300,300,300,300,300,377
895 004624' 377 003 003 .BYTE 377,003,003,003,003,003,003,003
896 004634' 003 003 003 .BYTE 003,003,003,003,003,003,003,377
897 004644' 377 300 300 .BYTE 377,300,300,300,300,300,300,300
898 004654' 000 000 000 .BYTE 000,000,000,000,000,000,000,300
899 004664' 300 000 000 .BYTE 300,000,000,000,000,000,000,000
900 004674' 003 000 000 .BYTE 003,000,000,000,000,000,000,000
901 004704' 000 000 000 .BYTE 000,000,000,000,000,000,000,003
902 004714' 000 030 074 .BYTE 000,030,074,146,347,000,000,000
903 004724' 030 030 070 .BYTE 030,030,070,060,140,060,070,030
904 004734' 000 074 146 .BYTE 000,074,146,303,303,000,000,000
905 004744' 030 070 160 .BYTE 030,070,160,140,140,160,070,030
906 004754' 000 000 000 .BYTE 000,000,000,000,037,030,030,030
907 004764' 030 030 030 .BYTE 030,030,030,030,370,000,000,000
908 004774' 030 030 030 .BYTE 030,030,030,030,037,000,000,000
909 005004' 000 000 000 .BYTE 000,000,000,000,370,030,030,030
910 .EVEN
911 .LIST BEX
912 000001 .END
  
```

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ACSR 000102R BR1 000012R INIT 000030R P02CHR 001014R SVR4 000072R
ADDR 000006R HR2 000013R INTR 000120R P03CHR 001014R SVR5 000074R
ADDR22= 001000 RTUDS = 104421 MAP22S= 104416 RANDS = 104417 SVR6 000076R
ASB 000106R CARX 000774R MGDNAM 000000R RANNUM 000054R SYSCNT 000052R
ASTAT 000104R CARY 000776R MGDSP 000222R RESTRT 000222R TRPDFD= 000022
AWAS 000110R CDATAS= 104412 MSGNS = 104403 RES1 000056R T0601 000274R
BAKEND 000762R CHAR 000772R MSGSS = 104402 RES2 000060R T0602 000330R
BASE 001004R CONFIG 000056R MSGS = 104401 RSTRT 000112R T0603 000332R
BASE1 001006R CSRA 000100R NULL = 000006 H6 =*000006 T0604 000416R
BEGIN 000000R DATCKS= 104411 OPEN = 000000 H7 =*000007 T0605 000424R
BIT0 = 000001 DATERS= 104404 OTOAS = 104420 SARDAT 001010R T0606 000436R
BIT1 = 000002 DEVHUF 000756R PASCNT 000034R SBADR 000102R T0607 000450R
BIT10 = 002000 DEVCSR 000754R PIRG6 = 000004 SDRDAT 001012R T0608 000454R
BIT11 = 004000 DVID1 000014R POPSP = 005726 SOFCNT 000042R T0609 000460R
BIT12 = 010000 ENDITS = 104413 POPSP2= 022626 SUPERS= 104406 T0610 000466R
BIT13 = 020000 ENDS = 104410 PFTY = 000000 SOFPAS 000046R T0611 000474R
BIT14 = 040000 ERRRTYP 000106R PRY0 = 000000 SPOINT 000032R T0612 000720R
BIT15 = 100000 EXITS = 104400 PRTY1 = 000040 SPSIZ = 000040 VECTOH 000010R
BIT2 = 000004 FLASH 000766R PRTY2 = 000100 SK1 000016R VIDEON 000764R
BIT3 = 000010 FORGND 000760R PRTY3 = 000140 SP2 000020R WASADK 000104R
BIT4 = 000020 GETPAS= 104415 PRTY4 = 000200 SR3 000022R WDFR 000116R
BIT5 = 000040 GWBUFFS= 104414 PRTY5 = 000240 SR4 000024R WDT0 000114R
BIT6 = 000100 HRDCNT 000044R PRTY6 = 000300 START 000222R XFLAG 000005R
BIT7 = 000200 HRDEFS= 104405 PRTY7 = 000340 STAT 000026R XMAX 001000R
BIT8 = 000400 HRDPAS 000050R PS = 177776 SVR0 000062R YMAX 001002R
BIT9 = 001000 ICUNT 000036R PSH = 177776 SVR1 000064R .
BLINK 000770R ICOUNT 000040R PUSH = 005746 SVR2 000066R = 005014R
BREAKS= 104407 IDNUM 000122R PUSH2 = 024646 SVR3 000070R
  
```

. ABS. 000000 000  
 005014 001

ERRORS DETECTED: 0  
 DEFAULT GLOBALS GENERATED: 0

XVTCOA.08J,XVTCOA.LPT=DDXCUM.P11,XVTCOA.P11  
 RUN-TIME: 4 7 .3 SECONDS  
 RUN-TIME RATIO: 28/13=2.1  
 CORE USED: 8K (15 PAGES)

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IDENTIFICATION  
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PRODUCT CODE: AC-S248A-MC  
PRODUCT NAME: CXXKGAO CSS KW11C DEC/X MOD  
DATE CREATED: JUL 15,1980  
MAINTAINER: CSS LOW VOLUME PRODUCTS  
AUTHOR(S): VIJAY ANANDWALA  
UPDATE AUTHOR(S):

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ABSTRACT:

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"CXKWG" IS A DEC/X11 SOFTWARE MODULE WHICH EXERCISES A  
KW11-C CLOCK.

THE MAIN CHARACTERISTICS OF THIS MODULE ARE:

- 1.SET/READ THE TIME OF THE CLOCK.
- 2.SET UPDATE INTERRUPT
- 3.SET TIME INTERVAL INTERRUPT

REQUIREMENTS:

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PDP11 PROCESSOR,KW11-C CLOCK,TERMINAL,16K MEMORY

PASS DEFINATION:

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ONE PASS OF THE CXKWG CONSISTS OF 10. ITERATIONS OF  
BASIC TEST SEQUENCE.(SET/READ TIME,UPDATE INTERRUPT,  
TIME INTERVAL INTERRUPT).

EXECUTION TIME:

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ONE PASS OF CXKWG ALONE ON PDP11/34 TAKES APPROXIMATELY  
1 MINUTE.

CONFIGARATION REQUIREMENTS:

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DEFAULT PARAMETERS:

DEVADR:160200, VECTOR: 300, BR1: 6, BR2: 6,DEV CNT:1,ICONST:10.

PRINTOUT:

-----

PRINTOUTS HAVE THE STANDARD FORMATS DESCRIBED IN THE DEC/X11  
DOCUMENT.

NOTE:

IN HARD AND SOFT ERROR WHERE

- ERRTYP: 1 = ACTUAL READ STATUS OF CSR NOT MATCH WITH EXPECTED.  
2 = ACTUAL READ VALUE OF TIME NOT MATCH WITH EXPECTED.

```
442
443
444 000000' IOMOD <KWGA >,160200,300,6,6,0,10.,0
(1) 000000' MODULE 140000,KWGA ,160200,300,6,6,0,10.,0
(2) .TITLE KWGA DEC/X11 SYSTEM EXERCISER MODULE
(2) ; DDXCOM VERSION 6 23-MAY-78
(2) .LIST BIN
(2) ;*****
(2) 000000' BEGIN:
(2) 000000' 053513 040507 040 MODNAM: .ASCII /KWGA / ;MODULE NAME.
(2) 000005' 000 XFLAG: .BYTE OPEN ;USED TO KEEP TRACK OF WBUFF USAGE
(2) 000006' 160200 ADDR: 160200+0 ;1ST DEVICE ADDR.
(2) 000010' 000300 VECTOR: 300+0 ;1ST DEVICE VECTOR.
(2) 000012' 300 BR1: .BYTE PRTY6+0 ;1ST BR LEVEL.
(2) 000013' 300 BR2: .BYTE PRTY6+0 ;2ND BR LEVEL.
(2) 000014' 000001 DVID1: 0+1 ;DEVICE INDICATOR 1.
(2) 000016' 000000 SR1: OPEN ;SWITCH REGISTER 1
(2) 000020' 000000 SR2: OPEN ;SWITCH REGISTER 2
(2) 000022' 000000 SR3: OPEN ;SWITCH REGISTER 3
(2) 000024' 000000 SR4: OPEN ;SWITCH REGISTER 4
(2) ;*****
(2) 000026' 140000 STAT: 140000 ;STATUS WORD.
(2) 000030' 000244' INIT: START ;MODULE START ADDR.
(2) 000032' 000224' SPOINT: MODSP ;MODULE STACK POINTER.
(2) 000034' 000000 PASCNT: 0 ;PASS COUNTER.
(2) 000036' 000012 ICONT: 10. ;# OF ITERATIONS PER PASS=10.
(2) 000040' 000000 ICOUNT: 0 ;LOC TO COUNT ITERATIONS
(2) 000042' 000000 SOFCNT: 0 ;LOC TO SAVE TOTAL SOFT ERRORS
(2) 000044' 000000 HRDCNT: 0 ;LOC TO SAVE TOTAL HARD ERRORS
(2) 000046' 000000 SOFPAS: 0 ;LOC TO SAVE SOFT ERRORS PER PASS
(2) 000050' 000000 HRDPAS: 0 ;LOC TO SAVE HARD ERRORS PER PASS
(2) 000052' 000000 SYSCNT: 0 ;# OF SYS ERRORS ACCUMULATED
(2) 000054' 000000 RANNUM: 0 ;HOLDS RANDOM # WHEN RAND MACRO IS CALLED
(2) 000056' CONFIG:
(2) 000056' 000000 RES1: 0 ;RESERVED FOR MONITOR USE
(2) 000060' 000000 RES2: 0 ;RESERVED FOR MONITOR USE
(2) 000062' 000000 SVR0: OPEN ;LOC TO SAVE R0.
(2) 000064' 000000 SVR1: OPEN ;LOC TO SAVE R1.
(2) 000066' 000000 SVR2: OPEN ;LOC TO SAVE R2.
(2) 000070' 000000 SVR3: OPEN ;LOC TO SAVE R3.
(2) 000072' 000000 SVR4: OPEN ;LOC TO SAVE R4.
(2) 000074' 000000 SVR5: OPEN ;LOC TO SAVE R5.
(2) 000076' 000000 SVR6: OPEN ;LOC TO SAVE R6.
(2) 000100' 000000 CSRA: OPEN ;ADDR OF CURRENT CSR.
(2) 000102' SBADR: ;ADDR OF GOOD DATA, OR
(2) 000102' 000000 ACSR: OPEN ;CONTENTS OF CSR.
(2) 000104' WASADR: ;ADDR OF BAD DATA, OR
(2) 000104' 000000 ASTAT: OPEN ;STATUS REG CONTENTS.
(2) 000106' ERR TYP: ;TYPE OF ERROR
(2) 000106' 000000 ASB: OPEN ;EXPECTED DATA.
(2) 000110' 000000 AWAS: OPEN ;ACTUAL DATA.
(2) 000112' 000430' RSTRT: RESTRT ;RESTART ADDRESS AFTER END OF PASS
(2) 000114' 000000 WDTU: OPEN ;WORDS TO MEMORY PER ITERATION
(2) 000116' 000000 WDFR: OPEN ;WORDS FROM MEMORY PER ITERATION
(2) 000120' 000000 INTR: OPEN ;# OF INTERRUPTS PER ITERATION
(2) 000122' 000000 IDNUM: 0 ;MODULE IDENTIFICATION NUMBER=0
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(2)          000040          .HEPT   SPS1Z          ;MODULE STACK STARTS HERE.
(2)          .NLIST
(2)          .WORD   0
(2)          .LIST
(3)          .ENDR
(2) 000224'
(2)
445
446 000224' 000000          CSR:   OPEN          ;CONTROL STATUS REGISTER
447 000226' 000000          RGO:   OPEN          ;YEAR,MONTH & DAY REGISTER
448 000230' 000000          RG2:   OPEN          ;HOUR & MINUTE REGISTER
449 000232' 000000          RG4:   OPEN          ;SECOND & TIMEINTERVAL REGISTER
450 000234' 000000          CNT:   OPEN          ;COUNTER
451 000236' 000000          GOOD:  OPEN
452 000240' 000000          BAD:   OPEN
453 000242' 000000          DREG:  OPEN
454 000244' 016700 177536          START: MOV   ADDR,   R0
455 000250' 010067 177752          MOV   R0,    RGO
456 000254' 062700 000002          ADD   #2,    R0
457 000260' 010067 177744          MOV   R0,    RG2
458 000264' 062700 000002          ADD   #2,    R0
459 000270' 010067 177736          MOV   R0,    RG4
460 000274' 062700 000002          ADD   #2,    R0
461 000300' 010067 177720          MOV   R0,    CSR
462 000304' 016700 177500          MOV   VECTOR, R0
463 000310' 012720 001016'          MOV   #DATINT,(R0)+ ;ADDRESS OF THE INTERRUPT ROUTINE
464 000314' 116710 177472          MOV   BH1,   (R0)   ;PRIORITY
465 000320' 005720          TST   (R0)+
466 000322' 012720 001262'          MOV   #TIMINT,(R0)+ ;ADDRESS OF TIME INTERRUPT
467 000326' 116710 177461          MOV   BH2,   (R0)
468 000332' 005077 177666          CLR   @CSR
469 000336' 012777 105201 177662          MOV   #105201,@RGO ;SETUP PRESET
470 000344' 004767 001144          JSR   PC,    WAIT   ;WAIT FOR RDY AND 'DE'BIT TO SET
471 000350' 032777 020000 177646          BIT   #BIT13,@CSR  ;IS 'VI' BIT SET?
472 000356' 001422          BEQ   1$,    ;NO;BR
473 000360' 005077 177640          CLR   @CSR      ;CLEAR CSR
474 000364' 012777 013470 177636          MOV   #13470,@RG2 ;PRESET FOR HOUR+MIN
475 000372' 004767 001116          JSR   PC,    WAIT   ;WAIT FOR RDY AND DE TO SET
476 000376' 032777 020000 177620          BIT   #BIT13,@CSR  ;IS 'VI' BIT SET?
477 000404' 001407          BEQ   1$,    ;NO;EXIT
478 000406' 005077 177612          CLR   @CSR      ;CLEAR CSR
479 000412' 012777 010045 177612          MOV   #10045,@RG4 ;PRESET SECOND
480 000420' 004767 001070          JSR   PC,    WAIT   ;WAIT FOR 'DE'AND RDY BIT TO SET
481 000424' 005077 177574          CLR   @CSR      ;CLEAR CSR
482
483 000430' 012700 000646'          RESTRT: MOV   #TABLE, R0          ;;POINTER TO THE TABLE
484 000434' 022700 000742'          1$:   CMP   #TABEND,R0          ;;IS IT END OF TABLE?
485 000440' 003004          BGT   2$,    ;;BRANCH IF NOT
486 000442' 005067 177440          CLR   ASB          ;;CLEAR THE LOCATION
487 000446' 000167 000272          JMP   TABEND+2      ;;EXIT
488 000452' 005077 177546          2$:   CLR   @CSR
489 000456' 012002          MOV   (R0)+, R2          ;;MASK TO SET BIT IN CSR.
490 000460' 012004          MOV   (R0)+, R4          ;;ADDRESS OF CSR
491 000462' 010274 000000          MOV   R2,    @R4        ;;VALUE AT THE ADDRESS
492 000466' 012002          MOV   (R0)+, R2          ;;VALUE TO BE WRITTEN IN DEV REG
493 000470' 012004          MOV   (R0)+, R4          ;;ADDRESS OF DEV REG

```

494	000472'	010274	000000	MOV	R2,	@(R4)	;;VALUE IN THE DEV REG
495	000476'	004767	001012	JSR	PC,	WAIT	;;WAIT FOR READY BIT TO SET
496	000502'	005077	177516	CLR	@CSR		;;CLEAR CSR
497	000506'	022027	177777	CMP	(R0)+,	#-1	;;IS IT END OF SET?
498	000512'	001350		BNE	16		;;GET MORE VALUES FROM TABLE
499							
500	000514'	012002		36:	MOV	(R0)+, R2	;;MASK TO READ DEV REGISTER
501	000516'	012004			MOV	(R0)+, R4	;;ADDRESS OF CSR
502	000520'	010274	000000		MOV	R2,	@(R4)
503	000524'	012067	177506		MOV	(R0)+,	GOOD
504	000530'	012004			MOV	(R0)+,	R4
505	000532'	017467	000000 177500		MOV	@(R4),	BAD
506	000540'	026767	177472 177472		CMP	GOOD,	BAD
507	000546'	001433			BEQ	46	;;EXPECTED=ACTUAL?
508	000550'	017467	000000 177462		MOV	@(R4),	BAD
509	000556'	026767	177454 177454		CMP	GOOD,	BAD
510	000564'	001424			BEQ	46	;;NOW THEY ARE EQUAL?
511	000566'	017767	177432 177306		MOV	@CSR,	ACSR
512	000574'	016767	177424 177276		MOV	CSR,	CSRA
513	000602'	012767	000236' 177272		MOV	#GOOD,	SBADR
514	000610'	012767	000240' 177266		MOV	#BAD,	WASADR
515	000616'	016767	177414 177262		MUV	GOOD,	ASB
516	000624'	016767	177410 177256		MOV	BAD,	AWAS
517							;;ACTUAL DATA WAS
(1)	000632'	104404	000000'				*****
(1)							DATER\$,BEGIN ;DATA ERROR!!!
518	000636'	022027	177777	46:	CMP	(R0)+,	#-1
519	000642'	001324			BNE	36	;;IS IT END OF SUBSET?
520	000644'	000673			BR	16	;;READ MORE VALUE
521							;;GET MORE VALUES
522	000646'	040000		TABLE:	BIT14		;;MASK TO SET YEAR BIT IN CSR
523	000650'	000224'			CSR		;;ADDRESS OF CSR
524	000652'	017073			17073		;;YEAR 1979
525	000654'	000226'			RGO		;;ADDRESS OF YEAR REGISTER
526	000656'	177777			177777		;;END OF SUBSET
527							
528	000660'	000004			BIT2		;;SET BIT TO READ YEAR
529	000662'	000224'			CSR		;;ADDRESS OF CSR
530	000664'	017073			17073		;;EXPECTED VALUE OF YEAR
531	000666'	000226'			RGO		
532	000670'	177777			177777		;;END OF SUBSET
533							
534	000672'	100000			BIT15		;;SET TIME
535	000674'	000224'			CSR		
536	000676'	002004			2004		;;FEBRUARY 4
537	000700'	000226'			RGO		
538	000702'	177777			177777		
539	000704'	000000			00000		;;
540	000706'	000224'			CSR		
541	000710'	002004			2004		
542	000712'	000226'			RGO		
543	000714'	177777			177777		
544							
545	000716'	100000			BIT15		
546	000720'	000224'			CSR		
547	000722'	000000			00000		

```
548 000724' 000230'          RG2
549 000726' 177777          177777
550 000730' 000000          000000
551 000732' 000224'          CSR
552 000734' 000000          000000
553 000736' 000230'          RG2
554 000740' 177777          177777
555 000742' 000000          TABEND: 000000
556
557                          ;THIS ROUTINE SETS UPDATE INTERRUPT
558                          ;BIT15 IN CSR IS SET TO WRITE THE TIME
559                          ;BIT6 IS SET IN CSR TO ENABLE THE INTERRUPT
560                          ;
561 000744' 012700 001206'    SETINT: MOV     #TABLE2,R0          ;;POINTER TO THE TABLE
562 000750' 022700 001216'    ONE:   CMP     #TABEN2,R0          ;;IS IT END OF TABLE
563 000754' 003002          BGT     TWO          ;;BRANCH IF NOT END
564 000756' 000167 000236          JMP     TABEN2+2          ;;EXIT,OTHERWISE
565 000762' 005077 177236          TWO:  CLR     @CSR          ;;CLEAR CSR
566 000766' 012777 100100 177230  MOV     #BIT15+BIT6,@CSR          ;;SET TIME WITH INTRPT ENABL
567 000774' 012067 177236          MOV     (R0)+, GOOD          ;;WRITE VALUE
568 001000' 013067 177236          MOV     @ (R0)+, DREG          ;;ADDRESS OF DEV REG
569 001004' 016777 177226 177230  MOV     GOOD, @DREG          ;;
570 001012' 104400 000000'    EXITS$,BEGIN          ;;EXIT TO MONITOR. MODULE WAIT FOR INTERRUPT.
571
572 001016' 017767 177202 177056  DATINT: MOV     @CSR, ACSR          ;;CURRENT CONTENT OF CSR
573 001024' 005077 177174          CLR     @CSR          ;;CLEAR CSR
574
(1) 001030' 000004 000000' 001036'  -----
(1)                                PIRQ$,BEGIN,1$          ; QUEUE UP TO CONTINUE AT 1$ AND RTI
(1)                                -----
575 001036' 017767 177200 177174 1$:  MOV     @DREG, BAD          ;;READ THE CURRENT VALUE
576 001044' 042767 040000 177030          BIC     #BIT14, ACSR          ;;CLEAR 'TI' IF SET
577 001052' 022767 100300 177022          CMP     #100300, ACSR          ;;EXPECTED STATUS OF CSR
578 001060' 001416          BEQ     2$          ;;BRANCH IF EQUAL
579 001062' 016767 177136 177010          MOV     CSR, CSRA          ;;ADDRESS OF CSR
580 001070' 012767 100300 177006          MOV     #100300,ASTAT          ;;EXPECTED STATUS
581 001076' 012767 000001 177002          MOV     #1, ERR TYP          ;;TYPE OF ERROR
582
(1) 001104' 104405 000000' 000000  ;*****
(1)                                HDRS$,BEGIN,NULL          ;EXPECTED STATUS OF CSR DIDNOT MATCH WITH ACTUAL STATUS
(1)                                ;*****
583 001112' 000167 177632          JMP     ONE
584 001116' 026767 177114 177114 2$:  CMP     GOOD, BAD          ;;EXPECTED=ACTUAL?
585 001124' 001711          BEQ     ONE
586 001126' 017767 177110 177104          MOV     @DREG, BAD          ;;TRY TO READ AGAIN
587 001134' 026767 177076 177076          CMP     GOOD, BAD          ;;ARE THEY EQUAL?
588 001142' 001702          BEQ     ONE
589 001144' 016767 177072 176726          MOV     DREG, CSRA          ;;ADDRESS OF DEV REG
590 001152' 017767 177064 176722          MOV     @DREG, ACSR          ;;CONTENT OF REG
591 001160' 016767 177052 176716          MOV     GOOD, ASTAT          ;;EXPECTED STATUS
592 001166' 012767 000002 176712          MOV     #2, ERR TYP          ;;TYPE OF ERROR
593
(1) 001174' 104406 000000' 000000  ;*****
(1)                                SIFER$,BEGIN,NULL          ;READ VALUE NOT MATCH WITH THE SET VALUE OF TIME
(1)                                ;*****
594 001202' 000167 177542          JMP     ONE
595 001206' 006005          TABLE2: 6005          ;;DECEMBER 5
596 001210' 000226'          RGO          ;;ADDRESS OF REG
597 001212' 006000          6000          ;;12HH,00MIN
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598 001214' 000230'          RG2          ;ADDRESS OF DEV REG
599 001216' 000000          TABEN2: 00000 ;END OF TABLE
600
601
602
603
604
605          ;
606          ;THIS ROUTINE SETS TIME INTERVAL INTERRUPT
607          ;
608 001220' 012700 001470'    DELTA:  MOV    #TIMTAB,R0          ;POINTER TO THE TABLE
609 001224' 022700 001502'    D1:    CMP    #TIMEND,R0          ;IS IT END OF TABLE?
610 001230' 001515          BEQ    D2          ;BRANCH IF END OF TABLE
611 001232' 012067 177000    MOV    (R0)+, GOOD          ;GET VALUE FROM THE TABLE
612 001236' 005077 176762    CLR    @CSR          ;CLEAR CSR
613 001242' 012777 000050 176754  MOV    #BIT5+BIT3,@CSR          ;SET TIME INT AND INTRPT ENBL
614 001250' 016777 176762 176754  MOV    GOOD, @RG4          ;INTO SECOND REG
615 001256' 104400 000000'    EXIT$,BEGIN          ;EXIT TO MONITOR, MODULE WAIT FOR INTERRUPT.
616
617 001262' 017767 176736 176612  TIMINT: MOV    @CSR, ACSR          ;STATUS OF CSR
618 001270' 005077 176730    CLR    @CSR
619 001274' 012777 000010 176722  MOV    #BIT3, @CSR          ;READ DELTAT
620 001302' 017767 176724 176730  MOV    @RG4, BAD          ;READ TIME INTERVAL VALUE
621
-----
(1) 001310' 000004 000000' 001316'  PIRQ$,BEGIN,$          ; QUEUE UP TO CONTINUE AT $ AND RTI
(1)
-----
622
623 001316' 042767 137727 176556 1$:  BIC    #137727,ACSR          ;CLEAR UNWANTED BITS
624 001324' 022767 040050 176550    CMP    #40050, ACSR          ;EXPCT STATUS OF CSR
625 001332' 001415          BEQ    2$          ;BRANCH IF EQUAL
626 001334' 016767 176664 176536    MOV    CSR, CSRA          ;
627 001342' 012767 040050 176534    MOV    #40050, ASTAT          ;EXPECTED STAT'S OF CSR
628 001350' 012767 000001 176530    MOV    #1, ERRTYP          ;TYPE OF ERROR
629
*****
(1) 001356' 104405 000000' 000000  HRDR$,BEGIN,NULL          ;STATUS DIDN'T MATCH
(1)
*****
630 001364' 000717          BR    D1          ;GET NEXT VALUE
631 001366' 042767 040000 176642 2$:  BIC    #40000, GOOD          ;CLEAR TIME INTERVAL ENABLE BIT
632 001374' 016702 176636    MOV    GOOD, R2          ;GET EXPECTED VALUE
633 001400' 016704 176634    MOV    BAD, R4          ; DELAT VALUE AT TIME OF INTRPT
634 001404' 160204          SUB    R2, R4          ;GET THE DIFFERENCE
635 001406' 002001          BGE    3$          ;BRANCH IF DIFFERENCE POSITIVE OR ZERO
636 001410' 005404          NEG    R4          ;MAKE POSITIVE
637 001412' 020427 000001 3$:  CMP    R4, #1          ;IS DIFF. MORE THAN 1?
638 001416' 003702          BLE    D1          ;GET NEXT VALUE
639 001420' 017767 176600 176454    MOV    @CSR, ACSR          ;CURRENT STATUS OF CSR
640 001426' 012767 000236' 176446    MOV    #GOOD, SBADR          ;ADDRESS OF EXP DATA
641 001434' 012767 000240' 176442    MOV    #BAD, WASADR          ;ADDRESS OF ACTUAL DATA
642 001442' 016767 176570 176436    MOV    GOOD, ASB          ;ACTUAL DATA SHOLD BE
643 001450' 016767 176564 176432    MOV    BAD, AWAS          ;ACTUAL DATA WAS
644
*****
(1) 001456' 104404 000000'    DATERS$,BEGIN          ;DATA ERROR!!
(1)
*****
645 001462' 000660          BR    D1          ;GET NEXT VALUE
646
647 001464' 000167 000014  D2:  JMP    TIMEND+2

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648 001470' 047777          TIMTAB: 47777          ;;
649 001472' 040304          40304
650 001474' 040506          40506
651 001476' 042043          42043
652 001500' 042333          42333
653 001502' 000000          TIMEND: 00000
654
655 001504' 104413 000000'          ENDITS,BEGIN          ;SIGNAL END OF ITERATION.
(1)                                     ;MONITOR SHALL TEST END OF PASS
656 001510' 000167 176714          JMP      RESTRT
657
658                                     ;THIS ROUTINE WAIT FOR READY BIT TO SET
659                                     ;IF READY BIT DIDN'T SET IN GIVEN TIME
660                                     ;THE MODULE WILL BE DROPPED
661                                     ;
662 001514' 012767 077777 176512  WAIT:  MOV      #77777,CNT          ;;SET UP TIMER
663
664 001522'          18:
(1) 001522' 104407 000000'          BREAK$,BEGIN          ;TEMPORARY RETURN TO MONITOR...
(1) 001526' 104407 000000'          BREAK$,BEGIN          ;THEN CONTINUE AT NEXT INSTRUCTION.
665 001532' 032777 100000 176464          BIT      #BIT15, @CSR          ;;IS RDY BIT SET?
666 001540' 001024          BNE      J$          ;;BRANCH IF YES
667 001542' 005367 176466          26:  DEC      CNT          ;;DECREMENT COUNT
668 001546' 001365          BNE      1$          ;;WAIT SOME MORE TIME AND TRY AGAIN
669 001550' 016767 176450 176322          MOV      CSR,    CSRA          ;;CSR ADDRESS
670 001556' 017767 176442 176316          MOV      @CSR,   ACSR          ;;CONTENT OF CSR
671 001564' 012767 100200 176312          MOV      #100200,ASTAT          ;;EXPECTED STATUS OF CSR
672 001572' 012767 000001 176306          MOV      #1,    ERRTYP          ;;
673                                     ;*****
(1) 001600' 104405 000000' 000000          HDR$,BEGIN,NULL          ;'DE' AND/OR 'RDY' BIT FAIL TO SET IN CSR
(1)                                     ;*****
674 001606' 104410 000000'          ENDS$,BEGIN          ;MODULE DROPPED;READY BIT FAIL TO SET IN TIME
675 001612' 032777 000200 176404  36:  BIT      #BIT7,  @CSR          ;;IS RDYBIT SET?
676 001620' 001001          BNE      4$          ;;YES:RETURN
677 001622' 000773          BR      36          ;;DECREMENT COUNTER
678 001624' 000207          48:  RTS      PC          ;;EXIT
679
680          000001          .END

```





SVR2	000066R	444#				
SVR3	000070R	444#				
SVR4	000072R	444#				
SVR5	000074R	444#				
SVR6	000076R	444#				
SYSCNT	000052R	444#				
TABEND	000742R	484	487	555#		
TABEN2	001216R	562	564	599#		
TABLE	000646R	483	522#			
TABLE2	001206R	561	595#			
TIMEND	001502R	609	647	653#		
TIMINT	001262R	466	617#			
TIMTAB	001470R	608	648#			
TRPDFD=	000022	444#				
TWO	000762R	563	565#			
VECTOR	000010R	444#	462			
WAIT	001514R	470	475	480	495	662#
WASADR	000104R	444#	514*	641*		
WDFR	000116R	444#				
WDTO	000114R	444#				
XFLAG	000005R	444#				

BKMOD	95#			
BREAK	185#	664		
BTOD	204#			
CKDATA	240#			
DATAK	249#			
DATERR	138#	517	644	
DFSEVN	272#	444		
DSEVNT	282#	444		
END	175#	674		
ENDIT	166#	655		
ENDMOD	171#			
EQUATS	288#	444		
EXIT	120#	570	615	
GETPA	231#			
GWBUFF	219#			
HRDER	128#	582	629	673
IOMOD	91#	444		
IOMODP	115#			
IOMODR	111#			
IOMODX	107#			
MAP22	235#			
MODULE	8#	444		
MSG	154#			
MSGN	158#			
MSGS	162#			
NBKMOD	103#			
OTOA	190#			
PIRO	179#	574	621	
RAND	124#			
SBKMOD	99#			
SOFER	144#	593		

. ABS. 000000 000  
001626 001

ERRORS DETECTED: 0  
DEFAULT GLOBALS GENERATED: 0

XKwGAO,XKWGAO.LST/CRF=DDXCOM.C6,CXKWGA.SRC  
RUN-TIME: 3 4 .7 SECONDS  
RUN-TIME RATIO: 44/9=4.8  
CORE USED: 7K (13 PAGES)

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## IDENTIFICATION

PRODUCT CODE: AC-F658A-MC  
 PRODUCT NAME: CXVTVA0 VTV30J/H-VT30H MODULE  
 PRODUCT DATE: OCT 1, 1979  
 MAINTAINER: COMPUTER SPECIAL SYSTEMS  
 DIGITAL EQUIPMENT CO. LTD.  
 READING  
 BERKS, U.K.

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## VTV30-H/J OR VT30-H DEC/X11 MODULE

## PROGRAM DESCRIPTION

1. ABSTRACT  
 VTV IS AN IOMOD WHICH EXERCISES A VTV30-H/J OR VT30-H COLOUR VIDEO DISPLAY CONTROLLER. THE EXERCISE CONSISTS OF CONTINUALLY UPDATING THE CONTENTS OF THE PICTURE STORE SO AS TO PRODUCE A MOVING PATTERN ON THE SCREEN OF THE T.V. MONITOR. NO SOFTWARE CHECKING IS CARRIED OUT, BUT ERRORS IN THE PICTURE STORE OR CHARACTER STORE AREAS WILL BE CLEARLY VISIBLE ON THE DISPLAY.
2. REQUIREMENTS
  - 2.1 HARDWARE  
 VTV30-H/J OR VT30-H CONTROLLER WITH T.V. MONITOR
  - 2.2 STORAGE  
 VTV REQUIRES 500 WORDS OF STORAGE.
3. PASS DEFINITION  
 ONE PASS OF THE VTV MODULE CONSISTS OF SIX COMPLETE SCREEN COLOUR CHANGES.
4. EXECUTION TIME  
 ONE PASS OF THE VTV MODULE TAKES APPROXIMATELY ONE MINUTE WHEN RUNNING ALONE.
5. CONFIGURATION REQUIREMENTS
  - 5.1 DEFAULT PARAMETERS  
 DVADR: 164000  
 VECTOR: 170  
 BR1: 4

1  
2  
3 5.2 REQUIRED PARAMETERS  
4  
5 NONE.  
6  
7 6. DEVICE/OPTION SETUP  
8  
9 ENSURE THAT THE CONTROLLER MODULES ARE CORRECTLY  
10 INSTALLED AND CONNECTED TO THE COLOUR T.V. MONITOR. THE  
11 MONITOR SHOULD BE POWERED UP.  
12  
13  
14 7. MODULE OPERATION  
15  
16 TEST SEQUENCE  
17  
18 A. SET UP DEVICE REGISTER ADDRESSES AND MODULE  
19 VARIABLES.  
20  
21 B. SET DISPLAY CHARACTER MATRIX (6 X 6 OR 8 X 8).  
22  
23 C. LOAD ENTIRE CHARACTER SET WITH DUMMY CHARACTERS (252  
24 PATTERN).  
25  
26 D. LOAD SPECIAL PATTERNS FOR USE IN DISPLAY INTO  
27 CHARACTERS 0 - 26 (6 X 6) OR CHARACTERS 0 - 36 (8 X  
28 8).  
29  
30 E. SET INITIAL COLOUR AND CHARACTER COMBINATION.  
31  
32 F. CHECK IF ALL COLOURS DONE. IF SO, REPORT END OF  
33 PASS AND GO TO E.  
34  
35 G. DRAW PATTERN ON SCREEN UNTIL FULL OF FOREGROUND  
36 COLOUR.  
37  
38 H. INCREMENT COLOUR COUNTER, GO TO F.  
39  
40  
41  
42 VISUAL OPERATION  
43  
44 THE PICTURE STORE IS PRESET TO BLUE BACKGROUND. A RED  
45 TRIANGLE IS THEN DRAWN, STARTING AT THE BOTTOM CENTRE OF  
46 THE SCREEN AND EXPANDING UPWARDS UNTIL THE SCREEN IS  
47 FULL OF RED FOREGROUND. THE SEQUENCE IS THEN REPEATED  
48 USING GREEN FOREGROUND ON RED BACKGROUND, BLUE ON GREEN,  
49 GREEN ON BLUE, RED ON GREEN AND BLUE ON RED. AT END OF  
50 PASS, THE DISPLAY IS DISABLED THEN RESTART TAKES PLACE  
51 AT THE BEGINNING OF THE COLOUR SEQUENCE.

1  
2 AT ALL TIMES, THE EDGES OF THE TRIANGLE SHOULD APPEAR  
3 SMOOTH AND MOVING STEADILY. THE APPEARANCE OF ANY  
4 VERTICALLY STRIPED CHARACTERS ON THE SCREEN INDICATES  
5 THAT AN ILLEGAL CHARACTER IS BEING DISPLAYED AT THAT  
6 POSITION. ANY OTHER PICTURE STORE OR CHARACTER STORE  
7 MALFUNCTIONS SHOULD BE JUST AS OBVIOUS.  
8  
9  
10 8. OPERATION OPTIONS  
11  
12 SR1 = 0, 6 X 6 CHARACTER MATRIX.  
13  
14 SR1 = 1, 8 X 8 CHARACTER MATRIX.  
15  
16  
17 9. NON STANDARD PRINTOUT  
18  
19 NONE.  
20



```

1          .ENDR
2 000000    IOMOD <VTVA >,164000,170,4,0,0,1,0
000000( ) 1212 -OTUNE 140000,VTVA ,164000,170,4,0,0,1,0
          ; TITLE VTVA DEC/X11 SYSTEM EXERCISER MODULE
          ; DDKCOM VERSION 6 28-NOV-78
          ; CSSUK
          .LIST BIN
;*****
000000 BEGIN;
000000 126 MODNAM: ,ASCII /VTVA / ;MODULE NAME.
000001 124
000002 126
000003 101
000004 040
000005 000 XFLAG: ,BYTE OPEN ;USED TO KEEP TRACK OF WRUFF USAGE
000006 164000 ADDR: 164000+0 ;1ST DEVICE ADDR.
000010 000170 VECTOR: 170+0 ;1ST DEVICE VECTOP.
000012 200 BR1: ,BYTE PRTY4+0 ;1ST BR LEVEL.
000013 000 BR2: ,BYTE PRTY0+0 ;2ND BR LEVEL.
000014 000001 DVID1: 0+1 ;DEVICE INDICATOR 1.
000016 000000 SR1: OPEN ;SWITCH REGISTER 1
000020 000000 SR2: OPEN ;SWITCH REGISTER 2
000022 000000 SR3: OPEN ;SWITCH REGISTER 3
000024 000000 SR4: OPEN ;SWITCH REGISTER 4
;*****
000026 140000 STAT: 140000 ;STATUS WORD.
000030 000620 INIT: START ;MODULE START ADDR.
000032 000224 SPOINT: MODSP ;MODULE STACK POINTER.
000034 000000 PASCNT: 0 ;PASS COUNTER.
000036 000001 ICNT: 1 ;# OF ITERATIONS PER PASS=1
000040 000000 ICOUNT: 0 ;LOC TO COUNT ITERATIONS
000042 000000 SOFCNT: 0 ;LOC TO SAVE TOTAL SOFT ERRORS
000044 000000 HRDCNT: 0 ;LOC TO SAVE TOTAL HARD ERRORS
000046 000000 SOFPAS: 0 ;LOC TO SAVE SOFT ERRORS PER PASS
000050 000000 HRDPAS: 0 ;LOC TO SAVE HARD ERRORS PER PASS
000052 000000 SYSCNT: 0 ;# OF SYS ERRORS ACCUMULATED
000054 000000 RANNUM: 0 ;HOLDS RANDOM# WHEN RAND MACRO IS CALLED
000056 000000 CONFIG: ;RESERVED FOR MONITOR USE
000058 000000 RES1: 0 ;RESERVED FOR MONITOR USE
000060 000000 RES2: 0 ;RESERVED FOR MONITOR USE
000062 000000 SVR0: OPEN ;LOC TO SAVE R0.
000064 000000 SVR1: OPEN ;LOC TO SAVE R1.
000066 000000 SVR2: OPEN ;LOC TO SAVE R2.
000070 000000 SVR3: OPEN ;LOC TO SAVE R3.
000072 000000 SVR4: OPEN ;LOC TO SAVE R4.
000074 000000 SVR5: OPEN ;LOC TO SAVE R5.
000076 000000 SVR6: OPEN ;LOC TO SAVE R6.
000100 000000 CSRA: OPEN ;ADDR OF CURRENT CSR.
000102 000000 SBADR: OPEN ;ADDR OF GOOD DATA, OR
000104 000000 ACSR: OPEN ;CONTENTS OF CSR.
000106 000000 WASADR: OPEN ;ADDR OF BAD DATA, OR
000108 000000 ASTAT: OPEN ;STATUS REG CONTENTS.
000110 000000 ERRTP: OPEN ;TYPE OF ERROR
000112 000000 ASB: OPEN ;EXPECTED DATA.
000114 000000 AWAS: OPEN ;ACTUAL DATA.
000116 001216 RSTRT: RESTRT ;RSTART ADDRESS AFTER END OF PASS
000118 000000 WDT0: OPEN ;WORDS TO MEMORY PER ITERATION

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000116 000000 WDFR: OPEN ;WORDS FROM MEMORY PER ITERATION
000120 000000 INTR: OPEN ;# OF INTERRUPTS PER ITERATION
000122 000000 IDNUM: 0 ;MODULE IDENTIFICATION NUMBER=0
000040 ,REPT SPSIZ ;MODULE STACK STARTS HERE.
          ,NLIST
          ,WORD 0
          ,LIST
          .ENDR
000224 MODSP:
;*****

```

```

1      |
2      |;CONSTANTS AND VARIABLES USED IN PROGRAM.
3      |
4      |      ,NLIST BEX
5 000224 000000 ACNT: OPEN      ;VARIABLES LOADED AT RUN-TIME.
6 000226 000000 BCNT: OPEN
7 000230 000000 CHARA: OPEN
8 000232 000000 CHARB: OPEN
9 000234 000000 ACHAR: OPEN
10 00236 000000 BCHAR: OPEN
11 00240 000000 CCHAR: OPEN
12 00242 000000 DCHAR: OPEN
13 00244 000000 MAXY: OPEN
14 00246 000000 MAXX: OPEN
15 00250 000000 LX: OPEN
16 00252 000000 RX: OPEN
17 00254 000000 CHROW: OPEN
18 00256 000000 CHCNT: OPEN
19 00260 000014 M6X6: 14      ;CONSTANTS FOR 6X6 CHARACTER MATRIX.
20 00262 000013          13
21 00264 000350* CHARA6
22 00266 000371* CHARB6
23 00270 000412* ACHAR6
24 00272 000420* BCHAR6
25 00274 000426* CCHAR6
26 00276 000434* DCHAR6
27 00300 000057          57
28 00302 000120          120
29 00304 000047          47
30 00306 000050          50
31 00310 000006          6
32 00312 000005          5
33 00314 000020 M8X8: 20      ;CONSTANTS FOR 8X8 CHARACTER MATRIX.
34 00316 000017          17
35 00320 000442* CHARA8
36 00322 000471* CHARB8
37 00324 000520* ACHAR8
38 00326 000530* BCHAR8
39 00330 000540* CCHAR8
40 00332 000550* DCHAR8
41 00334 000045          45
42 00336 000100          100
43 00340 000037          37
44 00342 000040          40
45 00344 000010          8.
46 00346 000007          7
47 00350 000 CHARA6: .BYTE 0,0,0,0,0,0 ;6X6 CHARACTER DATA
48 00356 004 .BYTE 4,14,34,74,174,374
49 00364 374 .BYTE 374,374,374,374,374,374
50 00371 000 CHARB6: .BYTE 0,0,0,0,0
51 00376 200 .BYTE 200,300,340,360,370,374
52 00404 374 .BYTE 374,374,374,374,374,374
53      .EVEN
54 00412 001 ACHAR6: .BYTE 1,2,3,4,5,6 ;6X6 CHARACTER TABLE.
55 00420 007 BCHAR6: .BYTE 7,10,11,12,13,13
56 00426 014 CCHAR6: .BYTE 14,15,16,17,20,21
57 00434 022 DCHAR6: .BYTE 22,23,24,25,26,26
    
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58 00442 000 CHARA8: .RYTE 0,0,0,0,0,0,0,0 ;8X8 CHARACTER DATA
59 00452 001 .BYTE 1,3,7,17,37,77,177,377
60 00462 377 .BYTE 377,377,377,377,377,377,377,377
61 00471 000 CHARB8: .RYTE 0,0,0,0,0,0,0,0
62 00500 200 .RYTE 200,300,340,360,370,374,376,377
63 00510 377 .BYTE 377,377,377,377,377,377,377,377
64      .EVEN
65 00520 001 ACHAR8: .BYTE 1,2,3,4,5,6,7,10 ;8X8 CHARACTER TABLE
66 00530 011 BCHAR8: .BYTE 11,12,13,14,15,16,17,17
67 00540 020 CCHAR8: .BYTE 20,21,22,23,24,25,26,27
68 00550 030 DCHAR8: .BYTE 30,31,32,33,34,35,36,36
69 00560 241 COL: .BYTE 241,212,224,242,221,214
70      .EVEN
71      .LIST REX
72 00566 001702*MSG1: MSG66
73 00570 177777          177777
74 00572 001725*MSG2: MSG88
75 00574 177777          177777
76 00576 000000 ITCNT: 0
77 00600 000000 CSR1: OPEN      ;CONTROL & STATUS REGISTER ADDRESS
78 00602 000000 DBUF: OPEN      ;DATA BUFFER REGISTER ADDRESS
79 00604 000000 CAR: OPEN       ;CURSOR REGISTER ADDRESS
80 00606 000000 CHSR: OPEN      ;CHARACTER STORE REGISTER ADDRESS
81 00610 000000 START1: OPEN    ;STARTING COORDINATE FOR LEFT DIAGONAL
82 00612 000000 START2: OPEN    ; DITTO FOR RIGHT DIAGONAL
83 00614 000000 XY1: OPEN       ;RUNNING COORDINATE FOR LEFT DIAGONAL
84 00616 000000 XY2: OPEN       ; DITTO FOR RIGHT DIAGONAL
    
```

```

1
2      ; INITIALISE AND LOAD CHARACTER SET.
3
4 000620 016700 START: MOV ADDR,R0 ;GET BASE ADDRESS
      177162
5 000624 010067 MOV R0,CSR ;LOAD UP TABLE; CONTROL & STATUS
      177750
6 000630 005720 TST (R0)+
7 000632 010067 MOV R0,DBUF ;DATA BUFFER
      177744
8 000636 005720 TST (R0)+
9 000640 010067 MOV R0,CAR ;CURSOR REGISTER
      177740
10 00644 005720 TST (R0)+
11 00646 010067 MOV R0,CHSR ;CHARACTER STORE REGISTER
      177734
12 00652 016700 MOV VECTOR,R0 ;NOW LOAD INTERRUPT VECTOR.
      177132
13 00656 012720 MOV #RDYINT,(R0)+ ;SERVICE ROUTINE
      001164*
14 00662 005010 CLR (R0)
15 00664 116710 MOVR BR1,(R0) ;AND PRIORITY.
      177122
16 00670 032767 BIT #1,SR1 ;SEE WHICH MMATRIX REQUIRED.
      000001
      177120
17 00676 001417 BEQ 28 ;BRANCH IF 6 X 6.
18 00700 005077 CLR #CSR ;SET UP FOR 8 X 8.
      177674
19 00704 012700 MOV #ACNT,R0 ;GET START OF VARIABLE TABLE
      000224*
20 00710 012701 MOV #M0X0,R1 ;GET START OF CONSTANT TABLE
      000314*
21 00714 012702 MOV #14,,R2 ;COUNT OF TABLE ENTRIES
      000016
22 00720 012120 18: MOV (R1)+,(R0)+ ;LOAD AN ENTRY
23 00722 005302 DEC R2 ;COUNT.
24 00724 001375 BNE 18 ;LOOP UNTIL TABLE FINISHED.
25 00726 104403 MSGN#,BEGIN,MSG2 ;ASCII MESSAGE CALL WITH COMMON HEADER
      00730 000000*
      00732 000572*
26 00734 000417 BR 48 ;THEN EXIT.
27 00736 012777 28: MOV #1400,#CSR ;SET UP FOR 6 X 6
      001400
      177634
28 00744 012700 MOV #ACNT,R0 ;GET START OF VARIABLE TABLE.
      000224*
29 00750 012701 MOV #M6X6,R1 ;GET START OF CONSTANT TABLE.
      000260*
30 00754 012702 MOV #14,,R2 ;COUNT OF TABLE ENTRIES.
      000016
31 00760 012120 38: MOV (R1)+,(R0)+ ;LOAD AN ENTRY.
32 00762 005302 DEC R2 ;COUNT.
33 00764 001375 BNE 38 ;LOOP UNTIL TABLE FINISHED.
34 00766 104403 MSGN#,BEGIN,MSG1 ;ASCII MESSAGE CALL WITH COMMON HEADER
      00770 000000*
      00772 000566*

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```

35 00774 005077 48: CLR RCAR ;ZERO CURSOR
      177604
36 01000 016700 MOV CHSR,R0 ;GET HIGH BYTE ADDRESS
      177602
37 01004 105060 CLRB 1(R0) ;CLR HIGH BYTE OF CHSR.
      000001
38
39 ; LOAD ALL DUMMY CHARACTERS FIRST.
40
41 01010 012700 LDCH: MOV #120,,R0 ;NUMBER OF CHARACTERS
      000200
42 01014 016701 18: MOV CHROW,R1 ;BYTES PER CHARACTER
      177234
43 01020 112777 28: MOV# #252,#CHSR ;LOAD A BYTE.
      000252
      177560
44 01026 005301 DEC R1 ;CHARACTER DONE?
45 01030 001373 BNE 28 ;BRANCH IF NOT
46 01032 005300 DEC R0 ;ALL CHARACTERS DONE?
47 01034 001367 BNE 18 ;BRANCH IF NOT.
48
49 ; DUMMY CHARACTERS LOADED, NOW LOAD SPECIALS.
50
51 01036 016700 MOV CHSR,R0 ;GET HIGH BYTE ADDRESS
      177544
52 01042 105060 CLRB 1(R0) ;ZERO CHAR ADDRESS.
      000001
53 01046 016700 MOV CHARA,R0 ;GET DATA POINTER.
      177156
54 01052 016702 MOV ACNT,R2 ;NUMBER OF CHARACTERS.
      177146
55 01056 010001 38: MOV R0,R1 ;COPY POINTER.
56 01060 016703 MOV CHROW,R3 ;BYTES PER CHARACTER.
      177170
57 01064 112177 48: MOV# (R1)+,#CHSR ;LOAD A BYTE.
      177516
58 01070 005303 DEC R3 ;CHARACTER DONE?
59 01072 001374 BNE 48 ;BRANCH IF NOT.
60 01074 005200 INC R0 ;UPDATE POINTER
61 01076 005302 DEC R2 ;THIS LOT DONE?
62 01100 001366 BNE 38 ;BRANCH IF NOT.
63 01102 016700 MOV CHARB,R0 ;NEXT POINTER
      177124
64 01106 016702 MOV BCNT,R2 ;NUMBER OF CHARACTERS.
      177114
65 01112 010001 58: MOV R0,R1 ;COPY POINTER.
66 01114 016703 MOV CHROW,R3 ;BYTES PER CHARACTER.
      177134
67 01120 112177 68: MOV# (R1)+,#CHSR ;LOAD A BYTE
      177462
68 01124 005303 DEC R3 ;CHAR DONE?
69 01126 001374 BNE 68
70 01130 005200 INC R0 ;UPDATE POINTER.
71 01132 005302 DEC R2 ;ALL DONE?
72 01134 001366 BNE 58 ;BRANCH IF NOT
73 01136 112777 MOV# #242,#DBUF ;SET GREEN ON BLUE.
      000242

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177436
74 01144 112777      MOVB  #300,0DBUF      ;CLEAR BLINK CONTROL.
      000300
      177430
75 01152 052777      BIS   #110,0CSR      ;SET INT. ENAB. AND PRESET.
      000110
      177420
76 01160 104400      EXIT$,BEGIN          ;EXIT TO MONITOR, WAIT FOR INTERRUPT.
      01162 000000'
77 01164 042777 RDYINT: BIC   #100,0CSR      ;CLEAR INT. ENABLE.
      000100
      177406
78
      01172 000004
      01174 000000'
      01176 001200'
      ;-----
      PIRQ$,BEGIN,10 ; QUEUE CONTINUE AT 10 AND RTI
      ;-----
79 01200 032777 10:  BIT   #BIT10,0CSR    ;CHECK FOR 525 LINES.
      002000
      177372
80 01206 001403      BEQ   RESTRT        ;IF NOT, OK
81 01210 162767      SUB   #10,MAXY      ;YES,REDUCE MAXIMUM Y COORD.
      000010
      177026
    
```

```

1 001216 052777 RESTRT: BIS   #1,0CSR      ;TURN DISPLAY ON.
      000001
      177354
2 001224 012777      MOV   #TIMINT,0VECTOR ;SET FOP TIMER SERVICE
      001356'
      176556
3 001232 005067.     CLP   ITCNT          ;RESET COLOUR COUNTER.
      177340
4 001236 026727 PASS: CMP   ITCNT,#6      ;ALL COLOURS DONE YET?
      177334
      000006
5 001244 001005      BNE   10            ;IF NOT, CARRY ON.
6 001246 042777      BIC   #40101,0CSR   ;DISPLAY OFF, DISABLE INTS.
      040101
      177324
7 001254 104413      ENDIT$,BEGIN        ;SIGNAL END OF ITERATION.
      001256 000000'
      ;MONITOR SHALL TEST END OF PASS
      ;GET COLOUR INDEX.
8 001260 016701 10:  MOV   ITCNT,R1
9 001264 116177      MOVB  COL(R1),0DBUF  ;SET COLOUR.
      000560'
      177310
10 01272 005267      INC   ITCNT         ;UPDATE COUNTER FOR NEXT TIME.
      177300
11 01276 116767      MOVB  LX,START1     ;SET 1ST X COORD (LEFT).
      176746
      177304
12 01304 116767      MOVB  MAXY,START1+1 ;SET 1ST Y COORD (LEFT).
      176734
      177277
13 01312 116767      MOVB  RX,START2     ;SET 1ST X COORD (RIGHT).
      176734
      177272
14 01320 116767      MOVB  MAXY,START2+1 ;SET 1ST Y COORD (RIGHT).
      176720
      177265
15 01326 005001 STROW: CLR   R1            ;R1 IS NOW CHARACTER INDEX.
16 01330 016767 FIPST: MOV   START1,XY1    ;SET RUNNING COORDINATES
      177254
      177256
17 01336 016767      MOV   START2,XY2   ;
      177250
      177252
18 01344 052777      BIS   #BIT14,0CSR   ;ENABLE TIMER
      040000
      177226
19 01352 104400      EXIT$,BEGIN          ;EXIT TO MONITOR, WAIT FOR INTERRUPT.
      01354 000000'
20 01356 042777 TIMINT: BIC   #BIT15|BIT14,0CSR ;DISABLE TIMER.
      140000
      177214
21
      01364 000004
      01366 000000'
      01370 001372'
      ;-----
      PIRQ$,BEGIN,CONT ; QUEUE CONTINUE AT CONT AND RTI
      ;-----
    
```

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22 01372 126767 CONT:  CMPB  XY1,MAXX      ;AT MAXIMUM X ?
    177216
    176646
23 01400 103013      BHIS  116
24 01402 016777      MOV   XY1,#CAR      ;SET FOR 1ST LEFT
    177206
    177174
25 01410 105777 18:  TSTB  #CSR      ;CHECK READY
    177164
26 01414 100375      BPL   18
27 01416 016702      MOV   ACHAR,R2      ;GET CHARACTER ADDRESS
    176612
28 01422 060102      ADD   R1,R2         ;ADD INDEX
29 01424 111277      MOVB  (R2),#DBUF    ;AND SEND IT.
    177152
30 01430 126767 118:  CMPB  XY2,MAXX      ;AT MAXIMUM X ?
    177162
    176610
31 01436 103013      BHIS  128
32 01440 016777      MOV   XY2,#CAR      ;MOVE CURSOR TO 1ST RIGHT.
    177152
    177136
33 01446 105777 28:  TSTB  #CSR      ;CHECK READY
    177126
34 01452 100375      BPL   28
35 01454 016702      MOV   CCHAR,R2      ;GET CHARACTER ADDRESS
    176560
36 01460 060102      ADD   R1,R2         ;ADD INDEX
37 01462 111277      MOVB  (R2),#DBUF    ;AND SEND IT.
    177114
38 01466 126767 128:  CMPB  XY1,LX        ;IS X AT CENTRE?
    177122
    176554
39 01474 001452      BEQ   NXCHAR        ;IF SO, SKIP 2ND PAIR.
40 01476 105267      INCB  XY1           ;UPDATE COORDINATES
    177112
41 01502 105367      DECB  XY2           ;FOR 2ND PAIR.
    177110
42 01506 126767      CMPB  XY1,MAXX      ;AT MAXIMUM X ?
    177102
    176532
43 01514 103013      BHIS  136
44 01516 016777      MOV   XY1,#CAR      ;SET CURSOR FOR LEFT.
    177072
    177060
45 01524 105777 38:  TSTB  #CSR      ;CHECK FOR READY
    177050
46 01530 100375      BPL   38
47 01532 016702      MOV   BCHAR,R2      ;GET CHARACTER ADDRESS
    176500
48 01536 060102      ADD   R1,R2         ;ADD INDEX
49 01540 111277      MOVB  (R2),#DBUF    ;AND SEND IT.
    177036
50 01544 126767 138:  CMPB  XY2,MAXX      ;AT MAXIMUM X ?
    177046
    176474
51 01552 103013      BHIS  146

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52 01554 016777      MOV   XY2,#CAR      ;SET CURSOR FOR RIGHT.
    177036
    177022
53 01562 105777 48:  TSTB  #CSR      ;CHECK FOR READY.
    177012
54 01566 100375      BPL   48
55 01570 016702      MOV   DCHAR,R2      ;GET CHARACTER ADDRESS
    176446
56 01574 060102      ADD   R1,R2         ;ADD INDEX
57 01576 111277      MOVB  (R2),#DBUF    ;AND SEND IT.
    177000
58 01602 105767 148:  TSTB  XY1+1        ;Y REACHED TOP?
    177007
59 01606 001405      BEQ   NXCHAR        ;IF SO, CHANGE CHARS.
60 01610 105367      DECB  XY1+1        ;NO, UPDATE COORDINATES
    177001
61 01614 105367      DECB  XY2+1        ;
    176777
62 01620 000664      BR   CONT          ;AND CONTINUE ROW.
63 01622 020167 NXCHAR:  CMP   R1,CHCNT      ;ALL CHARACTERS DONE?
    176430
64 01626 001402      BEQ   NXTROW        ;IF SO, START NEW ROW.
65 01630 005201      INC   R1           ;NO, NEXT SET OF CHARS
66 01632 000636      BP   FIRST          ;GO BACK TO ROW START.
67 01634 126767 NXTROW:  CMPB  START2,MAXX   ;RUN OUT OF X'S YET?
    176752
    176404
68 01642 001405      BEQ   NEXTY        ;IF SO, CHANGE Y STARTS.
69 01644 105367      DECB  START1       ;NO, CHANGE X STARTS.
    176740
70 01650 105267      INCB  START2
    176736
71 01654 000624      BP   STROW         ;START NEW ROW.
72 01656 105767 NEXTY:  TSTR  START1+1     ;Y START REACHED TOP?
    176727
73 01662 001002      BNE  18
74 01664 000167      JMP  PASS          ;IF SO, GO TO PASS COUNT.
    177346
75 01670 105367 18:  DECB  START1+1     ;NO, CHANGE Y STARTS
    176715
76 01674 105367      DECB  START2+1     ;
    176713
77 01700 000612      BR   STROW         ;START NEW ROW.
78
79 01702 115 MSG66:  .NLST BEX
80 01725 115 MSG88:  .ASCIZ "MATRIX IS 6 BY 6."
81
82
83
    .LIST BEX
    .EVEN
    .END
;END OF PROGRAM.

```

ACHAR 000234R	ACHAR6 000412R	ACHAR8 000520R
ACNT 000224R	ACSR 000102R	ADDR 000006R
ADDR22 001000	ASB 000106R	ASTAT 000104R
AWAS 000110P	BCHAR 000236R	BCHAR6 000420R
BCHAR8 000530R	BCNT 000226R	BEGIN 000000R
BIT0 000001	BIT1 000002	BIT10 000200
BIT11 000400	BIT12 010000	BIT13 020000
BIT14 040000	BIT15 100000	BIT2 000004
BIT3 000010	BIT4 000020	BIT5 000040
BIT6 000100	BIT7 000200	BIT8 000400
BIT9 001000	BREAK 104407	RR1 000012R
BR2 000013R	BTOD 104421	CAR 000604R
CCHAR 000240R	CCHAR6 000426R	CCHAR8 000540R
CDATA 104412	CHARA 000230R	CHARA6 000350R
CHARA8 000442R	CHARB 000232R	CHARB6 000371R
CHARB8 000471P	CHCNT 000256R	CHROW 000254R
CH8R 000606R	COL 000560R	CONFIG 000056R
CONT 001372R	CSR 000600R	CSRA 000100R
DATCK 104411	DATER 104404	DEUF 000602R
DCHAR 000242R	DCHAR6 000434R	DCHAR8 000550R
DVID1 000014R	ENDIT 104413	END 104410
ERRTY 000106R	EXIT 104400	FIRST 001330R
GETPA 104415	GWBUF 104414	HRDCNT 000044R
HRDER 104405	HRDPAS 000050R	ICONT 000036R
ICOUNT 000040R	IDNUM 000122P	INIT 000030R
INTR 000120R	ITCNT 000576R	LDCH 001010P
LX 000250R	MAP22 104416	MAXX 000246R
MAXY 000244R	MODNAM 000000R	MODSP 000224R
MSGN 104403	MSG8 104402	MSG 104401
MSG1 000566R	MSG2 000572R	MSG6 001702R
MSG88 001725P	M6X6 000260P	M8X8 000314R
NEXTY 001656R	NULL 000000	NXCHAR 001622R
NXTROW 001634R	OPEN 000000	OTOA 104420
PASCNT 000034R	PASS 001236R	PC 0000007
PIR0 000004	POPS 005726	POPS2 022626
PRTY 000000	PRTY0 000000	PRTY1 000040
PRTY2 000100	PRTY3 000140	PRTY4 000200
PRTY5 000240	PRTY6 000300	PRTY7 000340
PS 177776	PSW 177776	PUSH 005746
PUSH2 024646	RAND 104417	RANNUM 000054R
RDYINT 001164R	RESTR 001216R	RES1 000056R
RES2 000060R	RSTR 000112R	RX 000252R
R0 000000	R1 000001	R2 000002
R3 000003	R4 000004	R5 000005
R6 000006	R7 000007	SPADR 000102P
SOF CNT 000042R	SOFER 104406	SOFPAS 000046R
SP 000006	SPOINT 000032R	SFSIZ 000040
SR1 000016R	SR2 000020R	SR3 000022R
SR4 000024R	START 000620R	START1 000610R
START2 000612R	STAT 000026R	STROW 001326R
SVR0 000062R	SVR1 000064R	SVR2 000066R
SVR3 000070R	SVR4 000072R	SVR5 000074R
SVR6 000076R	SYSCNT 000052R	TIMINT 001356R
TRDPFD 000022	VECTOR 000010R	WASADP 000104R
WDFR 000116R	WDIO 000114R	XFLAG 000005R
XY1 000614R	XY2 000616R	
. ABS. 00000P	000	

001750 001  
 ERRORS DETECTED: 0  
 FREE CORE: 11488. WORDS

XVIVA0,OBJ,XVIVAP,SEQ=[40,0]NEWDDX,P11,[40,30]XVIVAP,P11

368  
369  
371  
372  
373  
374  
375  
376  
377  
378  
379  
380  
381  
382  
383  
384  
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.REM 3

IDENTIFICATION  
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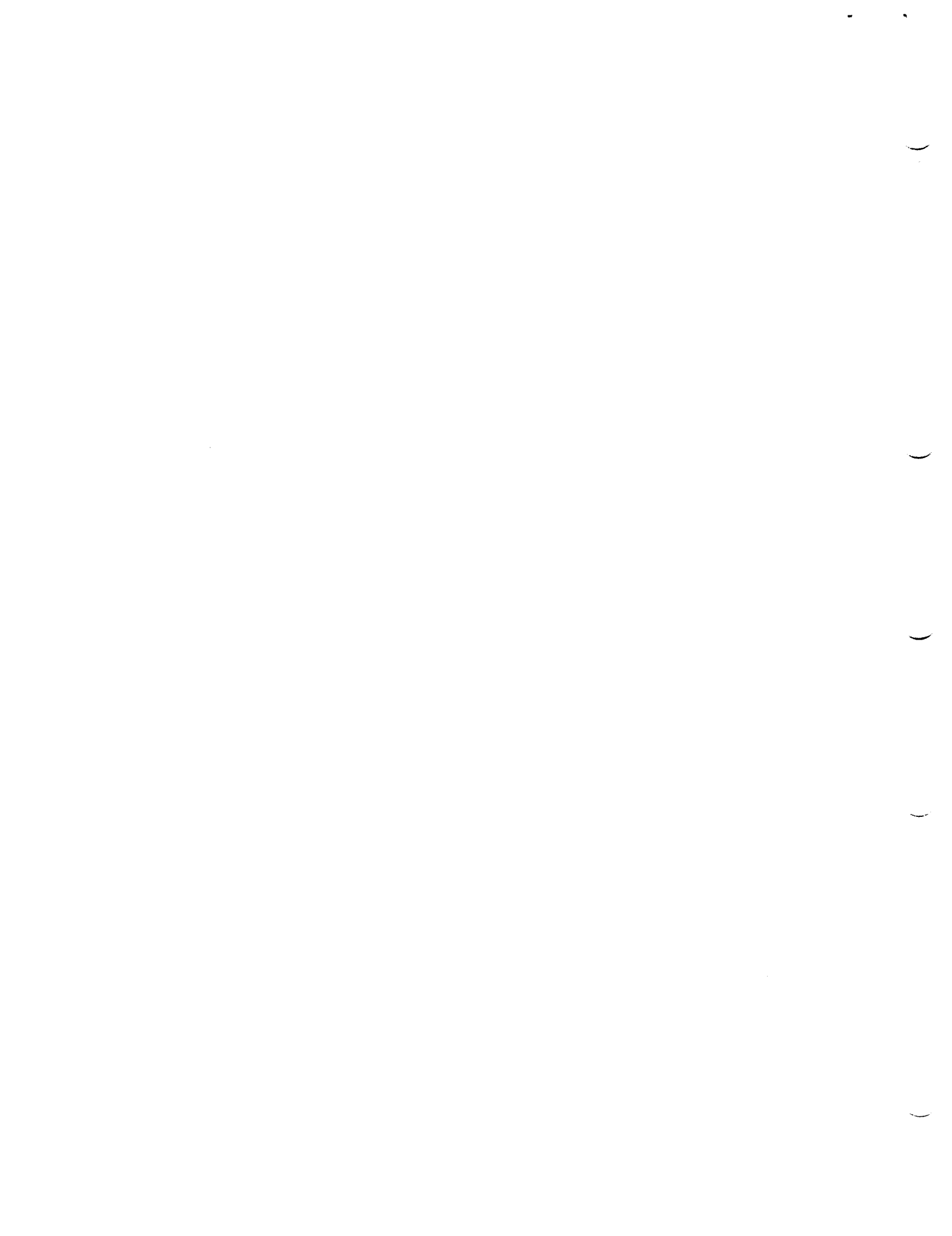
PRODUCT CODE:	AC-F542A-MC
PRODUCT NAME:	CXBMIA0 BOOT ROM MODULE
PROGRAM DATE:	MAY 1979
MAINTAINER:	DEC/X11 SUPPORT GROUP

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1. ABSTRACT

BMI IS A BKMOD THAT DOES A READ CRC AND  
LPC CHECK ON THE ROM SELECTED BY SRI.

2. REQUIREMENTS

HARDWARE: ANY PDP-11 WITH A ROM,  
STORAGE : BMI REQUIRES  
1. DECIMAL WORDS: 417  
2. OCTAL WORDS :641  
3. OCTAL BYTES :1502

3. PASS DEFINITION

ONE PASS CONSISTS OF DOING A CRC AND LPC CHECK ON  
THE ROM 3000(8) TIMES.

4. EXECUTION TIME

BMI RUNNING ALONE TAKES APPROXIMATELY ONE MINUTE  
45 SECONDS.

5. CONFIGURATION REQUIREMENTS

SPI IS USED TO SELECT THE VERSION OF THE ROM BOOTSTRAP TO BE TESTED  
ACCORDING TO THE FOLLOWING TABLE. NOTE: THESE SETTINGS ARE  
OCTAL NUMBERS. THEY ARE NOT PARTICULAR SWITCHES SET TO A  
ONE. FOR EXAMPLE, TO SELECT THE M9301-YH VERSION, SET  
SWITCHES #3 AND #1 IN SRI. THIS CORRESPONDS TO AN OCTAL 12.

SWR	MODULE VERSION
---	-----
1	M9301-YA
2	M9301-YB
3	M9301-YC
4	M9400-YA (OR YC)
5	M9301-YF
6	M7942-YB
7	M9301-YD
10	M9400-YH (OR YK)
11	M9311
12	M9301-YH
13	M9301-YE
14	M9301-YJ
15	M9400-YN
16	UNUSED

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6. DEVICE/OPTION SETUP

NONE

7. MODULE OPERATION

READS EACH ROM LOCATION AND CALCULATES A CRC WORD  
AND LPC WORD FOR THE SELECTED ROM. IT COMPARES BOTH  
WORDS AGAINST THE EXPECTED VALUE THAT IS GETS FROM  
THE TABLE.

8. OPERATING OPTIONS

NONE

9. NON-STANDARD PRINTOUTS

NONE

;

```

502
503 000000*
(1) 000000*
(2)
(2)
(2)
(2)
(2)
(2) 000000*
(2) 000000* 046502 040511 040
(2) 000005* 000
(2) 000006* 000000
(2) 000010* 000000
(2) 000012* 000
(2) 000013* 000
(2) 000014* 000001
(2) 000016* 000000
(2) 000020* 000000
(2) 000022* 000000
(2) 000024* 000000
(2)
(2) 000026* 040020
(2) 000030* 000252*
(2) 000032* 000224*
(2) 000034* 000000
(2) 000036* 000300
(2) 000040* 000000
(2) 000042* 000000
(2) 000044* 000000
(2) 000046* 000000
(2) 000050* 000000
(2) 000052* 000000
(2) 000054* 000000
(2) 000056* 000000
(2) 000060* 000000
(2) 000062* 000000
(2) 000064* 000000
(2) 000066* 000000
(2) 000070* 000000
(2) 000072* 000000
(2) 000074* 000000
(2) 000076* 000000
(2) 000100* 000000
(2) 000102*
(2) 000102* 000000
(2) 000104*
(2) 000104* 000000
(2) 000106*
(2) 000106* 000000
(2) 000110* 000000
(2) 000112* 000252*
(2) 000114* 000000
(2) 000116* 000000
(2) 000120* 000000
(2) 000122* 000013
(2) 000040

```

```

RKMOD <BMIA >,,,,,300,13
MODULE 40020,BMIA,,,,,300,13
.TITLE BMIA DEC/X11 SYSTEM EXERCISER MODULE
; DDACOM VFRSION 6 23-MAY-78
.LIST RIN
;*****
REGIN:
MODNAM: .ASCII /BMIA / ;MODULE NAME,
XFLAG: .RYTE OPEN ;USFD TO KEEP TRACK OF WBUFF USAGE
ADDR: +0 ;1ST DEVICE ADDR.
VECTOR: +0 ;1ST DEVICE VECTOR.
BR1: .RYTE PRTY+0 ;1ST BR LEVEL.
BR2: .RYTE PRTY+0 ;2ND BR LEVEL.
DVID1: +1 ;DEVICE INDICATOR 1.
SR1: OPEN ;SWITCH REGISTER 1
SR2: OPEN ;SWITCH REGISTER 2
SR3: OPEN ;SWITCH REGISTER 3
SR4: OPEN ;SWITCH REGISTER 4
;*****
STAT: 40020 ;STATUS WORD.
INIT: START ;MODULE START ADDR.
SPOINT: MODSP ;MODULE STACK POINTER.
PASCNT: 0 ;PASS COUNTER.
ICONT: 300 ;# OF ITERATIONS PER PASS=300
ICOUNT: 0 ;LOC TO COUNT ITERATIONS
SOFcnt: 0 ;LOC TO SAVE TOTAL SOFT ERRORS
HRDCNT: 0 ;LOC TO SAVE TOTAL HARD ERRORS
SOPPAS: 0 ;LOC TO SAVE SOFT ERRORS PER PASS
HRDPAS: 0 ;LOC TO SAVE HARD ERRORS PER PASS
SYSCNT: 0 ;# OF SYS EPRORS ACCUMULATED
RANNUM: 0 ;HOLDS RANDOM # WHEN RAND MACRO IS CALLED
CONFIG:
RES1: 0 ;RESERVED FOR MONITOR USE
RES2: 0 ;RESERVED FOR MONITOR USE
SVR0: OPEN ;LOC TO SAVE R0.
SVR1: OPEN ;LOC TO SAVE R1.
SVR2: OPEN ;LOC TO SAVE R2.
SVR3: OPEN ;LOC TO SAVE R3.
SVR4: OPEN ;LOC TO SAVE R4.
SVR5: OPEN ;LOC TO SAVE R5.
SVR6: OPEN ;LOC TO SAVE R6.
CSRA: OPEN ;ADDR OF CURRENT CSR.
SBADP: ;ADDR OF GOOD DATA, OR
ACSR: OPEN ;CONTENTS OF CSR.
WASADR: ;ADDR OF BAD DATA, OR
ASTAT: OPEN ;STATUS REG CONTENTS.
ERRTYP: ;TYPE OF ERROR
ASB: OPEN ;EXPECTED DATA.
AWAS: OPEN ;ACTUAL DATA.
RSTRT: RSTRT ;RESTART ADDRESS AFTER END OF PASS
WDTO: OPEN ;WORDS TO MEMORY PER ITERATION
WDFR: OPEN ;WORDS FROM MEMORY PER ITERATION
INTR: OPEN ;# OF INTERRUPTS PER ITERATION
IDNUM: 13 ;MODULE IDENTIFICATION NUMBER=13
.REPT SFSIZ ;MODULE STACK STARTS HEFE.

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```

(2)
(2)
(2)
(3)
(2) 000224*
(2)
504 000224* 173000
505 000226* 001000
506 000230* 165000
507 000232* 001000
508 000234* 000000
509 000236* 000000
510 000240* 000000
511 000242* 000000
512 000244* 000000
513 000246* 000000
514 000250* 000000
515
516 000252*
517 000252*
518 000252* 016700 177540
519 000256* 001005
520 000260* 104403 000000* 001442*
521 000266* 104410 000000*
522 000272* 006300
523 000274* 016067 001142* 177736
524 000302* 016067 001074* 177726
525 000310* 016067 001212* 177710
526 000316* 016067 001260* 177700
527 000324* 016067 001326* 177700
528 000332* 016067 001374* 177670
529 000340* 005067 177676
530 000344* 005067 177674
531 000350* 016700 177652
532 000354* 001413
533 000356* 016701 177642
534 000362* 004767 000200
535 000366* 016701 177632
536 000372* 016700 177630
537 000376* 006200
538 000400* 004767 000432
539 000404* 016701 177620
540 000410* 016700 177616
541 000414* 001411
542 000416* 004767 000144
543 000422* 016701 177602
544 000426* 016700 177600
545 000432* 006200
546 000434* 004767 000376
547 000440* 026767 177572 177574
548 000446* 001420
549 000450* 016767 177550 177424
550 000456* 016767 177542 177416
551 000464* 005067 177414
552 000470* 016767 177542 177410
553 000476* 016767 177540 177404

```

```

.NLIST
.WORD 0
.LIST
.ENDP
MODSP:
;*****
ROMSA1: 173000
DATLN1: 512.
ROMSA2: 165000
DATLN2: 512.
XORS: 0
EXCRC: 0
EXLPC: 0
ACTCRC: 0
ACTLPC: 0
PARCNT: 0
TYP0UT: 0
START:
RSTRT:
18: MOV SR1,R0 ;GET SR1
BNE ST2
MSG#s,REGIN,ADR ;ASCII MESSAGE CALL WITH COMMON HEADER
ENDs,REGIN ;
ST2: ASL R0
MOV TXLPC(R0),EXLPC ;FETCH EXPECT. LPC
MOV TXCRC(R0),EXCRC ;FETCH EXPECTED CRC
TDLN1(R0),DATLN1 ;FETCH 1ST LENGTH
TRMSA1(R0),ROMSA1 ;FETCH 1ST STARTING ADDR.
TDLN2(R0),DATLN2 ;FETCH 2ND LENGTH
TRMSA2(R0),ROMSA2 ;FETCH 2ND STARTING ADDR
18: CLR ACTCRC ;CLEAR STORAGE FOR ACTUAL CRC
CLR ACTLPC ;CLEAR STORAGE FOR ACTUAL LPC
MOV DATLN1,R0 ;SET LENGTH OF 1ST ROM SPACE
BFO CH0 ;IF NO VERSION SELECTED: BR
MOV ROMSA1,R1 ;POINT TO START OF 1ST ROM SPACE
PC,CRC ;COMPUTE FIRST HALF OF CRC
MOV ROMSA1,R1 ;POINT TO START OF 1ST ROM ADDR.
DATLN1,R0 ;SET LENGTH OF 1ST ROM ADDR.
ASR R0 ;CONVERT TO WORDS
JSR PC,LPC ;COMPUTE FIRST HALF OF CRC
CH0: MOV ROMSA2,R1 ;POINT TO 2ND ROM ADDR.
DATLN2,R0 ;SET LENGTH OF 2ND ROM ADDR.
CH1 ;IF THIS SPACE NOT USED
JSR PC,CRC ;COMPUTE REMAINDER OF CRC
MOV ROMSA2,R1 ;POINT TO START OF 2ND ROM ADDR.
DATLN2,R0 ;SET LENGTH OF 2ND ROM ADDR.
ASR R0 ;CONVERT TO WORDS
JSR PC,LPC ;COMPUTE REMAINDER OF LPC
CHI: CMP EXCRC,ACTCRC ;COMPUTED = EXPECTED ?
BEQ ;IF SO: BR
MOV ROMSA1,ACSR
MOV ROMSA1,SBADR
CLR WASADR
MOV EXCRC,ASB
MOV ACTCRC,AWAS

```

```

554 (1) 000504 104404 000000
555
556 000510 026767 177530 177522 CK1: CMP ACTLPC,EXLPC ;COMPARE EXPT. LPC=ACTUAL LPC
557 000516 001420 BEQ PASS ;IF SO: BR
558 000520 016767 177500 177354 MOV ROMSA1,ACSR
559 000526 016767 177472 177346 MOV POMS1,SPADR
560 000534 005067 177344 CLR WASADP
561 000540 016767 177474 177340 MOV EXLPC,ASB
562 000546 016767 177472 177334 MOV ACTLPC,AWAS
563 (1) 000554 104404 000000
564 (1)
565 000560 104413 000000 PASS: ENDT:BEGIN ;SIGNAL END OF ITERATION.
566 000564 000632 BP RFSTPT ;MONITOR SHALL TEST END OF PASS
567
568 000566 016767 177450 177440 CRC1: MOV ACTCRC,XORS
569 000574 111104 MOV (R1),R4 ;GET CHAR.
570 000576 022701 173024 CL0: CMP #173024,R1 ;LOCATION EFFECTED BY SWITCHES
571 000602 001004 BNE CL3 ;IF NOT: BR
572 000604 005300 DEC R0 ;FIX COUNTERS
573 000606 005300 DEC R0
574 000610 005721 TST (R1)+ ;FIX POINTER
575 000612 000770 RP CL0 ;CONTINUE
576 000614 004767 000114 CL3: JSR PC,PARITY ;GO GET PARITY
577 000620 004767 000166 JSR PC,XOR ;XOR CHAR
578 000624 000241 CLC
579 000626 006004 ROR R4 ;ROTATE 1 POS. RIGHT
580 000630 103014 BCC CL2 ;IF NO CARRY: BR
581 000632 052704 BIS #400,R4 ;SET BIT NINE
582 000636 000241 CLC
583 000640 010405 CL1: MOV R4,R5 ;SAVE CHAR
584 000642 042705 BIC #177703,R5
585 000646 005105 COM R5
586 000650 042705 BIC #177703,R5
587 000654 042704 RIC #74,R4
588 000660 050504 BIS R5,R4
589 000662 010467 177346 CL2: MOV #4,XORS
590 000666 005300 DEC R0
591 000670 001402 BFC CLLAST ;IF LAST CHAR: BR
592 000672 000167 177676 JMP CL0 ;GET NEXT CHAR.
593 000676 016704 177332 CLLAST: MOV XORS,R4
594 000702 005167 177326 COM XORS
595 000706 042767 177050 BIC #177050,XORS
596 000714 042704 177727 BIC #177727,R4 ;COMPLEMENT ALL BUT BITS 3 & 5
597 000720 050467 177310 BIS R4,XORS
598 000724 016767 177304 177310 MOV XORS,ACTCRC
599 000732 000207 RTS PC
600 000734 005067 177306 PARITY: CLR PARCNT ;CLEAR BIT COUNTER
601 000740 012703 000010 MOV #10,R3 ;SET NO. OF BITS
602 000744 032704 000001 CLP0: BIT #1,R4 ;SEE IF ONE BIT
603 000750 001402 REQ CLP1 ;IF NOT: BR

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604 000752 005267 177270 INC PARCNT ;BUMP COUNTER
605 000756 000241 CLP1: CLC
606 000760 006004 ROR R4 ;ROTATE TO NEXT BIT
607 000762 005303 DEC R3
608 000764 001367 BNE CLP0 ;CONTINUE FOR ALL BITS
609 000766 112104 MOV (R1)+,R4
610 000770 042704 177400 BIC #177400,R4
611 000774 032767 000001 177244 BIT #1,PARCNT ;SEE IF ODD # OF ONE BITS
612 001002 001002 BNE CLP2 ;IF SO: BR
613 001004 052704 000400 BIS #400,R4 ;SET PARITY BIT
614 001010 000207 CLP2: RTS PC ;EXIT
615
616 001012 010446 XOP: MOV R4,=(SP) ;XOP SUPROUTINE: R4 WITH XORS
617 001014 046716 177214 BIC XORS,(SP)
618 001020 040467 177210 BIC R4,XORS
619 001024 052667 177204 BIS (SP)+,XORS
620 001030 016704 177200 MOV XORS,R4
621 001034 000207 RTS PC
622
623 001036 016767 177202 177170 LPC1: MOV ACTLPC,XORS
624 001044 012104 LPC1: MOV (R1)+,R4
625 001046 022701 173026 CMP #173026,R1 ;LOCATION EFFECTED BY SWITCHES
626 001052 001402 BEQ LPC2 ;IF SO: SKIP LOC. BY BRANCHING
627 001054 004767 177732 JSR PC,XOR
628 001060 005300 DEC R0
629 001062 001370 BNE LPC1
630 001064 016767 177144 177152 MOV XORS,ACTLPC
631 001072 000207 RTS PC
632
633
634 001074 177777 TXCRC: -1 ;TABLE OF CPC VALUES
635 001076 000571 571 ;M9301 = YA VERSION
636 001100 000457 457 ;M9301 = YB VERSION
637 001102 000243 243 ;M9301 = YC VERSION
638 001104 000635 635 ;M9400 = YA(OR YC) VERSION
639 001106 000207 207 ;M9301 = YF VERSION
640 001110 000670 670 ;M7942 = YB VERSION
641 001112 000132 132 ;M9301 = YD VERSION
642 001114 000374 374 ;M9400 = YH (OR YK) VERSION
643 001116 000710 710 ;M9311 VERSION
644 001120 000536 536 ;M9301 = YH VERSION
645 001122 000752 752 ;M9301 = YE VERSION
646 001124 000633 633 ;M9301 = YJ VERSION
647 001126 000650 650 ;M9400 = YN VERSION
648 001130 177777 -1
649 001132 177777 -1
650 001134 177777 -1
651 001136 177777 -1
652 001140 177777 -1
653
654 001142 177777 TXLPC: -1 ;TABLE OF LPC VALUES
655 001144 133725 133725 ;M9301 = YA VERSION
656 001146 017563 17563 ;M9301 = YB VERSION
657 001150 141744 141744 ;M9301 = YC VERSION
658 001152 047613 47613 ;M9400 = YA(OR YC) VERSION
659 001154 114175 114175 ;M9301 = YF VERSION

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660	001156*	146126	146126	JM7942 = YB VERSION
661	001160*	132161	132161	JM9301 = YD VERSION
662	001162*	143466	143466	JM9400 = YH(OP YK) VERSION
663	001164*	036743	036743	JM9311 VERSION
664	001166*	125411	125411	JM9301 = YH VERSION
665	001170*	066246	066246	JM9301 = YE VERSION
666	001172*	132367	132367	JM9301 = YJ VERSION
667	001174*	030210	030210	JM9400 = YN VERSION
668	001176*	177777	-1	
669	001200*	177777	-1	
670	001202*	177777	-1	
671	001204*	177777	-1	
672	001206*	177777	-1	
673	001210*	177777	-1	
674				
675	001212*	177777	TDLN1: -1	TABLE OF THE LENGTH (BYTES) OF 1ST ROM ADDRESS SPACE
676	001214*	001000	1000	JM9301 = YA VERSION
677	001216*	001000	1000	JM9301 = YB VERSION
678	001220*	001000	1000	JM9301 = YC VERSION
679	001222*	001000	1000	JM9400 = YA(OP YC) VERSION
680	001224*	001000	1000	JM9301 = YF VERSION
681	001226*	004000	4000	JM7942 = YB VERSION
682	001230*	001000	1000	JM9301 = YD VERSION
683	001232*	001000	1000	JM9400 = YH(OP YK) VERSION
684	001234*	001000	1000	JM9311 VERSION
685	001236*	000734	734	JM9301 = YH VERSION
686	001240*	001000	1000	JM9301 = YE VERSION
687	001242*	001000	1000	JM9301 = YJ VERSION
688	001244*	001000	1000	JM9400 = YN VERSION
689	001246*	177777	-1	
690	001250*	177777	-1	
691	001252*	177777	-1	
692	001254*	177777	-1	
693	001256*	177777	-1	
694				
695	001260*	177777	TRMSA1: -1	TABLE OF THE STARTING ADDRESS OF 1ST ROM SPACE
696	001262*	173000	173000	JM9301 = YA VERSION
697	001264*	173000	173000	JM9301 = YB VERSION
698	001266*	173000	173000	JM9301 = YC VERSION
699	001270*	173000	173000	JM9400 = YA(OP YC) VERSION
700	001272*	173000	173000	JM9301 = YF VERSION
701	001274*	170000	170000	JM7942 = YB VERSION
702	001276*	173000	173000	JM9301 = YD VERSION
703	001300*	173000	173000	JM9400 = YH(OP YK) VERSION
704	001302*	163000	163000	JM9311 VERSION
705	001304*	173000	173000	JM9301 = YH VERSION
706	001306*	173000	173000	JM9301 = YE VERSION
707	001310*	173000	173000	JM9301 = YJ VERSION
708	001312*	173000	173000	JM9301 = YN VERSION
709	001314*	177777	-1	
710	001316*	177777	-1	
711	001320*	177777	-1	
712	001322*	177777	-1	
713	001324*	177777	-1	
714				
715	001326*	177777	TDLN2: -1	TABLE OF THE LENGTH (BYTES) OF 2ND ROM ADDRESS SPACE

716	001330*	001000	1000	JM9301 = YA VERSION
717	001332*	001000	1000	JM9301 = YB VERSION
718	001334*	001000	1000	JM9301 = YC VERSION
719	001336*	001000	1000	JM9400 = YA(OP YC) VERSION
720	001340*	001000	1000	JM9301 = YF VERSION
721	001342*	000000	0	JM7942 = YB VERSION
722	001344*	001000	1000	JM9301 = YD VERSION
723	001346*	001000	1000	JM9400 = YH(OP YK) VERSION
724	001350*	001000	1000	JM9311 VERSION
725	001352*	000764	764	JM9301 = YH VERSION
726	001354*	001000	1000	JM9301 = YE VERSION
727	001356*	001000	1000	JM9301 = YJ VERSION
728	001360*	001000	1000	JM9400 = YN VERSION
729	001362*	177777	-1	
730	001364*	177777	-1	
731	001366*	177777	-1	
732	001370*	177777	-1	
733	001372*	177777	-1	
734				
735	001374*	177777	TRMSA2: -1	TABLE OF THE STARTING ADDRESS OF 2ND ROM ADDRESS SPACE
736	001376*	165000	165000	JM9301 = YA VERSION
737	001400*	165000	165000	JM9301 = YB VERSION
738	001402*	165000	165000	JM9301 = YC VERSION
739	001404*	165000	165000	JM9400 = YA(OP YC) VERSION
740	001406*	165000	165000	JM9301 = YF VERSION
741	001410*	000000	0	JM7942 = YB VERSION
742				
743	001412*	165000	165000	JM9301 = YD VERSION
744	001414*	165000	165000	JM9400 = YH(OP YK) VERSION
745	001416*	166000	166000	JM9311 VERSION
746	001420*	165000	165000	JM9301 = YH VERSION
747	001422*	165000	165000	JM9301 = YE VERSION
748	001424*	165000	165000	JM9301 = YJ VERSION
749	001426*	165000	165000	JM9400 = YN VERSION
750	001430*	177777	-1	
751	001432*	177777	-1	
752	001434*	177777	-1	
753	001436*	177777	-1	
754	001440*	177777	-1	
755				
756	001442*	001446*	ADR: MES1	
757	001444*	177777	177777	
758	001446*	051445	MES1: .ASCIZ	/*BPI NOT SET TO SELECT ROM*/
		052117		
		051440		
		052117		
		020117		
		042514		
		052103		
		051040		
		046517		
759		000047		
760	000001		.END	.EVEN

ACSR	000102R	CNATA\$	104412	HRDPAS	000050R	PRTY3	000140	STAT	000026R
ACTCRC	000242R	CH0	000404R	ICONT	000036R	PRTY4	000200	ST2	000272R
ACTLPC	000244R	CH1	000440R	ICOUNT	000040R	PRTY5	000240	SVR0	000062R
ADDR	000006R	CK1	000510R	IDNUM	000122R	PRTY6	000300	SVR1	000064R
ADDR22	001000	CLLAST	000676R	INIT	000030R	PRTY7	000340	SVR2	000066R
ADR	001442R	CLP0	000744R	INTR	000120R	PS	177776	SVR3	000070R
ASB	000106R	CLP1	000756R	LPC	001036R	PSW	177776	SVR4	000072R
ASTAT	000104R	CLP2	001010R	LPC1	001044R	PUSH	005746	SVR5	000074R
AWAS	000110R	CL0	000574R	LPC2	001060R	PUSH2	024646	SVR6	000076R
BEGIN	000000R	CL1	000640R	MAP22\$	104416	RAND\$	104417	SYS CNT	000052R
BIT0	000001	CL2	000662R	MES1	001446R	RANNUM	000054R	TDLN1	001212R
BIT1	000002	CL3	000614R	MODNAM	000000R	RESTRT	000252R	TDLN2	001320R
BIT10	002000	CONFIG	000056R	MODSP	000224R	RES1	000056R	TRMSA1	001260R
BIT11	004000	CPC	000566R	MSGN\$	104403	RES2	000060R	TRMSA2	001374R
BIT12	010000	CSRA	000100R	MSG\$	104402	ROMSA1	000224R	TRPDFD	000022
BIT13	020000	DATCK\$	104411	NULL	000000	ROMSA2	000230R	TXCRC	001074R
BIT14	040000	DATLN1	000226R	OPEN	000000	RSTRT	000112R	TXLPC	001142R
BIT15	100000	DATLN2	000232R	OTOA\$	104420	R6	000006	TYPOUT	000250R
BIT2	000004	DVID1	000014R	PAPCNT	000246R	R7	000007	VECTOR	000010R
BIT3	000010	ENDIT\$	104413	PARITY	000734R	SBADP	000102R	WASADR	000104R
BIT4	000020	ENDS	104410	PASCNT	000034R	SOFCNT	000042R	WDFR	000116R
BIT5	000040	EPRTYP	000106R	PASS	000560R	SOFCRS	104406	WDTG	000114R
BIT6	000100	EXCRC	000236R	PIRQ\$	000004	SOFPAS	000046R	XFLAG	000005R
BIT7	000200	EXIT\$	104400	POPSP	005726	SPOINT	000032R	XOR	001012R
BIT8	000400	EXLPC	000240R	POPSP2	022626	SPSI\$	000040	XORS	000234R
BIT9	001000	GETPAS	104415	PRTY	000000	SR1	000016R		
BREAK\$	104407	GWBUFF\$	104414	PRTY0	000000	SR2	000020R		
BR1	000012R	HRDCNT	000044R	PRTY1	000040	SR3	000022R		
BR2	000013R	HRDRS\$	104405	PRTY2	000100	SR4	000024R		
BTD0\$	104421					START	000252R		

. ABS. 000000 000  
 001502 001

ERRORS DETECTED: 0  
 DEFAULT GLOBALS GENERATED: 0

PACK:XBMAI0,PACK:XBMAI0=DDXCOM,PACK:XBMAI0  
 RUN-TIME: 3 4 .4 SECONDS  
 RUN-TIME RATIO: 47/0=5.4  
 CORE USED: 7K (13 PAGES)

.REM\_

IDENTIFICATION  
-----

PRODUCT CODE: AC-F428A-MC  
PRODUCT NAME: CXMNEA0 MNCDO MODULE  
PRODUCT DATE: SEPTEMBER 1978  
MAINTAINER: DEC/X11 SUPPORT GROUP

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1.0 ABSTRACT  
-----

THE MNE IS AN IOMOD THAT EXERCISES THE MNCDO DIGITAL OUTPUT. THE MODULE CONSISTS OF A READ-WRITE SECTION OF THE INTERNAL DATA PATH'S OF THE MNCDO LOGIC. UP TO 8 MNCDO'S CAN BE EXERCISED WITH THIS MODULE. THE "MNB" MODULE CAN BE ENABLED TO USE THE MNCDO TO WRAP-AROUND DATA INTO THE MNCDI. IF YOU HAVE SELECTED THAT OPTION, YOU SHOULD Deselect "MNE" MODULE.

2.0 REQUIREMENTS  
-----

HARDWARE: ONE MNCDO (DIGITAL OUT).

STORAGE: MNE REQUIRES:  
DECIMAL WORDS: 407  
OCTAL WORDS: 627  
OCTAL BYTES: 1456

3.0 PASS DEFINITION  
-----

1000 OCTAL PASSES THRU THE LOGIC AND INTERRUPT TESTS.

4.0 EXECUTION TIME  
-----

ONE PASS OF THE MNE MODULE RUNNING ALONE TAKES APPROXIMATELY ONE MINUTE.

5.0 CONFIGURATION REQUIREMENTS  
-----

DEFAULT PARAMETERS:

DEVADP: 171260, VECTOR 340, BR1: 4

DEVcnt: 1, SR1: N/A

REQUIRED PARAMETERS:

NONE.

6.0 DEVICE/OUTPUT SET-UP  
-----

NONE IF THE "MNB" MODULE IS NOT SELECTED TO RUN WRAP-AROUND MODE.  
THIS MODULE MUST BE DESELECTED IF "MNB" IS IN WRAP-AROUND MODE  
AND THE BUS ADDRESS AND VECTOR IS THE DEFAULT VALUE.

7.0 MODULE OPERATION  
-----

THE MODULE PERFORMES THE FOLLOWING TESTS:

FLOAT A 1 ACROSS THE DATA OUTPUT REGISTER  
FLOAT A 0 ACROSS THE DATA OUTPUT REGISTER  
VERIFY BYTE OPERATION OF THE DATA OUTPUT REGISTER  
READ-WRITE TEST OF BIT 6 OF THE STATUS REGISTER  
READ-WRITE TEST OF BIT 4 OF THE STATUS REGISTER  
READ-WRITE TEST OF BIT 3 OF THE STATUS REGISTER  
OUTPUT DONE FLAG CAN SET  
OUTPUT DONE FLAG CAN BE WRITTEN TO A ZERO  
OUTPUT DONE FLAG CLEARS WHEN THE DATA OUTPUT REGISTER IS LOADED  
OUTPUT DONE FLAG GENERATES AN INTERRUPT

8.0 OPERATION OPTIONS  
-----

LOCATION DVID1 CAN BE MODIFIED TO SELECT ADDITIONAL UNITS.

9.0 NON-STANDARD PRINTOUTS  
-----

ALL PRINTOUTS HAVE THE STANDARD FORMATS DESCRIBED IN THE DEC/X11  
DOCUMENT.

```

141 .LIST MF
142 .MLIST MC,CND,MD
143 .TITLE MNEA DEC/X11 SYSTEM EXERCISER MODULE
144 ; DDXCOM VERSION 6 23-MAY-78
145 .LIST BIN
146 ;*****
147 BEGIN:
148 MODNAM: .ASCII /MNEA / MODULE NAME,
149 XFLAG: .BYTE OPEN ;USED TO KEEP TRACK OF %BUFF USAGE
150 ADDR: 171260+0 ;1ST DEVICE ADDR.
151 VECTOR: 340+0 ;1ST DEVICE VECTOR.
152 BR1: .BYTE PRTY4+0 ;1ST BR LEVEL.
153 BR2: .BYTE PRTY0+0 ;2ND BR LEVEL.
154 DVID1: 0+1 ;DEVICE INDICATOR 1.
155 SR1: OPEN ;SWITCH REGISTER 1
156 SR2: OPEN ;SWITCH REGISTER 2
157 SR3: OPEN ;SWITCH REGISTER 3
158 SR4: OPEN ;SWITCH REGISTER 4
159 ;*****
160 STAT: 140000 ;STATUS WORD.
161 INIT: START ;MODULE START ADDR.
162 SPOINT: MODSP ;MODULE STACK POINTER.
163 PASCNT: 0 ;PASS COUNTER.
164 ICOUNT: 1000 ;# OF ITERATIONS PER PASS=1000
165 SOFCNT: 0 ;LOC TO COUNT ITERATIONS
166 HRDCNT: 0 ;LOC TO SAVE TOTAL SOFT ERRORS
167 SOFPAS: 0 ;LOC TO SAVE TOTAL HARD ERRORS
168 HRDPAS: 0 ;LOC TO SAVE SOFT ERRORS PER PASS
169 SYSCHT: 0 ;LOC TO SAVE HARD ERRORS PER PASS
170 RANUM: 0 ;# OF SYS ERRORS ACCUMULATED
171 CONFIG: ;HOLDS RANDOM # WHEN HAND MACPO IS CALLED
172 PES1: 0 ;RESERVED FOR MONITOR USE
173 PES2: 0 ;RESERVED FOR MONITOR USE
174 SVR0: OPEN ;LOC TO SAVE R0.
175 SVR1: OPEN ;LOC TO SAVE R1.
176 SVR2: OPEN ;LOC TO SAVE R2.
177 SVR3: OPEN ;LOC TO SAVE R3.
178 SVR4: OPEN ;LOC TO SAVE R4.
179 SVR5: OPEN ;LOC TO SAVE R5.
180 SVR6: OPEN ;LOC TO SAVE R6.
181 CSRA: OPEN ;ADDR OF CURRENT CSR.
182 SBADR: ;ADDR OF GOOD DATA, OR
183 ACSR: OPEN ;CONTENTS OF CSR.
184 WABADR: ;ADDR OF BAD DATA, OR
185 ASAT: OPEN ;STATUS REG CONTENTS.
186 ERR1YP: ;TYPE OF ERROR
187 ASB: OPEN ;EXPECTED DATA.
188 AWAS: OPEN ;ACTUAL DATA.
189 RSTRT: RSTRT ;RESTART ADDRESS AFTER END OF PASS
190 WOTO: OPEN ;WORDS TO MEMORY PER ITERATION
191 WOFR: OPEN ;WORDS FROM MEMORY PER ITERATION
192 INTR: OPEN ;# OF INTERRUPTS PER ITERATION
193 IDNUM: 0 ;MODULE IDENTIFICATION NUMBER
194 MODSP:
195 ;*****
196

```

```

197
198 OCSR: ADDR
199 OCSR1: ADDR+1 ;HIGH BYTE ADDRESS
200
201 DOR: ADDR+2
202 DOR1: ADDR+3 ;HIGH BYTE ADDRESS
203
204 DODINV: VECTOR
205 DODINS: VECTOR+2
206
207 TEMP: 0
208 TEMP1: 1
209 BITDAT=BIT12 ;MAINT INPUT INHIBIT
210 BITEXT=BIT11 ;MAINT INPUT STROBE
211 RSTRT:
212 START:
213 CONT: MOV #BIT#,TEMP1 ;LOAD UNIT SELECT POINTER
214 MOV #OCSR,R0 ;LOAD ADDRESS POINTER
215 MOV ADDR,R1 ;LOAD INITIAL BUS ADDRESS
216 MOV R1,(R0)+ ;LOAD DEVICE ADDRESS
217 INC R1 ;UPDATE DEVICE ADDRESS VALUE
218 CMP R0,#DOR1+2 ;TEST IF DONE WITH BUS ADDRESSES
219 BNE 18 ;BR IF NOT
220 MOV VECTOR,R1 ;LOAD VECTOR POINTER
221 MOV R1,(R0)+ ;LOAD DEVICE VECTOR ADDRESS
222 TST (R1)+ ;UPDATE BUS VECTOR VALUE
223 MOV R1,(R0) ;LOAD 2ND ADDR.
224
225 ;VERIFY A MNCDO ADDRESS RESPONSE
226
227 MOV OCSR,CSPA ;LOAD BUS ADDRESS IF AN ERROR
228 TST #OCSR ;TEST OUTPUT STATUS REGISTER
229 TST #DOR ;TEST OUTPUT DATA REGISTER

```

```

230 000324*
231
232 000324* 012767 000001 177550
233 000332* 016777 177544 177670
234 000340* 017767 177664 177536
235 000346* 026767 177530 177530
236 000354* 001403
237
238 000356* 104405 000000* 000000
239
240 000364*
241 000364* 104407 000000*
242 000370* 104407 000000*
243 000374* 006367 177502
244 000400* 001354
245 000402*
246
247 000402* 012767 000001 177630
248 000410* 016767 177624 177464
249 000416* 005167 177460
250 000422* 016777 177454 177600
251 000430* 017767 177574 177446
252 000436* 026767 177440 177440
253 000444* 001403
254
255 000446* 104405 000000* 000000
256
257 000454*
258 000454* 104407 000000*
259 000460* 104407 000000*
260 000464* 006367 177550
261 000470* 001347
262 000472*
263
264 000472* 012777 177777 177530
265 000500* 012767 000377 177374
266 000506* 105077 177520
267 000512* 017767 177512 177364
268 000520* 026767 177356 177356
269 000526* 001403
270
271 000530* 104405 000000* 000000
272
273 000536* 012777 177777 177464
274 000544* 012767 177400 177330
275 000552* 105077 177452
276 000556* 017767 177446 177320
277 000564* 026767 177312 177312
278 000572* 001403
279
280 000574* 104405 000000* 000000
281
282 000602*
283 000602* 104407 000000*
284 000606* 104407 000000*
285 000612*

```

```

286
287 000612* 012767 000100 177262
288 000620* 016777 177256 177376
289 000626* 017767 177372 177250
290 000634* 026767 177242 177242
291 000642* 001403
292
293 000644* 104405 000000* 000000
294
295 000652* 046777 177224 177344
296 000660* 017767 177340 177216
297 000666* 026767 177210 177210
298 000674* 001003
299
300 000676* 104405 000000* 000000
301
302 000704*
303 000704* 104407 000000*
304 000710* 104407 000000*
305 000714*
306
307 000714* 012767 000020 177160
308 000722* 016777 177154 177274
309 000730* 017767 177270 177146
310 000736* 026767 177140 177140
311 000744* 001403
312
313 000746* 104405 000000* 000000
314
315 000754* 046777 177122 177242
316 000762* 017767 177236 177114
317 000770* 026767 177106 177106
318 000776* 001003
319
320 001000* 104405 000000* 000000
321
322 001006*
323 001006* 104407 000000*
324 001012* 104407 000000*

```

```

325 001016*          DO6:
326
327 001016* 012767 000010 177056      ;TEST THAT BIT3 OF MNCDO STATUS REGISTER IS READ-WRITE
328 001024* 016777 177052 177172      MOV  #BIT3,ACSR          ;LOAD EXPECTED
329 001032* 017767 177166 177044      MOV  #ACSR,#ACSR        ;LOAD BIT3 INTO MNCDO STATUS REGISTER
330 001040* 026767 177036 177036      MOV  #ACSR,ASTAT        ;READ MNCDO STATUS REGISTER
331 001046* 001403          CMP  #ACSR,ASTAT        ;TEST THAT IT SET
332                                HEO  18          ;BR IF SAME
333 001050* 104405 000000* 000000      ;*****
334                                HRDERS,BEGIN,NULL          ;BIT3 OF MNCDO STATUS REGISTER FAILED TO SET
335                                ;*****
335 001056* 046777 177020 177140      18:  BIC  #ACSR,#ACSR        ;CLEAR THAT BIT
336 001064* 017767 177134 177012      MOV  #ACSR,ASTAT        ;READ MNCDO STATUS REGISTER AGAIN
337 001072* 026767 177004 177004      CMP  #ACSR,ASTAT        ;TEST THE BIT
338 001100* 001003          BNE  28          ;BR IF CLEARED
339                                ;*****
340 001102* 104405 000000* 000000      HRDERS,BEGIN,NULL          ;BIT3 OF MNCDO STATUS REGISTER FAILED TO CLEAR
341                                ;*****
342 001110*
343 001110* 104407 000000*
344 001114* 104407 000000*
345
346
347 001120* 005077 177100          DO7:
348 001124* 012767 000200 176750      CLR  #OCSR              ;CLEAR CLEARED FLAG
349 001132* 105077 177072          MOV  #BIT7,ACSR        ;LOAD EXPECTED
350 001136* 112777 000001 177062      CLR  #DOR              ;ENABLE
351 001144* 017767 177054 176732      MOV  #BIT0,#OCSR1     ;GENERATE MAINT. REPLY
352 001152* 026767 176724 176724      MOV  #OCSR,ASTAT      ;READ OUTPUT STATUS REGISTER
353 001160* 001403          CMP  #OCSR,ASTAT      ;COMPARE
354                                BEQ  D010          ;BR IF SAME
355 001162* 104405 000000* 000000      ;*****
356                                HRDERS,BEGIN,NULL          ;OUTPUT DONE FLAG FAILED TO SET
357                                ;*****
358 001170* 105077 177034          DO10:
359 001174* 112777 000001 177024      CLR  #DOR              ;ENABLE
360 001202* 005067 176674          MOV  #BIT0,#OCSR1     ;GENERATE MAINT. REPLY
361 001206* 005077 177012          CLR  #ACSR            ;CLEAR EXPECTED
362 001212* 017767 177006 176664      MOV  #OCSR,ASTAT      ;CLEAR OUTPUT DONE FLAG
363 001220* 001403          BEQ  D011          ;READ STATUS
364                                ;*****
365 001222* 104405 000000* 000000      HRDERS,BEGIN,NULL          ;WRITING OUTPUT FLAG TO A ZERO FAILED TO CLEAR OUTPUT DO
366                                ;*****
367
368
369 001230* 105077 176774          DO11:
370 001234* 112777 000001 176764      CLR  #DOR              ;ENABLE
371 001242* 005067 176634          MOV  #BIT0,#OCSR1     ;GENERATE MAINT. REPLY
372 001246* 105077 176756          CLR  #ACSR            ;CLEAR EXPECTED
373 001252* 017767 176746 176624      MOV  #OCSR,ASTAT      ;WRITE THE OUTPUT DATA REGISTER
374 001260* 001403          BEQ  D012          ;READ OUTPUT STATUS REGISTER
375                                ;*****
376 001262* 104405 000000* 000000      HRDERS,BEGIN,NULL          ;OUTPUT DONE FLAG FAILED TO CLEAR
377                                ;*****
378                                ;WHEN OUTPUT DATA REGISTER WAS WRITTEN!

```

```

379                                ;INTERRUPT TEST -- VERIFY MNCDO DOES INTERRUPT
380 001270*          DO12:
381 001270* 104407 000000*          BREAKS,BEGIN          ;TEMPORARY RETURN TO MONITOR....
382 001274* 104407 000000*          BREAKS,BEGIN          ;THEN CONTINUE AT NEXT INSTRUCTION.
383 001300* 012777 001346* 176726      MOV  #18,#DODINV      ;LOAD RETURN VECTOR
384 001306* 116777 176500 176722      MOV  #R1,#DODINS      ;LOAD RETURN LEVEL
385 001314* 105077 176710          CLR  #DOR              ;ENABLE
386 001320* 112777 000001 176700      MOV  #BIT0,#OCSR1     ;GENERATE MAINT. REPLY
387 001326* 052777 000100 176670      RIS  #BIT6,#OCSR      ;ENABLE INTR.
388 001334* 000240          NOP
389 001336* 000240          NOP
390 001340* 000240          NOP
391 001342* 104400 000000*          EXITS,BEGIN          ;EXIT TO MONITOR. MODULE WAIT FOR INTERRUPT.
392
393 001346* 005077 176652          18:  CLR  #OCSR
394
395 001352* 000000 000000* 001360*      ;-----
396                                PTRQS,BEGIN,36          ; QUEUE UP TO CONTINUE AT 36 AND RTI
397                                ;-----
397 001360* 016777 176652 176646      38:  MOV  #DODINS,#DODINV  ;RESET VECTORS
398 001366* 005077 176644          CLR  #DODINS
399 001372* 005077 176626          CLR  #OCSR
400
401                                ;DETERMINE IF MOPF SEQUENTIAL MNCDO'S ARE TO BE TESTED
402 001376* 006367 176640          FINISH: ASL  TEMP1      ;MOVE LEFT
403 001402* 022767 000400 176632      CMP  #BIT8,TEMP1      ;TEST IF LAST VALID UNIT
404 001410* 001420          BEQ  38          ;BR IF YES
405 001412* 036767 176624 176374      BIT  TEMP1,DVID1      ;TEST IF SELECTED
406 001420* 001003          BNE  18
407 001422* 104413 000000*          EDDITS,BEGIN          ;SIGNAL END OF ITERATION.
408                                ;MONITOR SHALL TEST END OF PASS
409 001426* 000411          BF  38
410 001430* 012700 000224*          18:  MOV  #OCSR,R0          ;GET ADDRESS POINTER
411 001434* 062720 000004          28:  ADD  #4,(R0)+         ;UPDATE CONTNETS
412 001440* 020027 000240*          CMP  R0,#TEMP          ;TEST IF DONE ALL LOC.
413 001444* 001373          BNE  28
414 001446* 000167 176634          JMP  D00
415 001452* 000167 176566          38:  JMP  CONT          ;RUN NEXT UNIT
416
417                                .END

```



SOFcnt	000042R	166*			
SOFERS	104406	197*			
SOFPAS	000046R	168*			
SPOINT	000032R	162*			
SPSIZ	000040	1*	195		
SR1	000016R	155*			
SR2	000020R	156*			
SR3	000022R	157*			
SR4	000024R	158*			
START	000244R	161*	212*		
STAT	000026R	160*			
SVR0	000062R	175*			
SVR1	000064R	176*			
SVR2	000066R	177*			
SVR3	000070R	178*			
SVR4	000072R	179*			
SVR5	000074R	180*			
SVR6	000076R	181*			
SYSCNT	000052R	170*			
TEMP	000240R	207*	247*	248	260*
TEMP1	000242R	208*	213*	402*	403
TRPDFD	000022	197*			405
VECTOR	000010R	151*	204	205	220
WASADP	000104R	185*			
WDFR	000116R	192*			
WDTO	000114R	191*			
XFLAG	000005R	149*			

. ABS. 000000 000  
001456 001

ERRORS DETECTED: 0  
DEFAULT GLOBALS GENERATED: 0

XMNEAR,XMNEAR/SOL/CPF:SYM=DDXCOM,XMNEAR  
RUN-TIME: 1 1 .2 SECONDS  
RUN-TIME RATIO: 64/3=16.3  
CORE USED: 7K (13 PAGES)





.PEM -

IDENTIFICATION  
-----

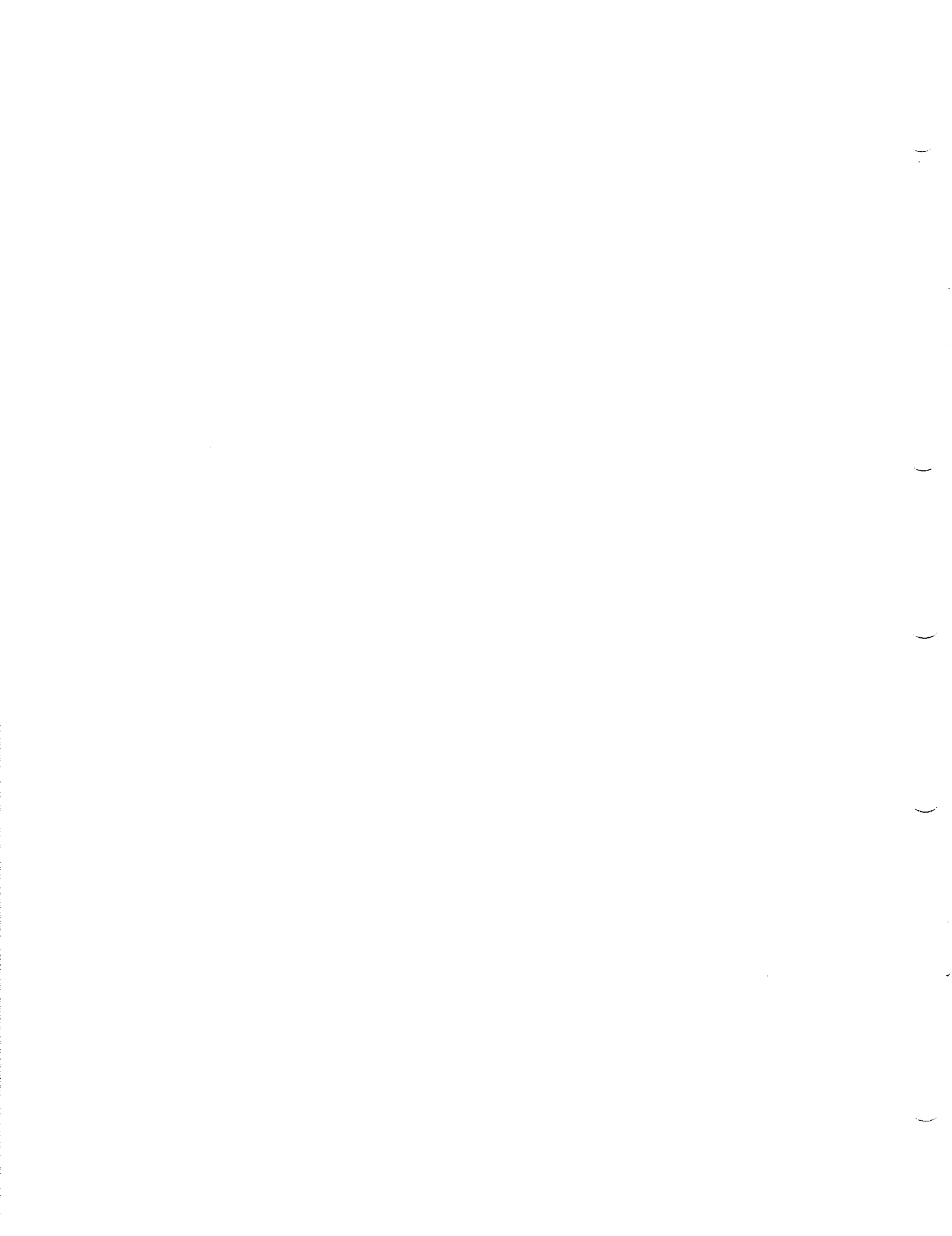
PRODUCT CODE: AC-F425A-MC  
PRODUCT NAME: CXMNDA0 MNCPA (D/A) MOD  
PRODUCT DATE: SEPTEMBER 1978  
MAINTAINER: DEC/X11 SUPPORT GROUP

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1. ABSTRACT:  
-----

"MND" IS A BKMOD THAT EXERCISES THE MNDA DIGITAL TO ANALOG CONVERTER. A CONFIDENCE LOGIC TEST IS EXECUTED ON THE DAC0, DAC1, DAC2 AND DAC3 REGISTERS. ALL LOGIC ERRORS ARE REPORTED TO THE CONSOLE TELETYPE. "MND" WILL TEST MULTIPLE UNITS AS SELECTED BY DEVCNT/DVID1.

2. REQUIREMENTS:  
-----

HARDWARE: ONE MNDA (D/A)

STORAGE: MND REQUIRES:  
DECIMAL WORDS: 399  
OCTAL WORDS: 617  
OCTAL BYTES: 1436

3. PASS DEFINITION:  
-----

ONE PASS OF THE MND MODULE CONSISTS OF FLOATING A 1 AND A 0 ACROSS THE FOUR D TO A REGISTERS 3000(8) TIMES. THIS RESULTS IN 340,000 BUS REFERENCES TO THE MNDA OPTION

4. EXECUTION TIME:  
-----

VARIES WITH THE NUMBER OF OTHER DEVICES BEING RUN. THIS SHOULD TAKE AN AVERAGE OF THIRTY SECONDS TO COMPLETE ONE PASS WHEN RUNNING ALONE.

5. CONFIGURATION PARAMETERS:  
-----

DEFAULT PARAMETERS:

DVA: 171060, VCT: N/A, BR1: N/A, DEVCNT: 1, SR1: N/A

REQUIRED PARAMETERS:

NONE

6. DEVICE OPTION SETUP:  
-----

NONE.

7. MODULE OPERATION:  
-----

START/RESTART:

THIS CODE WILL USE THE VALUE CONTAINED IN LOCATION "ADDR" TO BE THE BASE ADDRESS OF THE MNCDA. THE BUS ADDRESS OF EACH DAC IS PRINTED IN THIS ROUTINE. THE INITIAL PASS COUNTER IS ALSO PRESET.

TSDAC0:

THE ABILITY OF DAC 0 REGISTER TO HOLD A FLOATING 1 PATTERN IS VERIFIED IN THIS CODE. BIT 11 OF THE REGISTER IS INITIALLY SET (4000) AND THEN ROTATED TO THE RIGHT. UPON COMPLETION, THE SAME PROCEDURE IS REPEATED EXCEPT THE INITIAL VALUE IS 3777.

TSDAC1:, TSDAC2:, TSDAC3:

SAME AS TSDAC0

DUAL:

THIS ROUTINE WILL LOAD DIFFERENT DATA INTO EACH REGISTER AND VERIFY INDEPENDANT ADDRESS SELECTION.

DONE:

IN THIS ROUTINE, THE LOCATION "PASSX" IS DECREMENTED TO DETERMINE IF THE MODULE HAS BEEN EXERCISED. IF NOT THE PROGRAM WILL LOOP TO LOCATION "LOOPA" AND REPEAT THE SEQUENCE. WHEN THE PASS COUNT HAS BEEN COMPLETED, THE "END OF PASS" IS REPORTED.

8. OPERATOR OPTIONS:  
-----

LOCATION (PASSCT) CAN BE MODIFIED TO VARY THE NUMBER OF LOOPS THRU TEST BEFORE END OF PASS IS REPORTED (TO ACCOMMODATE SYSTEM CONFIGURATION).

9. NON-STANDARD PRINTOUTS:  
-----

NOTE: ALL PRINTOUTS HAVE THE STANDARD FORMATS DESCRIBED IN THE DEC/X11 DOCUMENT

```

151 000000*
152 000000*
153
154
155
156
157 000000*
158 000000* 047115 040504 040
159 000005* 000
160 000006* 171060
161 000010* 000000
162 000012* 000
163 000013* 000
164 000014* 000001
165 000016* 000000
166 000020* 000000
167 000022* 000000
168 000024* 000000
169
170 000026* 040023
171 000030* 000236*
172 000032* 000224*
173 000034* 000000
174 000036* 000000
175 000040* 000000
176 000042* 000000
177 000044* 000000
178 000046* 000000
179 000050* 000000
180 000052* 000000
181 000054* 000000
182 000056* 000000
183 000058* 000000
184 000060* 000000
185 000062* 000000
186 000064* 000000
187 000066* 000000
188 000070* 000000
189 000072* 000000
190 000074* 000000
191 000076* 000000
192 000100* 000000
193 000102* 000000
194 000104* 000000
195 000104* 000000
196 000104* 000000
197 000106* 000000
198 000106* 000000
199 000110* 000000
200 000112* 000236*
201 000114* 000000
202 000116* 000000
203 000120* 000000
204 000122* 000000
205 000040
206

```

```

RKMOD <MNDA>,171060,0,0,0,3000,0
MODULF 40020,MNDA,171060,0,0,0,3000,0
*TITLE MNDA DEC/X11 SYSTEM EXERCISFR MODULE
; EDXCOM VFRSION 6 23-MAY-78
;LIST BTN
;*****
;BEGIN
MODNAM: ASCII /MNDA /IMODULE NAME.
XFLAG: BYTE OPEN ;USED TO KEEP TRACK OF WBUFF USAGE
ADDR: 171060+0 ;1ST DEVICE ADDR.
VECTOR: 0+0 ;1ST DEVICE VECTOR.
BR1: BYTE PRTY0+0 ;1ST BR LEVEL.
BR2: BYTE PRTY0+0 ;2ND BR LEVEL.
DVID1: 0+1 ;DEVICE INDICATOR 1.
SR1: OPEN ;SWITCH REGISTER 1
SR2: OPEN ;SWITCH REGISTER 2
SR3: OPEN ;SWITCH REGISTER 3
SR4: OPEN ;SWITCH REGISTER 4
;*****
STAT: 40020 ;STATUS WORD.
INIT: START ;MODULE START ADDR.
SPOINT: MODSP ;MODULE STACK POINTER.
PASCNT: 0 ;PASS COUNTER.
ICONT: 3000 ;# OF ITERATIONS PER PASS=3000
ICOUNT: 0 ;LOC TO COUNT ITERATIONS
SOFcnt: 0 ;LOC TO SAVE TOTAL SOFT ERRORS
HRDCNT: 0 ;LOC TO SAVE TOTAL HARD ERRORS
SOPPAS: 0 ;LOC TO SAVE SOFT ERRORS PER PASS
HRDPAS: 0 ;LOC TO SAVE HARD ERRORS PER PASS
SYSCNT: 0 ;# OF SYS ERRORS ACCUMULATED
RANNUM: 0 ;HOLDS RANDOM # WHEN RAND MACRO IS CALLED
;RESERVED FOR MONITOR USE
RES1: 0 ;RESERVED FOR MONITOR USE
RES2: 0 ;RESERVED FOR MONITOR USE
SVR0: OPEN ;LOC TO SAVE R0.
SVR1: OPEN ;LOC TO SAVE R1.
SVR2: OPEN ;LOC TO SAVE R2.
SVR3: OPEN ;LOC TO SAVE R3.
SVR4: OPEN ;LOC TO SAVE R4.
SVP5: OPEN ;LOC TO SAVE P5.
SVR6: OPEN ;LOC TO SAVE R6.
CSRA: OPEN ;ADDR OF CURRENT CSR.
SRADR: ;ADDR OF GOOD DATA, OR
ACSR: OPEN ;CONTENTS OF CSR.
WASADR: ;ADDR OF BAD DATA, OR
ASTAT: OPEN ;STATUS REG CONTENTS.
ERRTY: ;TYPE OF ERROR
ASR: OPEN ;EXPECTED DATA.
AWAST: OPEN ;ACTUAL DATA.
RSTRT: RFRSTR ;RESTART ADDRESS AFTER END OF PASS
WDT0: OPEN ;WORDS TO MEMORY PER ITERATION
WDFR: OPEN ;WORDS FROM MEMORY PER ITERATION
INTP: OPEN ;# OF INTERRUPTS PER ITERATION
IDNUM: 0 ;MODULE IDENTIFICATION NUMBER=0
;REPT SPSTZ ;MODULE STACK STARTS HERE.
;LIST

```

```

207
208
209
210 000224*
211
212
213
214
215
216 000224* 000006*
217 000226* 000010*
218 000230* 000012*
219 000232* 000014*
220 000234* 000001
221
222 000236*
223 000236* 012767 000001 177770
224 000244* 016767 177536 177752
225 000252* 016767 177746 177746
226 000260* 02767 000002 177740
227 000266* 016767 177734 177734
228 000274* 02767 000002 177726
229 000302* 016767 177722 177722
230 000310* 02767 000002 177714
231 000316* 012767 010000 177556
232 000324* 012767 004000 177672
233 000332* 016767 177666 177540
234 000340* 006267 177536
235 000344* 001043
236 000346* 017767 177652 177530
237 000354* 026767 177522 177522
238 000362* 001403
239
240 000364* 104405 000000* 000000
241
242 000372* 005167 177504
243 000376* 005177 177622
244 000402* 042767 170000 177472
245 000410* 017767 177610 177466
246 000416* 026767 177460 177460
247 000424* 001403
248
249 000426* 104405 000000* 000000
250
251 000434* 005167 177442
252 000440* 005177 177560
253 000444* 042767 170000 177430
254 000452* 000732
255
256 000454*

```

```

;WORD 0
;LIST
;ENDR
MODSP:
;*****
;DEVICE BUS ADDRESS
DAC0: ADDR ;BUS ADDRESS OF DAC 0
DAC1: ADDR+2 ;BUS ADDRESS FOR DAC 1
DAC2: ADDR+4 ; 2
DAC3: ADDR+6 ; 3
TEMP: BIT0
;INITIALIZATION CODE
START:
RSTRT: MOV #BIT0,TEMP ;LOAD UNIT POINTER
LOCPA: MOV ADDR,DAC0 ;LOAD BUS ADDRESS
;FOR
ADD #2,DAC1
MOV DAC1,DAC2 ; DIFFERENT
ACD #2,DAC2
MOV DAC2,DAC3 ; DAC
ADD #2,DAC3 ;BUS ADDRESSES
TSDAC0: MOV #BIT12,ACSR ;LOAD EXPECTED
MOV #BIT11,0DAC0 ;LOAD DAC0 REGISTER
MOV DAC0,CSRA ;LOAD BUS ADDRESS
181 ASR ACSR ;SHIFT THE EXPECTED
RHE 48 ;RBR IF DONE
MOV 0DAC0,ASTAT ;READ THE REGISTER
CMP ACSR,ASTAT ;COMPARE
REQ 28 ;RBR IF SAME
;*****
HDRERS,REGIN,NULL ;DAC0 FAILED TO HOLD THE FLOATING 1 PATTERN
;*****
281 COM ACSR ;COMPLEMENT DATA
COM 0DAC0 ;COMPLEMENT DATA IN DAC0
RIC #170000,ACSR ;MASK OFF UNUSED BITS
MOV 0DAC0,ASTAT ;READ DAC0
CMP ACSR,ASTAT ;COMPARE
REQ 38 ;RBR IF SAME
;*****
HDRERS,REGIN,NULL ;DAC0 FAILED TO HOLD THE FLOATING 0 PATTERN
;*****
301 COM ACSR ;COMPLEMENT EXPECTED
COM 0DAC0 ;COMPLEMENT DATA
BIC #170000,ACSP ;MASK EXPECTED DATA
RR 18 ;TEST MORE BITS
;*****
;LIST

```

```

257 000454* 012767 010000 177420 TSDAC1: MOV #BIT12,ACSR ;LOAD EXPECTED
258 000462* 012777 004000 177536 MOV #BIT11,0DAC1 ;LOAD DAC1 REGISTER
259 000470* 016767 177532 177402 MOV DAC1,CSRA ;LOAD BUS ADDRESS
260 000476* 006267 177400 18: ASR ACSR ;SHIFT THE EXPECTED
261 000502* 001043 BNE 48 ;BR IF DONE
262 000504* 017767 177516 177372 MOV 0DAC1,ASTAT ;READ THE REGISTER
263 000512* 026767 177364 177364 CMP ACSR,ASTAT ;COMPARE
264 000520* 001403 BEQ 28 ;BR IF SAME
265
266 000522* 104405 000000* 000000
;*****
HRDERS,BEGIN,NULL ;DAC1 FAILED TO HOLD THE FLOATING 1 PATTERN
;*****
267
268 000530* 005167 177346 28: COM ACSR ;COMPLEMENT DATA
269 000534* 005177 177466 COM 0DAC1 ;COMPLEMENT DATA IN DAC1
270 000540* 042767 170000 177334 BIC #170000,ACSR ;MASK OFF UNUSED BITS
271 000546* 017767 177454 177330 MOV 0DAC1,ASTAT ;READ DAC1
272 000554* 026767 177322 177322 CMP ACSR,ASTAT ;COMPARE
273 000562* 001403 BEQ 38 ;BR IF SAME
274
275 000564* 104405 000000* 000000
;*****
HRDERS,BEGIN,NULL ;DAC1 FAILED TO HOLD THE FLOATING 0 PATTERN
;*****
276
277 000572* 005167 177304 38: COM ACSR ;COMPLEMENT EXPECTED
278 000576* 005177 177424 COM 0DAC1 ;COMPLEMENT DATA
279 000602* 042767 170000 177272 BIC #170000,ACSR ;MASK EXPECTED DATA
280 000610* 000732 BR 18 ;TEST MORE BITS
281
282 000612*
283 000612* 012767 010000 177262 48: TSDAC2: MOV #BIT12,ACSR ;LOAD EXPECTED
284 000620* 012777 004000 177402 MOV #BIT11,0DAC2 ;LOAD DAC2 REGISTER
285 000626* 016767 177376 177244 MOV DAC2,CSRA ;LOAD BUS ADDRESS
286 000634* 006267 177242 18: ASR ACSR ;SHIFT THE EXPECTED
287 000640* 001043 BNE 48 ;BR IF DONE
288 000642* 017767 177362 177234 MOV 0DAC2,ASTAT ;READ THE REGISTER
289 000650* 026767 177226 177226 CMP ACSR,ASTAT ;COMPARE
290 000656* 001403 BEQ 28 ;BR IF SAME
291
292 000660* 104405 000000* 000000
;*****
HRDERS,BEGIN,NULL ;DAC2 FAILED TO HOLD THE FLOATING 1 PATTERN
;*****
293
294 000666* 005167 177210 28: COM ACSR ;COMPLEMENT DATA
295 000672* 005177 177332 COM 0DAC2 ;COMPLEMENT DATA IN DAC2
296 000676* 042767 170000 177176 BIC #170000,ACSR ;MASK OFF UNUSED BITS
297 000704* 017767 177320 177172 MOV 0DAC2,ASTAT ;READ DAC2
298 000712* 026767 177164 177164 CMP ACSR,ASTAT ;COMPARE
299 000720* 001403 BEQ 38 ;BR IF SAME
300
301 000722* 104405 000000* 000000
;*****
HRDERS,BEGIN,NULL ;DAC2 FAILED TO HOLD THE FLOATING 0 PATTERN
;*****
302
303 000730* 005167 177146 38: COM ACSR ;COMPLEMENT EXPECTED
304 000734* 005177 177270 COM 0DAC2 ;COMPLEMENT DATA
305 000740* 042767 170000 177134 BIC #170000,ACSR ;MASK EXPECTED DATA
306 000746* 000732 BR 18 ;TEST MORE BITS
307
308 000750* 48:

```

```

309 000750* 012767 010000 177124 TSDAC3: MOV #BIT12,ACSR ;LOAD EXPECTED
310 000756* 012777 004000 177246 MOV #BIT11,0DAC3 ;LOAD DAC3 REGISTER
311 000764* 016767 177242 177106 MOV DAC3,CSRA ;LOAD BUS ADDRESS
312 000772* 006267 177104 18: ASR ACSR ;SHIFT THE EXPECTED
313 000776* 001043 BNE 48 ;BR IF DONE
314 001000* 017767 177226 177076 MOV 0DAC3,ASTAT ;READ THE REGISTER
315 001006* 026767 177070 177070 CMP ACSR,ASTAT ;COMPARE
316 001014* 001403 BEQ 28 ;BR IF SAME
317
318 001016* 104405 000000* 000000
;*****
HRDERS,BEGIN,NULL ;DAC3 FAILED TO HOLD THE FLOATING 1 PATTERN
;*****
319
320 001024* 005167 177052 28: COM ACSR ;COMPLEMENT DATA
321 001030* 005177 177176 COM 0DAC3 ;COMPLEMENT DATA IN DAC3
322 001034* 042767 170000 177040 BIC #170000,ACSR ;MASK OFF UNUSED BITS
323 001042* 017767 177164 177034 MOV 0DAC3,ASTAT ;READ DAC3
324 001050* 026767 177026 177026 CMP ACSR,ASTAT ;COMPARE
325 001056* 001403 BEQ 38 ;BR IF SAME
326
327 001060* 104405 000000* 000000
;*****
HRDERS,BEGIN,NULL ;DAC3 FAILED TO HOLD THE FLOATING 0 PATTERN
;*****
328
329 001066* 005167 177010 38: COM ACSR ;COMPLEMENT EXPECTED
330 001072* 005177 177134 COM 0DAC3 ;COMPLEMENT DATA
331 001076* 042767 170000 176776 BIC #170000,ACSR ;MASK EXPECTED DATA
332 001104* 000732 BR 18 ;TEST MORE BITS
333
334 001106* 48:
335
336 ;TEST FOR DUAL ADDRESSING
337
338 001106* 012777 000000 177110 DUAL: MOV #,0DAC0 ;LOAD DAC 0
339 001114* 012777 002525 177104 MOV #2525,0DAC1 ;LOAD DAC 1
340 001122* 012777 005252 177100 MOV #5252,0DAC2 ;LOAD DAC 2
341 001130* 012777 007777 177074 MOV #7777,0DAC3 ;LOAD DAC 3
342
343 001136* 016767 177062 176734 MOV DAC0,CSRA ;LOAD DAC 0 BUS ADDRESS
344 001144* 017767 177054 176732 MOV 0DAC0,ASTAT ;READ DAC 0
345 001152* 012767 000000 176722 MOV #0,ACSR ;LOAD EXPECTED
346 001160* 026767 176716 176716 CMP ACSR,ASTAT ;COMPARE
347 001166* 001403 BEQ 18 ;BR IF SAME
348
349 001170* 104405 000000* 000000
;*****
HRDERS,BEGIN,NULL ;DUAL ADDRESS ERROR ON DAC 0
;*****
350
351
352 001176* 016767 177024 176674 18: MOV DAC1,CSRA ;LOAD DAC 1 BUS ADDRESS
353 001204* 017767 177016 176672 MOV 0DAC1,ASTAT ;READ DAC 1
354 001212* 012767 002525 176662 MOV #2525,ACSR ;LOAD EXPECTED
355 001220* 026767 176656 176656 CMP ACSR,ASTAT ;COMPARE
356 001226* 001403 BEQ 28 ;BR IF SAME
357
358 001230* 104405 000000* 000000
;*****
HRDERS,BEGIN,NULL ;DUAL ADDRESS ERROR ON DAC 1
;*****
359
360

```



ICOUNT	000040R	175*															
IDNUM	000122R	204*															
INIT	000030R	171*															
INTR	000120R	203*															
LOOPA	000244R	224*	395														
MAP22*	104416	212*															
MODNAM	000000R	158*															
MODSP	000224R	172*	210*														
MSGN8	104403	212*															
MSG8	104402	212*															
MSG	104401	212*															
NULL	000000R	212*	240	249	266	275	292	301	318	327	349	358	367	376			
OPEN	000000R	159	165	166	167	168	185	186	187	188	189	190	191	192			
		194	196	198	199	201	202	203	212*								
OTOA*	104420	212*															
PASCNT	000034R	173*															
PIRO*	000004	212*															
POPS*	005726	212*															
POPS2*	022626	212*															
PRTY	000000R	212*															
PRTY0	000000R	162	163	212*													
PRTY1	000040	212*															
PRTY2	000100	212*															
PRTY3	000140	212*															
PRTY4	000200	212*															
PRTY5	000240	212*															
PRTY6	000300	212*															
PRTY7	000340	212*															
PS	177776	212*															
PSW	177776	212*															
PUSH	005746	212*															
PUSH2	024646	212*															
RAND*	104417	212*															
RANNUM	000054R	181*															
RESTRT	000236R	200*	223*														
RES1	000056P	183*															
RES2	000060R	184*															
RSTRT	000112R	200*															
SBADR	000102P	193*															
SOFcnt	000042R	176*															
SOFER*	104406	212*															
SOPPAS	000046R	178*															
SPOINT	000032P	172*															
SPSIZ	000040	1*	205														
SR1	000016R	165*															
SR2	000020R	166*															
SR3	000022P	167*															
SR4	000024P	168*															
START	000236R	171*	222*														
STAT	000026R	170*															
SVR0	000062R	185*															
SVR1	000064P	186*															
SVR2	000066R	187*															
SVR3	000070R	188*															
SVR4	000072R	189*															
SVR5	000074R	190*															

SVR6	000076P	191*															
SYSNT	000052P	180*															
TEMP	000234R	220*	223*	381*	382	384	386*										
TRPDFD*	000022	212*															
TSDAC0	000316P	231*	394														
TSDAC1	000454P	257*															
TSDAC2	000612R	283*															
TSDAC3	000750R	309*															
VECTOR	000010R	161*															
WASADR	000104R	195*															
WDFR	000116R	202*															
WDIO	000114R	201*															
XFLAG	000025R	159*															

ABS. 000000 000  
 001436 001

ERRORS DETECTED: 0  
 DEFAULT GLOBALS GENERATED: 0

XMNDA0, XMNDA0/SOL/CRF:SYN=NDXCON, XMNDA0  
 RUN-TIME: 1 1.2 SECONDS  
 RUN-TIME RATIO: 80/3=23.3  
 CORE USED: 7K (13 PAGES)



.REM\_

IDENTIFICATION

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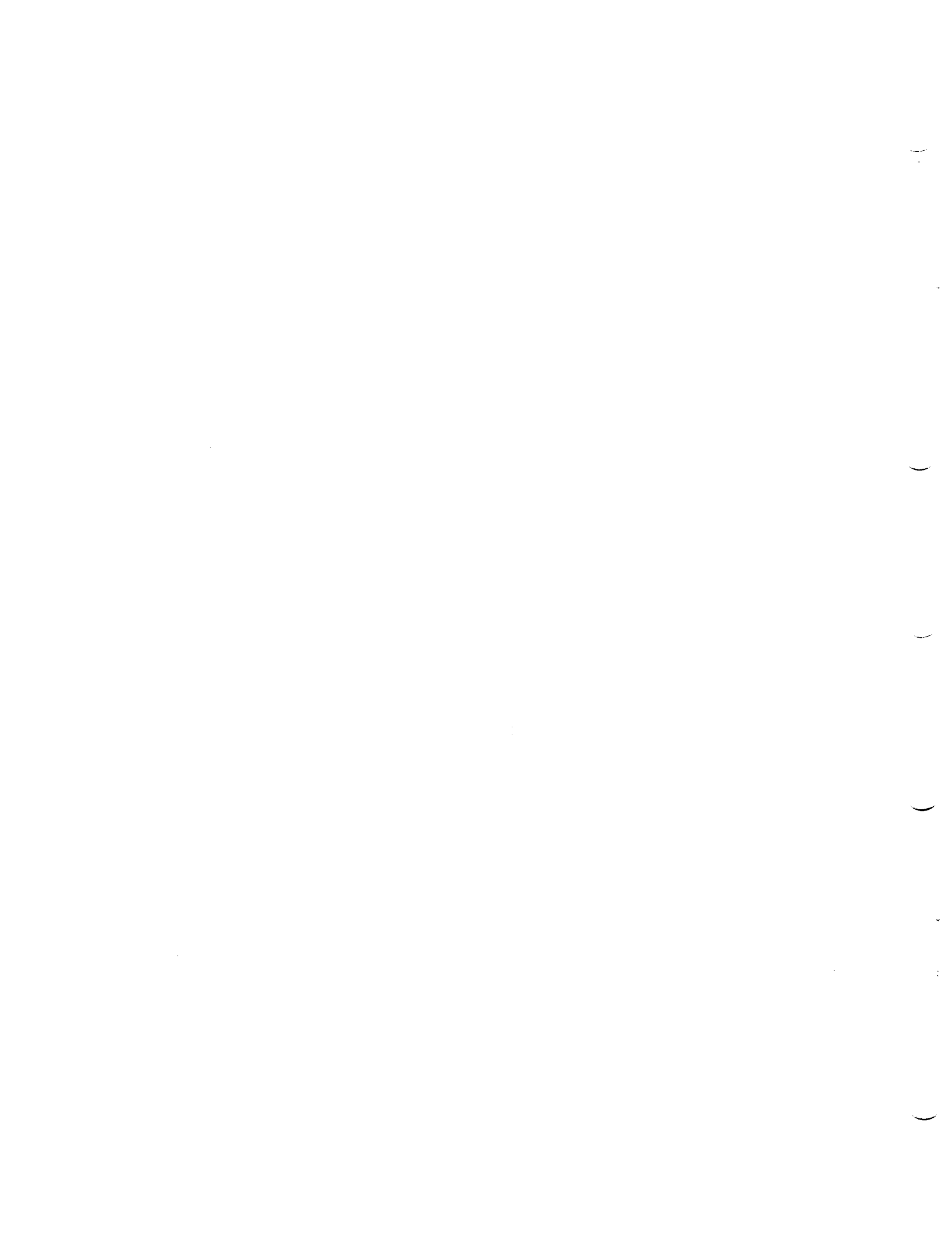
PRODUCT CODE: AC-F422A-MC  
PRODUCT NAME: CXMNCA0 MNCKW MODULE  
PRODUCT DATE: FEBPUARY 1979  
MAINTAINER: RAY SHOOP

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1.0 ABSTRACT

-----

THE MNC IS AN IOMOD THAT EXERCISES THE MNCKW REAL TIME CLOCK. ON START IT EXERCISES THE CSRS AND PRESET BUFFERS OF THE CLOCK. THEN AND FOR ALL RESTARTS, IT EXERCISES THE CLOCK AT EACH ONE OF ITS BASIC RATES. UP TO 6 MNCKW WILL BE EXERCISED WITH THIS MODULE. THE "MNA" (A/D) MODULE CAN BE ENABLED TO USE THE CLOCK TO START THE A/D. IF YOU HAVE ENABLED "MNA" TO USE THE CLOCK, YOU SHOULD DESELECT THIS EXERCISER MODULE.

2.0 REQUIREMENTS

-----

HARDWARE: ONE MNCKW (CLOCK).

STORAGE: MNC REQUIRES:  
DECIMAL WORDS: 508  
OCTAL WORDS: 775  
OCTAL BYTES: 1772

3.0 PASS DEFINITION

-----

ONE PASS OF THE MNC MODULE CONSISTS OF GENERATING INTERRUPTS FOR ONE SECOND AT EACH CLOCK RATE, UNTIL 60 SECONDS HAVE ELAPSED.

4.0 EXECUTION TIME

-----

ONE PASS OF THE MNC MODULE RUNNING ALONE TAKES APPROXIMATELY ONE MINUTE.

5.0 CONFIGURATION REQUIREMENTS

-----

DEFAULT PARAMETERS:

DEVADR: 171020, VECTOR 440, BR1: 4

DEVcnt: 1, SR1: 0

REQUIRED PARAMETERS:

NONE.

6.0 DEVICE/OUTPUT SET-UP  
-----

THE FRONT PANEL SWITCHES MUST SELECT THE SCHMITT TRIGGER INPUT  
(PULL OUT "ST1" AND "ST2" SWITCHES AND ROTATE TO THE END).

7.0 MODULE OPERATION  
-----

TEST SEQUENCE:

1. (START) PIT EXERCISE CSR, PRESET REGISTER OF CLOCK.
2. (RFSTRT) COUNT TESTS USING INTERRUPTS COUNT INTERRUPTS WILL  
OCCUR IN ONE SECOND AND ADVANCE THE TEST TO THE NEXT RATE.

AFTER A RATE HAS BEEN SELECTED, A CHECK IS MADE TO SEE IF THE  
OPERATOR HAS INHIBITED THAT RATE FROM TEST. IF NOT, CONTROL  
IS TRANSFERRED TO THE PARTICULAR RATE ROUTINE (LISTED BELOW).  
EACH RATE ROUTINE MUST PRELOAD THE BUFFER REGISTER OF THE  
CLOCK TO THE COUNT THAT WILL CAUSE IT TO INTERRUPT IN ONE  
SECOND. AFTER THE BUFFER IS LOADED, THE CSR IS LOADED WITH  
THE PROPER BITS THAT SELECT THE RATE.

- A. COUNT TEST CLOCK RATE: 1MHZ.
- P. COUNT TEST CLOCK RATE: 100KHZ.
- C. COUNT TEST CLOCK RATE: 10KHZ.
- D. COUNT TEST CLOCK RATE: 1KHZ.
- E. COUNT TEST CLOCK RATE: 100HZ.
- F. COUNT TEST CLOCK RATE: PSEUDO RANDOM (1 OF 3 RATES).

8.0 OPERATION OPTIONS

-----

VALID SRI VALUES

SRI BIT	ENABLE/DISABLE	FUNCTION
-----	-----	-----
0	0	ENABLE TESTING 1MHZ
	1	DISABLE TESTING 1MHZ
1	0	ENABLE TESTING 100KHZ
	1	DISABLE TESTING 100KHZ
2	0	ENABLE TESTING 10KHZ
	1	DISABLE TESTING 10KHZ
3	0	ENABLE TESTING 1KHZ
	1	DISABLE TESTING 1KHZ
4	0	ENABLE TESTING 100HZ
	1	DISABLE TESTING 100HZ
5	0	*ENABLE TESTING RANDOM
	1	DISABLE TESTING RANDOM

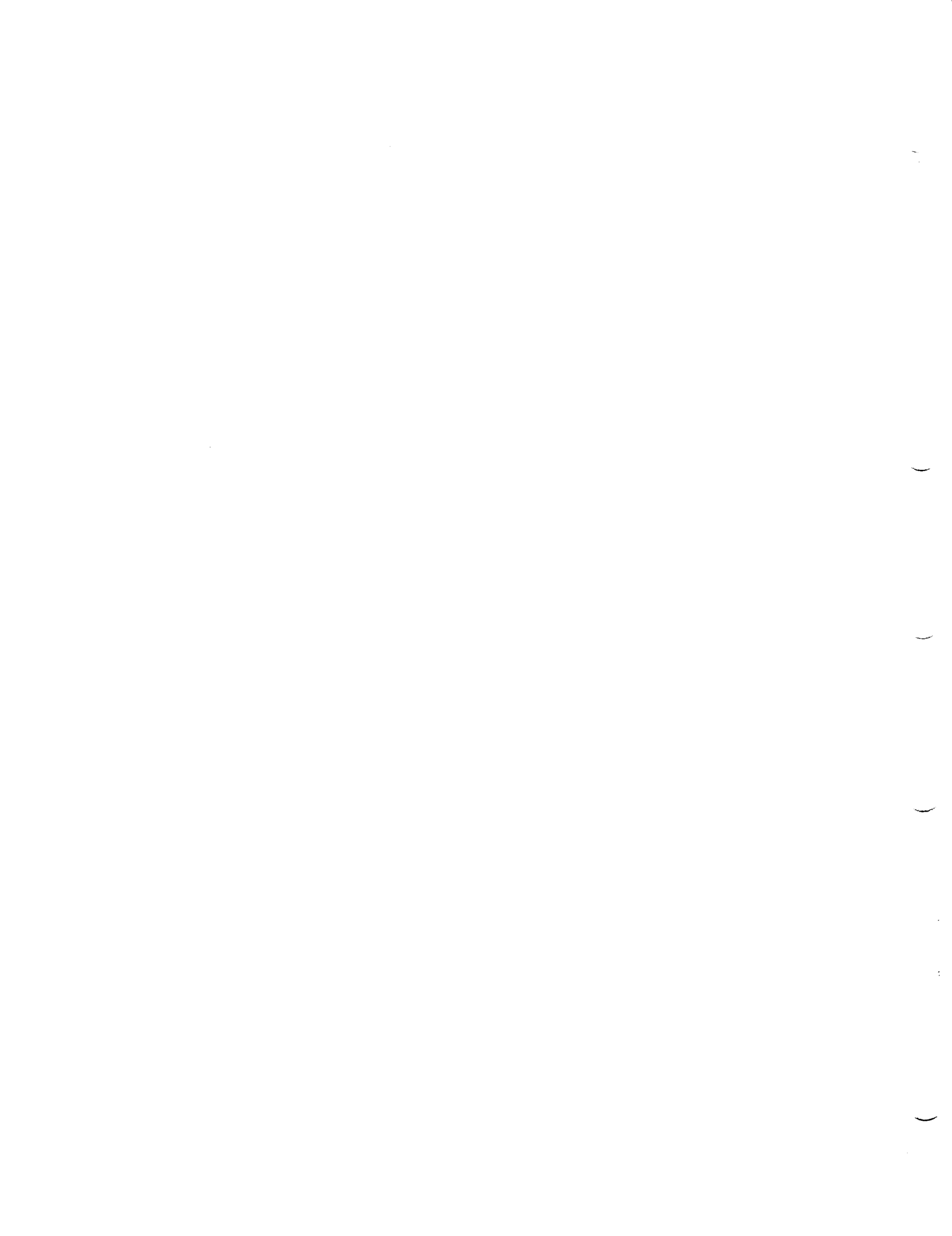
\*NOTE: IF RANDOM RATE SELECTED, THEN AN SRI BIT DISABLING A PARTICULAR RATE WILL BE IGNORED.

9.0 NON-STANDARD PRINTOUTS

-----

ALL PRINTOUTS HAVE THE STANDARD FORMATS DESCRIBED IN THE DEC/X11 DOCUMENT.

-



```

180 000000' IOMOD <MNCA >,171020,440,4,0,0,60,,0
181 000000' MODULE 140000,MNCA ,171020,440,4,0,0,60,,0
182 .TITLE MNCA DEC/X11 SYSTEM EXERCISER MODULE
183 ; DDXC0M VERSION 6 23-MAY-78
184 ,LIST BIN
185 ;*****
186 000000' BEGIN:
187 000000' 047115 040503 040 MODNAM: ,ASCII /MNCA / ,MODULE NAME,
188 000005' 000 XFLAG: ,BYTE OPEN ;USED TO KEEP TRACK OF WBUFF USAGE
189 000006' 171020 ADDR: 171020;0 ;1ST DEVICE ADDR.
190 000010' 000440 VECTOR: 440;0 ;1ST DEVICE VECTOR,
191 000012' 200 BR1: ,BYTE PRTY4+0 ;1ST BR LEVEL,
192 000013' 000 BR2: ,BYTE PRTY0+0 ;2ND BR LEVEL,
193 000014' 000001 DVID1: 0-1 ;DEVICE INDICATOR 1.
194 000015' 000000 SR1: OPEN ;SWITCH REGISTER 1
195 000020' 000000 SR2: OPEN ;SWITCH REGISTER 2
196 000022' 000000 SR3: OPEN ;SWITCH REGISTER 3
197 000024' 000000 SR4: OPEN ;SWITCH REGISTER 4
198 ;*****
199 000026' 140000 STAT: 140000 ;STATUS WORD,
200 000030' 000234' INITI: START ;MODULE START ADDR.
201 000032' 000224' SPOINT: MODSP ;MODULE STACK POINTER,
202 000034' 000000 PASCNT: 0 ;PASS COUNTER,
203 000036' 000074 ICONT: 60. ;# OF ITERATIONS PER PASS=60,
204 000040' 000000 ICOUNT: 0 ;LOC TO COUNT ITERATIONS
205 000042' 000000 SOFCNT: 0 ;LOC TO SAVE TOTAL SOFT ERRORS
206 000044' 000000 HRDCNT: 0 ;LOC TO SAVE TOTAL HARD ERRORS
207 000046' 000000 SOFPAS: 0 ;LOC TO SAVE SOFT ERRORS PER PASS
208 000050' 000000 HRDPAS: 0 ;LOC TO SAVE HARD ERRORS PER PASS
209 000052' 000000 SYSCNT: 0 ;# OF SYS ERRORS ACCUMULATED
210 000054' 000000 RANNUM: 0 ;HOLDS RANDOM # WHEN RAND MACRO IS CALLED
211 000056' 000000 CONFIG: ;RESERVED FOR MONITOR USE
212 000058' 000000 RES1: 0 ;RESERVED FOR MONITOR USE
213 000060' 000000 RES2: 0 ;RESERVED FOR MONITOR USE
214 000062' 000000 SVR0: OPEN ;LOC TO SAVE R0.
215 000064' 000000 SVR1: OPEN ;LOC TO SAVE R1.
216 000066' 000000 SVR2: OPEN ;LOC TO SAVE R2.
217 000070' 000000 SVR3: OPEN ;LOC TO SAVE R3.
218 000072' 000000 SVR4: OPEN ;LOC TO SAVE R4.
219 000074' 000000 SVR5: OPEN ;LOC TO SAVE R5.
220 000076' 000000 SVR6: OPEN ;LOC TO SAVE R6.
221 000100' 000000 CSRA: OPEN ;ADDR OF CURRENT CSR,
222 000102' 000000 SHADR: ;ADDR OF GOOD DATA, OR
223 000104' 000000 ACSR: OPEN ;CONTENTS OF CSR,
224 000106' 000000 WBSADR: ;ADDR OF BAD DATA, OR
225 000108' 000000 ASTAT: OPEN ;STATUS REG CONTENTS,
226 000110' 000000 ERRYP: ;TYPE OF ERROR
227 000112' 000000 ASB: OPEN ;EXPECTED DATA,
228 000114' 000000 AWAS: OPEN ;ACTUAL DATA,
229 000116' 000234' RSTRT: PESTRT ;RESTART ADDRESS AFTER END OF PASS
230 000118' 000000 WDT0: OPEN ;WORDS TO MEMORY PER ITERATION
231 000120' 000000 WDFR: OPEN ;WORDS FROM MEMORY PER ITERATION
232 000122' 000000 INTK: OPEN ;# OF INTERRUPTS PER ITERATION
233 000124' 000000 IONUM: 0 ;MODULE IDENTIFICATION NUMBER#
234 000126' 000040 ,REPT SPSIZ ;MODULE STACK STARTS HERE.
235 ,NLIST

```

```

236 ,WOPD 0
237 ,LIST
238 ,ENDR
239 000224' MODSP:
240 ;*****
241 000224' 000001 TEMP: 1 ;MASK OF CURRENT UNIT
242 ;*****
243 ;MODULE REQUIRED REGISTERS - SET UP BY THIS MODULE.
244
245 000226' 171020 ASR: ,WORD 171020 ;CLOCK A STATUS REG,
246 000230' 171022 ABR: ,WORD 171022 ;CLOCK A BUFFER REG,
247
248 000232' 000440 AVECT: ,WORD 440 ;CLOCK A INTERRUPT VECTOR,
249 000234' 000442 AVECT2: ,WORD 442
250
251 000236' 000444 BVECT: ,WORD 444 ;CLOCK INTERRUPT VECTOR,
252 000240' 000446 BVECT2: ,WORD 446
253
254 000242' 000001 RATEP: ,WORD 1 ;POINTS TO CURRENT RATE
255 000244' 000000 OFF: ,WORD 0 ;OFFSET TO TAKE US TO RATE ROUTINE
256 000246' 000000 RANA: ,WORD 0 ;RANDOM NUMBER,
257 000250' 000000 RANB: ,WORD 0 ;RANDOM NUMBER,
258 000252' 000000 AIFLG: ,WORD 0 ;FLAG TO SHOW THAT CLOCK A HAS INTERRUPTED,
259
260 000254' PESTPT:
261 000254' 004767 000002 START: JSR PC,START0 ;PRIME THE ADDRESSES
262 000260' 000442 BR LOG1 ;RUN LOGIC TEST
263
264 000262' 012767 000001 177734 START0: MOV #R10,TEMP ;LOAD CURRENT UNIT MASK
265 000270' 016767 177512 177730 START1: MOV ADDR,ASR ;GET BASE ADDR,
266 000276' 016767 177506 177726 MOV VECTOR,AVECT ;GET BASE VECTOR ADDR,
267 000304' 016767 177716 177716 START2: MOV ASR,ABR ;NOW WE'RE GONNA FIX
268 000312' 062767 000002 177710 ADD #2,ABR ;ALL CLOCK ADDRESSES BASED ON ASR.
269 000320' 016700 177706 MOV AVECT,R0 ;NOW FIX VECTOR ADDRESSES
270 000324' 062700 000004 ADD #4,R0
271 000330' 010067 177702 MOV R0,BVECT
272 000334' 016767 177672 177672 MOV AVECT,AVECT2
273 000342' 062767 000002 177664 ADD #2,AVECT2
274 000350' 016767 177662 177662 MOV BVECT,BVECT2
275 000356' 062767 000002 177654 ADD #2,BVECT2
276 000364' 000207 RTS PC ;EXIT

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277 ;*
278 ;*LOGIC TEST #1 BE SURE A CLOCK EXISTS AT THE
279 ;*SPECIFIED ADDR, IF NO CLOCK, THEN A
280 ;*DEC/X11 SYS ERROR WILL OCCUR,
281 ;*
282
283 000366 005777 177634 LOG1: TST #ASR ;ADDRESS THE CLOCK, IF SYS ERROR
284 ;OCCURS, THEN CLOCK DID NOT
285 ;RETURN SLAVE-SYN WHEN
286 ;ADDRESSED.
287
288 ;*
289 ;*LOGIC TEST #2, MAKE SURE CLOCK CSR BITS
290 ;*14,11,6,5,2 AND 0 CAN BE SET + CLEARED.
291 ;*
292
293 000372 012767 044125 177504 LOG2: MOV #044125,ASTAT ;GENERATE + RECORD PATTERN TO BE USED,
294 000400 016777 177500 177620 MOV ASTAT,#ASR ;SET THEM IN CSR OF CLOCK A,
295 000406 017767 177614 177466 MOV #ASR,ACSR ;READ THEM BACK
296 000414 026767 177464 177460 CMP ASTAT,ACSR ;DID THEY ALL SET?
297
298 BEQ 2# ;YES - GO TO NEXT TEST,
299 BREAKS,BEGIN ;TEMPORARY RETURN TO MONITOR....
300 BREAKS,BEGIN ;THEN CONTINUE AT NEXT INSTRUCTION,
301 000430 104407 000000 177436 10: MOV ASR,CSRA ;RECORD CSR'S ADDR
302 ;*****
303 000442 104405 000000 000000 HRDERS,BEGIN,NULL ;)PATTERN 044125 FAILED
304 ;*****
305 000450 005077 177552 20: CLR #ASR ;TRY CLEARING THE BITS
306 000454 017767 177546 177420 MOV #ASR,ACSR ;READ IT BACK,
307 000462 001414 BEQ LOG3 ;IF ZERO CSR GOOD,
308 000464 104407 000000 BREAKS,BEGIN ;TEMPORARY RETURN TO MONITOR....
309 000470 104407 000000 BREAKS,BEGIN ;THEN CONTINUE AT NEXT INSTRUCTION,
310 000474 005067 177404 30: CLR ASTAT ;EXPECT ZERO CSR,
311 000500 016767 177522 177372 MOV ASR,CSRA ;RECORD CSR'S ADDR,
312 ;*****
313 000506 104405 000000 000000 HRDERS,BEGIN,NULL ;)CSR FAILED TO CLEAR
314 ;*****

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315 ;*
316 ;*LOGIC TEST #3, MAKE SURE CLOCK CSR BITS
317 ;*13,5,3 AND 1 CAN BE SET + CLEARED
318 ;*
319
320 000514 012767 020052 177362 LOG3: MOV #020052,ASTAT ;GENERATE + RECORD PATTERN TO BE USED,
321 000522 016777 177356 177476 MOV ASTAT,#ASR ;SET THEM IN CSR OF CLOCK A,
322 000530 017767 177472 177344 MOV #ASR,ACSR ;READ THEM BACK
323 000536 026767 177342 177336 CMP ASTAT,ACSR ;DID THEY ALL SET?
324 000544 001412 BEQ 2# ;YES - GO TO NEXT TEST,
325 000546 104407 000000 BREAKS,BEGIN ;TEMPORARY RETURN TO MONITOR....
326 000552 104407 000000 BREAKS,BEGIN ;THEN CONTINUE AT NEXT INSTRUCTION,
327 000556 016767 177444 177314 10: MOV ASR,CSRA ;RECORD CSR'S ADDR,
328 ;*****
329 000564 104405 000000 000000 HRDERS,BEGIN,NULL ;)CSR PATTERN 020052 FAILED
330 ;*****
331 000572 005077 177430 20: CLR #ASR ;TRY CLEARING THE BITS
332 000576 017767 177424 177276 MOV #ASR,ACSR ;READ IT BACK,
333 000604 001414 BEQ LOG4 ;IF ZERO CSR GOOD,
334 000606 104407 000000 BREAKS,BEGIN ;TEMPORARY RETURN TO MONITOR....
335 000612 104407 000000 BREAKS,BEGIN ;THEN CONTINUE AT NEXT INSTRUCTION,
336 000616 005067 177262 30: CLR ASTAT ;EXPECT ZERO CSR,
337 000622 016767 177400 177250 MOV ASR,CSRA ;RECORD CSR'S ADDR,
338 ;*****
339 000630 104405 000000 000000 HRDERS,BEGIN,NULL ;)CSR FAILED TO CLEAR
340 ;*****
341 ;*
342 ;*LOGIC TEST #4, MAKE SURE CLOCK BUFFER REG
343 ;*PATTERN 125252 CAN BE SET + CLEARED.
344 ;*
345
346 000636 012767 125252 177240 LOG4: MOV #125252,ASTAT ;GENERATE + RECORD PATTERN TO BE USED,
347 000644 016777 177234 177356 MOV ASTAT,#ASR ;SET THEM IN BUFFER REG OF CLOCK,
348 000652 017767 177352 177222 MOV #ASR,ACSR ;READ THEM BACK
349 000660 026767 177220 177214 CMP ASTAT,ACSR ;DID THEY ALL SET?
350 000666 001412 BEQ 2# ;YES - GO TO NEXT TEST,
351 000670 104407 000000 BREAKS,BEGIN ;TEMPORARY RETURN TO MONITOR....
352 000674 104407 000000 BREAKS,BEGIN ;THEN CONTINUE AT NEXT INSTRUCTION,
353 000700 016767 177324 177172 10: MOV ASR,CSRA ;RECORD CSR'S ADDR,
354 ;*****
355 000706 104405 000000 000000 HRDERS,BEGIN,NULL ;)BUFFER REG PATTERN 125252 FAILED
356 ;*****
357 000714 005077 177310 20: CLR #ASR ;TRY CLEARING THE BITS
358 000720 017767 177304 177154 MOV #ASR,ACSR ;READ IT BACK,
359 000726 001414 BEQ LOG5 ;IF ZERO BUFFER GOOD,
360 000730 104407 000000 BREAKS,BEGIN ;TEMPORARY RETURN TO MONITOR....
361 000734 104407 000000 BREAKS,BEGIN ;THEN CONTINUE AT NEXT INSTRUCTION,
362 000740 005067 177140 30: CLR ASTAT ;EXPECT ZERO BUFFER,
363 000744 016767 177260 177126 MOV ASR,CSRA ;RECORD ADDR, OF BUFFER REG,
364 ;*****
365 000752 104405 000000 000000 HRDERS,BEGIN,NULL ;)BUFFER REG FAILED TO CLEAR
366 ;*****
367 ;*LOGIC TEST #5, MAKE SURE CLOCK BUFFER REG
368 ;*PATTERN 052525 CAN BE SET + CLEARED
369 000760 012767 052525 177116 LOG5: MOV #052525,ASTAT ;GENERATE + RECORD PATTERN TO BE USED,
370 000766 016777 177112 177234 MOV ASTAT,#ASR ;SET THEM IN BUFFER OF CLOCK A,

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371 000774 017767 177230 177100 MOV @ABR,ACSR ;READ THEM BACK
372 001002 026767 177076 177072 CMP ASTAT,ACSR ;DID THEY ALL SET?
373 001010 001412 BEQ Z8 ;YES - GO TO NEXT TEST.
374 001012 104407 000000 BREAK$,BEGIN ;TEMPORARY RETURN TO MONITOR....
375 001016 104407 000000 BREAK$,BEGIN ;THEN CONTINUE AT NEXT INSTRUCTION.
376 001022 016767 177202 177050 10: MOV ABR,CSRA ;RECORD BUFFER REG ADDR.
377 ;*****
378 001030 104405 000000 000000 HRDR$,BEGIN,NULL ;;BUFF REG PATTERN 052525 FAILED
379 ;*****
380 001036 005077 177166 20: CLR #ABR ;TRY CLEARING THE BITS
381 001042 017767 177162 177032 MOV @ABR,ACSR ;READ IT BACK.
382 001050 001414 BEQ RFSZ0 ;IF ZERO BUFFER GOOD.
383 001052 104407 000000 BREAK$,BEGIN ;TEMPORARY RETURN TO MONITOR....
384 001056 104407 000000 BREAK$,BEGIN ;THEN CONTINUE AT NEXT INSTRUCTION.
385 001062 005067 177016 30: CLR ASTAT ;EXPECT ZERO BUFFER.
386 001066 016767 177136 177004 MOV ABR,CSRA ;RECORD BUFFER REG A ADDR.
387 ;*****
388 001074 104405 000000 000000 HRDR$,BEGIN,NULL ;;BUFFER REG FAILED TO CLEAR
389 ;*****
390 001102 006367 177116 REST0: ASL TEMP ;CHECK FOR NEXT UNIT
391 001106 022767 000400 177110 CMP #BITS,TEMP ;TEST IF VALID UNIT
392 001114 001420 BEQ REST1 ;BR IF NOT
393 001116 036767 177102 176670 BIT TEMP,DVID1 ;TEST IF THIS UNIT IS SELECTED
394 001124 001414 BEQ REST1 ;BR IF NOT
395 001126 005077 177074 CLR #ASR ;ENSURE CLOCK IS STOPPED
396 001132 062767 000004 177066 ADD #4,ASR ;UPDATE STATUS ADDRESS
397 001140 062767 000010 177064 ADD #10,AVECT ;UPDATE VECTOR ADDRESS
398 001146 004767 177132 JSR PC,START2 ;FIX OTHER VALUES
399 001152 000167 177210 JMP LOG1 ;CONTINUE AT LOG1
400 001156 012767 000100 177056 REST1: MOV #BITS,RATEP ;FIRST PASS THRU LOOP, OFFSET=0, RATES WILL=1.
401 001164 012777 001574 177040 PEST2: MOV #INSRV,AVECT ;SET UP CLOCK A'S INTR. VECTOR.
402 001172 116777 176614 177034 REST3: MOV# BR1,AVECT2 ;SET PRIORITY ON CLOCK A'S INTR.
403 001200 106167 177036 LOOP: ROLB RATEP ;GET NEXT RATE.
404 001204 100005 BPL #8 ;IF NOT END THEN CONTINUE.
405 001206 005067 177032 CLR OFF ;CLEAR THE OFFSET
406 001212 012767 000001 177022 MOV #1,RATEP ;LOOK AT FIRST RATE
407 001220 062767 000002 177016 10: ADD #2,OFF
408 001226 022767 000016 177010 CMP #16,OFF ;TEST IF #16
409 001234 001761 BEQ LOOP ;BR
410 001236 036767 177000 176552 BIT RATEP,SRI ;IS THIS RATE INHIBITED?
411 001244 001355 BNE LOOP
412 001246 005067 177000 CLR ATFLG ;CLR FLAG INDICATING CLOCK A HAS INTERRUPTED.
413 001252 016701 176766 MOV OFF,R1 ;PICK UP OFFSET
414 001256 000171 001262 JMP @LISTP(R1) ;GO SET THE RATE + START THE CLOCK.

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415 ;THE FOLLOWING (LISTP) ARE POINTERS TO VARIOUS RATE
416 ;ROUTINES. THEY ARE USED BY "LOOP". "LOOP" GENERATES
417 ;AN OFFSET OF A RATE WE WISH TO EXERCISE. THE OFFSET
418 ;IS STORED IN R1. WE INDEX "LISTP" BY R1 (JMP @LISTP(R1))
419 ;TO GET THE ADDRESS OF THE RATE ROUTINE TO EXERCISE.
420 ;
421 ;
422 001262 000001 LISTP: .WORD 1 ;
423 001264 001340 .WORD RATE0 ;POINTER TO 1MHZ ROUTINE
424 001266 001362 .WORD RATE1 ;POINTER TO 100KHZ ROUTINE
425 001270 001404 .WORD RATE2 ;POINTER TO 10KHZ ROUTINE
426 001272 001426 .WORD RATE3 ;POINTER TO 1KHZ ROUTINE
427 001274 001450 .WORD RATE4 ;POINTER TO 100HZ ROUTINE
428 001276 001472 .WORD RATES ;POINTER TO RANDOM ROUTINE
429 001300 000001 .WORD 1 ;
430 ;
431 ;
432 ;THE FOLLOWING (RATEAL) ARE THE PRESET VALUES THAT THE
433 ;VARIOUS RATE ROUTINES NEED. THEY ARE LOADED INTO
434 ;CLOCK A'S PRESET BUFFER. "RATEAL" IS INDEXED BY
435 ;AN OFFSET IN R1 BY THE RATE ROUTINES TO GET THE
436 ;PRESET VALUE
437 ;
438 ;
439 001302 000001 RATEAL: .WORD 1 ;OFFSET ZERO, NO RATE.
440 001304 036260 .WORD -50000. ;VALUE FOR 1MHZ PRESET.
441 001306 036260 .WORD -50000. ;PRESET VALUE FOR 100 KHZ
442 001310 154360 .WORD -10000. ;PRESET VALUE FOR 10 KHZ
443 001312 176030 .WORD -1000. ;PRESET VALUE FOR 1 KHZ
444 001314 177634 .WORD -100. ;PRESET VALUE FOR 100 HZ
445 001316 000000 .WORD 0 ;PRESET VALUE FOR RANDOM
446 001320 000001 .WORD 1 ;
447 ;
448 ;
449 ;THE FOLLOWING (RSAL) IS USED BY THE RANDOM
450 ;RATE ROUTINE (RATES). THEY ARE THE VALUES NEEDED
451 ;TO BE PUT INTO THE CLOCK'S CSR FOR A PARTICULAR RATE.
452 ;
453 ;
454 001322 000000 RSAL: .WORD 0 ;OFFSET ZERO, NO RATE.
455 001324 000113 .WORD 113 ;11 MHZ, GO., MODE 1
456 001326 000123 .WORD 123 ;100 KHZ, GO., MODE 1
457 001330 000131 .WORD 131 ;10 KHZ, GO.
458 001332 000141 .WORD 141 ;1 KHZ, GO.
459 001334 000151 .WORD 151 ;100 HZ, GO.
460 001336 000000 .WORD 0 ;

```

```
461 ;*THIS ROUTINE PRESETS CLOCK A FOR
462 ;*1 MHZ RATE CLOCK A INTRN IN 1/20 SEC., 25 TIMES.
463 ;*
464 ;*
465
466 001340*          RATE0:
467 001340* 005077 176662 CLR  #ASR          ;CLEAR CLOCK A.
468 001344* 016177 001302* 176656 MOV  RATEAL(R1),#ASR ;PRESET COUNT IN CLOCK A.
469 001352* 016177 001322* 176646 MOV  RSAL(R1),#ASR  ;START CLOCK A.
470 ;NOW WAIT FOR INTERRUPT
471 001360* 104400 EXIT# ;EXIT TO MONITOR. MODULE WAIT FOR INTERRUPT.
472 ;*
473 ;*THIS ROUTINE PRESETS CLOCK A FOR
474 ;*100 KHZ RATE CLOCK A INTRN IN .5 SEC., TWICE.
475 ;*
476 ;*
477 ;*
478
479 001362*          RATE1:
480 001362* 005077 176640 CLR  #ASR          ;CLEAR CLOCK A.
481 001366* 016177 001302* 176634 MOV  RATEAL(R1),#ASR ;PRESET COUNT IN CLOCK A.
482 001374* 016177 001322* 176624 MOV  RSAL(R1),#ASR  ;START CLOCK A.
483 ;NOW WAIT FOR INTERRUPT
484 001402* 104400 EXIT# ;EXIT TO MONITOR. MODULE WAIT FOR INTERRUPT.
485 ;*
486 ;*THIS ROUTINE PRESETS CLOCK A FOR
487 ;*10 KHZ RATE CLOCK A INTRN IN 1.0 SEC.
488 ;*
489 ;*
490 ;*
491
492 001404*          RATE2:
493 001404* 005077 176616 CLR  #ASR          ;CLEAR CLOCK A.
494 001410* 016177 001302* 176612 MOV  RATEAL(R1),#ASR ;PRESET COUNT IN CLOCK A.
495 001416* 016177 001322* 176602 MOV  RSAL(R1),#ASR  ;START CLOCK A.
496 ;NOW WAIT FOR INTERRUPT
497 001424* 104400 EXIT# ;EXIT TO MONITOR. MODULE WAIT FOR INTERRUPT.
498 ;*
499 ;*THIS ROUTINE PRESETS CLOCK FOR
500 ;*1 KHZ RATE CLOCK A INTRN IN 1.0 SEC.
501 ;*
502 ;*
503 ;*
504
505 001426*          RATE3:
506 001426* 005077 176574 CLR  #ASR          ;CLEAR CLOCK A.
507 001432* 016177 001302* 176570 MOV  RATEAL(R1),#ASR ;PRESET COUNT IN CLOCK A.
508 001440* 016177 001322* 176560 MOV  RSAL(R1),#ASR  ;START CLOCK A.
509 ;NOW WAIT FOR INTERRUPT
510 001446* 104400 EXIT# ;EXIT TO MONITOR. MODULE WAIT FOR INTERRUPT.
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```
511 ;*THIS ROUTINE PRESETS CLOCK FOR
512 ;*100 HZ RATE CLOCK A INTRN IN 1.0 SEC.
513 ;*
514 ;*
515
516 001450*          RATE4:
517 001450* 005077 176552 CLR  #ASR          ;CLEAR CLOCK A.
518 001454* 016177 001302* 176546 MOV  RATEAL(R1),#ASR ;PRESET COUNT IN CLOCK A.
519 001462* 016177 001322* 176536 MOV  RSAL(R1),#ASR  ;START CLOCK A.
520 ;NOW WAIT FOR INTERRUPT
521 001470* 104400 EXIT# ;EXIT TO MONITOR. MODULE WAIT FOR INTERRUPT.
522 ;*THIS ROUTINE PRESETS CLOCK FOR
523 ;*RANDOM RATES
524 ;*
525
526 001472* 004767 000246 RATE5: JBR  PC,RANDOM  ;GET 2 RANDOM NUMBERS.
527
528 001476* 042767 177771 176542 BIC  #177771,RANA ;MAKE NUMBER < 10.
529 001504* 042767 177771 176536 BIC  #177771,RANB ;MAKE 2ND NUMBER < 10.
530 ;NUMBERS MUST BE 2, 4, OR 6
531
532
533 001512* 005767 176530 30: TST  RANA          ;IS NUMBER ZERO?
534 001516* 001003 BNE  40          ;NO - GO AHEAD.
535 001520* 062767 000002 176520 ADD  #2,RANA      ;MAKE IT NON-ZERO.
536 001526* 005767 176516 40: TST  RANB          ;IS NUMBER ZERO?
537 001532* 001003 BNE  50          ;NO GO AHEAD.
538 001534* 062767 000002 176506 ADD  #2,RANB      ;MAKE IT NON-ZERO.
539 001542*
540 001542* 005077 176460 50: CLR  #ASR          ;CLEAR CLOCK A
541 001546* 016701 176474 MOV  R1,OFF       ;RECORD THE OFFSET
542 001552* 010167 176466 MOV  RATEAL(R1),#ASR ;PRESET CLOCK A.
543 001556* 016177 001302* 176444 MOV  RSAL(R1),#ASR  ;START CLOCK A.
544 001564* 016177 001322* 176434
545
546 001572* 104400 EXIT# ;EXIT TO MONITOR. MODULE WAIT FOR INTERRUPT.
```



ICONT	000036R	203#																	
ICOUNT	000040R	204#																	
IDNUM	000122R	233#																	
INIT	000030R	200#																	
INSEPV	001574R	401	540#																
INTR	000120R	232#																	
LISTP	001262P	414	422#																
LOG1	000366P	262	203#	399															
LOG2	000372R	293#																	
LOG3	000514R	307	320#																
LOG4	000636R	333	346#																
LOG5	000760R	359	369#																
LOOP	001200R	403#	409	411															
MAP22#	104416	241#																	
MODNAM	000000R	187#																	
MODSP	000224R	201	239#																
MSG#	104403	241#																	
MSG#	104402	241#																	
MSG#	104401	241#																	
NULL	000000R	241#	303	313	329	339	355	365	378	388									
OFF	000244R	255#	405*	407*	400	413	542*	550	555										
OPEN	000000R	188	194	195	196	197	214	215	216	217	218	219	220	221					
		223	225	227	228	230	231	232	241#										
OTOA#	104420	241#																	
PASCNT	000034R	202#																	
PIRQ#	000004	241#	562																
POPSP	005726	241#																	
POPSP2	022626	241#																	
PRTY	000000	241#																	
PRTY0	000000	192	241#																
PRTY1	000040	241#																	
PRTY2	000100	241#																	
PRTY3	000140	241#																	
PRTY4	000200	191	241#																
PRTY5	000240	241#																	
PRTY6	000300	241#																	
PRTY7	000340	241#																	
PS	177776	241#																	
PSW	177776	241#																	
PUSH	005746	241#																	
PUSH2	024646	241#																	
RANA	000246R	256#	528*	533	535*	541	580	581*	582*	582*									
RAN#	000250R	257#	529*	536	538*	580*	582	583*											
RANDOM	001744R	526	580#																
RAND#	104417	241#																	
RANNUM	000054R	210#																	
RATEAL	001302R	439#	468	481	494	507	518	543											
RATEP	000242R	254#	400*	403*	406*	410													
RATE0	001340R	423	466#																
RATE1	001362R	424	479#																
RATE2	001404R	425	492#																
RATE3	001426R	426	505#																
RATE4	001450R	427	516#																
RATE5	001472R	428	526#																
RESTR	000254R	229	260#																
REST0	001102R	382	390#																

REST1	001156R	392	394	400#															
REST2	001164R	401#	401#	402#															
REST3	001172R	402#	403#	404#															
RES1	000056R	212#																	
RES2	000060R	213#																	
RESAL	001322R	454#	469	482	495	508	519	544											
RSTR	000112P	229#																	
SBADR	000102R	222#																	
SOPCNT	000042R	205#																	
SOFER#	104406	241#																	
SOPPAS	000046R	207#																	
SPOINT	000032R	201#																	
SPTSZ	000040	1#	234																
SR1	000016R	194#	410																
SR2	000020R	195#																	
SR3	000022R	196#																	
SR4	000024R	197#																	
START	000254R	200#	261#																
START0	000262R	261	264#																
START1	000270R	265#																	
START2	000304R	267#	398	575															
STAT	000026R	199#																	
SVR0	000062R	214#																	
SVR1	000064R	215#																	
SVR2	000066R	216#																	
SVR3	000068R	217#																	
SVR4	000070R	218#																	
SVR5	000072R	219#																	
SVR6	000074R	220#																	
SVR7	000076R	220#																	
SISCNT	000052P	209#																	
TEMP	000224R	241#	264*	390*	391	393	564*	565	571										
TRPDFD	000022	241#																	
VECTOR	000010R	190#	266																
WASADR	000104R	224#																	
WDFR	000116R	231#																	
WDTO	000114R	230#																	
XFLAG	000005R	188#																	

. ABS. 000000 000  
001772 001

ERRORS DETECTED: 0  
DEFAULT GLOBALS GENERATED: 0

PACK:XMNCA0,PACK:XMNCA0/SOL/CRF;SYN=DDXCOM,PACK:XMNCA0  
RUN-TIME: 3.57 SECONDS  
RUN-TIME RATIO: 43/10=4.1  
CORE USED: 7K (13 PAGES)

.REM\_

IDENTIFICATION  
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PRODUCT CODE: AC-F419A-MC  
PRODUCT NAME: CXMNBA0 MNC DI MODULE  
PRODUCT DATE: SEPTEMBER 1978  
MAINTAINER: DEC/X11 SUPPORT GROUP

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1.0 ABSTRACT

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THE MNB IS AN IOMOD THAT EXERCISES THE MNC DI DIGITAL INPUT. THE SOFTWARE MODULE CONSISTS OF THREE SECTIONS. THE FIRST IS THE DEFAULT CONDITION WHEN SR1 = 0. READ-WRITE TESTS ARE PERFORMED TO VERIFY THE INTERNAL DATA PATH'S OF THE MNC DI LOGIC. THE SECOND IS EXECUTED WHEN A MNC DO OUTPUT IS CONNECTED TO THE MNC DI INPUT. THIS PROVIDES ADDITIONAL TEST OF THE CONTROL SIGNALS. THE THIRD IS A WRAP-AROUND DATA TEST VERIFYING THE DATA OUTPUT AND INPUT CIRCUITS. UP TO 8 MNC DI'S CAN BE EXERCISED WITH THIS MODULE. IF SR1 = 1, BE SURE TO "DESELECT" THE MNE (DIGITAL OUT) MODULE.

2.0 REQUIREMENTS

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HARDWARE: ONE MNC DI (DIGITAL IN).  
          ONE MNC DO (DIGITAL OUT) <OPTIONAL>  
STORAGE: MNB REQUIRES:  
          DECIMAL WORDS: 1573  
          OCTAL WORDS: 3045  
          OCTAL BYTES: 6112

3.0 PASS DEFINITION

-----

WHEN SR1 = 0, ONE PASS OF THE MNB MODULE CONSISTS OF GENERATION  
4000 (8) INTERRUPTS.  
WHEN SR1 = 1, ONE PASS OF THE MNB MODULE CONSISTS OF GENERATING  
5000 (8) INTERRUPTS.

4.0 EXECUTION TIME

-----

WHEN SR1 = 0, ONE PASS OF THE MNB MODULE RUNNING ALONE TAKES  
APPROXIMATELY ONE MINUTE.  
WHEN SR1 = 1, ONE PASS OF THE MNB MODULE RUNNING ALONE TAKES  
APPROXIMATELY TWO MINUTES.

5.0 CONFIGURATION REQUIREMENTS

-----

DEFAULT PARAMETERS:

DEVADR: 171160, VECTOR 130, BR1: 4    DEVCNT: 1, SR1: 0

REQUIRED PARAMETERS:

NONE IF SR1 = 0            IF SR1 = 1 THEN:  
\$BASE1 MUST CONTAIN THE MNC DO BUS ADDRESS  
\$VECT1 MUST CONTAIN THE MNC DO INTERRUPT VECTOR AND BE  
SURE TO DESELECT THE "MNE" (DIGITAL OUT) MODULE.

6.0 DEVICE/OUTPUT SET-UP  
-----

THE FRONT PANEL SWITCHES MUST BE IN THE "-" POSITION,  
NO ADDITIONAL IF SR1 = 0 IF SR1 = 1 THEN:  
THE WRAP-AROUND CABLE MUST BE INSTALLED TO A MNCDO AND  
BE SURE TO DESELECT THE "MNE" (DIGITAL OUT) MODULE.

7.0 MODULE OPERATION  
-----

THE FOLLOWING TESTS ARE PERFORMED ON THE MNCDI (SR1=0)

FLOAT A 1 ACROSS THE STIMULUS BIT REGISTER  
FLOAT A 0 ACROSS THE STIMULUS BIT REGISTER  
BYTE OPERATION OF THE STIMULUS BIT REGISTER  
READ-WRITE TESTS OF BITS 1 - 6,8,9,12 AND 14 IN THE STATUS REGISTER  
BYTE OPERATION OF THE STATUS REGISTER  
MAINT. STROBE SETS INPUT READY FLAG  
INPUT READY CAN BE WRITTEN TO A ZERO  
INPUT READY WILL NOT SET IF NO "SBR MATCH"  
OVERRUN FLAG SETS  
OVERRUN FLAG CAN BE WRITTEN TO A ZERO  
INVERT DATA FUNCTIONS CORRECTLY  
EACH BIT OF THE INPUT REGISTER CAN BE CLEARED  
INPUT READY FLAG INTERRUPT TEST  
OVERRUN FLAG INTERRUPT TEST

8.0 OPERATION OPTIONS  
-----

SR1 = 0            RUN MNCDI LOGIC TEST  
  
SR1 = 1            RUN MNCDI LOGIC TEST  
                  RUN MNCDO LOGIC TEST  
                  RUN MNCDO TO MNCDI WRAPAROUND CONTROL TEST  
                  RUN MNCDO TO MNCDI WRAPAROUND DATA TEST

IF DEVCNT (DVID1) CONTAINS MORE THAN A 1, MULTIPLE MNCDI  
WILL BE TESTED. IF DEVCNT (DVID1) CONTAINS MORE THAN 1 AND SR1=1,  
THERE MUST AT LEAST THE SAME NUMBER OF MNCDO'S CONNECTED TO MNCDI'S.  
THE FIRST ADDRESS MNCDO UNIT MUST BE CONNECTED TO THE FIRST  
ADDRESS MNCDI WITH ADDITIONAL UNITS ALSO PAIRED TOGETHER.

9.0 NON-STANDARD PRINTOUTS  
-----

ALL PRINTOUTS HAVE THE STANDARD FORMATS DESCRIBED IN THE DEC/X11  
DOCUMENT.



```

155          .LIST ME
156          .NLIST MC,CND,MD
157          .TITLE MNBA DEC/X11 SYSTEM EXERCISER MODULE
158          ; 'DXCOM' VERSION 6 23-MAY-78
159          .LIST RJN
160          ;*****
161          BEGIN:
162          MODNAM: .ASCII /MNBA / ;MODULE NAME.
163          XFLAG: .BYTE OPEN ;USED TO KEEP TRACK OF WBUFF USAGE
164          ADDR: 171160+0 ;1ST DEVICE ADDR.
165          VECTOR: 130+0 ;1ST DEVICE VECTOR.
166          BR1: .BYTE PRTY4+0 ;1ST BR LEVEL.
167          BR2: .BYTE PRTY0+0 ;2ND BR LEVEL.
168          DVID1: 0+1 ;DEVICE INDICATOR 1.
169          SR1: OPEN ;SWITCH REGISTER 1
170          SR2: OPEN ;SWITCH REGISTER 2
171          SR3: OPEN ;SWITCH REGISTER 3
172          SR4: OPEN ;SWITCH REGISTER 4
173          ;*****
174          STAT: 140000 ;STATUS WORD.
175          INIT: START ;MODULE START ADDR.
176          SPOINT: MODSP ;MODULE STACK POINTER.
177          PASCNT: 0 ;PASS COUNTER.
178          ICNT: 2000 ;# OF ITERATIONS PER PASS=2000
179          ICOUNT: 0 ;LOC TO COUNT ITERATIONS
180          SOFCNT: 0 ;LOC TO SAVE TOTAL SOFT ERRORS
181          HRDCNT: 0 ;LOC TO SAVE TOTAL HARD ERRORS
182          SOFPAS: 0 ;LOC TO SAVE SOFT ERRORS PER PASS
183          HRDPAS: 0 ;LOC TO SAVE HARD ERRORS PER PASS
184          SYSCNT: 0 ;# OF SYS ERRORS ACCUMULATED
185          PANNUM: 0 ;HOLDS RANDOM # WHEN RAND MACRO IS CALLED
186          CONFIG:
187          RES1: 0 ;RESERVED FOR MONITOR USE
188          RES2: 0 ;RESERVED FOR MONITOR USE
189          SVR0: OPEN ;LOC TO SAVE R0.
190          SVR1: OPEN ;LOC TO SAVE R1.
191          SVR2: OPEN ;LOC TO SAVE R2.
192          SVR3: OPEN ;LOC TO SAVE R3.
193          SVR4: OPEN ;LOC TO SAVE R4.
194          SVR5: OPEN ;LOC TO SAVE R5.
195          SVR6: OPEN ;LOC TO SAVE R6.
196          CSRA: OPEN ;ADDR OF CURRENT CSR.
197          SBADR: ;ADDR OF GOOD DATA, OR
198          ACSR: OPEN ;CONTENTS OF CSR.
199          WASADR: ;ADDR OF BAD DATA, OR
200          ASTAT: OPEN ;STATUS REG CONTENTS.
201          ERRTP: ;TYPE OF ERROR
202          ASB: OPEN ;EXPECTED DATA.
203          AWAS: OPEN ;ACTUAL DATA.
204          RSTRT: RESTRT ;RESTART ADDRESS AFTER END OF PASS
205          WDTO: OPEN ;WORDS TO MEMORY PER ITERATION
206          WDFR: OPEN ;WORDS FROM MEMORY PER ITERATION
207          INTR: OPEN ;# OF INTERRUPTS PER ITERATION
208          IDNUM: 0 ;MODULE IDENTIFICATION NUMBER=0

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209          MODSP:
210          ;*****
211          ;BASE: 171260 ;INITIAL BUS ADDRESS OF THE MNCDO <IF SR1=1>
212          $VECT: 340 ;INITIAL INTERRUPT VECTOR OF THE MNCDO <IF SR1=1>
213          ;*****
214          ;OUTPUT ADDRESS
215          OCSR: 0
216          OCSR1: 0 ;HIGH BYTE ADDRESS
217          ;*****
218          ;INPUT ADDRESS
219          DOR: 0
220          DOR1: 0 ;HIGH BYTE ADDRESS
221          ;*****
222          ICSR: 0
223          ICSR1: 0 ;HIGH BYTE ADDRESS
224          DIR: 0
225          DIR1: 0
226          SBR: 0
227          SBR1: 0 ;HIGH BYTE ADDRESS
228          ;*****
229          DODINV: 0
230          DODINS: 0
231          ;*****
232          DIDINV: 0
233          DIDINS: 0
234          DIEINV: 0
235          DIEINS: 0
236          SRMINE: 0 ;MY COPY OF SR1
237          TEMP: 0
238          TEMP1: 0
239          TEMP2: 0
240          BIT0
241          BITDAT=BIT12 ;MAINT INPUT INHIBIT
242          BITEXT=BIT11 ;MAINT INPUT STROBE

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243 000300* 016767 177512 177762 START: MOV SRI,SRMINE ;COPY SRI FOR MY USE
244 ;INITIALIZE THE BUS ADDRESSES AND VECTORS
245 000306* 012767 000001 177762 RESTART: MOV #BIT0,TEMP2 ;LOAD UNIT SELECT FLAG
246 000314* 012700 000230* MOV #OCSR,R0 ;LOAD ADDRESS POINTER
247 000320* 016701 177700 MOV #BASE1,R1 ;LOAD INITIAL BUS ADDRESS
248 000324* 010120 181: MOV R1,(R0)+ ;LOAD DEVICE ADDRESS
249 000326* 005201 ;UPDATE BUS ADDRESS VALUE
250 000330* 020027 000240* CMP R0,#ICSR ;TEST IF DONE WITH BUS ADDRESSES
251 000334* 001373 RNE 10 ;BR IF NOT
252 000336* 016701 177444 MOV ADDR,R1 ;LOAD INPUT ADDRESS
253 000342* 010120 281: MOV R1,(R0)+ ;LOAD THE ADDRESS
254 000344* 005201 INC R1 ;UPDATE ADDRESS
255 000346* 020027 000254* CMP R0,#DODINV ;TEST IF AT END
256 000352* 001373 BNE 20 ;BR IF NOT
257 000354* 016701 177646 MOV $VECT1,R1 ;LOAD VECTOR POINTER
258 000360* 010120 381: MOV R1,(R0)+ ;LOAD DEVICE VECTOR ADDRESS
259 000362* 005721 TST (R1)+ ;UPDATE BUS VECTOR VALUE
260 000364* 020027 000260* CMP R0,#DODINS+2 ;TEST IF DONE WITH BUS VECTORS
261 000370* 001373 BNE 30 ;BR IF NOT
262 000372* 016701 177412 MOV VFCTOR,R1 ;LOAD INPUT VECTOR
263 000376* 010120 461: MOV R1,(R0)+ ;LOAD THE VECTOR
264 000400* 005721 TST (R1)+ ;BUMP THE ADDRESS
265 000402* 020027 000270* CMP R0,#DIEINS+2 ;TEST IF DONE
266 000406* 001373 RNE 40 ;BR IF NOT
267
268 ;TEST IF UNIT IS TO RUN
269 000410* 036767 177662 177376 CONT1: BIT TEMP2,DVID1 ;IS UNIT SELECTED ?
270 000416* 001040 BNE INPUT ;BR IF SELECTED
271 ;UNIT IS NOT SELECTED -- CORRECT THE ADDRESSES
272 000420* 012700 000230* CONT2: MOV #OCSR,R0 ;GET ADDRESS
273 000424* 012701 000004 MOV #4,R1 ;GET NEW OFFSET VALUE
274 000430* 000110 ADD R1,(R0) ;UPDATE THE VALUE
275 000432* 000110 ADD R1,(R0) ;UPDATE THE VALUE
276 000434* 000110 ADD R1,(R0) ;UPDATE THE VALUE
277 000436* 000110 ADD R1,(R0) ;UPDATE THE VALUE
278 000440* 012701 000010 MOV #10,R1 ;RELOAD NEW OFFSET
279 000444* 000110 ADD R1,(R0) ;UPDATE THE VALUE
280 000446* 000110 ADD R1,(R0) ;UPDATE THE VALUE
281 000450* 000110 ADD R1,(R0) ;UPDATE THE VALUE
282 000452* 000110 ADD R1,(R0) ;UPDATE THE VALUE
283 000454* 000110 ADD R1,(R0) ;UPDATE THE VALUE
284 000456* 000110 ADD R1,(R0) ;UPDATE THE VALUE
285 000460* 000110 ADD R1,(R0) ;UPDATE THE VALUE
286 000462* 000110 ADD R1,(R0) ;UPDATE THE VALUE
287 000464* 000110 ADD R1,(R0) ;UPDATE THE VALUE
288 000466* 000110 ADD R1,(R0) ;UPDATE THE VALUE
289 000470* 000110 ADD R1,(R0) ;UPDATE THE VALUE
290 000472* 000110 ADD R1,(R0) ;UPDATE THE VALUE
291 ;DETERMINE IF NEXT UNIT IS TO BE TESTED
292 000474* 006367 177576 ASL TEMP2 ;CHANGE UNITS
293 000500* 022767 000400 177570 CMP #BITS,TEMP2 ;TEST IF LAST UNIT
294 000506* 001340 BNE CONT1 ;BR IF NOT
295 000510* 104413 000000* ENDDITS,BEGIN ;SIGNAL END OF ITERATION.
296 ;MONITOR SHALL TEST END OF PASS
297 000514* 000167 177566 JMP RESTART

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298 ;VERIFY A MNCDI BUS ADDRESS RESPONSE
299 000520* 016767 177514 177352 INPUT: MOV ICSR,CSRA ;LOAD ADDR.
300 000526* 005777 177506 TST #ICSR ;TEST INPUT STATUS
301 000532* 005777 177506 TST #DIR ;TEST INPUT DATA REGISTER
302 000536* 005777 177506 TST #SBR ;TEST STIM. BUFFER REGISTER
303 000542*
304 DI1: ;FLOAT A 1 ACROSS THE MNCDI STIMULUS BIT REGISTER
305 000542* 012767 000001 177332 MOV #BIT0,ACSR ;LOAD EXPECT BIT
306 000550* 016777 177326 177472 181: MOV ACSR,#SBR ;LOAD MNCDI STIMULUS BIT REGISTER
307 000556* 017767 177466 177320 MOV #SBR,ASTAT ;READ MNCDI STIMULUS BIT REGISTER
308 000564* 026767 177312 177312 CMP ACSR,ASTAT ;COMPARE
309 000572* 001403 BEQ 20 ;BR IF SAME
310 ;*****
311 000574* 104405 000000* 000000 HRDERS,BEGIN,NULL ;MNCDI STIMULUS BIT REGISTER FAILED TO HOLD A FLOATING
312 ;*****
313 000602*
314 000602* 104407 000000* 281: BREAKS,BEGIN ;TEMPORARY RETURN TO MONITOR....
315 000606* 104407 000000* BREAKS,BEGIN ;THEN CONTINUE AT NEXT INSTRUCTION.
316 000612* 006367 177264 ASL ACSR ;CHANGE THE DATA
317 000616* 001354 BNE 10 ;BR IF MORE DATA
318 000620*
319 DI2: ;FLOAT A 0 ACROSS THE MNCDI STIMULUS BIT REGISTER
320 000620* 012767 000001 177444 MOV #BIT0,TEMP ;LOAD INITIAL BIT
321 000626* 016767 177440 177246 181: MOV TEMP,ACSR ;LOAD EXPECTED
322 000634* 005167 177242 COM ACSR ;COMPLEMENT
323 000640* 016777 177236 177402 MOV ACSR,#SBR ;LOAD MNCDI STIMULUS BIT REGISTER
324 000646* 017767 177376 177230 MOV #SBR,ASTAT ;READ MNCDI STIMULUS BIT REGISTER
325 000654* 026767 177222 177222 CMP ACSR,ASTAT ;COMPARE
326 000662* 001403 BEQ 20 ;BR IF SAME
327 ;*****
328 000664* 104405 000000* 000000 HRDERS,BEGIN,NULL ;MNCDI STIMULUS BIT REGISTER FAILED TO HOLD A FLOATING 0
329 ;*****
330 000672*
331 000672* 104407 000000* 281: BREAKS,BEGIN ;TEMPORARY RETURN TO MONITOR....
332 000676* 104407 000000* BREAKS,BEGIN ;THEN CONTINUE AT NEXT INSTRUCTION.
333 000702* 006367 177364 ASL TEMP ;CHANGE THE DATA
334 000706* 001347 BNE 10 ;BR IF MORE DATA
335 000710*
336 DI3: ;VERIFY BYTE OPERATION ON THE MNCDI STIMULUS BIT REGISTER
337 000710* 012777 177777 177332 181: MOV #-1,#SBR ;LOAD MNCDI STIMULUS BIT REGISTER
338 000716* 012767 000377 177156 MOV #377,ACSR ;LOAD EXPECTED
339 000724* 105077 177322 CLRB #SBR1 ;CLEAR HIGH BYTE
340 000730* 017767 177314 177146 MOV #SBR,ASTAT ;READ MNCDI STIMULUS BIT REGISTER
341 000736* 026767 177140 177140 CMP ACSR,ASTAT ;COMPARE
342 000744* 001403 BEQ 20 ;BR IF SAME
343 ;*****
344 000746* 104405 000000* 000000 HRDERS,BEGIN,NULL ;CLEARING HIGH BYTE CHANGED LOW BYTE
345 ;*****
346 000754* 012777 177777 177266 281: MOV #-1,#SBR ;LOAD MNCDI STIMULUS BIT REGISTER
347 000762* 012767 177400 177112 MOV #177400,ACSR ;LOAD EXPECTED
348 000770* 105077 177254 CLRB #SBR ;CLEAR LOW BYTE
349 000774* 017767 177250 177102 MOV #SBR,ASTAT ;READ MNCDI STIMULUS BIT REGISTER
350 001002* 026767 177074 177074 CMP ACSR,ASTAT ;COMPARE
351 001010* 001403 BEQ 30 ;BR IF SAME
352 ;*****
353 001012* 104405 000000* 000000 HRDERS,BEGIN,NULL ;CLEARING LOW BYTE CHANGED HIGH BYTE

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354 ;*****
355 001020* 381 BREAKS,BEGIN ;TEMPORARY RETURN TO MONITOR,...
356 001020* 104407 000000* BREAKS,BEGIN ;THEN CONTINUE AT NEXT INSTRUCTION.
357 001024* 104407 000000*
358 001030* DI4:
359 ;TEST THAT BIT1 OF MNCDI STATUS REGISTER IS READ-WRITE
360 001030* 012767 000002 177044 MOV #BIT1,ACSR ;LOAD EXPECTED
361 001036* 016777 177040 177174 MOV ACSR,ICSR ;LOAD BIT1 INTO MNCDI STATUS REGISTER
362 001044* 017767 177170 177032 MOV #ICSR,ASTAT ;READ MNCDI STATUS REGISTER
363 001052* 042767 000200 177024 BIC #BIT7,ASTAT ;CLEAR BIT 7
364 001060* 026767 177016 177016 CMP ACSR,ASTAT ;TEST THAT IT SET
365 001066* 001403 BEQ 18 ;BR IF SAME
366 ;*****
367 001070* 104405 000000* 000000 HRDRS,BEGIN,NULL ;BIT1 OF MNCDI STATUS REGISTER FAILED TO SET
368 ;*****
369 001076* 046777 177000 177134 181 BIC ACSR,ICSR ;CLEAR THAT BIT
370 001104* 017767 177130 176772 MOV #ICSR,ASTAT ;READ MNCDI STATUS REGISTER AGAIN
371 001112* 026767 176764 176764 CMP ACSR,ASTAT ;TEST THE BIT
372 001120* 001003 BNE 28 ;BR IF CLEARED
373 ;*****
374 001122* 104405 000000* 000000 HRDRS,BEGIN,NULL ;BIT1 OF MNCDI STATUS REGISTER FAILED TO CLEAR
375 ;*****
376 001130* 281
377 001130* 104407 000000* BREAKS,BEGIN ;TEMPORARY RETURN TO MONITOR,...
378 001134* 104407 000000* BREAKS,BEGIN ;THEN CONTINUE AT NEXT INSTRUCTION.
379 001140* DI5:
380 ;TEST THAT BIT2 OF MNCDI STATUS REGISTER IS READ-WRITE
381 001140* 012767 000004 176734 MOV #BIT2,ACSR ;LOAD EXPECTED
382 001146* 016777 176730 177064 MOV ACSR,ICSR ;LOAD BIT2 INTO MNCDI STATUS REGISTER
383 001154* 017767 177060 176722 MOV #ICSR,ASTAT ;READ MNCDI STATUS REGISTER
384 001162* 042767 000200 176714 BIC #BIT7,ASTAT ;CLEAR BIT 7
385 001170* 026767 176706 176706 CMP ACSR,ASTAT ;TEST THAT IT SET
386 001176* 001403 BEQ 18 ;BR IF SAME
387 ;*****
388 001200* 104405 000000* 000000 HRDRS,BEGIN,NULL ;BIT2 OF MNCDI STATUS REGISTER FAILED TO SET
389 ;*****
390 001206* 046777 176670 177024 181 BIC ACSR,ICSR ;CLEAR THAT BIT
391 001214* 017767 177020 176662 MOV #ICSR,ASTAT ;READ MNCDI STATUS REGISTER AGAIN
392 001222* 026767 176654 176654 CMP ACSR,ASTAT ;TEST THE BIT
393 001230* 001003 BNE 28 ;BR IF CLEARED
394 ;*****
395 001232* 104405 000000* 000000 HRDRS,BEGIN,NULL ;BIT2 OF MNCDI STATUS REGISTER FAILED TO CLEAR
396 ;*****
397 001240* 281
398 001240* 104407 000000* BREAKS,BEGIN ;TEMPORARY RETURN TO MONITOR,...
399 001244* 104407 000000* BREAKS,BEGIN ;THEN CONTINUE AT NEXT INSTRUCTION.
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400 001250* DI6:
401 ;TEST THAT BIT3 OF MNCDI STATUS REGISTER IS READ-WRITE
402 001250* 012767 000010 176624 MOV #BIT3,ACSR ;LOAD EXPECTED
403 001256* 016777 176620 176754 MOV ACSR,ICSR ;LOAD BIT3 INTO MNCDI STATUS REGISTER
404 001264* 017767 176750 176612 MOV #ICSR,ASTAT ;READ MNCDI STATUS REGISTER
405 001272* 042767 000200 176604 BIC #BIT7,ASTAT ;CLEAR BIT 7
406 001300* 026767 176576 176576 CMP ACSR,ASTAT ;TEST THAT IT SET
407 001306* 001403 BEQ 18 ;BR IF SAME
408 ;*****
409 001310* 104405 000000* 000000 HRDRS,BEGIN,NULL ;BIT3 OF MNCDI STATUS REGISTER FAILED TO SET
410 ;*****
411 001316* 046777 176560 176714 181 BIC ACSR,ICSR ;CLEAR THAT BIT
412 001324* 017767 176710 176552 MOV #ICSR,ASTAT ;READ MNCDI STATUS REGISTER AGAIN
413 001332* 026767 176544 176544 CMP ACSR,ASTAT ;TEST THE BIT
414 001340* 001003 BNE 28 ;BR IF CLEARED
415 ;*****
416 001342* 104405 000000* 000000 HRDRS,BEGIN,NULL ;BIT3 OF MNCDI STATUS REGISTER FAILED TO CLEAR
417 ;*****
418 001350* 281
419 001350* 104407 000000* BREAKS,BEGIN ;TEMPORARY RETURN TO MONITOR,...
420 001354* 104407 000000* BREAKS,BEGIN ;THEN CONTINUE AT NEXT INSTRUCTION.
421 001360* DI7:
422 ;TEST THAT BIT4 OF MNCDI STATUS REGISTER IS READ-WRITE
423 001360* 012767 000020 176514 MOV #BIT4,ACSR ;LOAD EXPECTED
424 001366* 016777 176510 176644 MOV ACSR,ICSR ;LOAD BIT4 INTO MNCDI STATUS REGISTER
425 001374* 017767 176640 176502 MOV #ICSR,ASTAT ;READ MNCDI STATUS REGISTER
426 001402* 042767 000200 176474 BIC #BIT7,ASTAT ;CLEAR BIT 7
427 001410* 026767 176466 176466 CMP ACSR,ASTAT ;TEST THAT IT SET
428 001416* 001403 BEQ 18 ;BR IF SAME
429 ;*****
430 001420* 104405 000000* 000000 HRDRS,BEGIN,NULL ;BIT4 OF MNCDI STATUS REGISTER FAILED TO SET
431 ;*****
432 001426* 046777 176450 176604 181 BIC ACSR,ICSR ;CLEAR THAT BIT
433 001434* 017767 176600 176442 MOV #ICSR,ASTAT ;READ MNCDI STATUS REGISTER AGAIN
434 001442* 026767 176434 176434 CMP ACSR,ASTAT ;TEST THE BIT
435 001450* 001003 BNE 28 ;BR IF CLEARED
436 ;*****
437 001452* 104405 000000* 000000 HRDRS,BEGIN,NULL ;BIT4 OF MNCDI STATUS REGISTER FAILED TO CLEAR
438 ;*****
439 001460* 281
440 001460* 104407 000000* BREAKS,BEGIN ;TEMPORARY RETURN TO MONITOR,...
441 001464* 104407 000000* BREAKS,BEGIN ;THEN CONTINUE AT NEXT INSTRUCTION.
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442 001470* DI10: ;TEST THAT BITS OF MNCDI STATUS REGISTER IS READ-WRITE
443 ;*****
444 001470* 012767 000040 176404 MOV #BIT5,ACSR ;LOAD EXPECTED
445 001476* 016777 176400 176534 MOV ACSR,@ICSR ;LOAD BIT5 INTO MNCDI STATUS REGISTER
446 001504* 017767 176530 176372 MOV @ICSR,ASTAT ;READ MNCDI STATUS REGISTER
447 001512* 042767 000200 176364 BIC #BIT7,ASTAT ;CLEAR BIT 7
448 001520* 026767 176356 176356 CMP ACSR,ASTAT ;TEST THAT IT SET
449 001526* 001403 BEQ 18 ;BR IF SAME
450 ;*****
451 001530* 104405 000000* 000000 HRDERS,BEGIN,NULL ;BITS OF MNCDI STATUS REGISTER FAILED TO SET
452 ;*****
453 001536* 046777 176340 176474 181 BIC ACSR,@ICSR ;CLEAR THAT BIT
454 001544* 017767 176470 176332 MOV @ICSR,ASTAT ;READ MNCDI STATUS REGISTER AGAIN
455 001552* 026767 176324 176324 CMP ACSR,ASTAT ;TEST THE BIT
456 001560* 001003 BNE 28 ;BR IF CLEARED
457 ;*****
458 001562* 104405 000000* 000000 HRDERS,REGIN,NULL ;BITS OF MNCDI STATUS REGISTER FAILED TO CLEAR
459 ;*****
460 001570* 281 BREAKS,BEGIN ;TEMPORARY RETURN TO MONITOR....
461 001570* 104407 000000* BREAKS,BEGIN ;THEN CONTINUE AT NEXT INSTRUCTION.
462 001574* 104407 000000*
463 001600* DI11:
464 ;TEST THAT BIT6 OF MNCDI STATUS REGISTER IS READ-WRITE
465 001600* 012767 000100 176274 MOV #BIT6,ACSR ;LOAD EXPECTED
466 001606* 016777 176270 176424 MOV ACSR,@ICSR ;LOAD BIT6 INTO MNCDI STATUS REGISTER
467 001614* 017767 176420 176262 MOV @ICSR,ASTAT ;READ MNCDI STATUS REGISTER
468 001622* 042767 000200 176254 BIC #BIT7,ASTAT ;CLEAR BIT 7
469 001630* 026767 176246 176246 CMP ACSR,ASTAT ;TEST THAT IT SET
470 001636* 001403 BEQ 18 ;BR IF SAME
471 ;*****
472 001640* 104405 000000* 000000 HRDERS,BEGIN,NULL ;BIT6 OF MNCDI STATUS REGISTER FAILED TO SET
473 ;*****
474 001646* 046777 176230 176364 181 BIC ACSR,@ICSR ;CLEAR THAT BIT
475 001654* 017767 176360 176222 MOV @ICSR,ASTAT ;READ MNCDI STATUS REGISTER AGAIN
476 001662* 026767 176214 176214 CMP ACSR,ASTAT ;TEST THE BIT
477 001670* 001003 BNE 28 ;BR IF CLEARED
478 ;*****
479 001672* 104405 000000* 000000 HRDERS,REGIN,NULL ;BIT6 OF MNCDI STATUS REGISTER FAILED TO CLEAR
480 ;*****
481 001700* 281 BREAKS,BEGIN ;TEMPORARY RETURN TO MONITOR....
482 001700* 104407 000000* BREAKS,BEGIN ;THEN CONTINUE AT NEXT INSTRUCTION.
483 001704* 104407 000000*
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484 001710* DI12: ;TEST THAT BITS OF MNCDI STATUS REGISTER IS READ-WRITE
485 ;*****
486 001710* 012767 000400 176164 MOV #BIT8,ACSR ;LOAD EXPECTED
487 001716* 016777 176160 176314 MOV ACSR,@ICSR ;LOAD BIT8 INTO MNCDI STATUS REGISTER
488 001724* 017767 176310 176152 MOV @ICSR,ASTAT ;READ MNCDI STATUS REGISTER
489 001732* 042767 000200 176144 BIC #BIT7,ASTAT ;CLEAR BIT 7
490 001740* 026767 176136 176136 CMP ACSR,ASTAT ;TEST THAT IT SET
491 001746* 001403 BEQ 18 ;BR IF SAME
492 ;*****
493 001750* 104405 000000* 000000 HRDERS,BEGIN,NULL ;BITS OF MNCDI STATUS REGISTER FAILED TO SET
494 ;*****
495 001756* 046777 176120 176254 181 BIC ACSR,@ICSR ;CLEAR THAT BIT
496 001764* 017767 176250 176112 MOV @ICSR,ASTAT ;READ MNCDI STATUS REGISTER AGAIN
497 001772* 026767 176104 176104 CMP ACSR,ASTAT ;TEST THE BIT
498 002000* 001003 BNE 28 ;BR IF CLEARED
499 ;*****
500 002002* 104405 000000* 000000 HRDERS,BEGIN,NULL ;BITS OF MNCDI STATUS REGISTER FAILED TO CLEAR
501 ;*****
502 002010* 281 BREAKS,BEGIN ;TEMPORARY RETURN TO MONITOR....
503 002010* 104407 000000* BREAKS,BEGIN ;THEN CONTINUE AT NEXT INSTRUCTION.
504 002014* 104407 000000*
505 002020* DI13:
506 ;TEST THAT BIT9 OF MNCDI STATUS REGISTER IS READ-WRITE
507 002020* 012767 001000 176054 MOV #BIT9,ACSR ;LOAD EXPECTED
508 002026* 016777 176050 176204 MOV ACSR,@ICSR ;LOAD BIT9 INTO MNCDI STATUS REGISTER
509 002034* 017767 176200 176042 MOV @ICSR,ASTAT ;READ MNCDI STATUS REGISTER
510 002042* 042767 000200 176034 BIC #BIT7,ASTAT ;CLEAR BIT 7
511 002050* 026767 176026 176026 CMP ACSR,ASTAT ;TEST THAT IT SET
512 002056* 001403 BEQ 18 ;BR IF SAME
513 ;*****
514 002060* 104405 000000* 000000 HRDERS,BEGIN,NULL ;BIT9 OF MNCDI STATUS REGISTER FAILED TO SET
515 ;*****
516 002066* 046777 176010 176144 181 BIC ACSR,@ICSR ;CLEAR THAT BIT
517 002074* 017767 176140 176002 MOV @ICSR,ASTAT ;READ MNCDI STATUS REGISTER AGAIN
518 002102* 026767 175774 175774 CMP ACSR,ASTAT ;TEST THE BIT
519 002110* 001003 BNE 28 ;BR IF CLEARED
520 ;*****
521 002112* 104405 000000* 000000 HRDERS,BEGIN,NULL ;BIT9 OF MNCDI STATUS REGISTER FAILED TO CLEAR
522 ;*****
523 002120* 281 BREAKS,BEGIN ;TEMPORARY RETURN TO MONITOR....
524 002120* 104407 000000* BREAKS,BEGIN ;THEN CONTINUE AT NEXT INSTRUCTION.
525 002124* 104407 000000*
```

```
526 002130* 012767 010000 175744 DI14: ;TEST THAT BIT12 OF MNCDI STATUS REGISTER IS READ-WRITE
527 MOV #BIT12,ACSR ;LOAD EXPECTED
528 002130* 012767 010000 175744 ;LOAD EXPECTED
529 002136* 016777 175740 176074 MOV ACSR,#ICSR ;LOAD BIT12 INTO MNCDI STATUS REGISTER
530 002144* 017767 176070 175732 MOV #ICSR,ASTAT ;READ MNCDI STATUS REGISTER
531 002152* 042767 000200 175724 JIC #BIT7,ASTAT ;CLEAR BIT 7
532 002160* 026767 175716 175716 CMP ACSR,ASTAT ;TEST THAT IT SET
533 002166* 001403 BEQ 18 ;BR IF SAME
534 ;*****
535 002170* 104405 000000* 000000 HRDR$,BEGIN,NULL ;BIT12 OF MNCDI STATUS REGISTER FAILED TO SET
536 ;*****
537 002176* 046777 175700 176034 18: BIC ACSR,#ICSR ;CLEAR THAT BIT
538 002204* 017767 176030 175672 MOV #ICSR,ASTAT ;READ MNCDI STATUS REGISTER AGAIN
539 002212* 026767 175664 175664 CMP ACSR,ASTAT ;TEST THE BIT
540 002220* 001003 BNE 28 ;BR IF CLEARED
541 ;*****
542 002222* 104405 000000* 000000 HRDR$,BEGIN,NULL ;BIT12 OF MNCDI STATUS REGISTER FAILED TO CLEAR
543 ;*****
544 002230* 28:
545 002230* 104407 000000* BREAK$,BEGIN ;TEMPORARY RETURN TO MONITOR...
546 002234* 104407 000000* BREAK$,BEGIN ;THEN CONTINUE AT NEXT INSTRUCTION.
547 002240*
548 DI15:
549 002240* 012767 040000 175634 ;TEST THAT BIT14 OF MNCDI STATUS REGISTER IS READ-WRITE
550 002246* 016777 175630 175764 MOV #BIT14,ACSR ;LOAD EXPECTED
551 002254* 017767 175760 175622 MOV ACSR,#ICSR ;LOAD BIT14 INTO MNCDI STATUS REGISTER
552 002262* 042767 000200 175614 MOV #ICSR,ASTAT ;READ MNCDI STATUS REGISTER
553 002270* 026767 175606 175606 BIC #BIT7,ASTAT ;CLEAR BIT 7
554 002276* 001403 CMP ACSR,ASTAT ;TEST THAT IT SET
555 BEQ 18 ;BR IF SAME
556 ;*****
557 002300* 104405 000000* 000000 HRDR$,BEGIN,NULL ;BIT14 OF MNCDI STATUS REGISTER FAILED TO SET
558 ;*****
559 002306* 046777 175570 175724 18: BIC ACSR,#ICSR ;CLEAR THAT BIT
560 002314* 017767 175720 175562 MOV #ICSR,ASTAT ;READ MNCDI STATUS REGISTER AGAIN
561 002322* 026767 175554 175554 CMP ACSR,ASTAT ;TEST THE BIT
562 RNE 28 ;BR IF CLEARED
563 ;*****
564 002332* 104405 000000* 000000 HRDR$,BEGIN,NULL ;BIT14 OF MNCDI STATUS REGISTER FAILED TO CLEAR
565 ;*****
566 002340* 104407 000000* 28: BREAK$,BEGIN ;TEMPORARY RETURN TO MONITOR...
567 002344* 104407 000000* BREAK$,BEGIN ;THEN CONTINUE AT NEXT INSTRUCTION.
```

```
568 ;VERIFY HIGH BYTE OPERATION ON THE INPUT STATUS REGISTER
569 002350* 012777 040424 175662 DI16: MOV #40424,#ICSR ;LOAD INPUT REG. BIT
570 002356* 105077 175660 CLR# #ICSR1 ;CLEAR HIGH BYTE
571 002362* 012767 000024 175512 MOV #BIT4|BIT2,ACSR ;LOAD EXPECTED
572 002370* 017767 175644 175506 MOV #ICSR,ASTAT ;READ INPUT STATUS REG.
573 002376* 042767 000200 175500 BIC #BIT7,ASTAT ;REMOVE BIT 7
574 002404* 026767 175472 175472 CMP ACSR,ASTAT ;COMPARE
575 002412* 001403 BEQ DI17
576 ;*****
577 002414* 104405 000000* 000000 HRDR$,BEGIN,NULL ;CLEARING HIGH BYTE CHANGED LOW BYTE
578 ;*****
579
580 ;VERIFY LOW BYTE OPERATION ON THE INPUT STATUS REGISTER
581 002422* 012777 040426 175610 DI17: MOV #40426,#ICSP ;LOAD INPUT REG.
582 002430* 105077 175604 CLR# #ICSR ;CLEAR LOW BYTE
583 002434* 012767 040400 175440 MOV #40400,ACSR ;LOAD EXPECTED
584 002442* 017767 175572 175434 MOV #ICSR,ASTAT ;READ INPUT STATUS REG.
585 002450* 026767 175426 175426 CMP ACSR,ASTAT ;COMPARE
586 002456* 001403 BEQ DI20 ;BR IF SAME
587 ;*****
588 002460* 104405 000000* 000000 HRDR$,BEGIN,NULL ;CLEARING LOW BYTE CHANGED HIGH BYTE
589 ;*****
590
591 ;VERIFY THAT MAINT. STROBE SETS "INPUT DATA READY"
592 002466* 005077 175546 DI20: CLR #ICSR ;ENSURE CLEAR FLAG
593 002472* 012767 000200 175402 MOV #BIT7,ACSR ;LOAD EXPECTED DATA
594 002500* 012777 004200 175532 MOV #RITEXT|BIT7,#ICSR ;GENERATE MAINT. STROBE
595 002506* 017767 175526 175370 MOV #ICSR,ASTAT ;READ INPUT STATUS REGISTER
596 002514* 026767 175362 175362 CMP ACSR,ASTAT ;COMPARE RESULTS
597 002522* 001403 BEQ DI21 ;BR IF SAME
598 ;*****
599 002524* 104405 000000* 000000 HRDR$,BEGIN,NULL ;MAINT. STROBE FAILED TO SET "INPUT DATA READY"
600 ;*****
```

```
601 ;VERIFY THAT "INPUT DATA READY" CAN BE WRITTEN TO A ZERO
602 CLR ACSR ;LOAD EXPECTED DATA
603 MOV #PITEXT,@ICSR ;GENERATE MAINT. STROBE
604 CLR @ICSR ;CLEAR DATA READY FLAG
605 MOV @ICSR,ASTAT ;READ INPUT STATUS REGISTER
606 CMP ACSR,ASTAT ;COMPARE
607 BEQ D122 ;BR IF SAME
608 ;*****
609 HRDERS,BEGIN,NULL ;"INPUT DATA READY" FAILED TO BE WRITTEN TO A ZERO
610 ;*****
611
612 ;VERIFY THAT "INPUT DATA READY" WILL NOT SET IF IN STIMULUS MODE AND NO SBR MATC
613 CLR @SBR ;CLEAR SBR REGISTER
614 MOV #-1,@DIR ;CLEAR INPUT REGISTER
615 MOV #RIT2,@ICSR ;SET STILILUS MODE
616 BIS #RITEXT,@ICSR ;GENERATE MAINT. STROBE
617 MOV #RIT2,ACSR ;LOAD EXPECTED
618 MOV @ICSR,ASTAT ;READ STATUS
619 CMP ACSR,ASTAT ;COMPARE
620 BEQ 18 ;BR IF SAME
621 ;*****
622 HRDERS,BEGIN,NULL ;INPUT STROBE SET INPUT READY WHEN IN STIMULUS MODE
623 ;*****
624
625 BREAKS,BEGIN ;TEMPORARY RETURN TO MONITOR....
626 BREAKS,BEGIN ;THEN CONTINUE AT NEXT INSTRUCTION.
627 ;VERIFY THAT "OVERRUN ERROR" SETS
628 MOV #RIT15BIT7BIT1,ACSR ;LOAD EXPECTED
629 MOV #RIT1,@ICSR ;SET STROBE MODE
630 BIS #RITEXTBIT7,@ICSR ;GENERATE MAINT. STROBE
631 BIS #RIT15BITEXTBIT7,@ICSR ;GENERATE MAINT. STROBE AGAIN
632 MOV @ICSR,ASTAT ;READ INPUT STATUS REGISTER
633 CMP ACSR,ASTAT ;COMPARE
634 BEQ D124 ;BR IF SAME
635 ;*****
636 HRDERS,BEGIN,NULL ;"OVER RUN" FAILED TO SET
637 ;*****
638
639 ;VERIFY THAT "OVERRUN ERROR" CAN BE WRITTEN TO A ZERO
640 MOV #RIT7BIT1,ACSR ;LOAD EXPECTED VALUE
641 MOV #BIT1,@ICSR ;SET STROBE MODE
642 BIS #RITEXTBIT7,@ICSR ;GENERATE MAINT. STROBE
643 BIS #RIT15BITEXTBIT7,@ICSR ;GENERATE MAINT. STROBE AGAIN
644 CLRB @ICSR1 ;CLEAR HIGH BYTE OF THE INPUT STATUS REGISTER
645 MOV @ICSR,ASTAT ;READ INPUT STATUS REGISTER
646 CMP ACSR,ASTAT ;COMPARE
647 BEQ D125 ;BR IF SAME
648 ;*****
649 HRDERS,BEGIN,NULL ;"OVERRUN ERROR" FAILED TO BE WRITTEN TO A ZERO
650 ;*****
651
```

```
652 ;VERIFY INVERT DATA FUNCTION
653 CLR ACSR ;LOAD EXPECTED
654 MOV #RITDAT,@ICSR ;SET INPUT INHIBIT
655 MOV @DIR,ASTAT ;READ INPUT
656 CMP ACSR,ASTAT ;COMPARE
657 BEQ 18 ;BR IF SAME
658 ;*****
659 HRDERS,BEGIN,NULL ;INPUT INHIBIT FAILED TO INHIBIT INPUT
660 ;*****
661 MOV #RITDATIBIT5BIT4,@ICSR ;SET INVERT DATA AND INPUT INHIBIT
662 MOV #-1,ACSR ;LOAD EXPECTED
663 MOV @DIR,ASTAT ;READ INPUT
664 BNE 28 ;BR IF NON-ZERO
665 ;*****
666 HRDERS,BEGIN,NULL ;INVERT DATA FUNCTION FAILED
667 ;*****
668 CMP ACSR,ASTAT ;COMPARE DATA
669 BEQ 38 ;BR IF SAME
670 ;*****
671 HRDERS,BEGIN,NULL ;INVERT DATA - DATA PATH ERROR
672 ;*****
673
674 MOV #BITDAT,@ICSR ;SET INPUT INHIBIT
675 CLR ACSR ;CLEAR EXPECTED
676 MOV @DIR,ASTAT ;READ INPUT
677 CMP ACSR,ASTAT ;COMPARE
678 BEQ D126 ;BR IF SAME
679 ;*****
680 HRDERS,BEGIN,NULL ;INVERT DATA FUNCTION OR INPUT INHIBIT FAILED
681 ;*****
682
683 ;VERIFY EACH BIT OF THE MNCDI INPUT DATA REGISTER CAN BE CLEARED
684 MOV #RIT0,TEMP ;LOAD INITIAL BIT
685 MOV #RITDATIBIT4IBIT5,@ICSR ;LOAD INHIBIT INPUT AND INVERT DATA
686 MOV TEMP,ACSR ;LOAD EXPECTED
687 COM ACSR ;MAKE OPPOSITE
688 MOV @DIR,R0 ;READ INPUT
689 MOV TEMP,@DIR ;CLEAR THE INPUT BIT
690 BIC #RIT5,@ICSR ;REM INVERT DATA BITOVE
691 BIS #RIT1,@ICSR ;ENABLE EXT. STROBE TO PREVENT DATA INPUT BEING
692 MOV @DIR,ASTAT ;READ INPUT REG.
693 CMP ACSR,ASTAT ;COMPARE
694 BEQ 28 ;BR IF SAME
695 ;*****
696 HRDERS,BEGIN,NULL ;INPUT REGISTER BIT FAILED TO CLEAR
697 ;*****
698
699 BREAKS,BEGIN ;TEMPORARY RETURN TO MONITOR....
700 BREAKS,BEGIN ;THEN CONTINUE AT NEXT INSTRUCTION.
701 ASL TEMP ;SHIFT THE DATA
702 BNE 18 ;TRY MORE BITS
```

```

703
704
705 003302* 012777 010062 174730 DI27: ;VERIFY THAT A 2ND STROBE PULSE WILL NOT CHANGE THE DIR DATA
706 003310* 052777 004200 174722 MOV #BIT12BIT5BIT4BIT1,ACSR ;DISABLE INPUTS, ENABLE INVERT DATA, EXT
707 003316* 042777 000000 174714 BIS #RITEXTIBIT7,ACSR ;GENERATE MAINT, STROBE
708 003324* 052777 004200 174706 RIC #BITS,ACSR ;REMOVE INVERT DATA
709 003332* 012767 177777 174542 FIS #RITEXTIBIT7,ACSR ;SET MAINT, STROBE AGAIN
710 003340* 017767 174700 174536 MOV #-1,ACSR ;LOAD EXPECTED DATA
711 003346* 026767 174530 174530 MOV #DIR,ASTAT ;READ REGISTER
712 003354* 001403 CMP ACSR,ASTAT ;COMPARE
713 BEQ DI30 ;BR IF SAME
714 003356* 104405 000000* 000000 ;*****
715 HRDRS,BEGIN,NULL ;DATA READY FAILED TO INHIBIT 2ND
716 ;*****
717 ;INTERRUPT TEST -- VERIFY MNCDI INTERRUPTS VIA DATA READY VECTOR
718 003364* 012777 003430* 174666 DI30: MOV #10,DDIDINV ;LOAD RETURN VECTOR
719 003372* 116777 174414 174662 MOVB BR1,DDIDINS ;LOAD RETURN STATUS
720 003400* 012777 000102 174632 MOV #BIT6BIT1,ACSR ;SET STROBE MODE
721 003406* 052777 004200 174624 BIS #RITEXTIBIT7,ACSR ;GENERATE MAINT. STROBE
722 003414* 000240 NOP
723 003416* 000240 NOP
724 003420* 000240 NOP
725 003422* 000240 NOP
726 003424* 104400 000000* EXIT0,BEGIN ;EXIT TO MONITOR, MODULE WAIT FOR INTERRUPT.
727
728 003430* 005077 174604 18: CLR #ICSR
729 ;-----
730 003434* 000004 000000* 003442* PIR0,BEGIN,28 ; QUEUE UP TO CONTINUE AT 28 AND RTI
731 ;-----
732 003442* 012777 000262* 174610 28: MOV #DIDINS,DDIDINV
733 003450* 005077 174606 CLR #DIDINS
734
735
736 ;INTERRUPT TEST -- VERIFY MNCDI INTERRUPTS VIA OVERRUN ERROR
737 003454* 012777 003524* 174602 DI31: MOV #10,DDIEINV ;LOAD RETURN VECTOR
738 003462* 116777 174324 174576 MOVB BR1,DDIEINS ;LOAD RETURN STATUS
739 003470* 012777 040002 174542 MOV #IT14IBIT1,ACSR ;ENABLE INTR. AND STROBE MODE
740 003476* 052777 104200 174534 BIS #IT15IBITEXTIBIT7,ACSR ;GENERATE MAINT. STROBE
741 003504* 052777 104200 174526 BIS #IT15IBITEXTIBIT7,ACSR ;GENERATE MAINT. STROBE AGAIN TO SET OVE
742 003512* 000240 NOP
743 003514* 000240 NOP
744 003516* 000240 NOP
745 003520* 104400 000000* EXIT0,BEGIN ;EXIT TO MONITOR, MODULE WAIT FOR INTERRUPT.
746 003524* 005077 174510 18: CLR #ICSR
747 ;-----
748 003530* 000004 000000* 003536* PIR0,BEGIN,28 ; QUEUE UP TO CONTINUE AT 28 AND RTI
749 ;-----
750 003536* 012777 000266* 174520 28: MOV #DIEINS,DDIEINV
751 003544* 005077 174516 CLR #DIEINS
752 003550* 036767 174522 174512 RIT TEMP2,SRMINE ;TEST IF INPUT ONLY
753 003556* 001002 BNE D00 ;BR IF WRAP-AROUND MODE
754 003560* 000167 174634 JMP CONT2 ;NO -- TRY NEXT UNIT

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755 ;VERIFY CORRECT MNCDO ADDRESS RESPONSE
756 003564* 016767 174440 174306 D00: MOV #0CSR,CSRA ;LOAD BUS ADDRESS
757 003572* 005777 174432 TST #0CSR ;TEST OUTPUT STATUS REGISTER
758 003576* 005777 174432 TST #DOR ;TEST OUTPUT DATA REGISTER
759 003602*
760 ;FLOAT A 1 ACROSS THE MNCDO DATA REGISTER
761 003602* 012767 000001 174272 MOV #RIT0,ACSR ;LOAD EXPECT BIT
762 003610* 016777 174266 174416 18: MOV #DOR,ASTAT ;LOAD MNCDO DATA REGISTER
763 003616* 017767 174412 174260 MOV #DOR,ASTAT ;READ MNCDO DATA REGISTER
764 003624* 026767 174252 174252 CMP ACSR,ASTAT ;COMPARE
765 003632* 001403 BEQ 28 ;BR IF SAME
766 ;*****
767 003634* 104405 000000* 000000 HRDRS,BEGIN,NULL ;MNCDO DATA REGISTER FAILED TO HOLD A FLOATING 1
768 ;*****
769 003642* 28: BREAK0,BEGIN ;TEMPORARY RETURN TO MONITOR....
770 003642* 104407 000000* BREAK0,BEGIN ;THEN CONTINUE AT NEXT INSTRUCTION.
771 003646* 104407 000000* ASL ACSR ;CHANGE THE DATA
772 003652* 006367 174224 BNE 18 ;BR IF MORE DATA
773 003656* 001354
774 003660*
775 D02: ;FLOAT A 0 ACROSS THE MNCDO DATA REGISTER
776 003660* 012767 000001 174404 MOV #RIT0,TEMP ;LOAD INITIAL BIT
777 003666* 016767 174400 174206 18: MOV #TEMP,ACSR ;LOAD EXPECTED
778 003674* 005167 174202 COM ACSR ;COMPLEMENT
779 003700* 016777 174176 174326 MOV #DOR,ASTAT ;LOAD MNCDO DATA REGISTER
780 003706* 017767 174322 174170 MOV #DOR,ASTAT ;READ MNCDO DATA REGISTER
781 003714* 026767 174162 174162 CMP ACSR,ASTAT ;COMPARE
782 003722* 001403 BEQ 28 ;BR IF SAME
783 ;*****
784 003724* 104405 000000* 000000 HRDRS,BEGIN,NULL ;MNCDO DATA REGISTER FAILED TO HOLD A FLOATING 0
785 ;*****
786 003732* 28: BREAK0,BEGIN ;TEMPORARY RETURN TO MONITOR....
787 003732* 104407 000000* BREAK0,BEGIN ;THEN CONTINUE AT NEXT INSTRUCTION.
788 003736* 104407 000000* ASL TEMP ;CHANGE THE DATA
789 003742* 006367 174324 BNE 18 ;BR IF MORE DATA
790 003746* 001347
791 003750*
792 D03: ;VERIFY BYTE OPERATION ON THE MNCDO DATA REGISTER
793 003750* 012777 177777 174256 18: MOV #-1,#DOR ;LOAD MNCDO DATA REGISTER
794 003756* 012767 000377 174116 MOV #377,ACSR ;LOAD EXPECTED
795 003764* 105077 174246 CLR0 #DOR1 ;CLEAR HIGH BYTE
796 003770* 017767 174240 174106 MOV #DOR,ASTAT ;READ MNCDO DATA REGISTER
797 003776* 026767 174100 174100 CMP ACSR,ASTAT ;COMPARE
798 004004* 001403 BEQ 28 ;BR IF SAME
799 ;*****
800 004006* 104405 000000* 000000 HRDRS,BEGIN,NULL ;CLEARING HIGH BYTE CHANGED LOW BYTE
801 ;*****
802 004014* 012777 177777 174212 28: MOV #-1,#DOR ;LOAD MNCDO DATA REGISTER
803 004022* 012767 177400 174052 MOV #177400,ACSR ;LOAD EXPECTED
804 004030* 105077 174200 CLR0 #DOR ;CLEAR LOW BYTE
805 004034* 017767 174174 174042 MOV #DOR,ASTAT ;READ MNCDO DATA REGISTER
806 004042* 026767 174034 174034 CMP ACSR,ASTAT ;COMPARE
807 004050* 001403 BEQ 38 ;BR IF SAME
808 ;*****
809 004052* 104405 000000* 000000 HRDRS,BEGIN,NULL ;CLEARING LOW BYTE CHANGED HIGH BYTE
810 ;*****

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011 004060*
012 004060* 104407 000000*
013 004064* 104407 000000*
014 004070*
015
016 004070* 012767 000100 174004
017 004076* 016777 174000 174124
018 004104* 017767 174120 173772
019 004112* 042767 000200 173764
020 004120* 026767 173756 173756
021 004126* 001403
022
023 004130* 104405 000000* 000000
024
025 004136* 046777 173740 174064
026 004144* 017767 174060 173732
027 004152* 026767 173724 173724
028 004160* 001003
029
030 004162* 104405 000000* 000000
031
032 004170*
033 004170* 104407 000000*
034 004174* 104407 000000*
035 004200*
036
037 004200* 012767 000020 173674
038 004206* 016777 173670 174014
039 004214* 017767 174010 173662
040 004222* 042767 000200 173654
041 004230* 026767 173646 173646
042 004236* 001403
043
044 004240* 104405 000000* 000000
045
046 004246* 046777 173630 173754
047 004254* 017767 173750 173622
048 004262* 026767 173614 173614
049 004270* 001003
050
051 004272* 104405 000000* 000000
052
053 004300*
054 004300* 104407 000000*
055 004304* 104407 000000*

301
BREAKS,BEGIN ;TEMPORARY RETURN TO MONITOR,...
BREAKS,BEGIN ;THEN CONTINUE AT NEXT INSTRUCTION.

D041
;TEST THAT BIT6 OF MNCDO STATUS REGISTER IS READ-WRITE
MOV #BIT6,ACSR ;LOAD EXPECTED
MOV ACSR,#OCSR ;LOAD BIT6 INTO MNCDO STATUS REGISTER
MOV #OCSR,ASTAT ;READ MNCDO STATUS REGISTER
BIC #BIT7,ASTAT ;CLEAR BIT 7
CMP ACSR,ASTAT ;TEST THAT IT SET
BEQ 16 ;BR IF SAME
;*****
HRDRS,BEGIN,NULL ;BIT6 OF MNCDO STATUS REGISTER FAILED TO SET
;*****
181
BIC ACSR,#OCSR ;CLEAR THAT BIT
MOV #OCSR,ASTAT ;READ MNCDO STATUS REGISTER AGAIN
CMP ACSR,ASTAT ;TEST THE BIT
BNE 28 ;BR IF CLEARED
;*****
HRDRS,BEGIN,NULL ;BIT6 OF MNCDO STATUS REGISTER FAILED TO CLEAR
;*****

281
BREAKS,BEGIN ;TEMPORARY RETURN TO MONITOR,...
BREAKS,BEGIN ;THEN CONTINUE AT NEXT INSTRUCTION.

D051
;TEST THAT BIT4 OF MNCDO STATUS REGISTER IS READ-WRITE
MOV #BIT4,ACSR ;LOAD EXPECTED
MOV ACSR,#OCSR ;LOAD BIT4 INTO MNCDO STATUS REGISTER
MOV #OCSR,ASTAT ;READ MNCDO STATUS REGISTER
BIC #BIT7,ASTAT ;CLEAR BIT 7
CMP ACSR,ASTAT ;TEST THAT IT SET
BEQ 16 ;BR IF SAME
;*****
HRDRS,BEGIN,NULL ;BIT4 OF MNCDO STATUS REGISTER FAILED TO SET
;*****
181
BIC ACSR,#OCSR ;CLEAR THAT BIT
MOV #OCSR,ASTAT ;READ MNCDO STATUS REGISTER AGAIN
CMP ACSR,ASTAT ;TEST THE BIT
BNE 28 ;BR IF CLEARED
;*****
HRDRS,BEGIN,NULL ;BIT4 OF MNCDO STATUS REGISTER FAILED TO CLEAR
;*****

281
BREAKS,BEGIN ;TEMPORARY RETURN TO MONITOR,...
BREAKS,BEGIN ;THEN CONTINUE AT NEXT INSTRUCTION.

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056 004310*
057
058 004310* 012767 000010 173564
059 004316* 016777 173560 173704
060 004324* 017767 173700 173552
061 004332* 042767 000200 173544
062 004340* 026767 173536 173536
063 004346* 001403
064
065 004350* 104405 000000* 000000
066
067 004356* 046777 173520 173644
068 004364* 017767 173640 173512
069 004372* 026767 173504 173504
070 004400* 001003
071
072 004402* 104405 000000* 000000
073
074 004410*
075 004410* 104407 000000*
076 004414* 104407 000000*
077
078 004420* 005077 173604
079 004424* 012767 000200 173450
080 004432* 105077 173576
081 004436* 112777 000001 173566
082 004444* 017767 173560 173432
083 004452* 026767 173424 173424
084 004460* 001403
085
086 004462* 104405 000000* 000000
087
088
089 004470* 105077 173540
090 004474* 112777 000001 173530
091 004502* 005067 173374
092 004506* 005077 173516
093 004512* 017767 173512 173364
094 004520* 001403
095
096 004522* 104405 000000* 000000
097
098
099 004530* 105077 173500
090 004534* 112777 000001 173470
091 004542* 005067 173334
092 004546* 005077 173462
093 004552* 017767 173452 173324
094 004560* 001403
095
096 004562* 104405 000000* 000000
097

D061
;TEST THAT BIT3 OF MNCDO STATUS REGISTER IS READ-WRITE
MOV #BIT3,ACSR ;LOAD EXPECTED
MOV ACSR,#OCSR ;LOAD BIT3 INTO MNCDO STATUS REGISTER
MOV #OCSR,ASTAT ;READ MNCDO STATUS REGISTER
BIC #BIT7,ASTAT ;CLEAR BIT 7
CMP ACSR,ASTAT ;TEST THAT IT SET
BEQ 16 ;BR IF SAME
;*****
HRDRS,BEGIN,NULL ;BIT3 OF MNCDO STATUS REGISTER FAILED TO SET
;*****
181
BIC ACSR,#OCSR ;CLEAR THAT BIT
MOV #OCSR,ASTAT ;READ MNCDO STATUS REGISTER AGAIN
CMP ACSR,ASTAT ;TEST THE BIT
BNE 28 ;BR IF CLEARED
;*****
HRDRS,BEGIN,NULL ;BIT3 OF MNCDO STATUS REGISTER FAILED TO CLEAR
;*****

281
BREAKS,BEGIN ;TEMPORARY RETURN TO MONITOR,...
BREAKS,BEGIN ;THEN CONTINUE AT NEXT INSTRUCTION.
;VERIFY THAT MNCDO DONE FLAG SETS
D071
CLR #OCSR ;CLEAR CLEARED FLAG
MOV #BIT7,ACSR ;LOAD EXPECTED
CLRB #DOR ;ENABLE
MOV #BIT0,#OCSR1 ;GENERATE MAINT. REPLY
MOV #OCSR,ASTAT ;READ OUTPUT STATUS REGISTER
CMP ACSR,ASTAT ;COMPARE
BEQ D010 ;BR IF SAME
;*****
HRDRS,BEGIN,NULL ;OUTPUT DONE FLAG FAILED TO SET
;*****
;VERIFY THAT MNCDO DONE FLAG CLEARS WHEN WRITTEN TO A 0
D0101
CLRB #DOR ;ENABLE
MOV #BIT0,#OCSR1 ;GENERATE MAINT. REPLY
CLR ACSR ;CLEAR EXPECTED
CLR #OCSR ;CLEAR OUTPUT DONE FLAG
MOV #OCSR,ASTAT ;READ STATUS
BEQ D011 ;BR IF SAME
;*****
HRDRS,BEGIN,NULL ;CLEARING THE OUTPUT FLAG FAILED TO CLEAR OUTPUT DONE FL
;*****
;VERIFY THAT MNCDO DONE FLAG CLEARS WHEN OUTPUT DATA REGISTER IS WRITTEN
D0111
CLRB #DOR ;ENABLE
MOV #BIT0,#OCSR1 ;GENERATE MAINT. REPLY
CLR ACSR ;CLEAR EXPECTED
CLR #DOR ;WRITE THE OUTPUT DATA REGISTER
MOV #OCSR,ASTAT ;READ OUTPUT STATUS REGISTER
BEQ D012 ;BR IF CLEARED
;*****
HRDRS,BEGIN,NULL ;OUTPUT DONE FLAG FAILED TO CLEAR WHEN THE DOR REGISTER
;*****

```



```
908 ;INTERRUPT TEST -- VERIFY MNCDO DOES INTERRUPT
909 004570' 012777 004636' 173456 DO12: MOV #19,#DODINV ;LOAD RETURN VECTOR
910 004576' 116777 173210 173452 MOVB BR1,#DODINS ;LOAD RETURN LEVEL
911 004604' 105077 173424 CLRB #DOR ;ENABLE
912 004610' 112777 000001 173414 MOVB #BIT0,#OCSR1 ;GENERATE MAINT. REPLY
913 004616' 052777 000100 173404 FIS #BIT6,#OCSR ;ENABLE INTR.
914 004624' 000240 ;OP
915 004626' 000240 ;OP
916 004630' 000240 ;OP
917 004632' 104400 000000' EXIT0,BEGIN ;EXIT TO MONITOR, MODULE WAIT FOR INTERRUPT.
918
919 004636' 005077 173366 181 CLR #OCSR
920 ;-----
921 004642' 000004 000000' 004650' PIR0,BEGIN,30 ; QUEUE UP TO CONTINUE AT 30 AND RTI
922 ;-----
923 004650' 016777 173402 173376 381 MOV DODINS,#DODINV ;RESET VECTORS
924 004656' 005077 173374 CLR #DODINS
925 004662' 005077 173342 CLR #OCSR
```

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926 ;WRAP AROUND TESTS
927
928 ;VERIFY MODE 00 -- INPUT STROBE WILL SET THE INPUT DATA READY FLAG
929 004666' 012777 177777 173350 DI32: MOV #-1,#DIR ;CLEAR INPUT REG.
930 004674' 005077 173350 CLR #SBR ;CLEAR STIM. REG.
931 004700' 005077 173334 CLR #ICSR ;CLEAR INPUT DATA READY FLAG
932 004704' 005077 173320 CLR #OCSR ;CLEAR OUTPUT STATUS
933 004710' 012777 025252 173316 MOV #25252,#DOR ;WRITE TO THE OUTPUT DATA REG.
934 004716' 012767 000200 173156 MOV #BIT7,#ACSR ;LOAD EXPECTED
935 004724' 017767 173310 173152 MOV #ICSR,#ASTAT ;READ STATUS
936 004732' 026767 173144 173144 CMP #ACSR,#ASTAT ;COMPARE
937 004740' 001406 BEQ D133 ;BR IF SAME
938
939 004742' 104405 000000' 000000 HRDERS,BEGIN,NULL ;MODE 00 -- EXT. STROBE FAILED TO SET INPUT DATA READY
940 ;-----
941 004750' 104403 000000' 006030' MSGN0,BEGIN,MSG1 ;ASCII MESSAGE CALL WITH COMMON HEADER
942
943 ;VERIFY MODE 01 -- INPUT STROBE WILL SET THE INPUT DATA READY FLAG
944 004756' 012777 177777 173260 DI33: MOV #-1,#DIR ;CLEAR INPUT REG.
945 004764' 005077 173260 CLR #SBR ;CLEAR STIM. REG.
946 004770' 012777 000002 173242 MOV #BIT1,#ICSR ;CLEAR INPUT DATA READY FLAG
947 004776' 005077 173226 CLR #OCSR ;CLEAR OUTPUT STATUS
948 005002' 012777 052525 173224 MOV #52525,#DOR ;WRITE TO THE OUTPUT DATA REG.
949 005010' 012767 000202 173064 MOV #BIT7|BIT1,#ACSR ;LOAD EXPECTED
950 005016' 017767 173216 173060 MOV #ICSR,#ASTAT ;READ STATUS
951 005024' 026767 173052 173052 CMP #ACSR,#ASTAT ;COMPARE
952 005032' 001403 BEQ D134 ;BR IF SAME
953
954 005034' 104405 000000' 000000 HRDERS,BEGIN,NULL ;MODE 01 -- EXT. STROBE FAILED TO SET INPUT DATA READY
955 ;-----
956
957 ;VERIFY MODE 10 -- INPUT STROBE WILL NOT SET INPUT DATA READY FLAG
958 005042' 012777 177777 173174 DI34: MOV #-1,#DIR ;CLEAR INPUT REG.
959 005050' 005077 173174 CLR #SBR ;CLEAR STIM. REG.
960 005054' 012777 000004 173156 MOV #BIT2,#ICSR ;CLEAR INPUT DATA READY FLAG
961 005062' 005077 173142 CLR #OCSR ;CLEAR OUTPUT STATUS
962 005066' 012777 070707 173140 MOV #70707,#DOR ;WRITE TO THE OUTPUT DATA REG.
963 005074' 012767 000004 173000 MOV #BIT2,#ACSR ;LOAD EXPECTED
964 005102' 017767 173132 172774 MOV #ICSR,#ASTAT ;READ STATUS
965 005110' 026767 172766 172766 CMP #ACSR,#ASTAT ;COMPARE
966 005116' 001403 BEQ D135 ;BR IF SAME
967
968 005120' 104405 000000' 000000 HRDERS,BEGIN,NULL ;MODE 10 -- EXT. STROBE SET INPUT DATA READY FLAG IN ER
969 ;-----
```

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970 ;VERIFY INPUT REPLY SETS OUTPUT DONE FLAG
971 005126* 005077 173102 DI35: CLR #DOR ;CLEAR OUTPUT DATA
972 005132* 012767 000200 172742 MOV #RIT7,ACSR ;LOAD EXPECTED
973 005140* 005077 173064 CLR #OCSR ;CLEAR OUTPUT DONE FLAG
974 005144* 012777 004200 173066 MOV #RITEXT|BIT7,#ICSR ;SET INPUT READY FLAG
975 005152* 005077 173062 CLR #ICSR ;CLEAR INPUT READY FLAG<GEN, INPUT REPLY>
976 005156* 017767 173046 172720 MOV #OCSR,ASTAT ;READ OUTPUT STATUS
977 005164* 026767 172712 172712 CMP ACSR,ASTAT ;COMPARE
978 005172* 001403 BEQ DI36 ;BR IF SAME
979 ;*****
980 005174* 104405 000000* 000000 HRDERS,BEGIN,NULL ;INPUT REPLY FAILED TO SET OUTPUT DONE FLAG
981 ;*****
982 ;VERIFY THE MNCDO - WRAPAROUND - MNCDI DATA PATH
983 005202* 012777 177777 173034 DI36: MOV #-1,#DIR ;CLEAR INPUT REG.
984 005210* 005077 173014 CLR #OCSR ;CLEAR OUTPUT STATUS
985 005214* 005077 173020 CLR #ICSR ;CLEAR INPUT STATUS
986 005220* 012767 000001 173044 MOV #RIT0,TEMP ;LOAD EXPECTED
987 005226* 016767 173040 172646 18: MOV TEMP,ACSR ;LOAD TYPEOUT EXPECTED
988 005234* 016777 172642 172772 MOV ACSR,#DOR ;LOAD OUTPUT DATA REG.
989 005242* 017767 172776 172634 MOV #DIR,ASTAT ;READ INPUT DATA REGISTER
990 005250* 026767 172626 172626 CMP ACSR,ASTAT ;COMPARE
991 005256* 001403 BEQ 28 ;BR IF SAME
992 ;*****
993 005260* 104405 000000* 000000 HRDERS,BEGIN,NULL ;INPUT DATA PATH ERROR
994 ;*****
995 005266* 104407 000000* 28: BREAK$,BEGIN ;TEMPORARY RETURN TO MONITOR....
996 005272* 104407 000000* BREAK$,BEGIN ;THEN CONTINUE AT NEXT INSTRUCTION.
997 005276* 006367 172770 ASL TEMP ;TRY NEXT BIT
998 005302* 001351 BNE 18 ;BR IF MORE BITS
999 ;VERIFY THE MNCDO - WRAPAROUND - MNCDI INVERTED DATA PATH
1000 1001 005304* 012777 177777 172732 DI37: MOV #-1,#DIR ;CLEAR INPUT REG.
1002 005312* 005077 172712 CLR #OCSR ;CLEAR OUTPUT STATUS
1003 005316* 012777 000000 172714 MOV #RIT5|BIT4,#ICSR ;LOAD INPUT STATUS <INVERT DATA>
1004 005324* 012767 000001 172740 MOV #RIT0,TEMP ;LOAD INITIAL BIT
1005 005332* 016777 172734 172674 18: MOV TEMP,#DOR ;LOAD OUTPUT DATA REG.
1006 005340* 017767 172700 172536 MOV #DIR,ASTAT ;READ INPUT DATA REGISTER
1007 005346* 016767 172720 172526 MOV TEMP,ACSR ;GET THE BIT
1008 005354* 005167 172522 COM ACSR ;INVERT EXPECTED INPUT DATA
1009 005360* 026767 172516 172516 CMP ACSR,ASTAT ;COMPARE
1010 005366* 001403 BEQ 28 ;BR IF SAME
1011 ;*****
1012 005370* 104405 000000* 000000 HRDERS,BEGIN,NULL ;INVERTED INPUT DATA PATH ERROR
1013 ;*****
1014 005376* 104407 000000* 28: BREAK$,BEGIN ;TEMPORARY RETURN TO MONITOR....
1015 005402* 104407 000000* BREAK$,BEGIN ;THEN CONTINUE AT NEXT INSTRUCTION.
1016 005406* 006367 172660 ASL TEMP ;TRY NEXT BIT
1017 005412* 001347 BNE 18 ;BR IF MORE BITS
1018
1019

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1020 ;VERIFY IN 10 MODE THAT SBR AND INPUT BITS SET INPUT READY
1021 ; INPUT DATA READY FLAG
1022 ; LOAD A FLOATING 1 ACROSS THE SBR
1023 ; VERIFY THAT ONLY THE CORRECT BIT SET DATA READY
1024
1025 005414* 005077 172614 DI40: CLR #DOR ;LOAD INITIAL BIT
1026 005420* 012767 000001 172644 MOV #RIT0,TEMP
1027
1028 005426* 005077 172602 18: CLR #DOR ;CLEAR OUTPUT BITS
1029 005432* 005077 172612 CLR #SBR ;CLEAR SBR REG.
1030 005436* 012777 000004 172574 MOV #RIT2,#ICSR ;CLEAR INPUT READY AND SET MODE 10
1031 005444* 012777 177777 172572 MOV #-1,#DIR ;CLEAR INPUT REG.
1032
1033 005452* 016777 172614 172570 MOV TEMP,#SBR ;LOAD SBR REG.
1034 005460* 042777 000200 172552 BIC #RIT7,#ICSR ;CLEAR INPUT READY BIT
1035 005466* 016777 172600 172540 MOV TEMP,#DOR ;LOAD OUTPUT REG.
1036
1037 005474* 012767 100204 172400 MOV #BIT15|BIT7|BIT2,ACSR ;LOAD EXPECTED STATUS
1038 005502* 017767 172532 172374 MOV #ICSR,ASTAT ;READ STATUS
1039 005510* 026767 172366 172366 CMP ACSR,ASTAT ;COMPARE
1040 005516* 001403 BEQ 28 ;BR IF SAME
1041 ;*****
1042 005520* 104405 000000* 000000 HRDERS,BEGIN,NULL ;INPUT DATA READY FLAG FAILED
1043 ;*****
1044 ;TO SET IN MODE 10 <STIMULUS MODE>
1045
1046 ;NOW LOAD ALL BITS EXCEPT THE FLOATING BIT AND ENSURE INPUT DATA READY DOES NOT SET
1047 005526* 016767 172540 172540 28: MOV TEMP,TEMP1 ;COPY EXPECTED
1048 005534* 005167 172534 COM TEMP1 ;USE REVERSE PATTERN
1049 005540* 005077 172470 CLR #DOR ;CLEAR OUTPUT REG.
1050 005544* 012777 177777 172472 MOV #-1,#DIR ;CLEAR INPUT REG.
1051 005552* 042777 100200 172460 BIC #BIT15|BIT7,#ICSR ;CLEAR INPUT DATA READY
1052 005560* 012767 000004 172314 MOV #RIT2,ACSR ;LOAD EXPECTED
1053
1054 005566* 016777 172502 172440 MOV TEMP1,#DOR ;LOAD ALL OTHER DATA BITS
1055 005574* 017767 172440 172302 MOV #ICSR,ASTAT ;READ INPUT STATUS
1056 005602* 026767 172274 172274 CMP ACSR,ASTAT ;COMPARE
1057 005610* 001403 BEQ 36 ;BR IF SAME
1058 ;*****
1059 005612* 104405 000000* 000000 HRDERS,BEGIN,NULL ;INPUT DATA READY FLAG SET IN ERROR
1060 ;*****
1061 ;UNEXPECTED SBR BIT SET INPUT DATA READY
1062
1063 005620* 104407 000000* 36: BREAK$,BEGIN ;TEMPORARY RETURN TO MONITOR....
1064 005624* 104407 000000* BREAK$,BEGIN ;THEN CONTINUE AT NEXT INSTRUCTION.
1065 005630* 006367 172436 ASL TEMP ;TRY NEXT BIT
1066 005634* 001274 BNE 18 ;BR IF MORE BITS
1067

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1068      ;TEST THE TRANSITION ENABLE AND TRANSITION DETECTION
1069      005636  005077  172372  DI41: CLR      #DOR          ;CLEAR INPUT REGISTER
1070      005642  012777  172374  MOV      #-1,#DIR        ;LOAD INITIAL TRANSITION BIT
1071      005650  012767  010000  172414  MOV      #BIT12,TEMP     ;LOAD INITIAL TRANSITION BIT
1072      005656  012777  000404  172354  MOV      #BIT8|BIT2,#ICSR ;SET STIM, CLEAR READY, ENABLE TRANS.
1073      005664  012767  100604  172210  MOV      #BIT15|BIT8|BIT7|BIT2,ACSR ;LOAD EXPECTED STATUS
1074      005672  042777  100200  172340  BIC      #BIT15|BIT7,#ICSR ;CLEAR READY
1075      005700  016777  172366  172342  MOV      TEMP,#SBR      ;LOAD STIMULUS REG.
1076      005706  016777  172360  172320  MOV      TEMP,#DOR      ;LOAD INPUT REG. <VIA OUTPUT REG.>
1077      005714  017767  172320  172162  MOV      #ICSR,ASTAT    ;READ INPUT STATUS
1078      005722  026767  172154  172154  CMP      ACSR,ASTAT     ;COMPARE
1079      005730  001403  ;      BEQ      28      ;BR IF SAME
1080      ;*****
1081      005732  104405  000000  000000  HRDERS,BEGIN,NULL      ;TRANSITION ENABLE OR TRANSITION TO A ONE FAILED
1082      ;*****
1083      ;NOW REMOVE THE TRANSITION DATA BIT (THIS SHOULD CAUSE THE INPUT READY FLAG TO SET AGAIN)
1084      281
1085      005740  012777  177777  172276  MOV      #-1,#DIR      ;CLEAR INPUT REG
1086      005746  042777  100200  172264  BIC      #BIT15|BIT7,#ICSR ;CLEAR INPUT READY FLAG
1087      005754  046777  172312  172252  BIC      TEMP,#DOR      ;REMOVE THE INPUT DATA
1088      ;THIS SHOULD CAUSE THE TRANSITION TO A ZERO
1089      005762  017767  172252  172114  MOV      #ICSR,ASTAT    ;READ INPUT STATUS
1090      005770  026767  172106  172106  CMP      ACSR,ASTAT     ;COMPARE
1091      005776  001403  ;      BEQ      38      ;BR IF SAME
1092      ;*****
1093      006000  104405  000000  000000  HRDERS,BEGIN,NULL      ;TRANSITION TO A ZERO FAILED
1094      ;*****
1095      381
1096      006006  ;      BREAKS,BEGIN      ;TEMPORARY RETURN TO MONITOR....
1097      006006  104407  000000  ;      BREAKS,BEGIN      ;THEN CONTINUE AT NEXT INSTRUCTION,
1098      006012  104407  000000  ;      ASL      TEMP      ;TRY ANOTHER BIT ?
1099      006016  006367  172250  ;      BNE      18      ;BR IF YES
1100      006022  001323  ;
1101      1102  006024  000167  172370  JMP      CONT2          ;TRY NEXT UNIT
1103      ;
1104      006030  006034  ;MSG1: PRI1          ;POINTER TO TEXT
1105      006032  177777  ;      -1          ;TERMINATOR
1106      006034  053045  051105  043111  PRI1: .ASCIZ  \&VERIFY MNCDO WRAP-AROUND CONNECTION TO MNCDA\
1107      006042  020131  047115  042103  ;
1108      006050  020117  051127  050101  ;
1109      006056  040455  047522  047125  ;
1110      006064  020104  047503  047116  ;
1111      006072  041505  044524  047117  ;
1112      006100  052040  020117  047115  ;
1113      006106  042103  000111  ;
1114      ;EVEN
1115      ;END

```

ACSR	000102R	198*	305*	306	308	316*	321*	322*	323	325	338*	341	347*	350
		360*	361	364	369	371	381*	382	385	390	392	402*	403	406
		411	413	423*	424	427	432	434	444*	445	448	453	455	465*
		466	469	474	476	486*	487	490	495	497	507*	508	511	516
		518	528*	529	532	537	539	549*	550	553	557*	560	571*	574
		583*	585	593*	596	602*	606	617*	619	628*	633	640*	646	653*
		656	662*	668	675*	677	686*	687*	693	709*	711	761*	762	764
		772*	777*	778*	779	781	794*	797	803*	806	816*	817	820	825
		827*	837*	838	841	846	848	856*	859	862	867	869	879*	883
		891*	901*	934*	936	949*	951	963*	965	972*	977	987*	988	990
		1007*	1008*	1009	1037*	1039	1052*	1056	1073*	1078	1090			
ADDR	000006R	164*	252											
ADDR22*	001000	211*												
ASB	000106R	202*												
ASTAT	000104R	200*	307*	308	324*	325	340*	341	349*	350	362*	363*	364	370*
		371	383*	384*	385	391*	392	404*	405*	406	412*	413	425*	426*
		427	433*	434	446*	447*	448	454*	455	467*	468*	469	475*	476
		488*	489*	490	496*	497	509*	510*	511	517*	518	530*	531*	532
		538*	539	551*	552*	553	559*	560	572*	573*	574	584*	585	595*
		596	605*	606	618*	619	632*	633	645*	646	655*	656	663*	668
		676*	677	692*	693	710*	711	763*	764	780*	781	796*	797	805*
		806	818*	819*	826*	827	839*	840*	841	847*	848	860*	861*	
		862	868*	869	882*	883	893*	903*	935*	936	950*	951	964*	965
		976*	977	989*	990	1006*	1009	1038*	1039	1055*	1056	1077*	1078	1089*
		1090												
AWAS	000110R	203*												
BEGIN	000000R	161*	295	311	314	315	328	331	332	344	353	356	357	367
		374	377	378	388	395	398	399	409	416	419	420	430	437
		440	441	451	458	461	462	472	479	482	483	493	500	503
		504	514	521	524	525	535	542	545	546	556	563	566	567
		577	588	599	609	622	625	626	636	649	659	666	671	680
		696	699	700	714	726	730	745	748	767	770	771	784	787
		788	800	809	812	813	823	830	833	834	844	851	854	855
		865	872	875	876	886	896	906	917	921	939	941	954	968
		980	993	996	997	1012	1015	1016	1042	1059	1063	1064	1081	1093
		1097	1098											
BITDAT*	010000	241*	654	661	674	685		642	643	706	708	721	740	741
BITEXT*	004000	242*	594	603	616	630	631	642	643					
		974												
BIT0	= 000001	211*	240	245	305	320	684	761	776	881	890	900	912	986
		1004	1026											
BIT1	= 000002	211*	360	628	629	640	641	691	705	720	739	946	949	
BIT10	= 002000	211*												
BIT11	= 004000	211*	242											
BIT12	= 010000	211*	241	528	705	1071								
BIT13	= 020000	211*												
BIT14	= 040000	211*	549	739										
BIT15	= 100000	211*	628	631	643	740	741	1037	1051	1073	1074	1086		
BIT2	= 000004	211*	381	571	615	617	960	963	1030	1037	1052	1072	1073	
BIT3	= 000010	211*	402	858										
BIT4	= 000020	211*	423	571	661	685	705	837	1003					
BIT5	= 000040	211*	444	661	685	690	705	707	1003					
BIT6	= 000100	211*	465	720	816	913								
BIT7	= 000200	211*	363	384	405	426	447	468	489	510	531	552	573	593
		594	620	630	631	640	642	643	706	708	721	740	741	819
		840	861	879	934	949	972	974	1034	1037	1051	1073	1074	1086

BIT0	= 000400	211#	293	486	1072	1073								
BIT9	= 001000	211#	507											
BREAK#	= 104407	211#	314	315	331	332	356	357	377	378	390	399	419	420
		440	441	461	462	482	483	503	504	524	525	545	546	566
		567	625	626	699	700	770	771	787	788	812	813	833	834
		854	855	875	876	996	997	1015	1016	1063	1064	1097	1098	
BR1	000012R	166#	719	738	910									
BR2	000013R	167#												
BTOD#	= 104421	211#												
CDATA#	= 104412	211#												
CONFIG	000056R	186#												
CONT1	000410R	269#	294											
CONT2	000420R	272#	754	1102										
CSRA	000100R	196#	299#	756#										
DATCK#	= 104411	211#												
DATER#	= 104404	211#												
DIDINS	000262R	234#	719#	732	733#									
DIDINV	000260R	233#	718#	732#										
DIEINS	000266R	236#	265	738#	750	751#								
DIEINV	000264R	235#	737#	750#										
DIR	000244R	225#	301	614#	655	663	676	688	689#	692	710	929#	944#	958#
		983#	989	1001#	1006	1031#	1050#	1070#	1085#					
DIR1	000246R	226#												
DI1	000542R	303#												
DI10	001470R	442#												
DI11	001600R	463#												
DI12	001710R	484#												
DI13	002020R	505#												
DI14	002130R	526#												
DI15	002240R	547#												
DI16	002350R	569#												
DI17	002422R	575	581#											
DI2	000620R	318#												
DI20	002466R	586	592#											
DI21	002532R	597	602#											
DI22	002574R	607	613#											
DI23	002664R	628#												
DI24	002740R	634	640#											
DI25	003020R	647	653#											
DI26	003164R	678	684#											
DI27	003302R	705#												
DI3	000710R	335#												
DI30	003364R	712	718#											
DI31	003454R	737#												
DI32	004666R	929#												
DI33	004756R	937	944#											
DI34	005042R	952	958#											
DI35	005126R	966	971#											
DI36	005202R	978	983#											
DI37	005304R	1001#												
DI4	001030R	358#												
DI40	005414R	1025#												
DI41	005636R	1069#												
DI5	001140R	379#												
DI6	001250R	400#												
DI7	001360R	421#												

DODINS	000256R	231#	260	910#	923	924#								
DODINV	000254R	230#	255	909#	923#									
DOR	000234R	219#	758	762#	763	779#	780	793#	796	802#	804#	805	800#	809#
		899#	902#	911#	933#	948#	962#	971#	988#	1005#	1025#	1020#	1035#	1049#
		1054#	1069#	1076#	1087#									
DOR1	000236R	220#	795#											
D00	003564R	753	756#											
D01	003602R	759#												
D010	004470R	884	889#											
D011	004530R	894	899#											
D012	004570R	904	909#											
D02	003660R	774#												
D03	003750R	791#												
D04	004070R	814#												
D05	004200R	835#												
D06	004310R	856#												
D07	004420R	878#												
DVID1	000014R	160#	269											
ENDIT#	= 104413	211#	295											
END#	= 104410	211#												
ERRTYP	000100R	201#												
EXIT#	= 104400	211#	726	745	917									
GETPA#	= 104415	211#												
GWBUF#	= 104414	211#												
HRDCNT	000044R	181#												
HRDR#	= 104405	211#	311	328	344	353	367	374	388	395	409	416	430	437
		451	458	472	479	493	500	514	521	535	542	556	563	577
		588	599	609	622	636	649	659	666	671	680	696	714	767
		784	800	809	823	830	844	851	865	872	886	896	906	939
		954	968	980	993	1012	1042	1059	1081	1093				
HRDPAS	000050R	183#												
ICONT	000036R	178#												
ICOUNT	000040R	179#												
ICSR	000240R	223#	250	299	300	361#	362	369#	370	382#	383	390#	391	403#
		404	411#	412	424#	425	432#	433	445#	446	453#	454	466#	467
		474#	475	487#	488	495#	496	508#	509	516#	517	529#	530	537#
		538	550#	551	558#	559	569#	572	581#	582	584	592#	594	595
		603#	604#	605	615#	616	618	629#	630#	631#	632	641#	642#	643#
		645	654#	661#	674#	685#	690#	691#	705#	706#	707#	708#	720#	721#
		728#	739#	740#	741#	746#	931#	935	946#	950	960#	964	974#	975#
		985#	1003#	1030#	1034#	1038	1051#	1055	1072#	1074#	1077	1086#	1089	
ICSR1	000242R	224#												
IDNUM	000122R	208#												
INIT	000030R	175#												
INPUT	000520R	270	299#											
INTR	000120R	207#												
MAP22#	= 104416	211#												
MODNAM	000000R	162#												
MODSP	000224R	176	209#											
MSGN#	= 104403	211#	941											
MSG#	= 104402	211#												
MSG#	= 104401	211#												
MSG1	000030R	941	1104#											
NULL	= 000000	211#	311	328	344	353	367	374	388	395	409	416	430	437
		451	458	472	479	493	500	514	521	535	542	556	563	577
		580	599	609	622	636	649	659	666	671	680	696	714	767

		784	800	809	823	830	844	851	865	872	886	896	906	939
		954	968	980	993	1012	1042	1059	1081	1093				
OCSR	000230R	216*	246	272	756	757	817*	818	825*	826	838*	839	846*	847
		859*	860	867*	868	878*	882	892*	893	903	913*	919*	925*	932*
		947*	961*	973*	976	984*	1002*							
OCSR1	000232R	217*	881*	890*	900*	912*								
OPEN	000000	163	169	170	171	172	189	190	191	192	193	194	195	196
		198	200	202	203	205	206	207	211*					
OTOA#	104420	211*												
PASCHT	000034R	177*												
PIRQ#	000004	211*	730	740	921									
POPSP	005726	211*												
POPSP2	022626	211*												
PR11	006034R	1104	1106*											
PRTY	000000	211*												
PRTY0	000000	167	211*											
PRTY1	000040	211*												
PRTY2	000100	211*												
PRTY3	000140	211*												
PRTY4	000200	166	211*											
PRTY5	000240	211*												
PRTY6	000300	211*												
PRTY7	000340	211*												
PS	177776	211*												
PSW	177776	211*												
PUSH	005746	211*												
PUSH2	024646	211*												
RAND#	104417	211*												
RANNUM	000054R	185*												
RESTR1	000306R	204	245*	297										
RES1	000056R	187*												
RES2	000060R	188*												
RSTR	000112R	204*												
SADR	000102R	197*												
SBR	000250R	227*	302	306*	307	323*	324	337*	340	346*	348*	349	613*	930*
		945*	959*	1029*	1033*	1075*								
SBR1	000252R	228*												
SOPCNT	000042R	180*												
SOPER#	104406	211*												
SOPPAS	000046R	182*												
SPOINT	000032R	176*												
SPSIZ	000040	1	209											
SRMINE	000270R	237*	752											
SR1	000016R	169*	243											
SR2	000020R	170*												
SR3	000022R	171*												
SR4	000024R	172*												
START	000300R	175	243*											
STAT	000026R	174*												
SVR0	000062R	189*												
SVR1	000064R	190*												
SVR2	000066R	191*												
SVR3	000070R	192*												
SVR4	000072R	193*												
SVR5	000074R	194*												
SVR6	000076R	195*												

SYSCNT	000052R	184*												
TEMP	000272R	238*	320*	321	333*	604*	606	609	701*	776*	777	789*	986*	987
		998*	1004*	1005	1007	1017*	1026*	1033	1035	1047	1065*	1071*	1075	1076
		1087	1099*											
TEMP1	000274R	239*	1047*	1048*	1054									
TEMP2	000276R	240*	245*	269	292*	293	752							
TRPDFD	000022	211*												
VECTOR	000010R	165*	262											
WASADR	000104R	199*												
WDFR	000116R	206*												
WDTO	000114R	205*												
XFLAG	000005R	163*												
SBASE1	000224R	212*	247											
SVECT1	000226R	213*	257											

ABS. 000000 000  
006112 001

ERRORS DETECTED: 0  
DEFAULT GLOBALS GENERATED: 0

XMNBA0, XMNBA0/SOL/CRF;SYM=DDXCOM, XMNBA0  
RUN-TIME: 24.5 SECONDS  
RUN-TIME RATIO: 185/8=21.5  
CORE USED: 7K (13 PAGES)

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10-11-1944

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IDENTIFICATION  
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PRODUCT CODE: AC-F416A-MC  
PRODUCT NAME: CXMNAA0 MNCAD (A/D) MODULE  
PRODUCT DATE: SEPTEMBER 1978  
MAINTAINER: DEC/X11 SUPPORT GROUP

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1.0 ABSTRACT  
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MNA IS AN IOMOD THAT EXERCISES THE MNCAD (A/D) ANALOG MODULE. THIS MODULE REQUIRES ONLY AN ANALOG GROUND ON CHANNEL ZERO IN ORDER TO BE RUN, HOWEVER; WITH SPECIAL SETUP, MORE OPTIONS CAN BE CHOSEN. ONE OPTION IS THE USE OF THE MNCKW (REAL TIME CLOCK) DEVICE. THIS OPTION ALLOWS EXERCISING THE MNCAD ASYNCHRONOUSLY WITH THE LSI-11 CPU, THAT IS, MNCAD CONVERSIONS WILL BE STARTED AT RANDOM TIMES TO ALLOW FOR MAXIMUM BUS NOISE DURING THE CONVERSION. IF THIS OPTION IS SELECTED, YOU MUST DESELECT MNC FROM A DEC/X11 RUN AND ONLY ONE MNCAD (A/D) CAN BE RUN.

WITH NORMAL OPERATION, RMS NOISE AND PEAK NOISE ARE SAMPLED ON CHANNEL ZERO AND COMPARED AGAINST A LIMIT. WITH THE SECOND OPERATION OPTION, MORE CHANNELS MAY BE SPECIFIED TO RUN THE NOISE TESTS ON. THE THIRD OPTION ALLOWS FOR SAMPLING OF ONE TO ALL THE CHANNELS OF THE MNCAD. A CHECK IS MADE TO SEE THAT THE INPUT VOLTAGE REMAINS STABLE WITHIN AN ALLOWED TOLERANCE. LOCATIONS WITHIN THIS MODULE ARE PROVIDED TO CHANGE ANY LIMIT, OR TO FORCE TIMEOUT OF ANY VALUE. UP TO FOUR MNCAD (A/D)'S CAN BE EXERCISED WITH THIS MODULE. WHEN SR1 BITS 1 OR 2 ARE SET AND ADDITIONAL MNCAD'S ARE ENABLED, THE SAME NUMBER OF CHANNELS ON EACH MNCAD WILL BE TESTED. IF SR1 BIT 0 IS SET, ONLY ONE MNCAD CAN BE BE TESTED.

2.0 REQUIREMENTS  
-----

HARDWARE: ONE MNCAD (A/D)  
ONE MNCKW (CLOCK) <OPTIONAL>

STORAGE: MNA REQUIRES:  
DECIMAL WORDS: 901  
OCTAL WORDS: 1605  
OCTAL BYTES: 3412

3.0 PASS DEFINITION  
-----

ONE PASS OF THE MNA MODULE CONSISTS OF GENERATING 8244(DECIMAL) INTERRUPTS (CONVERSIONS).

4.0 EXECUTION TIME  
-----

ONE PASS OF THE MNA MODULE PUNNING ALONE WITH SR1 = 0, 1 OR 2, WILL TAKE APPROXIMATELY ONE MINUTE. THE TIME FOR A PASS WITH SR1 = 4 WILL DEPEND UPON THE NUMBER OF CHANNELS SPECIFIED IN LOCATION "NLSTCH".

5.0 CONFIGURATION REQUIREMENTS  
-----

DEFAULT PARAMETERS:

DEVADR: 171000, VECTOR: 400, BR1: 4

DEVCNT: 1, SR1: 0

REQUIRED PARAMETERS:

NONE IF SR1=0

IF SR1 BITS 0, 1 OR 2 = 1 THEN SEE OPERATION OPTIONS  
IF SR1 BIT 0 IS SET, ONLY 1 MNCAD WILL BE TESTED.

6.0 DEVICE/OPTION SETUP  
-----

AN ANALOG GROUND MUST BE PUT ON CH. 0  
(BY SET CH0 FRONT PANEL SWITCH TO "TEST").

- SR1 BIT0=1 USE THE MNCKW OPTION FOR A TO D STARTS  
(THE MNCKW <MNC> MODULE MUST BE DESELECTED)
- SR1 BIT1=1 ALL CHANNELS SPECIFIED MUST HAVE  
AN ANALOG GROUND.
- SR1 BIT2=1 SELECT ADDITIONAL CHANNELS FOR NOISE TESTING.

7.0 MODULE OPERATION  
-----

1. (STAPT) (RESTR) INITLIZE POINTERS AND TYPE-OUT VALUES.
2. (LOOP) BIT EXERCISE CSR
3. (ADPMS1) PREFORM RMS NOISE CHECK ON SPECIFIED CHANNEL. WE FIRST USE SAR TO FIND THE DAC VALUE THAT PRODUCES A 16/84 SPLIT FOR THE LEFT BOUNDARY OF NOISE. THEN WE USE SAR TO FIND THE DAC VALUE THAT PRODUCES A 84/16 SPLIT FOR THE RIGHT VALUE. WE THEN SUBTRACT THE TWO DAC VALUES AND WE HAVE A VALUE FOR THE 68% AREA OF NOISE (RMS). IT IS THEN COMARED AGAINST THE ALLOWED LIMIT TO SEE IF EXCESSIVE NOISE IS ON THE CHANNEL.
4. (ADPK1) PREFORM PEAK NOISE CHECK ON SPECIFIED CHANNEL. WE FIRST USE SAR TO FIND THE DAC VALUE THAT PRODUCES A .6% SPLIT FOR THE LEFT BOUNDARY OF NOISE. THEN WE USE SAR TO FIND THE DAC VALUE THAT PRODUCES A .6% SPLIT FOR THE RIGHT BOUNDARY. WE THEN SUBTRACT THE TWO DAC VALUES AND WE HAVE A VALUE OF 98% AREA OF NOISE (PEAK). IT IS THEN COMPARED AGAINST THE ALLOWED LIMIT TO SEE IF EXCESSIVE NOISE IS ON THE CHANNEL.
5. IF MULTIPLE CHANNELS ARE SELECTED FOR NOISE TESTING TEST NEXT CHANNEL, IF SINGULAR RETEST CHAN. 0.
6. IF MULTI-CHANNEL SAMPLING IS SELECTED, TAKE SAMPLES ON EACH CHANNEL SPECIFIED, AND COMPARE THE AVERAGE OF THE SAMPLES AGAINST THE OLD AVERAGE FOR THE CHANNEL. IF THE DIFFERENCE IS GREATER THAN THE TOLERANCE, REPORT THE DATA ON THAT CHANNEL.
7. REPORT END PASS IF LAST UNIT, BRANCH TO "LOOP" IF MORE UNITS.
8. (SAR) SAR IS A SUCCESSIVE APPROXIMATION ROUTINE. IT IS USED TO FIND A DAC VALUE THAT PRODUCES A DESIRED SPLIT. IT DOES THIS BY TRYING A DAC VALUE AND TAKING 512 CONVERSIONS ON THE A/D. IF THE AMOUNT OF THE SAMPLES IS LOWER THEN SPECIFIED IT INCREASES THE DAC VALUE, IF THE AMOUNT OF SAMPLES IS HIGHER THEN SPECIFIED, IT DECREASES THE DAC VALUE. IF THE END DAC VALUE IS EITHER 000 OR 377 WE HAVE A "WARPAROUND" ERROR. THIS OCCURS WHEN WE ARE UNABLE TO ADJUST THE DAC TO PRODUCE A DESIRED SPLIT, AND INDICATES EXCFSSIVE NOISE ON A CHANNEL.
9. (PANDY) THIS IS A RANDOM NUMBER GENERATOR. IF THE MNCKW CLOCK OPTION IS SELECTED WE GFT THE NUMBER THAT WE PUT INTO THE CLOCK PRESET REGISTER FROM THIS ROUTINE.

8.0 OPERATION OPTIONS

-----

1. VALID SRI VALUES

SRI BIT	ENABLE/DISABLE	FUNCTION
0	0	INHIBIT USE OF MNCKW OPTION.
0	1	ENABLE USE OF MNCKW OPTION. NOTE: IF ENABLED, YOU MUST DESELECT "MNC" FROM DEC/X11 RUN AND ONLY 1 MNCAD WILL BE RUN.
1	0	INHIBIT SAMPLING ADDITIONAL CHANNELS FOR STABLE INPUT.
1	1	ENABLE SAMPLING CHANNEL ZERO THROUGH CHANNEL SPECIFIED BY CLSTCH FOR STABLE INPUT (+-) TOLERANCE SPECIFIED BY OFFALL
2	0	USE CHANNEL ZERO ONLY FOR NOISE TESTING.
2	1	USE CHANNEL ZERO THROUGH THE CHANNEL SPECIFIED IN NLSTCH FOR NOISE TESTING.

2. THE FOLLOWING ARE LOCATIONS WITHIN THIS MODULE THAT ENABLE THE USER TO CHANGE LIMITS AND SPECIFY CHANNELS.

LOCATION	FUNCTION
ARMLIM	SPECIFIES MAXIMUM LIMIT FOR RMS NOISE. MAY BE CHANGED TO ZERO TO FORCE TYPEOUT OF RMS NOISE ON A CHANNEL RUNNING IN A SYSTEM ENVIRONMENT.
APKLIM	SPECIFIES MAXIMUM LIMIT FOR PEAK NOISE. MAY BE CHANGED TO ZERO TO FORCE TYPEOUT OF PEAK NOISE ON A CHANNEL RUNNING IN A SYSTEM ENVIRONMENT.
CLSTCH	IF SRI BIT1=1, USED TO SPECIFY END CHANNEL FOR SAMPLING STABLE INPUT.
OFFALL	IF SRI BIT1=1, USED TO SPECIFY TOLERANCE OF STABLE CHANNEL. PRESET BY MODULE TO "000002".
NLSTCH	IF SRI BIT2=1, USED TO SPECIFY LAST CHANNEL FOR NOISE TESTING.

9.0 NON-STANDARD PRINTOUTS  
-----

1. IF A CHANNEL HAS EXCESSIVE RMS NOISE, IT REPORTS IT IN AN ERROR CALL AND A MSGN CALL:

(EXAMPLE)

MNCAD (A/D) UNIT #0 ON CHAN 00  
A/D RMS NOISE = 0.52 LSB (LIMIT = .20 LSB)

2. IF A CHANNEL HAS EXCESSIVE PEAK NOISE, IT REPORTS IT IN AN ERROR CALL AND A MSGN CALL:

(EXAMPLE)

MNCAD (A/D) UNIT #0 ON CHAN 00  
A/D PEAK NOISE = 2.57 LSB (LIMIT = 2.00 LSB)

3. IF THERE IS AN EXCESSIVE AMOUNT OF NOISE SO THAT THE DAC CANNOT BE ADJUSTED, IT REPORTS IT IN AN ERROR CALL AND A MSGN CALL:

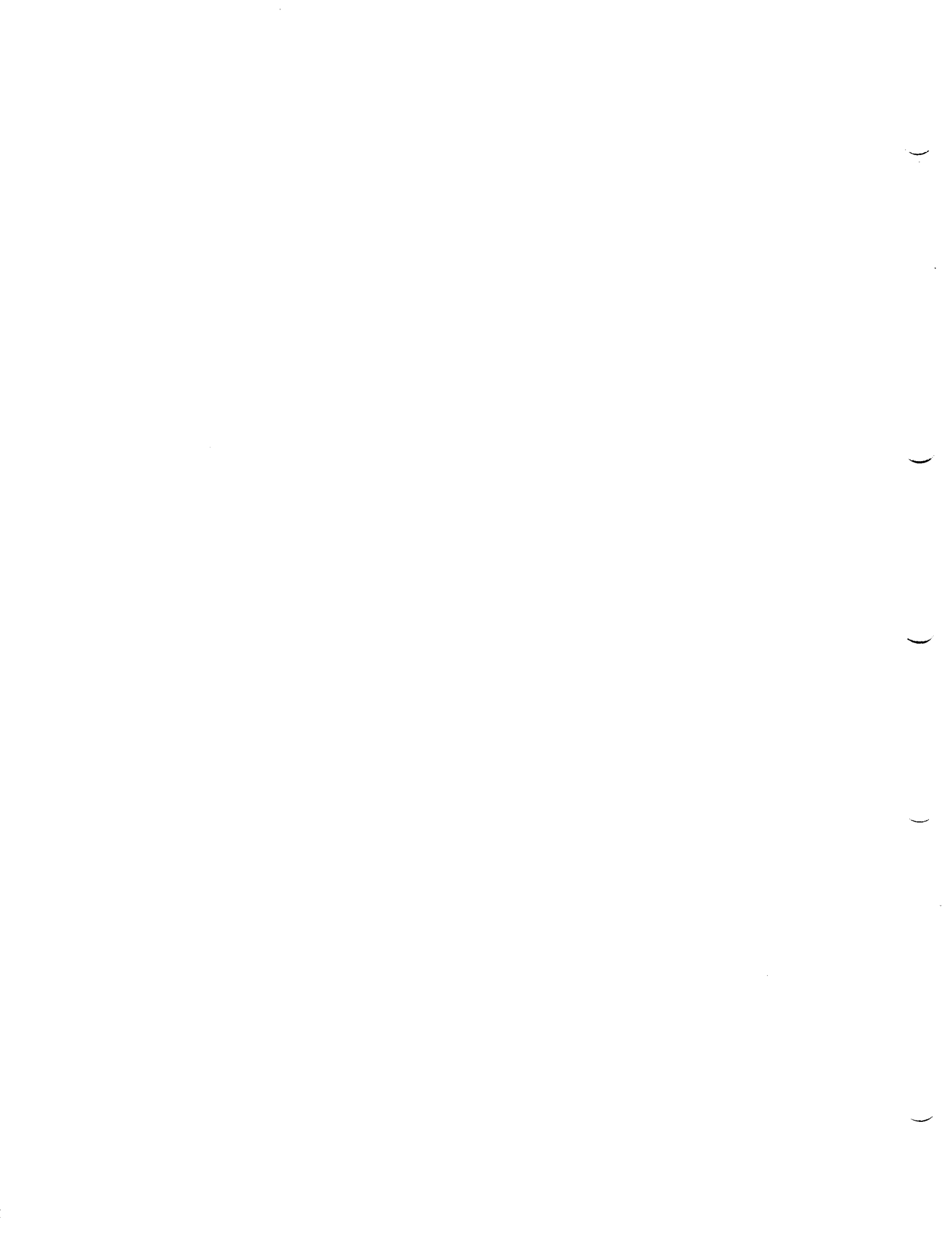
(EXAMPLE)

MNCAD (A/D) UNIT #0  
A/D PEAK WRAPAROUND ERROR ON CHAN 00

4. IF A CHANNEL IS FOUND TO BE UNSTABLE IN STABLE INPUT SAMPLING, IT REPORTS IT IN A MSGN CALL:

(EXAMPLE)

A/D ERROR ON CHAN 14; OLD VALUE = 4066 NEW VALUE = 4000  
!



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.TITLE MNAA DEC/X11 SYSTEM EXERCISER MODULE
1 DEXCON VERSION 6 23-MAY-78
.LIST RTN
;*****
REGIN:
000000* 047115 040501 040 MODNAM: ASCII /MNAA / MODULE NAME.
000005* 000 XFLAG: BYTE OPEN ;USED TO KEEP TRACK OF WBUFF USAGE
000010* 171000 ADDR: 171000+0 ;1ST DEVICE ADDR.
000015* 000400 VECTOR: 400+0 ;1ST DEVICE VECTOR.
000020* 300 RR1: BYTE PRTY6+0 ;1ST BR LEVEL.
000025* 000 RR2: BYTE PRTY0+0 ;2ND BR LEVEL.
000030* 000001 DVID1: 0+1 ;DEVICE INDICATOR 1.
000035* 000000 SR1: OPEN ;SWITCH REGISTER 1
000040* 000000 SR2: OPEN ;SWITCH REGISTER 2
000045* 000000 SR3: OPEN ;SWITCH REGISTER 3
000050* 000000 SR4: OPEN ;SWITCH REGISTER 4
;*****
000226* 140000 STAT: 140000 ;STATUS WORD.
000230* 000274 INIT: START ;MODULE START ADDR.
000232* 000224 SPOINT: MODSP ;MODULE STACK POINTER.
000234* 000000 PASCNT: 0 ;PASS COUNTER.
000236* 000005 ICONT: 5 ;# OF ITERATIONS PER PASS=5
000240* 000000 ICOUNT: 0 ;LOC TO COUNT ITERATIONS
000242* 000000 SOFCNT: 0 ;LOC TO SAVE TOTAL SOFT ERRORS
000244* 000000 HRDCNT: 0 ;LOC TO SAVE TOTAL HARD ERRORS
000246* 000000 SOFPAS: 0 ;LOC TO SAVE SOFT ERRORS PER PASS
000250* 000000 HRDPAS: 0 ;LOC TO SAVE HARD ERRORS PER PASS
000252* 000000 SYSCNT: 0 ;# OF SYS ERRORS ACCUMULATED
000254* 000000 RANNUM: 0 ;HOLDS RANDOM # WHEN RAND MACRO IS CALLED
000256* 000000 CONFIG: ;RESERVED FOR MONITOR USE
000260* 000000 PES1: 0 ;RESERVED FOR MONITOR USE
000262* 000000 PES2: 0 ;RESERVED FOR MONITOR USE
000264* 000000 SVR0: OPEN ;LOC TO SAVE R0.
000266* 000000 SVR1: OPEN ;LOC TO SAVE R1.
000268* 000000 SVR2: OPEN ;LOC TO SAVE R2.
000270* 000000 SVR3: OPEN ;LOC TO SAVE R3.
000272* 000000 SVR4: OPEN ;LOC TO SAVE R4.
000274* 000000 SVR5: OPEN ;LOC TO SAVE R5.
000276* 000000 SVR6: OPEN ;LOC TO SAVE R6.
000280* 000000 CSRA: OPEN ;ADDR OF CURRENT CSR.
000282* 000000 SBADRA: ;ADDR OF GOOD DATA, OR
000284* 000000 ACSRA: OPEN ;CONTENTS OF CSP.
000286* 000000 WASAPRA: ;ADDR OF BAD DATA, OR
000288* 000000 ASTAT: OPEN ;STATUS REG CONTENTS.
000290* 000000 ERRTYPE: ;TYPE OF ERROR
000292* 000000 ASB: OPEN ;EXPECTED DATA.
000294* 000000 AWAS: OPEN ;ACTUAL DATA.
000296* 000274 PSTRT: PESTRT ;PRESTART ADDRESS AFTER END OF PASS
000298* 000000 WDTO: OPEN ;WORDS TO MEMORY PER ITERATION
000300* 000000 WDFR: OPEN ;WORDS FROM MEMORY PER ITERATION
000302* 000000 INTR: OPEN ;# OF INTERRUPTS PER ITERATION
000304* 000000 IDNUM: 0 ;MODULE IDENTIFICATION NUMBER=0
000306* 000224 MODSP: 0
;*****

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333

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334 ;*OPTIONAL USER SUPPLIED VALUES
335 000224* 000000 CLSTICH: WORD 0 ;USER SUPPLIED LAST SAMPLED CH
336 000226* 000000 OFFALL: WORD 2 ;USER SUPPLIED TOLERANCE FOR SAMPLE
337 000230* 000000 NLSTICH: WORD 0 ;USER SUPPLIED LAST NOISE CH.
338 000232* 000262 ARWLM: 50 ;RMS NOISE LIMIT
339 000234* 000310 APKLM: 200 ;A/D PEAK NOISE LIMIT.
340 ;*
341 ;*REGISTER AND VECTOR ADDRESS
342 ;*
343 000236* 171000 ADSP: WORD 171000 ;A/D CSR ADDRESS
344 000240* DAC: ;DAC (WRITE ONLY) AND BUFFER REG (READ ONLY)
345 000242* 171002 ADDR: WORD 171002
346 ;*
347 ;*THE FOLLOWING ARE CLOCK ADDRESSES IF A MNCKW EXISTS.
348 ;*
349 000242* 171020 ASP: WORD 171020 ;CLOCK A CSR.
350 000244* 171022 ABR: WORD 171022 ;CLOCK A PRESET BUFFER.
351 ;*
352 ;*
353 ;*FLAGS, COUNTERS AND OTHER REGISTERS
354 ;*
355 ;*
356 000246* 000000 WHO: WORD 0 ;#RMS NOISE, 1=PEAK NOISE
357 000250* 000000 INTFLG: WORD 0 ;USED IN LOGIC TEST TO INDICATE AN A/D INTR.
358 000252* 000000 FRED: WORD 0 ;USED TO HOLD CURRENT DAC VALUE.
359 000254* 000000 EDGE: WORD 0 ;HOLD CURRENT EDGE VALUE.
360 000256* 000000 TMP: WORD 0 ;TEMP WORKING AREA.
361 000260* 000000 ARMX: WORD 0 ;USED TO HOLD RMS NOISE VALUE.
362 000262* 000000 APKX: WORD 0 ;USED TO HOLD A/D PEAK NOISE VALUE.
363 000264* 000000 NCCH: WORD 0 ;CURRENT NOISE CHAN.
364 000266* 000000 CCH: WORD 0 ;CURRENT SAMPLE CHAN.
365 000270* 000001 TEMP: BIT0 ;CURRENT UNIT BIT
366 000272* 000400 VECTAD: 400 ;A/D VECTOR

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367
368
369          ;*
370          ;*START-UP AND INITILIZE SECTION
371          ;*
372          000274'          RESTRT:
373          000274'          START:
374
375          000274' 012767 000001 177766 LOOP1: MOV    #1,TEMP          ;LOAD UNIT BIT
376          000302' 016700 177500          MOV    ADDR,R0          ;GET BASE ADDR OF A/D.
377          000306' 010067 177724          MOV    P0,ADSR          ;FIX A/D CSR ADDR.
378          000312' 062700 000002          ADD    #2,R0            ;BUFFER REG=CSR+2.
379          000316' 010067 177716          MOV    R0,ADBR          ;FIX BUFFER REG ADDR.
380          000322' 016767 177462 177742          MOV    VECTOR,VECTAD   ;LOAD VECTOR VALUE
381          000330' 005067 177730          CLR    NCCCH            ;CLEAR 1ST CHAN, FOR NOISE.
382
383
384          ;*****
385          ;CONVERT ARMLIM TO ASCII AND
386          ;STORE AT DECIM
387          000334' 104421 000000' 000232' RTOD$,BEGIN,ARMLIM,DECIM
388          000342' 002600'
389          ;*****
390          000344' 116767 002232 002451          MOVB   DECIM+2,P7       ;NOW WE WILL PUT IT
391          000352' 116767 002225 002445          MOVB   DECIM+3,P7+2    ;INTO THE ASCII
392          000360' 116767 002220 002440          MOVB   DECIM+4,P7+3    ;MESSAGE THAT WE TYPE OUT.
393
394          ;*****
395          ;CONVERT APKLM TO ASCII AND
396          ;STORE AT DECIM
397          000366' 104421 000000' 000234' RTOD$,BEGIN,APKLM,DECIM
398          000374' 002600'
399          ;*****
400          000376' 116767 002200 002431          MOVB   DECIM+2,P8       ;NOW WE WILL PUT IT
401          000404' 116767 002173 002425          MOVB   DECIM+3,P8+2    ;INTO THE ASCII MESSAGE
402          000412' 116767 002166 002420          MOVB   DECIM+4,P8+3    ;THAT WE TYPE OUT.
403
404
405          ;LOOP TO HERE IF MULTIPLE MNCAD'S
406          ;FIX THE UNIT # FOR THE ERROR TYPEOUT
407          000420' 016700 177644          ; LOOP: MOV    TEMP,R0          ;GET CURRENT UNIT #
408          000424' 005001          CLR    R1                ;INIT VALUE
409          000426' 006200          ASR    R0                ;MOVE IT
410          000430' 001402          REG    26                ;RR IF LAST
411          000432' 005201          INC    R1                ;UPDATE COUNT
412          000434' 000774          BR    15                ;
413          000436' 062701 000060          28:  ADD    #60,R1         ;MAKE ASCII VALUE
414          000442' 110167 002442          MOVB   R1,P10U          ;SAVE IN MESSAGE
415

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416
417          ;*
418          ;*LOGIC TEST #1
419          ;*IN THIS TEST WE WILL SEE IF THE A/D RESPONDS TO ITS
420          ;*ADDR. IF NOT, A DEC/X11 "SYS ERROR..." WILL OCCUR
421          ;*
422          000446' 005777 177564          LOG1:  TST    ADDR          ;ADDRESS THE A/D
423
424          ;*
425          ;*LOGIC TEST #2
426          ;*IN THIS TEST WE WILL SEE IF THE CLOCK RESPONDS TO ITS ADDR, ONLY
427          ;*IF THE CLOCK OPTION IS SELECTED BY SR1.
428
429          000452'          LOG2:  BREAK$,BEGIN          ;TEMPORARY RETURN TO MONITOR....
430          000452' 104407 000000'          BREAK$,BEGIN          ;THEN CONTINUE AT NEXT INSTRUCTION.
431          000456' 104407 000000'          BIT    BIT0,SR1        ;IS CLOCK OPTION SELECTED?
432          000462' 032767 000001 177326          BEQ    LOG3             ;BR IF NOT TO NEXT TEST.
433          000470' 001402
434
435          000472' 005777 177544          TST    ADDR          ;CLOCK WAS SELECTED, WILL IT RESPOND?
436
437          ;*
438          ;*LOGIC TEST #3
439          ;*IN THIS TEST WE WILL SEE IF THE A/D WILL INTERRUPT.
440          ;*
441          000476' 005067 177546          LOG3:  CLR    INTFLG          ;CLEAR A/D DID INTR. FLAG.
442          000502' 012777 000566' 177562          MOV    ADDR,VECTAD     ;SET UP VECTOR ADDR.
443          000510' 012777 000101 177520          MOV    #01,ADSR        ;START A CONVERSION, SHOULD INTERRUPT.
444          ; BEFORE BREAK TIME IS OVER.
445
446          000516' 104407 000000'          BREAK$,BEGIN          ;TEMPORARY RETURN TO MONITOR....
447          000522' 104407 000000'          BREAK$,BEGIN          ;THEN CONTINUE AT NEXT INSTRUCTION.
448
449          000526' 005767 177516          TST    INTFLG          ;DID THE A/D INTERRUPT?
450          000532' 001024          BNE    LOG4             ;IF SO, NEXT TEST
451          000534' 016767 177476 177336          MOV    ADDR,CSRA       ;SET ADDR. OF AD CSR FOR ERROR TYPEOUT.
452          000542' 017767 177470 177332          MOV    ADDR,ACSR       ;RECORD CONTENTS OF CSR.
453          000550' 005077 177462          CLR    ADDR            ;STOP A/D
454
455          000554' 104405 000000' 000000          HRDEP$,BEGIN,NULL     ;A/D FAILED TO INTERRUPT
456          ;*****
457          000562' 104410 000000'          ENDS$,BEGIN           ;DROP MODULE ON THIS FAILURE
458
459          ;*A/D SHOULD INTR. TO HERE.
460          000566' 005077 177444          18:  CLR    ADDR            ;STOP A/D.
461          000572' 005777 177442          TST    ADDR            ;CLEAR CSR BIT07.
462          000576' 005267 177446          INC    INTFLG          ;INDICATE THAT IT DID INTR.
463          000602' 000002          RTI                    ;EXIT INTR.

```



```

464 ;*LOGIC TEST #4
465 ;*THIS TEST WILL ONLY BE DONE IF THE CLOCK OPTION IS SELECTED
466 ;*IN THIS TEST WE WILL SPE IF THE OVERFLOW OF CLOCK 1 WILL
467 ;*TRIGGER A CONVERSION IN THE A/D
468 ;*
469
470 000604* 032767 000001 177204 LOG4: BIT #BIT0,SR1 ;IS THE CLOCK OPTION SELECTED?
471 000612* 001446 REG LOG5 ;IF NOT, GOTO NEXT TEST.
472
473 000614* 012777 177777 177422 MOV #177777,#ABR ;PRESET CLOCK FOR ALL ONES.
474 000622* 012777 000040 177406 MOV #BITS,#ADSR ;SET OVERFLOW ENABLE IN A/D.
475 000630* 012777 000011 177404 MOV #11,#ASR ;START CLOCK, 1MHZ, GO.
476 000636* 005000 CLR R0 ;SET FOR DELAY LOOP.
477
478 000640* 18: BREAKS,BEGIN ;TEMPORARY RETURN TO MONITOR....
479 000640* 104407 000000* BREAKS,BEGIN ;THEN CONTINUE AT NEXT INSTRUCTION.
480 000644* 104407 000000* TSTB #ASR ;IS THE CLOCK OVERFLOW SET?
481 000650* 105777 177366 BMI 20 ;YES=EXIT LOOP.
482 000654* 100402 INCR R0 ;NO,IS DELAY EXCEEDED?
483 000656* 105200 RNE 10 ;NO=CONTINUE DELAY.
484 000660* 001367
485 000662* 26: MOV #ASP,ASTAT ;RECORD CONTENTS OF CLOCK CSR.
486 000662* 017767 177354 177214 CLR #ASR ;CLEAR THE CLOCK.
487 000670* 005077 177346
488
489 000674* 105777 177336 TSTR #ADSR ;IS DONE FLAG SET?
490 000700* 100413 RMI LOG5 ;IF YES NEXT TEST.
491 ;IF NOT, ERROR
492 000702* 016767 177330 177170 MOV #ASP,CSRA ;RECORD A/D CSR ADDR.
493 000710* 017767 177322 177164 MOV #ADSR,ACSP ;RECORD CONTENTS OF A/D CSR,
494 ;*****
495 000716* 104405 000000* 000000 HRDERS,BEGIN,NULL ;CLOCK OVERFLOW FAILED TO TRIGGER A/D CONVERSION
496 ;*****
497 ;FOR THIS ERROR YOU MIGHT CHECK
498 ;TO SEE IF A OVERFLOW IS WIRED TO
499 ;A/D INPUT.
500 ;DROP THIS MODULE = FATAL ERROR.
501 ;CAN'T CONTINUE IF OVERFLOW DOESN'T TRIGGER THE CONVERSI
502 000724* 104410 000000* ENDS,BEGIN

```

```

503 ;*
504 ;*LOGIC TEST #5
505 ;*IN THIS TEST WE CHECK THAT WHEN CHANNEL 0 IS CONVERTED
506 ;*WITH THE MAINTENANCE BIT SET THE RESULT IS 0
507
508 000730* LOG5: BREAKS,BEGIN ;TEMPORARY RETURN TO MONITOR....
509 000730* 104407 000000* BREAKS,BEGIN ;THEN CONTINUE AT NEXT INSTRUCTION.
510 000734* 104407 000000* TST #ADPR ;FALSE READ OF A/D BUFFER
511 000740* 005777 177274 MOV #0,#VECTAD ;SET UP INTERRUPT VECTOR
512 000744* 012777 001014* 177320 MOV #105,#ADSP ;START A/D WITH MAINTENANCE SET
513 000752* 012777 000105 177256 EXITS,BEGIN ;EXIT TO MONITOR, MODULE WAIT FOR INTERRUPT.
514 000760* 104400 000000* 18: TST #ADPR ;TEST FOR ALL 0'S RESULT
515 000764* 005777 177250 ;NOTE: INTERRUPT SERVICE WILL
516 ;BEGIN USING HERE AFTER A PIRO.
517 ;IF RESULT IS ALL 0'S, GOTO NEXT TEST
518 000770* 001414 REG LOG6 ;RECORD A/D CSR ADDRESS
519 000772* 016767 177240 177100 MOV #ADSR,CSRA ;RECORD CONTENTS OF A/D CSR
520 001000* 017767 177232 177074 MOV #ADSR,ACSR
521 ;*****
522 001006* 104405 000000* 000000 HRDERS,BEGIN,NULL ;FAILED TO GET ALL 0'S RESULT FROM CONVERSION
523 ;*****
524 ;*A/D INTERRUPTS TO HERE
525
526 001014* 26: ;-----
527 ;-----
528 001014* 000004 000000* 000764* PIROs,BEGIN,10 ; QUEUE UP TO CONTINUE AT 10 AND RTI
529 ;-----
530
531 ;*
532 ;*LOGIC TEST #6
533 ;*IN THIS TEST WE CHECK THAT WHEN CHANNEL 1 IS CONVERTED
534 ;*WITH THE MAINTENANCE BIT SET THE RESULT IS 777
535
536 001022* 012777 001074* 177242 LOG6: MOV #0,#VECTAD ;SET UP INTERRUPT VECTOR
537 001030* 012777 000505 177200 MOV #505,#ADSP ;START A/D WITH MAINTENANCE SET
538 001036* 104400 000000* EXITS,BEGIN ;EXIT TO MONITOR, MODULE WAIT FOR INTERRUPT.
539 001042* 022777 007777 177170 16: CMP #7777,#ADBR ;TEST FOR ALL 1'S RESULT
540 001050* 001414 REG LOG7 ;IF RESULT IS ALL 1'S, GOTO NEXT TEST
541 001052* 016767 177160 177020 MOV #ADSR,CSRA ;RECORD A/D CSR ADDRESS
542 001060* 017767 177152 177014 MOV #ADSR,ACSR ;RECORD CONTENTS OF A/D CSR
543 ;*****
544 001066* 104405 000000* 000000 HRDERS,BEGIN,NULL ;FAILED TO GET ALL 1'S RESULT FROM CONVERSION
545 ;*****
546 ;*A/D INTERRUPTS TO HERE
547
548
549 001074* 000004 000000* 001042* PIROs,BEGIN,10 ; QUEUE UP TO CONTINUE AT 10 AND RTI
550 ;-----

```

```

551                                     I*
552                                     I*LOGIC TEST #7
553                                     I*IN THIS TEST WE WILL SAMPLE ALL CHANNELS SELECTED
554                                     I*FOR TEST AND STORE AWAY THEIR RESULTS.
555
556 001102* 000000* 000000* LOG7: BREAK$,REGIN ;TEMPORARY RETURN TO MONITOR....
557 001102* 104407 000000* BREAK$,BEGIN ;THEN CONTINUE AT NEXT INSTRUCTION.
558 001106* 104407 000000* MOV #200,ADDR ;LOAD CORRECTION DAC
559 001112* 012777 000200 177120 TST ADDR ;FALSE READ OF A/D BUFFER.
560 001120* 005777 177114 CLR ADDR ;CLEAR A/D'S CSR
561 001124* 005077 177106 CLR CCH ;START ON CH. 0.
562 001130* 005067 177132 MOV #4,AVECTAD ;SET UP INTR. VECTOR.
563 001134* 012777 001262* 177130
564
565 001142* 012700 177770 1$: MOV #8,R0 ;SET TO DO 8 CONVERSIONS.
566 001146* 005001 CLR R1 ;R1 WILL CONTAIN SUM OF CONVERSIONS.
567 001150* 016767 177112 177100 MOV CCH,TMP ;GET CH. NUMBER.
568 001156* 000367 177074 SWAB TMP ;PUT IN CORRECT CSR POSITION.
569 001162* 052767 000101 177066 BIS #BIT6|BIT0,TMP ;ADD INTR. ENABLE AND GO.
570 001170* 016777 177062 177040 2$: MOV TMP,ADSR ;START A/D.
571 001176* 104400 000000* EXIT$,REGIN ;EXIT TO MONITOR. MODULE WAIT FOR INTERRUPT.
572
573 001202* 067701 177032 3$: ADD ADDR,R1 ;SUM THIS RESULT.
574 ;NOTE INTR SERVICE WILL
575 ;BEING USE HERE AFTER A PIRQ
576 001206* 005200 INC R0 ;DONE 8 CONVERSIONS?
577 001210* 100767 BMI 2$ ;NO - DO ANOTHER ONE.
578
579 001212* 006201 ASR R1 ;NOW WE MUST
580 001214* 006201 ASR P1 ;CALCULATE THE AVERAGE
581 001216* 006201 ASR R1 ;OF THE SAMPLES THAT
582 001220* 003501 ADC R1 ;WE JUST TOOK.
583 001222* 016702 177040 MOV CCH,R2 ;PICK UP CH. NUMBER.
584 001226* 006302 ASL P2 ;USE IT AS AN OFFSET.
585 001230* 010162 003162* MOV R1,RECSAM(2) ;STORE THIS AVERAGE.
586
587 001234* 005267 177026 INC CCH ;UPDATE CH. NUMBER.
588 001240* 026767 177022 176762 CMP CCH,NLSTCH ;DONE ALL NOISE CHANNELS?
589 001246* 003735 BLE 1$ ;NO-DO NEXT ONE.
590 001250* 026767 177012 176746 CMP CCH,CLSTCH ;DONE ALL STABLE CHS?
591 001256* 003731 BLE 1$ ;NO -DO NEXT ONE.
592 001260* 000403 BR REST0 ;YES-GOTO NOISE TESTS.
593
594 ;*A/D INTERRUPTS TO HERE
595
596
597 001262* 4$: ;
598 ;-----
599 001262* 000004 000000* 001202* PIRQ$,BEGIN,3$ ; QUEUE UP TO CONTINUE AT 3$ AND RTI
600 ;-----
601

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602 001270* 005077 176742 REST0: CLR ADSR ;CLEAR A/D'S CSR.
603 001274* 016700 176772 MOV VECTAD,R0 ;SET VECTOR AND PSW
604 001300* 012720 002262* MOV #WAIT,(R0)+ ;LOAD INTR. ADDR. INTO VECTOR ADDR.
605 001304* 116710 176502 MOVVB R1,(R0) ;LOAD PRIORITY
606 001310* 005067 176750 CLR NCCH ;INIT THE CHANNEL #
607 ;CALCULATE A/D RMS NOISE USING VERNIER DAC.
608
609 001314* 012700 000657 ADMRS1: MOV #431,R0 ;R0 = 84.13% OF 512 CONVERSIONS
610 001320* 016767 176740 176730 MOV NCCH,TMP ;GET THE CHAN #
611 001326* 000367 176724 SWAB TMP ;PUT IN PROPER CSR POSITION.
612 001332* 052767 000100 176716 BIS #100,TMP ;ADD INTR. ENABLE.
613 001340* 016701 176720 MOV NCCH,R1 ;PICK UP CH. NUMBER.
614 001344* 006301 ASL R1 ;FORM AN OFFSET.
615 001346* 016167 003162* 176700 MOV RECSAM(R1),EDGE ;GET EDGE VALUE FOR THIS CH.
616 001354* 005267 176666 INC WHO ;INDICATE RMS NOISE TEST.
617 001360* 016777 176672 176650 MOV TMP,ADSR ;CH,#0, AND INTERRUPT ENABLE
618 001366* 016701 176646 MOV DAC,R1 ;R1 = ADDRESS OF SAR DAC
619 001372* 004767 000532 JSR PC,SAR ;GET DAC VALUE THAT PRODUCES 16/84 SPLIT
620 001376* 016705 176650 MOV FRED,R5 ;SAVE LEFT BOUNDARY
621 001402* 012700 000121 MOV #81,R0 ;R0 = 15.87% OF 512 CONVERSIONS
622 001406* 004767 000536 JSR PC,SAR ;GET DAC VALUE THAT PRODUCES 84/16 SPLIT
623 001412* 166705 176634 SUB FRED,R5 ;R5 = BREADTH OF NOISE # 68% AREA
624 001416* 010567 176636 MOV R5,ARMX ;SAVE FOR DAC NOISE CALCULATIONS
625 001422* 020567 176604 CMP R5,ARMLIM ;< OR = RMS LIMIT?
626 001426* 003410 BLE APK1 ;IF WITHIN LIMIT THEN CONTINUE AT ADMRS2
627 001430* 004767 001052 JSR PC,ERCOM ;GET ERROR PARAMETERS
628 ;ERROR PARAMETERS LOADED BY "ERCOM"
629
630 001434* 104405 000000* 000000 ;*****
631 ;*****
632 001442* 104403 000000* 002606* ;*****
633 ;*****
634 ;CALCULATE A/D PEAK NOISE USING VERNIER DAC.
635
636 001450* 012700 000775 ADPK1: MOV #509,R0 ;FOR EAK OF 2 1/2 SIGMA
637 001454* 016767 176604 176574 MOV NCCH,TMP ;GET CHAN. NUMBER.
638 001462* 000367 176570 SWAB TMP ;PUT IN CORRECT CSR POSITION.
639 001466* 052767 000100 176562 BIS #100,TMP ;ADD INTR. ENABLE
640 001474* 005067 176546 CLR WHO ;INDICATE PEAK NOISE TEST.
641 001500* 016777 176552 176530 MOV TMP,ADSR ;CH,AND INTERRUPT ENABLE, AND CH
642 001506* 016701 176526 MOV DAC,R1 ;R1 = ADDRESS OF SAR DAC
643 001512* 004767 000432 JSR PC,SAR ;GET DAC VALUE THAT GIVES .6% LOW COUNT
644 001516* 016705 176530 MOV FRED,R5 ;R5 = LEFT BOUNDARY
645 001522* 012700 000003 MOV #3,R0 ;CHANGE SPLIT FOR RIGHT BOUNDARY
646 001526* 004767 000416 JSR PC,SAR ;GET DAC VALUE THAT GIVES .6% HIGH COUNT
647 001532* 166705 176514 SUB FRED,R5 ;R5 = A/D PEAK TO PEAK NOISE
648 001536* 010567 176520 MOV R5,APKX ;SAVE FOR DAC NOISE CALCULATIONS
649 001542* 020567 176466 CMP R5,APK1 ;< OR = A/D PEAK NOISE LIMIT?
650 001546* 003410 BLE ENDP ;BRANCH IF NO ERROR
651 001550* 004767 000732 JSR PC,ERCOM ;GET ERROR PARAMETERS
652 ;ERROR PARAMETERS LOADED BY "ERCOM"
653 ;*****
654 ;*****
655 001554* 104405 000000* 000000 ;*****
656 ;*****
657 001562* 104403 000000* 002630* ;*****
658 ;*****

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XNNA0,P11 19-FEB-79 11117
656 001570* 032767 000004 176220 ENDP: BIT #BIT2,SR1 ;DOING MULTIPLE NOISE CHANNELS?
657 001576* 001411 REQ 3S ;NO = BYPASS
658 001600* 000402 BR 2S
659 001602* 000167 177506 18: JMP ATRMS1 ;NO DO AGAIN.
660 001606* 005267 176452 26: INC NCCH ;POINT TO NEXT CH.
661 001612* 026767 176446 176410 CMP NCCH,NLSTCH ;EXCEED LAST NOISE CH?
662 001620* 101770 RLOS 1S ;NO-TEST THIS CH.
;BY NOW THE PROGRAM HAS RUN THE LOGIC AND NOISE TESTS ON ALL SELECTED CHANNELS ON THIS U
663 001622* 032767 000002 176166 38: BIT #BIT1,SR1 ;ARE WE DOING VOLTAGE SAMPLING?
664 001630* 001510 REQ ENDP ;NO = END PASS1
665 001632* 005067 176430 CLR CCH ;YES = START WITH CH 0.
;VOLTAGE SAMPLING HAS BEEN ENABLED == NOW TEST IF BEFORE REPORTING EOP
666 001636* 026767 176424 176360 48: CMP CCH,CLSTCH ;DOME ALL CHANNELS?
667 001644* 003102 BGT EENDP ;YES = REPORT END PASS.
668 001646* 005003 CLR R3 ;R3 WILL HOLD SAMPLE.
669 001650* 012700 000010 MOV #R,,R0 ;SET TO TAKE 8 SAMPLES
670 001654* 012777 001704* 176410 MOV #8,AVECTAD ;SET VECTOR FOR INTERRUPT
671 001662* 016701 176400 MOV CCH,R1 ;GET CH. NUMBER
672 001666* 000301 SWAB R1 ;FIX RIGHT POSITION IN CSR.
673 001670* 052701 000101 BIS #101,R1 ;ADD INTR. ENABLE AND GO.
674 001674* 010177 176336 58: MOV R1,ADDSR ;START A/D.
675 001700* 104400 000000* EXITS,REGIN ;EXIT TO MONITOR, MODULE WAIT FOR INTERRUPT.
676 001704* 68: ;-----
677 001704* 000004 000000* 001712* ;PIPOS,REGIN,78 ; QUEUE UP TO CONTINUE AT 78 AND RTI
678 001712* 067703 176322 78: ADD #ADBR,R3 ;ADD THIS SAMPLE TO TOTAL
679 001716* 005300 DEC R0 ;ALL DONE ALL SAMPLES?
680 001720* 001365 RNE 5S ;NO = DO NEXT ONE.
681 001722* 006203 ASP R3 ;YES NOW WE
682 001724* 006203 ASP R3 ;MUST DIVIDE BY
683 001726* 006203 ASP R3 ;8 TO GET THE
684 001730* 005503 ADC R3 ;SAMPLE AVERAGE.
685 001732* 016700 176330 MOV CCH,R0 ;NOW GET CH. NUMBER
686 001736* 006300 ASL R0 ;FORM AN OFFSET
687 001740* 010301 MOV R3,R1 ;SAVE NEW SAMPLE VALUE.
688 001742* 166003 003162* SUB #RCSAM(R0),R3 ;GET THE DIFFERENCE BETWEEN
;AND THE NEW ONE WE JUST GOT
689 001746* 100001 RPL R5 ;IF POSITIVE WE'RE OK.
690 001750* 005403 NFG R3 ;OTHERWISE MAKE IT POSITIVE.
691 001752* 020367 176250 88: CMP R3,OFFALL ;IS IT WITHIN TOLERANCE?
692 001756* 003431 RLE 10S ;YES DO NEXT CH
;*****
;CONVERT CCH TO ASCII AND
;STORE AT CHANN
OTOAS,REGIN,CCH,CHANN
;*****
MOV #RCSAM(R0),OLDS ;GET OLD VALUE
;*****
;CONVERT OLDS TO ASCII AND
;STORE AT OLDS

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XNNA0,P11 19-FEB-79 11117
712 001776* 104420 000000* 003362* OTOAS,REGIN,OLDS,OLDS
713 002004* 003362* ;*****
714 002006* 010167 001360 MOV R1,NEWS ;GET NEW VALUE
715 ;*****
716 ;CONVERT NEWS TO ASCII AND
717 ;STORE AT NEWS
718 OTOAS,REGIN,NEWS,NEWS
719 ;*****
720 002022* 010160 003162* MOV R1,RECSAM(R0) ;PUT NEW INTO OLD.
721 ;*****
722 ;*****
723 ;*****
724 ;*****
725 ;*****
726 ;*****
727 002026* 104405 000000* 000000 HDRPS,REGIN,NULL ;REPORT NEW VALUE EXCEEDS THE ALLOWED TOLERANCE
728 ;*****
729 002034* 104403 000000* 002672* MSGNS,REGIN,MSG12 ;ASCII MESSAGE CALL WITH COMMON HEADER
730 ;*****
731 002042* 005267 176220 108: INC CCH ;LOOK AT NEXT CHAN.
732 002046* 000167 177564 JMP 4S ;GO TEST IT
733 ;*****
734 002052* 032767 000001 175736 FENDP: BIT #BIT0,SR1 ;TEST IF MCKW ENABLED
735 002060* 001025 BNE 1S ;RR IF YES <DO NOT DO MULTIPLE UNITS>
736 002062* 006367 176202 ASL TEMP ;MOVE OVER
737 002066* 022767 000020 176174 CMP #BIT4,TEMP ;TEST IF MAX. UNIT
738 002074* 001417 BEQ 1S ;RR IF MAX
739 002076* 036767 176166 175710 BIT TEMP,DVID1 ;TEST IF SELECTED
740 002104* 001413 BEQ 1S ;RR IF NOT
741 002106* 062767 000004 176122 ADD #4,ADSR ;UPDATE ADDRESS
742 002114* 062767 000004 176116 ADD #4,ADBR
743 002122* 062767 000010 176142 ADD #10,VECTAD ;UPDATE VECTOR
744 002130* 000167 176264 JMP LOOP ;LOOP AND TEST NEXT UNIT
745 002134* 005067 176124 18: CLR NCCH ;INIT THE CHANNEL #
746 002140* 104413 000000* FNDITS,REGIN ;SIGNAL END OF ITERATION.
747 ;MONITOR SHALL TEST END OF PASS
748 002144* 000167 176124 JMP RESTRT
749 ;USING SUCCESSIVE APPROXIMATION AND VERNIER DAC DEFINED IN R1.
750 ;*****
751 ;*****
752 002150* 012702 000200 SAR: MOV #200,R2 ;R2 = MSB OF DAC
753 002154* 005011 CLR (R1) ;GET RID OF 'ONES'
754 002156* 005067 176070 CLR FRED ;START WITH ZERO DAC
755 002162* 006267 176064 BIT: ADD R2,FRED ;TRY THIS BIT
756 002166* 016711 176060 MOV FRED,(R1) ;LOAD DAC.
757 002172* 005003 CLR R3 ;INIT HIGH COUNT
758 002174* 012704 001000 CONV: MOV #512,,R4 ;R4 = # OF SAMPLES IN A BURST
759 002200* ;*****
760 ;*****
761 002200* 032767 000001 175610 BIT #BIT0,SR1 ;IS CLOCK SELECTED?
762 002206* 001004 BNE 1S ;YES GOTO HANDLER.
763 ;*****
764 002210* 052777 000001 176020 BIS #BIT0,ADDSR ;NO = SET GO BIT IN A/D.
765 002216* 000420 BR 2S ;GOTO 28
766 ;*****
767 002220* 004767 000172 18: JSR PC,RANDY ;GET A RANDOM NUMBER.

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768 002224* 005077 176012      CLR  #ASR          ;MAKE SURE THE CLOCK'S CSR IS CLEAR,
769 002230* 052767 177770 000242  BIS  #177770,RNA  ;MAKE SURE OF HIGH NUMBER,
770 002236* 016777 000236 176000  MOV  #R0,#ASR    ;SET CLOCK PRESET REG.
771 002244* 052777 000040 175764  BIS  #R15,#ADSR  ;SET OVERFLOW ENABLE.
772 002252* 012777 000011 175762  MOV  #11,#ASR    ;START CLOCK
773 002260*          ;
774 002260* 104400          20:  EXIT#          ;RETURN TO MONITOR.
775 002262*          WAIT#          ;
776          ;
777 002262* 000004 000000* 002270*  PIRG$,BEGIN,1$      ; QUEUE UP TO CONTINUE AT 1$ AND RTI
778          ;
779 002270* 027767 175744 175756 16:  CMP  #ADPR,EDGE    ; > OR = EDGE?
780 002276* 103401          BLO  2$          ;BRANCH IF <EDGE
781 002300* 005203          INC  R3          ;COUNT IF > OR =EDGE
782 002302* 005304          DEC  R4          ;COUNT THROUGH BURST
783 002304* 001335          BNE  CONV       ;BRANCH IF NOT DONE
784 002306* 020300          CMP  R3,R0       ;HIGH COUNT > % OF 512 CONVERSIONS?
785 002310* 003402          BLE  3$          ;BRANCH TO LAVE BIT IN
786 002312* 160267 175734          SUB  R2,FRED     ;TAKE BIT OUT
787 002316* 006202          ASP  R2          ;NEXT BIT
788 002320* 001320          BNE  RIT        ;AND GO RESOLVE IT IF NOT DONE
789 002322* 005767 175724          TST  FRED       ;CHECK FOR ALL 'ZEROS'
790 002326* 001405          BEQ  #WPERR     ;BRANCH IF INVALID RESULT
791 002330* 026727 175716 000377  CMP  FRED,#377  ;CHECK FOR ALL 'ONES'
792 002336* 001401          BEQ  #WRPERR    ;BRANCH IF INVALID RESULT
793 002340* 000207          RTS  PC        ;RETURN TO ADRM$1 OR ADPK1.
794          ;VOLTAGE NEEDED TO PRODUCE # OF HIGH COUNTS SPECIFIED IS OUT OF THE
795          ;RANGE OF THE WRAPAROUND DAC.
796          ;CLEAR INTERRUPTS, PRINT MESSAGE AND DROP MODULE.
797          ;
798 002342* 005077 175670          WRPERR: CLR  #ADSR          ;STOP A/D
799 002346* 004767 000134          JSR  PC,ERCOM    ;GET ERROR PARAMETERS
800          ;ERROR PARAMETERS LOADED BY "ERCOM"
801          ;
802 002352* 104405 000000* 000000  HDRS$,BEGIN,NULL  ;NOISE TEST WRAPAROUND ERROR
803          ;
804 002360* 005767 175662          TST  #0          ;SEE WHICH TEST WE WERE DOING.
805 002364* 001004          BNE  1$        ;WHOM1 THEN RMS NOISE TEST.
806 002366* 104403 000000* 002652*  MSGN$,BEGIN,MSG11 ;ASCII MESSAGE CALL WITH COMMON HEADER
807 002374* 000403          BR   2$        ;GO AHEAD.
808          ;
809 002376* 104403 000000* 002716* 18:  MSGN$,BFGIN,MSG13 ;ASCII MESSAGE CALL WITH COMMON HEADER
810 002404* 005267 175440          INC  #RDPAS     ;UPDATE THE ERROR COUNT.
811 002410* 005726          TST  (#SP)+     ;RESTORE STACK FROM THE JSR PC THAT
812          ;
813 002412* 000167 177152          JMP  #FDP       ;GET NEXT DIRECTIVE.

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814          ;GENERATE A RANDOM NUMBER
815          ;
816 002416* 066767 000060 000054  RANDY: ADD  #B,RNA  ;THESE FOLLOW SEQUENCE OF
817 002424* 066767 000054 000046  ADD  #C,RNA  ; INSTRUCTIONS GENERATE
818 002432* 005567 000042          ADC  #A          ; A RANDOM NUMBER
819 002436* 066767 000036 000036  ADD  #NA,RNB ; TO BE USED
820 002444* 066767 000034 000030  ADD  #RC,RNB ; TO BE LOADED
821 002452* 005567 000024          ADC  #B          ; INTO THE CLOCK'S PRESET
822 002456* 066767 000016 000020  ADD  #PA,RNC  ; BUFFER, IF SELECTED FOR TEST.
823 002464* 066767 000012 000012  ADD  #B,RNC   ; ONLY RANA WILL BE USED.
824 002472* 005567 000006          ADC  #C          ;
825 002476* 000207          RTS  PC        ;RETURN TO "CONV"
826 002500* 063241          RNA:  063241    ;RANDOM TO NUMBER A.
827 002502* 142315          PNB:  142315    ;RANDOM NUMBER B.
828 002504* 127623          RNC:  127623    ;RANDOM NUMBER C.
829          ;
830          ;
831          ;
832 002506* 016767 175524 175364  ERCOM: MOV  #ASR,CSR  ;LOAD HEADER FOR ERROR CALL
833 002514* 017767 175516 175360  MOV  #ADSR,ACSR
834 002522* 016767 175300 175354  MOV  #STAT,ASTAT
835 002530* 010567 000044          MOV  #R5,DECIM ;STACK BINARY NOISE VALUE
836          ;
837          ;
838          ;
839 002534* 104421 000000* 002600*  BTOD$,BEGIN,DECIM,DECIM
840 002542* 002600*          ;
841          ;
842 002544* 116767 000032 000363  MOVB  #FCIM+2,VALUE ;MOVE CONVERTED VALUES TO ASCII BUFFER
843 002552* 116767 000025 000357  MOVB  #DECIM+3,VALUE+2 ;FOR TYPEOUT OF ERROR
844 002560* 116767 000020 000352  MOVB  #FCIM+4,VALUE+3 ; ON RETURN
845          ;
846          ;
847          ;
848 002566* 104420 000000* 000264*  OTOA$,BEGIN,NCCH,CHANN
849 002574* 003402*          ;STORE AT CHANN
850          ;
851          ;
852          ;
853 002576* 000207          RTS  PC        ; EXIT TO CALLER.
854          ;
855          ;
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998          ;
999          ;
1000          ;

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```

;ASCII MESSAGES AND POINTERS
854
855
856 002606* 003065* MSC11: P10 ;UNIT #
857 002610* 002744* P2 ;ON CHAN
858 002612* 003406* CHANN+4 ; (CHAN NUMBER 2 DIGITS)
859 002614* 002736* P1 ; % A/D
860 002616* 002775* P4 ; RMS
861 002620* 003011* P6 ; NOISE =
862 002622* 003135* VALUE ; (CONVERTED BELOW) X,XX LSR (LIMIT =
863 002624* 003023* P7 ; 0.50 LSR) "THIS VALUE WILL CHANGE IF OPERATOR CHANGES
864 002626* 177777 -1 ; MESSAGE TERMINATOR.
865
866 002630* 003065* MSG5: P10 ;UNIT #
867 002632* 002744* P2 ;ON CHAN
868 002634* 003406* CHANN+4 ; (CHAN NUMBER 2 DIGITS)
869 002636* 002736* P1 ; % A/D
870 002640* 003003* P5 ; PEAK
871 002642* 003011* P6 ; NOISE =
872 002644* 003135* VALUE ; (CONVERTED VALUE) X,XX LSR (LIMIT =
873 002646* 003035* P8 ; 2.00 LSR) "THIS VALUE WILL CHANGE IF OPERATOR CHANGES
874 002650* 177777 -1 ; MESSAGE TERMINATOR
875
876 002652* 003065* MSG11: P10 ;UNIT #
877 002654* 002736* P1 ; % A/D
878 002656* 003003* P5 ; PEAK
879 002660* 003113* P13 ; WRAPAROUND
880 002662* 003127* P15 ; ERROR
881 002664* 002744* F2 ; ON CHAN
882 002666* 003406* CHANN+4 ; (CHAN NUMBER 2 DIGITS)
883 002670* 177777 -1 ; MESSAGE TERMINATOR
884
885 002672* 003065* MSG12: F10 ;UNIT #
886 002674* 002736* P1 ; % A/D
887 002676* 003127* F15 ; ERROR
888 002700* 002744* F2 ; ON CHAN
889 002702* 003406* CHANN+4 ; (CHAN NUMBER 2 DIGITS)
890 002704* 002756* P3 ; OLD VALUE =
891 002706* 003364* OLDS+2 ; "A/D READING 4 DIGITS
892 002710* 003047* P9 ; NEW VALUE =
893 002712* 003374* NEWS+2 ; "NEW A/D READING 4 DIGITS
894 002714* 177777 -1 ; MESSAGE TERMINATOR.
895
896 002716* 003065* MSG13: P10 ;UNIT #
897 002720* 002736* P1 ; % A/D
898 002722* 002775* P4 ; RMS
899 002724* 003113* P13 ; WRAPAROUND
900 002726* 003127* P15 ; ERROR
901 002730* 002744* F2 ; ON CHAN
902 002732* 003406* CHANN+4 ; (CHAN NUMBER 2 DIGITS)
903 002734* 177777 -1 ; MESSAGE TERMINATOR.
  
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904 002736* 040445 042057 000040 P1: ;ASCIZ '%A/D '
905 002744* 047440 020116 044103 P2: ;ASCIZ ' ON CHAN '
906 002752* 047101 000040 P3: ;ASCIZ '; OLD VALUE = '
907 002756* 020073 046117 020104
908 002764* 040526 052514 020105
909 002772* 020075 000
910 002775* 122 051515 000040 P4: ;ASCIZ 'RMS '
911 003002* 000 ;BYTE
912 003003* 120 040505 020113 P5: ;ASCIZ 'PEAK '
913 003010* 000
914 003011* 116 044517 042523 P6: ;ASCIZ 'NOISE = '
915 003016* 036440 000040
916 003022* 000 ;BYTE
917 003023* 060 032456 020060 P7: ;ASCIZ '(0.50 LSR)'
918 003030* 051514 024502 000 P8: ;ASCIZ '(2.00 LSR)'
919 003035* 062 030056 020060
920 003042* 051514 024502 000
921
922 003047* 040 042516 020127 P9: ;ASCIZ ' NEW VALUE = '
923 003054* 040526 052514 020105
924 003062* 020075 000
925 003065* 045 047115 040503 P10: ;ASCII '%MNCAD (A/D) UNIT #'
926 003072* 020104 040450 042057
927 003100* 020051 047125 052111
928 003106* 021440
929 003110* 060 040 000 P10U: ;BYTE 60,40,0
930 003113* 127 040522 040520 P13: ;ASCIZ '%WRAPAROUND '
931 003120* 047522 047125 020104
932 003126* 000
933 003127* 105 051122 051117 P15: ;ASCIZ 'ERROR'
934 003134* 000
935
936 003135* 130 054056 020130 VALUE: ;ASCIZ 'X,XX LSR (LIMIT = '
937 003142* 051514 020102 046050
938 003150* 046511 052111 036440
939 003156* 000040
940 003160* 000 ;BYTE
941
942 003162* ;EVEN
943 003162* 000100 RECSAM: ;BLKW 64. ;USED TO STORE A/D RESULTS ON UP TO 64. CHANS.
944
945 003362* 000003 OLDS: ;BLKW 3 ;USED FOR STORE OF ASCII OF OLD SAMPLE VALUE.
946 003370* 000000 ;WORD 0
947 003372* 000003 NEWS: ;BLKW 3 ;USED FOR STORE OF ASCII NEW SAMPLE VALUE.
948 003400* 000000 ;WORD 0
949 003402* 000003 CHANN: ;BLKW 3 ;USED FOR STORAGE OF ASCII OF CHAN. NUMBER.
950 003410* 000000 ;WORD 0
951 000001 ;END
  
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IDENTIFICATION

PRODUCT CODE: AC-F234A-MC  
PRODUCT NAME: CXNCBA0 NCV-11A MODULE  
PRODUCT DATE: FEB 1979  
MAINTAINER: DEC/X11 SUPPORT GROUP

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PAGE  
1.

ABSTRACT

"NCB" IS AN "IOMODX" THAT EXERCISES ONE NCV-11A INTERFACE. THE NCV-11A INTERFACE DOES MEMORY INCREMENTS VIA NPR UNTIL A WORD OR BYTE REACHES MAXIMUM CAPACITY AND ATTEMPS TO OVERFLOW. AT THIS TIME AN INTERRUPT IS GENERATED AT RR LEVEL 6. THE INTERFACE ALSO DOES TRANSFERS OF DATA TO SERIAL LOCATIONS IN CORE VIA NPR. THIS MODE IS TERMINATED BY A WORD COUNT OVERFLOW AND CONSEQUENT INTERRUPT. THE RATE OF INCREMENT OR TRANSFER IS SET BY A CLOCK SIGNAL DEVELOPED ON THE NCV11 CONTROLLER.

2. REQUIREMENTS

HARDWARE: NCV-11A INTERFACE  
STORAGE: NCB MODULE REQUIRES 1400. WORDS OF STORAGE

3. PASS DEFINITION

ONE PASS OF NCA MODULE CONSISTS OF EIGHTY ITERATIONS OF EACH BASIC TEST SEQUENCE, WHICH RESULTS IN:

200 PROGRAM INTERRUPTS, 87,000 NON-PROCESSOR REQUESTS.

4. EXECUTION TIME

NCB RUNNING ALONE ON PDP-11/34 TAKES APPROXIMATELY 60 SECONDS.

5. CONFIGURATION REQUIREMENTS

DEFAULT PARAMETERS:

DEVADR: 172760, VECTOR: 370, BR1: 6, DEVCNT: N/A

REQUIRED PARAMETERS:

NONE

6. DEVICE/OPTION SETUP

NONE.

7. MODULE OPERATION

THE MODULE CONSISTS OF A LOGIC TEST, LIST MODE, AND MATRIX MODE DATA TRANSFERS. THE LOGIC TEST PROVIDES A QUICK TEST OF THE MAJOR REGISTER FOR OPERATION. THE LIST MODE TRANSFERS ARE FIRST EXECUTED IN MAINTENCE MODE AND AT FULL SPEED. THE MODULE WILL THEN COLLECT DATA IN MATRIX MODE AT FULL SPEED. THE SEQUENCE IS REPEATED UNTIL THE PASS COUNTER IS EXHAUSTED. UPON COMPLETION, AN END OF PASS IS REPORTED AND THE MODULE IS RESTARTED.

8. OPERATION OPTIONS

SRI IS USED TO INHIBIT TESTING MODES OF OPERATION OF THE NCV11A. IF THE BITS ARE USED, THE INTERATION COUNT LOCATION "PASS" MUST BE MODIFIED TO EXTEND THE EXECUTION TIME BEFORE THE END OF PASS REPORT.

SRI BIT0 = 1 INHIBIT MATRIX WORD INCREMENT MODE.  
SRI BIT1 = 1 INHIBIT LIST MODE.

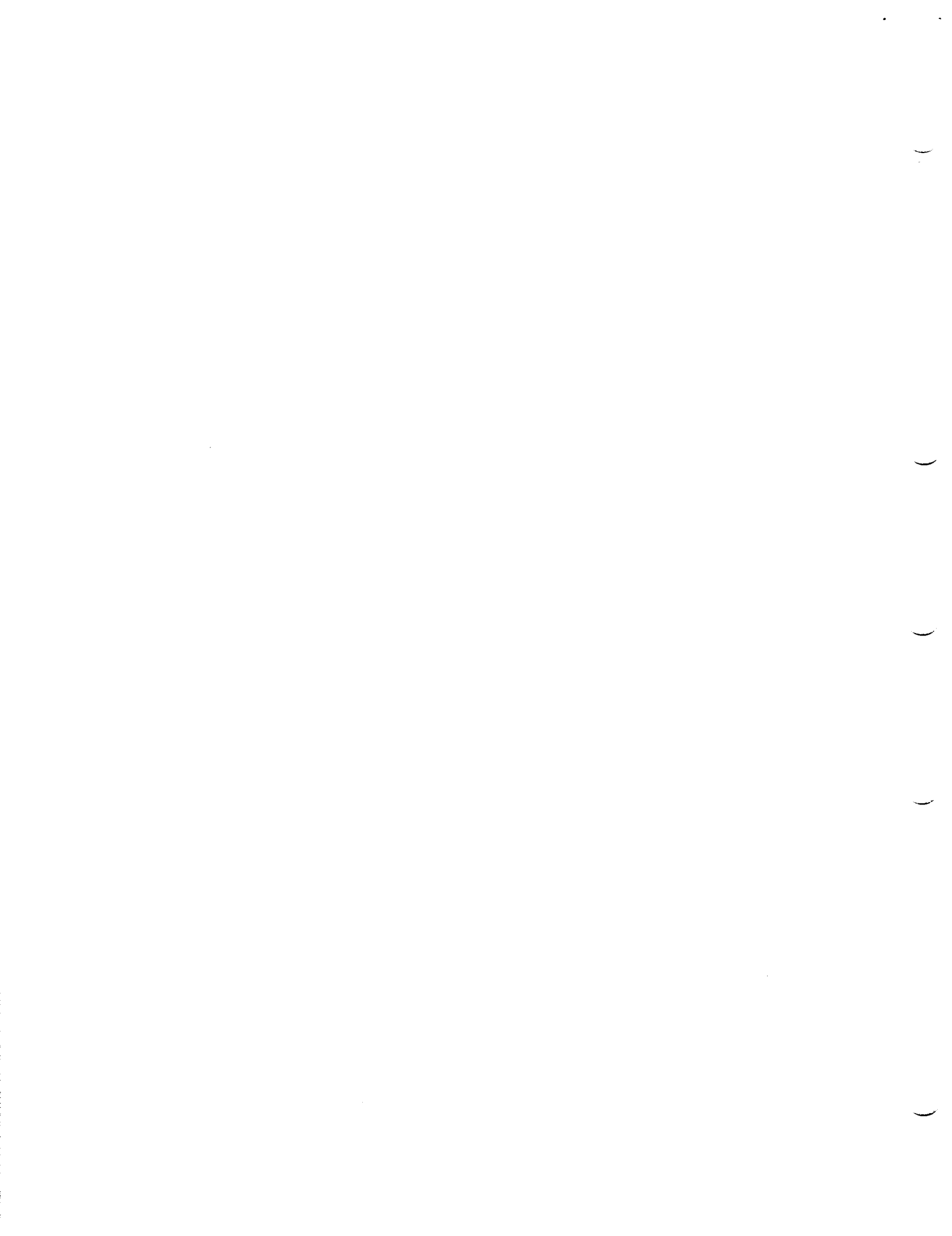
9. NON STANDARD PRINTOUTS

NONE. ALL PRINTOUTS HAVE STANDARD MEANINGS AS REPRESENTED IN DEC/X11 DOCUMENTATION.

10. MODULE TEST ENVIROMENT

THE NCA MODULE IS KNOWN TO OPERATE UNDER THIS ENVIROMENT:

#1	PDP-11/34 CPU WITH 64K	#2	PDP-11/40 CPU WITH 28K
	TC11 2 DRIVES		RK11-D 1 DRIVE
	TW11 1 DRIVE		T411 1 DRIVE
	WSV01 1 SCOPE		NCV11A 1 UNIT
	NCV11A 1 UNIT		KW11L 1 UNIT
	LP11 1 UNIT		
#3	PDP-11/34 CPU WITH 32K		
	RK11-D 1 DRIVE		
	NCV11A 1 UNIT		
	KW11L 1 UNIT		



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142
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148 00000000 041516 040502 040
149 00000005 000000
150 00000006 172760
151 00000010 000370
152 00000012 300
153 00000013 000
154 00000014 000001
155 00000016 000000
156 00000020 000000
157 00000022 000000
158 00000024 000000
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- LIST SEQ,BIN
- LIST MC,MD,CMD
- LIST ME
- LIST NCBA DEC/X11 SYSTEM EXERCISER MODULE
- LIST DDTCOM VERSION 6 23-MAY-78
- LIST BIN
*****
BEGIN: ASCII /NCBA / ;MODULE NAME
WORDM: OPEN ;USED TO KEEP TRACK OF WBUF USAGE
KFLAG: 172760+0 ;1ST DEVICE ADDR.
ADDR: 370+0 ;1ST DEVICE VECTOR.
VECTOR: 370+0 ;1ST RR LEVEL.
BR1: -BYTE PRTY0+0 ;2ND RR LEVEL.
BR2: -BYTE PRTY0+0 ;DEVICE INDICATOR 1.
DVID1: 0+1 ;SWITCH REGISTER 1.
SR1: OPEN ;SWITCH REGISTER 2.
SR2: OPEN ;SWITCH REGISTER 3.
SR3: OPEN ;SWITCH REGISTER 4.
SR4: OPEN
*****
STAT: 150000 ;STAT'S WORD.
START: START ;MODULE START ADDR.
SPDINT: MODSP ;MODULE STACK POINTER.
PASCNT: 0 ;PASS COUNTER.
ICOUNT: 0 ;# OF ITERATIONS PER PASS=80.
ICOUNT: 0 ;LOC TO COUNT ITERATIONS
SOPCNT: 0 ;LOC TO SAVE TOTAL SOFT ERRORS
SOPPAS: 0 ;LOC TO SAVE TOTAL HARD ERRORS
HRDPAS: 0 ;LOC TO SAVE SOFT ERRORS PER PASS
SYSCNT: 0 ;LOC TO SAVE HARD ERRORS PER PASS
CANUM: 0 ;# OF SYS ERRORS ACCUMULATED
CONFIC: 0 ;HOLDS RANDOM # WHEN RAWD MACRO IS CALLED
RES1: 0 ;RESERVED FOR MONITOR USE
RES2: 0 ;RESERVED FOR MONITOR USE
RES3: 0 ;RESERVED FOR MONITOR USE
SVR0: OPEN ;LOC TO SAVE R0.
SVR1: OPEN ;LOC TO SAVE R1.
SVR2: OPEN ;LOC TO SAVE R2.
SVR3: OPEN ;LOC TO SAVE R3.
SVR4: OPEN ;LOC TO SAVE R4.
SVR5: OPEN ;LOC TO SAVE R5.
SVR6: OPEN ;LOC TO SAVE R6.
CSR: OPEN ;ADDR OF CURRENT CSR.
SADDR: OPEN ;ADDR OF GOOD DATA, OR
ACSR: OPEN ;CONTENTS OF CSR
WASADR: OPEN ;ADDR OF BAD DATA, OR
ASTAT: OPEN ;STATUS REG CONTENTS.
ERRTYP: 0 ;TYPE OF ERROR
ASB: OPEN ;EXPECTED DATA.
AWC: OPEN ;ACTUAL DATA.
RSTART: RSTART ;RSTART ADDRESS AFTER END OF PASS
WDT0: OPEN ;WORDS TO MEMORY PER ITERATION
WDP1: OPEN ;WORDS FROM MEMORY PER ITERATION
INTR: OPEN ;# OF INTERRUPTS PER ITERATION
IDNUM: 0 ;MODULE IDENTIFICATION NUMBER=0

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196 000124 003500
197 000125 000000
198 000126 000000
199 000127 000400
200 000134 000000
201 000136 000000
202 000137 000000
203 000138 000000
204 000142 000000
205 000144 000000
206 000150 000000
207 000252
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RBUFA: BUFD ;READ BUFFER VIRTUAL ADDRESS
RBUFA: BUFD ;READ BUFFER PHYSICAL ADDRESS
RBUFA: OPEN ;READ BUFFER EA BITS
RBUFSZ: 756 ;SIZE OF THE READ BUFFER
WBUFA: OPEN ;WRITE BUFFER PHYSICAL ADDRESS
WBUFA: OPEN ;WRITE BUFFER EA BITS
WBUFG: OPEN ;WRITE BUFFER SIZE REQUESTED
WBUFSZ: OPEN ;WRITE BUFFER SIZE AVAILABLE
CDERCT: OPEN ;CDATA/DATCK ERROR COUNT
CDWDCT: OPEN ;CDATA/DATCK WORD COUNT
FREE: OPEN ;RESERVED FOR FUTURE USE
MODSP:
*****
STX=0
ABASE=0
AVECT1=0
TSTDMA=BIT3
TSTDMA=BIT8
TSTZ=BIT1
TSTCOM=BIT2
TSTDMA=BIT3
CLEARL=BIT11
CSR:
ABASE:
OFF: ABASE+2
WCR: ABASE+4
SPR: ABASE+6
ADM: ABASE+10
JOY: ABASE+12
BACK: ABASE+14
CARR: ABASE+16
SPRNB: ABASE+11
VECTA0: AVECT1
VECTA1: AVECT1+2
VECTA2: AVECT1+4
VECTB1: AVECT1+6
START:
RSTART:
LOGIC:
MOV #CLRALL, @SPR ;CLEAR NCV11
MOV #CSR, R0 ;LOAD ADDRESS POINTER
MOV #R0, R1 ;SET BUS ADDRESS
1S: MOV #R1, (R0)+ ;LOAD DEVICE ADDRESS
ADD #2, R1 ;UPDATE VALUE
CMP R0, #CSRNB ;TEST IF DONE
BNE 1S
MOV ADDR, (R0) ;LOAD BYTE ADDRESSES
INC (R0)+ ;LOAD BYTE OF SPR
ADD #1, (R0)+
MOV VECTOR, R1 ;LOAD VECTORS
2S: MOV R1, (R0)+ ;UPDATE
CWE #VECTB1+2, R0 ;RBR UNTIL DONE
MOV ADDR, CSRA ;LOAD FOR TYPE OUT

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*****
;VERIFY THE 8 NCV11 BUS ADDRESSES RESPOND
;*****
TST1:
BREAKS,BEGIN ;TEMPORARY RETURN TO MONITOR.
BREAKS,BEGIN ;THEN CONTINUE AT NEXT INSTRUCTION.
TST OCSPR ;ADDRESS
TST OFF ;THE
TST MCSR ;NCV11
TST OBAR ;
TST OSPR ;ADDRESSES
TST OADM ;
TST OARI ;
;*****
;FLOAT A 1 ACROSS 10 BITS OF THE COMMAND/STATUS REG.
;*****
TST2:
BREAKS,BEGIN ;TEMPORARY RETURN TO MONITOR.
BREAKS,BEGIN ;THEN CONTINUE AT NEXT INSTRUCTION.
MOV #BIT11,STEMP ;LOAD INITIAL REG. VALUE
MOV #OCSPR,ACSR ;LOAD CSR REG.
MOV #ACSR,ASTAT ;READ CSR
MOV #STEMP,ACSR ;LOAD EXPECTED
RIS #BIT7,ACSR ;LOAD THE "READY" BIT
CMP #ACSR,ASTAT ;COMPARE THE VALUES
BEQ #25 ;/BR IF SAME
;*****
;UNEXPECTED VALUE IN THE CSR REGISTER
;*****
TST3:
BREAKS,BEGIN ;TEMPORARY RETURN TO MONITOR.
BREAKS,BEGIN ;THEN CONTINUE AT NEXT INSTRUCTION.
ASR #STEMP ;TRY THE NEXT DATA BIT
CMP #1,STEMP ;TEST IF NOW BIT 0
BNE #25 ;BR IF NOT
;*****
;VERIFY THAT "CLEAR ALL" CLEARS THE CSR REGISTER
;*****
TST3:
BREAKS,BEGIN ;TEMPORARY RETURN TO MONITOR.
BREAKS,BEGIN ;THEN CONTINUE AT NEXT INSTRUCTION.
MOV #BIT7,OCSPR ;LOAD "CLR ALL 1"
MOV #BIT7,ACSR ;LOAD EXPECTED VALUE
MOV #ACSR,ASTAT ;READ THE CSR REG.
CMP #ACSR,ASTAT ;COMPARE VALUES
BEQ #25 ;/BR IF SAME
;*****
; "CLR ALL 1" FAILED TO CLEAR CSR REG.
;*****

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*****
;VERIFY LOW BYTE OPERATION OF THE "CSR" REGISTER
;*****
TST4:
BREAKS,BEGIN ;TEMPORARY RETURN TO MONITOR.
BREAKS,BEGIN ;THEN CONTINUE AT NEXT INSTRUCTION.
MOV #3636,OCSPR ;LOAD CSR REGISTER
MOV #3600,ACSR ;LOAD EXPECTED VALUE
CLR #OCSPR ;CLEAR LOW BYTE
MOV #ACSR,ASTAT ;READ STATUS REG.
CMP #ACSR,ASTAT ;COMPARE VALUES
BEQ #25 ;/BR IF SAME
;*****
;CLEARING LOW BYTE OF THE CSR CHANGED THE HIGH BYTE
;*****
TST5:
BREAKS,BEGIN ;TEMPORARY RETURN TO MONITOR.
BREAKS,BEGIN ;THEN CONTINUE AT NEXT INSTRUCTION.
MOV #BIT4,STEMP ;LOAD INITIAL REG. VALUE
MOV #STEMP,OSPR ;LOAD SPR REG.
MOV #STEMP,ACSR ;LOAD EXPECTED
CMP #ACSR,ASTAT ;COMPARE THE VALUES
BEQ #25 ;/BR IF SAME
;*****
;UNEXPECTED VALUE IN THE SPR REGISTER
;*****
TST6:
BREAKS,BEGIN ;TEMPORARY RETURN TO MONITOR.
BREAKS,BEGIN ;THEN CONTINUE AT NEXT INSTRUCTION.
ASR #STEMP ;TRY THE NEXT DATA BIT
CMP #1,STEMP ;TEST IF NOW BIT 0
BNE #25 ;BR IF NOT
;*****

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447
448 001478- 104407 000000- 176554
449 001478- 104407 000000- 176554
450 001474- 104407 000000- 176554
451
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454 001500- 012777 004900 176554
455 001500- 012777 023333 176536
456 001500- 012777 033333 176536
457 001500- 012777 000036 176524
458 001500- 012777 007636 176506
459
460
461 001544- 012767 011111 176330
462 001544- 012767 176476 176330
463 001560- 026767 176316 176316
464 001566- 001403
465
466 001570- 104405 000000- 000000
467
468 001576- 012767 022222 176276
469 001604- 012767 176446 176272
470 001612- 026767 176264 176264
471 001620- 001403
472
473 001622- 104405 000000- 000000
474
475 001630- 012767 033333 176244
476 001630- 012767 176416 176244
477 001644- 026767 176232 176232
478 001652- 001403
479
480 001654- 104405 000000- 000000
481
482 001652- 012767 000036 176212
483 001652- 012767 176306 176206
484 001704- 001403
485
486 001706- 104405 000000- 000000
487
488 001714- 012767 007636 176160
489 001714- 012767 176436 176154
490 001730- 026767 176146 176146
491 001736- 001403
492
493 001740- 104405 000000- 000000
494 001746- 012777 004000 176306

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495
496
497
498 001754- 104407 000000- 176112
499 001760- 104407 000000- 176112
500 001764- 005067 176112
501 001770- 012777 004000 176264
502 001776- 012777 000003 176250
503 002004- 012777 004000 176250
504 002006- 001403 176236 176064
505
506 002022- 104405 000000- 000000
507
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520 002030- 104407 000000- 176214
521 002034- 104407 000000- 176214
522 002040- 012777 004000 176206
523 002044- 052777 000022 176170
524
525 002062- 012767 000027 176012
526 002066- 012777 000027 176104
527 002070- 012767 176406 176104
528 002104- 012767 175472 175742
529 002112- 001406
530 002114- 052777 000400 176140
531
532 002122- 104405 000000- 000000
533
534 002130- 052777 000400 176124
535 002136- 012767 000222 175736
536 002144- 012767 176102 175732
537 002160- 001403 175724 175724
538
539 002162- 104405 000000- 000000
540
541 002170- 012767 000200 175704
542 002176- 012767 000000 175704
543 002204- 052777 004000 175656
544 002212- 012767 176034 175664
545 002220- 026767 175656 175656
546 002226- 001403
547
548 002230- 104405 000000- 000000

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*****
;ENABLE A 512 WORD TRANSFER SECTION LIST MODE (MAINT MODE)
*****
TST16:
BREAKS,BEGIN ;TEMPORARY RETURN TO MONITOR.
BREAKS,BEGIN ;THEN CONTINUE AT NEXT INSTRUCTION.
BIT #17,SRI ;TEST IF INHIBIT TEST IS SET
BNE #17,C ;//BR IF SET
MOV #17,RO ;CLEAR THE DEVICE
MOV #17,RO ;LOAD BUFFER POINTER
MOV #17,RO ;PRESET THE BUFFER WITH DATA
BREAKS,BEGIN ;TEMPORARY RETURN TO MONITOR.
BREAKS,BEGIN ;THEN CONTINUE AT NEXT INSTRUCTION.
CMP #17,RO ;TEST IF DONE
BNE #17,C ;//BR IF NOT
GETPAS,BEGIN,RBUFVA ;GET PHYSICAL ADDRESS FROM 16-BIT RBUFVA
MOV #17,RO ;GET EXTENDED ADDRESS BITS
ASR RO
ASR RO
ASR RO
BIC #177774,RO ;CLEAR OFF EXCESSIVE
MOV #17,RO ;LOAD EXTENDED ADDRESS BITS
MOV #17,RO ;SET UP 512 WORD TRANSFER
MOV #17,RO ;LOAD BUS ADDRESS FOR RESULT
MOV #17,RO ;ENABLE TEST CONTROL AND DMA FLOPS
MOV #17,RO ;ENABLE DEVICE
MOV #17,RO ;LOAD THE COUNTER
BIS #17,RO ;ENABLE INT Z
BIS #17,RO ;DISABLE INT Z
BREAKS,BEGIN ;ALLOW DMA TRANSFER
BREAKS,BEGIN ;TEMPORARY RETURN TO MONITOR.
DEC #17 ;THEN CONTINUE AT NEXT INSTRUCTION.
BNE #17,C ;FINISHED ALL WORDS?
;THE TRANSFER IS NOW COMPLETE
CLR ACSR ;CLEAR EXPECTED
MOV #17,RO ;READ OFFSET REG.
BIC #17,RO ;CLEAR LOW 2 BITS
BEQ #17,C ;//BR IF HIGH 14 BITS ARE CLEARED
*****
RDERS,BEGIN,NULL ;UNEXPECTED OFFSET REGISTER BIT SET
*****
MOV #17,RO ;CLEAR THE DEVICE
MOV #17,RO ;READ BUS ADDRESS
MOV #17,RO ;GET PHYSICAL ADDRESS
ADD #17,RO ;UPDATE TO THE END OF BUFFER
CMP #17,RO ;COMPARE VALUES
BEQ #17,C ;//BR IF SAME
*****
RDERS,BEGIN,NULL ;INCORRECT BUS ADDRESS VALUE AFTER A 1 WORD TRANSFER
*****
MOV #17,RO ;READ W.C. REGISTER
MOV #17,RO ;LOAD EXPECTED W.C. VALUE
CMP #17,RO ;COMPARE VALUES
BEQ #17,C ;//BR IF SAME

```

```

*****
;ENABLE A 512 WORD TRANSFER SECTION LIST MODE
*****
TST17:
BREAKS,BEGIN ;TEMPORARY RETURN TO MONITOR.
BREAKS,BEGIN ;THEN CONTINUE AT NEXT INSTRUCTION.
BIT #17,SRI ;TEST IF INHIBIT TEST IS SET
BNE #17,C ;//BR IF SET
MOV #17,RO ;CLEAR THE DEVICE
MOV #17,RO ;LOAD BUFFER POINTER
MOV #17,RO ;PRESET THE BUFFER WITH DATA
BREAKS,BEGIN ;TEMPORARY RETURN TO MONITOR.
BREAKS,BEGIN ;THEN CONTINUE AT NEXT INSTRUCTION.
CMP #17,RO ;TEST IF DONE
BNE #17,C ;//BR IF NOT
GETPAS,BEGIN,RBUFVA ;GET PHYSICAL ADDRESS FROM 16-BIT RBUFVA
MOV #17,RO ;GET EXTENDED ADDRESS BITS
ASR RO
ASR RO
ASR RO
BIC #177774,RO ;CLEAR OFF THE EXCESS
MOV #17,RO ;LOAD EXTENDED ADDRESS BITS
MOV #17,RO ;LOAD RETURN VECTOR
MOV #17,RO ;SET UP 512 WORD TRANSFER
MOV #17,RO ;LOAD BUS ADDRESS FOR RESULT
MOV #17,RO ;GET PHYSICAL ADDRESS
ADD #17,RO ;POINT TO END OF BUFFER
CMP #17,RO ;COMPARE VALUES
BEQ #17,C ;//BR IF SAME
*****
RDERS,BEGIN,NULL ;UNEXPECTED HIGH 14 BITS OF OFFSET REGISTER BIT SET
*****
MOV #17,RO ;CLEAR THE DEVICE
MOV #17,RO ;READ BUS ADDRESS
MOV #17,RO ;GET PHYSICAL ADDRESS
ADD #17,RO ;POINT TO END OF BUFFER
CMP #17,RO ;COMPARE VALUES
BEQ #17,C ;//BR IF SAME

```

```

661 003066~ 104405 000000~ 000000 *****
662 *****;RDERS,BEGIN,NULL *****;INCORRECT BUS ADDRESS VALUE AFTER A 512 WORD TRANSFER
663 003074~ 017767 175156 175002 15: MOV @WCR,@STAT ;READ W.C. REGISTER
664 003102~ 017767 174988 174772 MOV @0,@CSR ;LOAD EXPECTED W.C. VALUE
665 003116~ 001403 174988 174766 CMP @CSR,@STAT ;COMPARE VALUES
666 BEQ @S ;//BR IF SAME
667 *****;RDERS,BEGIN,NULL *****;INCORRECT WORD COUNT REGISTER VALUE AFTER A 512 WORD T
668 003120~ 104405 000000~ 000000 *****
669 003126~ 016777 175146 175142 75: MOV @VECTA1,@VECTA0
670 003134~ 005077 175146 CLR @VECTA1 ;RESET VECTOR

```

```

672 *****;MATRIX MODE DATA COLLECTOR *****
673 *****;ST20: *****
674 003140~ 104407 000000~ 000000 *****
675 003140~ 104407 000000~ 000000 *****
676 003148~ 017767 000000~ 174640 *****
677 003156~ 001180 *****
678 003156~ 001180 *****
679 003156~ 001180 *****
680 003160~ 017760 003470~ *****
681 003162~ 017760 170300~ *****
682 003170~ 104407 000000~ *****
683 003174~ 104407 000000~ *****
684 003174~ 104407 000000~ *****
685 003204~ 001367 005500~ *****
686 *****
687 003206~ 012777 004000~ 175046 *****
688 003214~ 104415 000000~ 000124 *****
689 003214~ 104415 000000~ 000124 *****
690 003236~ 012777 003230~ 175052 *****
691 003236~ 012777 174668~ 175046 *****
692 003242~ 042780 000003 *****
693 003246~ 016701 174656 *****
694 003254~ 006201 *****
695 003254~ 006201 *****
696 003254~ 006201 *****
697 003254~ 006201 *****
698 003262~ 042701 177774 *****
699 003266~ 000100 *****
700 003278~ 010077 174760 *****
701 003278~ 010077 174760 *****
702 003302~ 015777 000000~ 174750 *****
703 003310~ 012777 000062~ 174734 *****
704 003318~ 054777 000000~ 174736 *****
705 003324~ 052777 000001~ 174720 *****
706 003332~ 104400 000000~ *****
707 *****
708 003336~ 052777 000400~ 174716 105: BIS @ENDDMA,@SFR ;STOP TRANSFERS
709 003344~ 052777 004000~ 174710 BIS @CLRALL,@SFR ;CLEAR NCV11
710 *****
711 003352~ 000004 000000~ 003360 *****;PIRQS,BEGIN,115 ;QUEUE UP TO CONTINUE AT 115 AND RTI
712 *****
713 003360~ 016777 174720~ 174714 115: MOV @VECTB1,@VECTB0
714 003366~ 000240 *****;RESET THE VECTOR
715 003374~ 000240 *****
716 003376~ 000240 *****
717 *****

```

718  
 719  
 720  
 721  
 722 003400 - 000000 -  
 723 003400 - 10 4407 000000 -  
 724 003400 - 10 4407 000000 -  
 725 003410 - 10 4413 000000 -  
 726  
 727 003414 - 000167 174670  
 728  
 729  
 730  
 731 003420 - 000024  
 732  
 733  
 734  
 735 003470 - 000240  
 736 003472 - 000240  
 737 003474 - 000240  
 738 003476 - 000240  
 739 003500 - 000400  
 740 004500 - 000400  
 741 005500 - 000000  
 742  
 743 000001

```

;*****
;END OF PASS
;*****
TST21:
BREAK2,BEGIN      ;TEMPORARY RETURN TO MONITOR...
BREAK3,BEGIN      ;THEN CONTINUE AT NEXT INSTRUCTION.
ENDIT5,BEGIN      ;SIGNAL END OF ITERATION
;MONITOR SHALL TEST END OF PASS
;RUN AGAIN

1S: JMP LOGIC

;MODULE PATCH SPACE
.BLKW 20.

;MODULE READ BUFFER SPACE
BUFA: NOP ;NOP'S MUST REMAIN <NOT A PATCH AREA>
NOP
NOP
NOP
NOP
BUF0: .BLKW 256.
BUF1: .BLKW 256.
BUF2: 0

.END

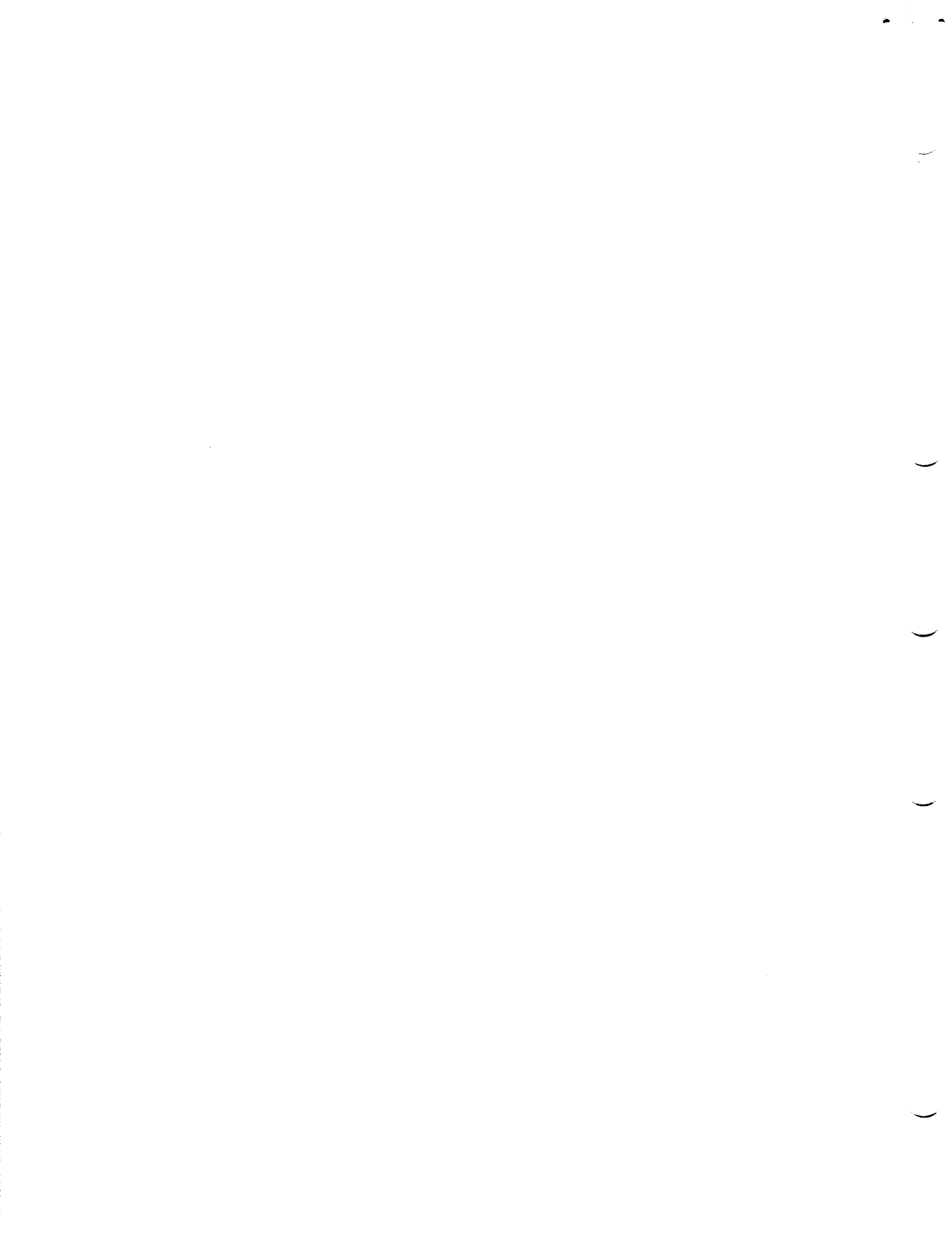
```

ABASE = 000000	210#	218	219	220	221	222	223	224	225	226	227	337	355*
ACSR = 000102R	185#	274*	375*	385*	478	490*	499*	407*	410	421*	429*	432	434
	359#	482*	488*	489*	488	505*	523*	524	480*	482	489*	501*	521*
	443#	491*	491*	491*	544	585*	641*	649	475	482	489	501*	521*
	524#	488*	489*	489*	544	585*	641*	649	475	482	489	501*	521*
	593#	491*	491*	491*	544	585*	641*	649	475	482	489	501*	521*
ADDR 000006R	151#	237	242	244	251				595*	602*	603	648*	656*
ADDR22= 001000	209#												
ADM 000264R	184#	263											
ASR 000104R	187#	273*	276	296*	297	311*	312	320*	321	335*	337	357*	358
ASTAT 000104R	187#	273*	276	296*	297	311*	312	320*	321	335*	337	357*	358
	481#	489*	490*	489*	482	505*	523*	524	480*	482	489	501*	521*
	593#	491*	491*	491*	544	585*	641*	649	475	482	489	501*	521*
AVECT1= 000000	228	228	229	230	231				596	602*	603	648*	656*
AWS 000104R	187#	273*	276	296*	297	311*	312	320*	321	335*	337	357*	358
BAR 000264R	187#	273*	276	296*	297	311*	312	320*	321	335*	337	357*	358
BAR1 000270R	228	228	229	230	231				596	602*	603	648*	656*
BEGIN 000600R	148#	261	410*	411	455*	474	573*	593	635*	655	702*		
	311#	264											
	324#	257	269	270	279	282	283	291	292	306	306	307	307
	330#	330	339	340	348	344	345	353	361	366	366	369	369
	340#	344	349	349	348	344	345	353	361	366	366	369	369
	349#	344	349	349	348	344	345	353	361	366	366	369	369
	357#	344	349	349	348	344	345	353	361	366	366	369	369
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	393#	344	349	349	348	344	345	353	361	366	366	369	369
	402#	344	349	349	348	344	345	353	361	366	366	369	369
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	915#	344	349	349	348	344	345	353	361	366	366	369	369
	924#	344	349	349	348	344	345	353	361	366	366	369	369
	933#												



TST4	000640R	298	305#																		
TST5	000760R	322	329#																		
TST6	001062R	351#																			
TST7	001346R	359#	367#																		
VECTAI	000300R	448#	634*	670*	671*																
VECTBI	000302R	250#	689*	670	713*																
VECTRI	000304R	231#	749	690*	713	714*															
VECTOR	000010R	152#	246																		
WASADR	000104R	186#																			
WBUPEA	000136R	201#																			
WBUPEB	000138R	208#																			
WBUPEZ	000140R	202#																			
WBUFSZ	000142R	203#																			
WCR	000156R	220#	260	388*	389	454*	467	572*	601	634*	663	701*									
WDR	000116R	193#																			
WDC	000114R	192#																			
XFLAG	000005R	156#																			
XTMP	000306R	232#	271*	272	274	284*	285	332*	334	336	345*	346	576*	582*							
STX	= 000021	209#	252#	265#	287#	298	302#	322	326#	348#	355	364#	374	379#							
.	= 005502R	401#	423#	445#	495#	506	511#	545	549#	556	604	609#	616	672#							
.		679	719#	739#	740#																
.		731#																			
.	ABS.	000000	000																		
.		005502	001																		

ERRORS DETECTED: 0  
 DEFAULT GLOBALS GENERATED: 0  
 XNCBA0,XNCBA0/SOL/CRF:SYM=DDXCOM,XNCBA0  
 RUN-TIME: 2 3 4 SECONDS  
 RUN-TIME RATIO: 33/55 7  
 CORE USED: 7K (13 PAGES)



1

.REM\_

IDENTIFICATION  
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PRODUCT CODE: AC-F805C-MC  
PRODUCT NAME: CXDMDC0 DMP/DHV11 DECX MSTR MOD  
PRODUCT DATE: AUGUST 1981  
MAINTAINER: DIAGNOSTIC ENGINEERING CC:38P  
AUTHORS: DAVID HOFFMAN  
CHRIS BRIENEN

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1.

ABSTRACT  
-----

THERE ARE 2 DEC/X11 IOMODX MODULES WRITTEN FOR THE DMP/DHV11. THESE ARE DMD AND DME. TOGETHER, THESE 2 MODULES CAN OPERATE UP TO 16 (DEC.) DMP/DHV11 DEVICES IN POINT-TO-POINT LINKS, OR A SINGLE DEVICE CONFIGURED AS A MULTIPOINT CONTROL STATION COMMUNICATING WITH UP TO 32 (DEC.) TRIBUTARIES, OR UP TO 16 (DEC.) DEVICES CONFIGURED AS MULTIPOINT TRIBUTARIES ON THE SAME PDP-11 UNIBUS (16/16 W/DHV11 QBUS). THE BASIC OPERATION IS TO TRANSMIT, RECEIVE, AND CHECK 32(DEC.) DATA MESSAGES OF 1024 (DEC.) BYTES EACH, ON A GIVEN PHYSICAL LINK. BY DEFAULT, THIS WOULD INVOLVE A SINGLE PDP-11 SYSTEM WITH 1 OR MORE DEVICES OPERATED IN INTERNAL OR EXTERNAL LOOPBACK MODE. HOWEVER, BY OPERATOR SELECTION OF NON-DEFAULT MODES, ACTUAL POINT-TO-POINT OR MULTIPOINT OPERATION IS POSSIBLE. IN ONE SUCH MODE, THE MODULE CAN DRIVE THE OPERATION OF A CONTROL STATION WHICH SENDS AND RECEIVES BACK MESSAGES FROM A SPECIFIED LIST OF TRIBUTARIES, ON THE SAME OR DIFFERENT PDP-11 SYSTEMS. IN ANOTHER SUCH MODE, THE MODULE CAN DRIVE THE OPERATION OF A GROUP OF TRIBUTARIES WHICH RESIDE ON A SINGLE SYSTEM AND REFLECT BACK MESSAGES SENT TO THEM BY A CONTROL STATION, ON THE SAME OR DIFFERENT SYSTEM.

DMD IS THE MASTER MODULE WHICH CAN OPERATE UP TO 16 (DEC.) DEVICES IN LOOPED-BACK OR POINT-TO-POINT MASTER MODES, OR A SINGLE DEVICE IN MULTIPOINT CONTROL MODE.

DME IS THE SLAVE MODULE WHICH WHICH CAN OPERATE UP TO 16 (DEC.) DEVICES IN POINT-TO-POINT SLAVE OR MULTIPOINT TRIBUTARY MODES.

A MASTER MODULE CAN BE SELF-SUFFICIENT (IF LOOPBACK IS USED) OR IT CAN COMMUNICATE WITH A SLAVE MODULE(S) ON THE SAME OR ANOTHER PROCESSOR. THE REST OF THIS DOCUMENT WILL DESCRIBE THE USE AND OPERATION OF THE MASTER MODULE, DMD.

IT IS REQUIRED THAT THE OPERATOR CONFIGURE THE DEC/X11 EXERCISER WITH A SEPARATE COPY OF MODULE DMD FOR EACH GROUP OF LOOPED-BACK DEVICES, EACH CONTROL STATION DEVICE, OR EACH GROUP OF POINT-TO-POINT DEVICES. THE ACTUAL OPERATING MODE OF EACH COPY IS SELECTED BY THE SETTINGS OF SW1-SW4 (SOFTWARE SWITCHES) FOR THAT COPY. FOR MULTI-PROCESSOR CONFIGURATIONS, A DEC/X11 EXERCISER MUST BE CONFIGURED AND RUN ON EACH PDP-11 PROCESSOR, SIMULTANEOUSLY.

FOR ACTUAL LINK MODE OPERATION, NOTE THE FOLLOWING :  
DO NOT ALLOW THE MODULE TO RELOCATE; USE A NON-RELOCATING MONITOR OR USE THE RUN LOCK COMMAND (RUNL).

AFTER THE MASTER MODULE (DMD) HAS BEEN STARTED ON ONE PROCESSOR, THE OPERATOR IS ALLOWED AT LEAST 5 MINUTES TO START THE SLAVE MODULE(S) (DME) ON THE OTHER PROCESSOR(S), BEFORE GETTING A PROTOCOL TRANSMIT THRESHOLD ERROR ON THE MASTER MODULE PROCESSOR.

THE M8203 LINE UNIT CONTAINS HARDWARE SWITCHES WHICH CAN DEFINE THE PHYSICAL LINK AND DMP11 OPERATING MODE, AND A TRIBUTARY ADDRESS (IF MULTIPOINT). EVERY EFFORT IS MADE TO OPERATE WITH THESE SWITCH VALUES, UNLESS THEY MUST BE OVERRIDDEN BY THE PROGRAM. FOR EXAMPLE, WITH LOOPBACK SELECTED, A TRIBUTARY COULD ONLY BE RUN AS A CONTROL STATION, AND A HALF-DUPLEX LINK COULD ONLY BE RUN AS A FULL DUPLEX LINK. IN ANY CASE, THE PROGRAM DOES NOT REQUIRE

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OPERATOR INTERVENTION TO CHANGE HARDWARE SWITCH SETTINGS OR CONFIGURE CABLES, ETC.

2. REQUIREMENTS  
-----

HARDWARE: 1 TO 16 (DEC.) DMP/DMV11'S AND ASSOCIATED CABLES AND CONNECTORS.  
STORAGE: DMD REQUIRES ABOUT 2K WORDS OF STORAGE

3. PASS DEFINITION  
-----

ONE PASS OF THE DMD MODULE CONSISTS OF TRANSMITTING AND RECEIVING A TOTAL OF 32,768 (DEC.) 8-BIT CHARACTERS, PER DMP/DMV11.

4. EXECUTION TIME  
-----

DMD RUNNING ALONE ON A PDP11/40 PROCESSOR TAKES APPROXIMATELY 1 MINUTE TO COMPLETE ONE PASS, AT 9600 BAUD. THIS TIME INCREASES ONLY SLIGHTLY AS MORE MASTER DEVICES ARE ADDED, BUT IS INVERSELY PROPORTIONAL TO THE BAUD RATE.

5. CONFIGURATION PARAMETERS  
-----

DEFAULT PARAMETERS :

DEVADR: 160170, VECTOR:300, BR1:5, BR2: NOT USED , DEVCNT:1

SOFTWARE SWITCH REGISTER OPTIONS :

THE ALLOWABLE OCTAL VALUES OF SR4 ARE:

SR4=000000 : IF TESTING DMP11  
SR4=000001 : IF TESTING DMV11  
SR4=000002 : IF TESTING DMV11 (AND Q22 SOFTWARE MODE IS DESIRED)

THE ALLOWABLE OCTAL VALUES OF SR1 ARE: 000000, 000001, AND 000002. A DESCRIPTION OF SR1-SR3 (AND THEIR MEANING) ARE GIVEN BELOW:

FOR SR1 = 000000 :

ALL UNITS SELECTED IN DVID1 WILL BE RUN IN POINT-TO-POINT, FULL-DUPLEX MODE, WITH INTERNAL OR EXTERNAL LOOPBACK ON ALL DEVICES. WHEN SR1 = 000000, SR2 HAS THE FOLLOWING MEANING :

- SR2 = 000000 IF INTERNAL LOOPBACK SHOULD BE PROVIDED BY PROGRAM, USING TTL-LEVEL LOOPBACK ON THE LINE UNIT. THIS IS THE DEFAULT MODE OF OPERATION.
- SR2 = 000001 IF EXTERNAL LOOPBACK WILL BE PROVIDED BY THE OPERATOR VIA H3254,5 TEST CONNECTORS ON EACH DEVICE. FOR DMV11'S, INTERFACE TYPE IS SELECTED BY HARDWARE. FOR THE DMP11, THE OPERATOR MUST SPECIFY THE DESIRED MODEM INTERFACE TO BE SELECTED FOR TESTING, USING SR3 :

FOR SR3 = 000000, V.35 WILL BE RUN BY DEFAULT.  
FOR SR3 = 000010, INTEGRAL MODEM WILL BE RUN.

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FOR SR3 = 000020, V.35 WILL BE RUN.  
FOR SR3 = 000100, RS449/RS232C/RS423 WILL BE RUN.  
FOR SR3 = 000200, RS422 WILL BE RUN.

THE SELECTED INTERFACE WILL BE RUN AT THE BAUD RATE SELECTED IN THE LINE UNIT BAUD RATE SWITCHES.

- SR2 = 000002 IF EXTERNAL LOOPBACK OTHER THAN H3254,5 IS PROVIDED (FOR EXAMPLE, CABLE LOOPBACK). (NOTE THAT MODEM LOOPBACK IS NOT SUPPORTED IN THIS MODULE).

THE HEADER ENTRY DVC MUST BE IN THE RANGE 1-20 (OCTAL).

FOR SR1 = 000001 :

ALL UNITS SELECTED IN DVID1 WILL BE RUN IN POINT-TO-POINT, FULL OR HALF-DUPLEX MODE (DEPENDING ON LINE UNIT SWITCH SETTINGS) WITHOUT LOOPBACK, THE MODULE COMMUNICATES WITH SLAVE MODULE(S) ON THE SAME AND/OR OTHER PDP-11 SYSTEM(S). WHEN SR1 = 000001 : SR2 AND SR3 ARE UNUSED, AND THE HEADER ENTRY DVC MUST BE IN THE RANGE 1-20 (OCTAL).

\*\*\*\*\*  
NOTE: WHEN IN POINT-TO-POINT OPERATION DO NOT ALLOW RELOCATION OF THE MONITOR. USE EITHER A MONITOR THAT DOES NOT ALLOW RELOCATION OR USE THE RUN LOCK COMMAND (RUNL).  
\*\*\*\*\*

FOR SR1 = 000002 :

ONLY 1 UNIT MUST BE SELECTED IN DVID1 (DVID1 = 000001), AND IT WILL BE RUN AS A MULTI-POINT CONTROL STATION, IN FULL OR HALF-DUPLEX MODE (DEPENDING ON LINE UNIT SWITCH SETTINGS) WITHOUT LOOPBACK. THE MODULE COMMUNICATES WITH SLAVE MODULE(S) ON THE SAME AND/OR OTHER PDP-11 SYSTEM(S).

WHEN SR1 = 000002, THE FOLLOWING MEANING IS GIVEN TO SR2 AND SR3 :

- SR2 = THE TOTAL NUMBER OF TRIBUTARIES (OCTAL) ON THIS MULTIPOINT LINK. THE ALLOWABLE RANGE IS 000001-000040(DMP; DMV = 000020).
- SR3 = THE STARTING TRIBUTARY ADDRESS (OCTAL). THE ALLOWABLE RANGE IS 000001-000377. THE PROGRAM WILL USE THIS STARTING TRIB ADRS TO COMPUTE THE OTHER TRIB ADDRESSES ON THE MULTIPOINT LINK, AND THE ADDRESSES CAN "WRAPAROUND" 377 TO 001, IF NECESSARY.

THE HEADER ENTRY DVC MUST = 1.

\*\*\*\*\*  
NOTE: WHEN IN MULTIPOINT OPERATION DO NOT ALLOW RELOCATION OF THE MONITOR. USE EITHER A MONITOR THAT DOES NOT ALLOW RELOCATION OR USE THE RUN LOCK COMMAND (RUNL).  
\*\*\*\*\*

6. DEVICE/OPTION SETUP  
-----

IF EXTERNAL LOOPBACK IS DESIRED (SR1 = 000000 AND SR2 = 000001 OR 2), TEST CONNECTOR(S) MUST BE INSTALLED BY THE OPERATOR ON ALL DEVICES, PRIOR TO STARTING THE EXERCISER.



## 7. DMD MODULE OPERATION

-----

## TEST SEQUENCE:

- A. INITIALIZE ALL MASTER DEVICES
- B. LOAD INPUT AND OUTPUT INTERRUPT VECTORS ON ALL MASTERS
- C. PERFORM MODE DEFINITION ON ALL MASTERS
- D. VERIFY OPERATION OF INPUT AND OUTPUT INTERRUPTS ON ALL MASTERS
- E. START THE DDCMP PROTOCOL ON ALL MASTERS, TO ALL SLAVES, AND SCAN EACH LOGICAL LINK UNTIL IT ENTERS THE RUNNING STATE.
- F. EXIT TO MONITOR WITH INTERRUPTS ENABLED
- G. INPUT INTERRUPT SERVICE :
  - 1) IF LOGICAL LINK WAS JUST STARTED, INPUT A RCV BUFFER
  - 2) IF RCV BUFFER WAS PREVIOUSLY INPUT, INPUT A TRANSMIT BUFFER
  - 3) IF TRANSMIT AND RCV BUFFERS HAVE BEEN INPUT ON ALL LOGICAL LINKS ON THIS DEVICE, DISABLE INPUT INTRPTS ON THIS DEVICE.
- H. OUTPUT INTERRUPT SERVICE :
  - 1) IF TRANSMIT BUFFER WAS JUST RETURNED, CHECK FOR CORRECT BA, EA, TRANSMIT CHAR COUNT BITS.
  - 2) IF RCV BUFFER WAS JUST RETURNED, CHECK FOR CORRECT BA, EA, RCV CHAR COUNT BITS, AND DO DATA CHECK ON RCV BUFFER.
  - 3) IF ALL LOGICAL LINKS HAVE TRANSMITTED AND RECEIVED CURRENT DATA, SELECT A NEW BUFFER FULL OF DATA.
  - 4) IF ALL LOGICAL LINKS HAVE DONE REQUIRED NUMBER OF WRITE, READ, CHECK SEQUENCES, REPORT AN END OF PASS. THEN, RESTART THE PROTOCOL ON ALL MASTERS TO ALL SLAVES.
  - 5) REPORT ANY ERRORS PROVIDED BY ANY MASTER DEVICE(S). ALSO, AT THE END OF EACH PASS, THE PROGRAM WILL REPORT ANY SOFT (DATA OR HEADEK CRC) ERRORS THAT HAVE BEEN DETECTED DURING THE PASS.
- I. REPEAT G AND H

## 8. OPERATION OPTIONS

-----

- A. LOCATION DVID1 (DMD 14) MAY BE CHANGED TO SELECT ANY COMBINATION OF DEVICES BIT0=DEV0, BIT1=DEV1 .....BIT15=DEV15.

NOTE: IF DVID1 IS INITIALLY = 0, DMD WILL BE DROPPED FROM TEST.

## 9. ERROR REPORTS

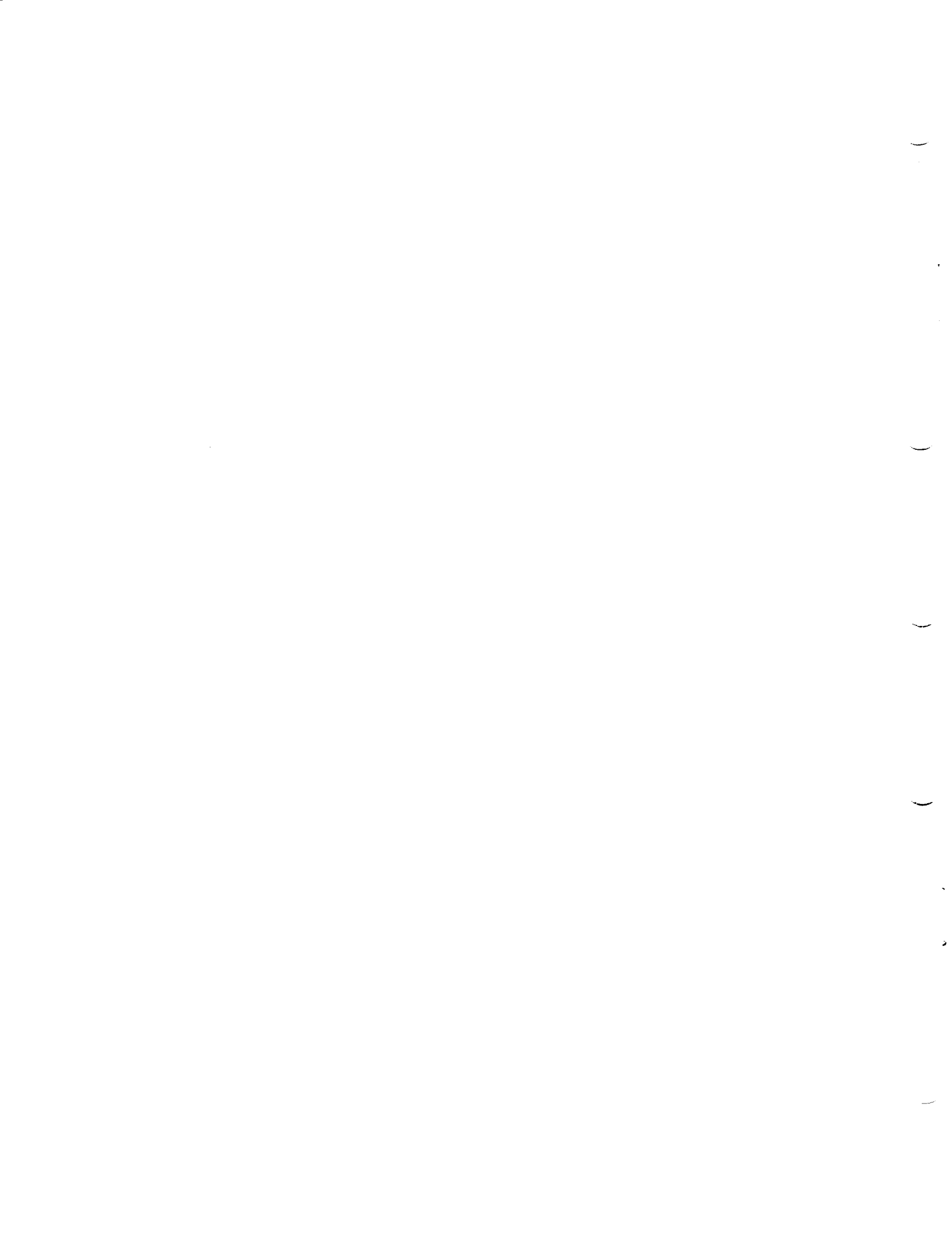
-----

ALL ERROR REPORTS HAVE STANDAND FORMATS AS DESCRIBED IN THE DEC/X11 USER'S GUIDE. IN EACH ERROR PRINTOUT, THE SAME INFORMATION IS ALWAYS REPORTED, ALONG WITH THE ERROR TYPE AND PC, WHICH UNIQUELY IDENTIFY THE ERROR. THE FOLLOWING IS AN EXAMPLE PRINTOUT :

```
DMDA0 PA 00046706 APC 004462 PASS #00001 SUFT ERR #00001
CSNA: 160210 CSRC: 100000 ASTAT: 000602 ERRTP: 000001
```

```
100000 000602 001005 000112 NNNNNN
```

ERRTP = 1 INDICATES A DATA ERROR (AS DESCRIBED IN THE DEC/X11 USER'S GUIDE), WHICH IS A SOFT ERROR. CSRA IS THE DEVICE CSR ADDRESS, CSRC AND ASTAT ARE THE CONTENTS OF THE FIRST TWO CSR REGISTERS (SEL0 AND SEL2). THE FOUR (5 IF DMV) FIELDS OF NUMBERS ON THE THIRD LINE ARE THE CONTENTS OF ALL FOUR CSR REGISTERS (SEL0,2,4,6 (AND 10 IF DMV)). WHENEVER ERRTP = 0 IS REPORTED (UNDEFINED ERROR) THE USER MUST REFER TO THE DIAGNOSTIC LISTING FOR THE ACTUAL EXPLANATION OF THE ERROR. THIS IS RECOMMENDED FOR ALL ERRORS, BECAUSE THE DEC/X11 ERROR TYPE DEFINITIONS ARE QUITE GENERAL.



```

275
276
277          .TITLE  DMDC DEC/X11 SYSTEM EXERCISER MODULE
278          ;      DDXCOM VERSION 6      23-MAY-78
279          LIST  BIN
280          ;*****
281          BEGIN:
282          MODNAM: .ASCII /DMDC / ;MODULE NAME.
283          XFLAG: .BYTE  OPEN          ;USED TO KEEP TRACK OF WBUFF USAGE
284          ADDR: 160170+0             ;1ST DEVICE ADDR.
285          VECTOR: 300+0              ;1ST DEVICE VECTOR.
286          BR1: .BYTE  PRTY5+0        ;1ST BR LEVEL.
287          BR2: .BYTE  PRTY5+0        ;2ND BR LEVEL.
288          DVID1: 0+1                 ;DEVICE INDICATOR 1.
289          SR1:  OPEN                  ;SWITCH REGISTER 1
290          SR2:  OPEN                  ;SWITCH REGISTER 2
291          SR3:  OPEN                  ;SWITCH REGISTER 3
292          SR4:  OPEN                  ;SWITCH REGISTER 4
293          ;*****
294          STAT: 150000                 ;STATUS WORD.
295          INIT: START                 ;MODULE START ADDR.
296          SPOINT: MODDSP              ;MODULE STACK POINTER.
297          PASCNT: 0                   ;PASS COUNTER.
298          ICOUNT: 32                  ;# OF ITERATIONS PER PASS=32.
299          SOFCNT: 0                   ;LOC TO COUNT ITERATIONS
300          HRDCNT: 0                   ;LOC TO SAVE TOTAL SOFT ERRORS
301          SOFPAS: 0                   ;LOC TO SAVE TOTAL HARD ERRORS
302          HRDPAS: 0                   ;LOC TO SAVE SOFT ERRORS PER PASS
303          SYSCNT: 0                   ;LOC TO SAVE HARD ERRORS PER PASS
304          RANUM: 0                    ;# OF SYS ERRORS ACCUMULATED
305          CONFIG:                     ;HOLDS RANDOM # WHEN RAND MACRO IS CALLED
306          RES1: 0                     ;RESERVED FOR MONITOR USE
307          RES2: 0                     ;RESERVED FOR MONITOR USE
308          SVR0: OPEN                   ;LOC TO SAVE R0.
309          SVR1: OPEN                   ;LOC TO SAVE R1.
310          SVR2: OPEN                   ;LOC TO SAVE R2.
311          SVR3: OPEN                   ;LOC TO SAVE R3.
312          SVR4: OPEN                   ;LOC TO SAVE R4.
313          SVR5: OPEN                   ;LOC TO SAVE R5.
314          SVR6: OPEN                   ;LOC TO SAVE R6.
315          CSRA: OPEN                   ;ADDR OF CURRENT CSR.
316          SBADR:                       ;ADDR OF GOOD DATA, OR
317          ACSR: OPEN                   ;CONTENTS OF CSR.
318          WASADR:                       ;ADDR OF BAD DATA, OR
319          ASTAT: OPEN                   ;STATUS REG CONTENTS.
320          ERR1YP:                       ;TYPE OF ERROR
321          ASB: OPEN                     ;EXPECTED DATA.
322          AWAS: OPEN                    ;ACTUAL DATA.
323          RSTRT: RSTRT                  ;RESTART ADDRESS AFTER END OF PASS
324          WDTO: OPEN                    ;WORDS TO MEMORY PER ITERATION
325          WDFR: OPEN                    ;WORDS FROM MEMORY PER ITERATION
326          INTR: OPEN                    ;# OF INTERRUPTS PER ITERATION
327          IDNUM: 0                      ;MODULE IDENTIFICATION NUMBER=0
328          RBUFVA: BUFIN                 ;READ BUFFER VIRTUAL ADDRESS
329          RBUFPA: OPEN                  ;READ BUFFER PHYSICAL ADDRESS
330          RBUFEA: OPEN                  ;READ BUFFER EA BITS

```

```

331          RBUFSZ: 512                  ;SIZE OF THE READ BUFFER
332          WBUFPA: OPEN                  ;WRITE BUFFER PHYSICAL ADDRESS
333          WBUFEA: OPEN                  ;WRITE BUFFER EA BITS
334          WBUFQ: 512                   ;WRITE BUFFER SIZE REQUESTED
335          WBUFSZ: OPEN                  ;WRITE BUFFER SIZE AVAILABLE
336          CDRECT: OPEN                  ;CDATA/DATCK ERROR COUNT
337          CDWDCT: OPEN                  ;CDATA/DATCK WORD COUNT
338          FREE: OPEN                    ;RESERVED FOR FUTURE USE
339          MODDSP:
340          ;*****

```

```

341
342 ;BIT DEFINITIONS
343 BIT15=100000
344 BIT14=40000
345 BIT13=20000
346 BIT12=10000
347 BIT11=4000
348 BIT10=2000
349 BIT9=1000
350 BIT8=400
351 BIT7=200
352 BIT6=100
353 BIT5=40
354 BIT4=20
355 BIT3=10
356 BIT2=4
357 BIT1=2
358 BIT0=1
359
360
361
362 000305 GOODIN = 305 ;CODE LEFT IN BSEL6 AFTER INIT, IF NO ERRORS
363
364 021344 RDLU16 = 021344 ;M0207 INSTRUCTION TO MOVE LU REG 16 INTO BSEL4
365
366 121000 SWPBOT = 121000 ;DMV-11 ADDRESS OF "SWPBOT" SWITCHES
367 000200 MRDY = 200 ;DMV-11 BIT INDICATING M-LOOP IS READY FOR NEXT COMMAND
368 000001 REDLOC = 1 ;DMV-11 M-LOOP COMMAND TO READ INTERNAL MEMORY
369 000006 TLOOP = 6 ;DMV-11 M-LOOP COMMAND TO SETUP 56K BPS/INT LOOPBACK
370
371 ;*****
372 ;* SWITCH REGISTER 1 (SR1) BIT DEFINITIONS
373 ;*****
374 000001 NONLUP = BIT0 ;LOOPBACK MODE IF BIT = 0
375
376 ;*****
377 ;* SWITCH REGISTER 4 (SR4) BIT DEFINITIONS
378 ;*****
379 000001 DMVBIT = BIT0 ;UNIT IS DMV IF BIT = 1
380 000002 MODE22 = BIT1 ;UNIT IS IN Q22 MODE IF BIT = 1
381
382 ;*****
383 ;* DEC/X11 ERROR CODES
384 ;*****
385 000000 NOTDFN = 0 ;ERROR NOT DEFINED
386 000001 DATERR = 1 ;DATA ERROR
387 000007 SELERR = 7 ;SELECTION ERROR
388 000011 ILGINT = 11 ;ILLEGAL INTERRUPT OCCURRED, OR DONE NOT SET
389 000023 NOINTR = 23 ;DEVICE FAILED TO INTERRUPT
390 000034 NOINIT = 34 ;DEVICE WILL NOT INITIALIZE
391 000036 BADRED = 36 ;UNABLE TO EXECUTE READ FUNCTION
392 000037 BADWRT = 37 ;UNABLE TO EXECUTE WRITE FUNCTION
393
394
395
396 ;*****

```

```

397 ;* OFFSETS FOR DMP/DMV11 CSR ADDRESSES
398 ;*****
399 000000 SEL0 = 0
400 000000 BSEL0 = 0
401 000001 BSEL1 = 1
402 000002 SEL2 = 2
403 000002 BSEL2 = 2
404 000003 BSEL3 = 3
405 000004 SEL4 = 4
406 000004 BSEL4 = 4
407 000005 BSEL5 = 5
408 000006 SEL6 = 6
409 000006 BSEL6 = 6
410 000007 BSEL7 = 7
411 000010 SEL10 = 10
412 000010 BSEL10 = 10
413 000011 BSEL11 = 11
414
415
416 ;*****
417 ;* INPUT COMMAND TYPE CODES - BSEL2
418 ;*****
419 000002 MODDEF = 2
420 000001 CTLIN = 1
421 000004 BACCIT = 4
422 000000 BACCIR = 0
423
424
425 ;*****
426 ;* OUTPUT COMMAND TYPE CODES - BSEL2
427 ;*****
428
429 000001 CTLOUT = 1
430 000004 BACCOT = 4
431 000000 BACCOR = 0
432 000003 BACORU = 3
433 000006 BACOTS = 6
434 000007 BACOTN = 7
435 000002 INFOUT = 2
436
437
438 ;*****
439 ;* BIT MASKS FOR FIELDS IN COMMANDS
440 ;*****
441
442 000370 CHDMASK = 370 ;MASK FOR COMMAND TYPE IN BSEL2
443 000370 MODMSK = 370 ;MASK FOR MODE FIELD IN BSEL6 FOR MODE DEF'N CMND
444 000340 REQMSK = 340 ;MASK FOR REQUEST KEY IN BSEL6 FOR CTL IN CMND
445 000340 RTNMSK = 340 ;MASK FOR RETURN KEY IN BSEL6 FOR INFO OUT CMND
446 140000 CCMSK = 140000 ;MASK FOR CHARACTER COUNT IN SEL6
447 000077 EAMSK = 077 ;MASK FOR BUS ADRS EA BITS IN BSEL7
448 000361 MODESW = 361 ;MASK FOR DMP11 MODE SWITCHES IN IBUS REG 16
449
450
451
452 ;*****

```

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DEC/X11-1 SYSTEM EXERCISER MACRO DEFINITION MODULE

```

453          ;* COMMAND CONTROL, INTERRUPT ENABLE BITS - BSEL0
454          ;*****
455          RQI   = BIT7
456          IEO   = BIT4
457          IEI   = BIT0
458
459
460          ;*****
461          ;* COMMAND CONTROL BITS - BSEL2
462          ;*****
463          RDIO  = BIT7
464          RDIY  = BIT4
465          Q22BIT = BIT3
466
467
468          ;*****
469          ;* MICROPROCESSOR CONTROL COMMAND - BSEL1
470          ;*****
471          RUN   = BIT7
472          MCLK  = BIT6
473          STEPLU = BIT4
474          LULOOP = BIT3
475          ROMO  = BIT2
476          ROMI  = BIT1
477          STEPMP = BIT0
478
479
480          ;*****
481          ;* MODE DEFINITION COMMAND - BSEL6
482          ;*****
483
484          HDPPDC = 0
485          FDPDC  = 1
486          HDPP   = 2
487          FDP    = 3
488          HDCS  = 4
489          FDCS  = 5
490          HDTS  = 6
491          FDTSS = 7
492
493
494          ;*****
495          ;* CONTROL IN COMMAND - BSEL6
496          ;*****
497
498          WRITSS = BIT7
499          RDOCTSS = BIT6
500          REDTSS = BIT5
501          ESTRIB = 01
502          KILLTR = 02
503          ISTART = 03
504          MAINT  = 04
505          HALTST = 05
506          REDMDM = 20
507          WRIMDM = 21
508          INTFDG = 22

```

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DEC/X11-1 SYSTEM EXERCISER MACRO DEFINITION MODULE

```

509          SELMDM = 23
510          DEROTB = 112
511          DERINB = 113
512
513
514          ;*****
515          ;* CONTROL IN COMMAND - BSEL7
516          ;*****
517
518          DISPIG = BIT7
519          ENBPIG = BIT6
520          LATPS  = BIT5
521          ULATPS = BIT4
522          DISCMP = BIT1
523          ENACMP = BIT0
524
525
526          ;*****
527          ;* BUFFER ADDRESS / CHARACTER COUNT IN/OUT COMMAND - SEL4
528          ;*****
529
530          BA15 = BIT15
531          BA14 = BIT14
532          BA13 = BIT13
533          BA12 = BIT12
534          BA11 = BIT11
535          BA10 = BIT10
536          BA9  = BIT9
537          BA8  = BIT8
538          BA7  = BIT7
539          BA6  = BIT6
540          BA5  = BIT5
541          BA4  = BIT4
542          BA3  = BIT3
543          BA2  = BIT2
544          BA1  = BIT1
545          BA0  = BIT0
546
547
548          ;*****
549          ;* BUFFER ADDRESS / CHARACTER COUNT IN/OUT - SEL6
550          ;*****
551
552          BA17 = BIT15
553          BA16 = BIT14
554          CC13 = BIT13
555          CC12 = BIT12
556          CC11 = BIT11
557          CC10 = BIT10
558          CC9  = BIT9
559          CC8  = BIT8
560          CC7  = BIT7
561          CC6  = BIT6
562          CC5  = BIT5
563          CC4  = BIT4
564          CC3  = BIT3

```

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DEC/X11-1 SYSTEM EXERCISER MACRO DEFINITION MODULE

```

565      000004      CC2  = BIT2
566      000002      CC1  = BIT1
567      000001      CC0  = BIT0
568
569
570
571      ;*****
572      ;* CONTROL OUT COMMAND - BSEL6
573      ;*****
574      000002      RTHRER = 2
575      000004      TTHRER = 4
576      000006      STHRER = 6
577      000010      STARUN = 10
578      000012      MNTRUN = 12
579      000014      MNTHLT = 14
580      000020      RINGDT = 20
581      000022      DEADTR = 22
582      000024      RUNST  = 24
583      000026      BABTRB = 26
584      000030      STRTRB = 30
585      000300      BUFTSM = 300
586      000302      NONEXM = 302
587      000304      DISCOM = 304
588      000306      QUQVRN = 306
589
590
591
592      ;*****
593      ;* INFORMATION OUT COMMAND - BSEL6
594      ;*****
595      000100      RDCISS = BIT6
596      000040      REDTSS = BIT5
597      000000      NORET  = 00
598      000010      RETMST = 10
599      000020      BUFRTC  = 20
600      000012      EROTB  = 12
601      000013      ERINB  = 13
602
603
604
605      ;*****
606      ;* DMP OBUS REG 10 - TRANSMITTER BUFFER
607      ;*****
608      000200      TX7   = BIT7
609      000100      TX6   = BIT6
610      000040      TX5   = BIT5
611      000020      TX4   = BIT4
612      000010      TX3   = BIT3
613      000004      TX2   = BIT2
614      000002      TX1   = BIT1
615      000001      TX0   = BIT0
616
617      ;*****
618      ;* DMP OBUS REG 11
619      ;*****
620      000200      OC    = BIT7

```

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DEC/X11-1 SYSTEM EXERCISER MACRO DEFINITION MODULE

```

621      000010      GOAH  = BIT3
622      000004      ABORT  = BIT2
623      000002      EOM   = BIT1
624      000001      SOM   = BIT0
625
626
627      ;*****
628      ;* DMP OBUS REG 12
629      ;*****
630      000200      IC    = BIT7
631      000100      BPOLL = BIT6
632      000040      LULP  = BIT5
633
634      ;*****
635      ;* DMP OBUS REG 13
636      ;*****
637      000200      POLL  = BIT7
638      000100      DTR   = BIT6
639      000040      SELFR = BIT5
640      000020      HDX   = BIT4
641      000010      MAINT1 = BIT3
642      000004      MAINT2 = BIT2
643      000002      SELSBY = BIT1
644
645      ;*****
646      ;* DMP OBUS REG 14
647      ;*****
648      000100      TXEN  = BIT6
649      000040      DISSI = BIT5
650      000020      RDAX  = BIT4
651      000010      WAX   = BIT3
652      000004      ENAX  = BIT2
653      000002      AX2   = BIT1
654      000001      AX1   = BIT0
655
656      ;*****
657      ;* DMP OBUS REG 17
658      ;*****
659      000200      CRC2  = BIT7
660      000100      CRC1  = BIT6
661      000040      IDLE  = BIT5
662      000020      SECA  = BIT4
663      000010      STRIP = BIT3
664      000004      RDALL = BIT2
665      000002      IERR  = BIT1
666      000001      DDCMP = BIT0
667
668      ;*****
669      ;* DMP IBUS REG 10 - RECEIVER BUFFER
670      ;*****
671      000200      RX7   = BIT7
672      000100      RX6   = BIT6
673      000040      RX5   = BIT5
674      000020      RX4   = BIT4
675      000010      RX3   = BIT3
676      000004      RX2   = BIT2
677      000002      RX1   = BIT1

```

```

677          000001          RX0      = BIT0
678
679          ;*****
680          ;* DMP IBUS REG 12
681          ;*****
682          000200          IC       = BIT7
683          000100          IACT     = BIT6
684          000040          LULP     = BIT5
685          000020          IRDY     = BIT4
686          000010          OVRR     = BIT3
687          000004          RAB      = BIT2
688          000002          EBLK     = BIT1
689          000001          BCC      = BIT0
690
691          ;*****
692          ;* DMP IBUS REG 13
693          ;*****
694          000200          RING     = BIT7
695          000100          DTR      = BIT6
696          000040          RTS      = BIT5
697          000020          HDX      = BIT4
698          000010          MODR     = BIT3
699          000004          CS       = BIT2
700          000002          STBY     = BIT1
701          000001          CARR     = BIT0
702
703          ;*****
704          ;* DMP IBUS REG 14
705          ;*****
706          000200          READY    = BIT7
707          000100          TXEN     = BIT6
708          000040          DISS1    = BIT5
709          000020          RDAX     = BIT4
710          000010          WAX      = BIT3
711          000004          ENAX     = BIT2
712          000002          AX2      = BIT1
713          000001          AX1      = BIT0
714
715          ;*****
716          ;* DMP IBUS REG 16 -- DMV "SWPBDT" (W/BITS RE-ARRANGED)
717          ;*****
718          000020          MODSW0    = BIT4
719          000040          MODSW1    = BIT5
720          000100          MODSW2    = BIT6
721          000200          ENABSW    = BIT7
722
723          ;*****
724          ;* DMP IBUS REG 17
725          ;*****
726          000200          SIGR     = BIT7
727          000100          SIGG     = BIT6
728          000040          TXDATA    = BIT5
729          000020          OCUR     = BIT4
730          000010          ICIR     = BIT3
731          000004          TESTMD    = BIT2
732          000002          MCLK     = BIT1

```

```

733          000001          DDCMP    = BIT0
734
735
736
737          ;*****
738          ;* DMP AX3-15 - USYRT REG 6
739          ;*****
740          000200          1422     = BIT7
741          000100          XYZ      = BIT6
742          000040          C32BCC   = BIT5
743          000020          V35      = BIT4
744          000010          INTGRL   = BIT3
745          000004          C32ENB   = BIT2
746          000002          OP       = BIT1
747          000001          TEST     = BIT0
748
749
750          ;*****
751          ;* INTERRUPT FLAG BIT DEFINITIONS IN "FLAGS"
752          ;*****
753          000001          ININT     = BIT0          ; =1 IF INPUT INTRPT OCCURRED
754          000002          OUTINT    = BIT1          ; =1 IF OUTPUT INTRPT OCCURRED
755          000004          NONQ1     = BIT2          ; =1 FOR NON-QUEUED (REQUEST-AND-WAIT) INPUT OPERATION
756          000010          NONQ0     = BIT3          ; =1 FOR NON-QUEUED (REQUEST-AND-WAIT) OUTPUT OPERATION
757
758
759          ;*****
760          ;* INTERRUPT STATUS WORD (INTABL) BIT DEFINITIONS
761          ;*****
762          000001          MFDON     = BIT0          ;MODE DEFINITION DONE
763          000002          HLTDON    = BIT1          ;HALT DONE
764          000004          ISTDON    = BIT2          ;ISTART DONE
765          000010          BIRDON    = BIT3          ;BACCIR DONE
766          000020          BITDON    = BIT4          ;BACCIT DONE
767          000040          BORDON    = BIT5          ;BACCOR DONE
768          000100          BOTDON    = BIT6          ;BACCOT DONE
769          000200          OERDON    = BIT7          ;DATA ERRORS OUTBOUND HAVE BEEN REQUESTED
770          000400          IERDON    = BIT8          ;DATA ERRORS INBOUND HAVE BEEN REQUESTED
771          100000          OWAIT     = BIT15         ;WAITING FOR OUTPUT ON LOG LINK
772
773
774          ;*****
775          ;* ERROR FLAG BIT DEFINITIONS IN "ERRORS"
776          ;*****
777          000001          INTIMO    = BIT0          ;INPUT INTERRUPT TIMED-OUT
778          000002          OUTIMO    = BIT1          ;OUTPUT INTERRUPT TIMED-OUT
779          000004          BADINI    = BIT2          ;MASTER CLEAR FAILED ON DEVICE
780
781
782
783
784
785          000252' 000000          WBUFEX: .WORD 0          ;ADJUSTED WBUFEA
786          000254' 000000          RBUFEX: .WORD 0          ;ADJUSTED RBUFEA
787
788          000256' 000000          N.DEVS: .WORD 0          ;NUMBER OF DEVICES PRESENT

```

```

789 000260' 000000 TOTAL: .WORD 0 ;NUMBER OF DMP11'S CURRENTLY ACTIVE
790 000262' 000000 COUNT: .WORD 0 ;ITERATION COUNT FOR MODULE
791 000264' 000000 SAVBF: .WORD 0 ;RCV ISR TEMPORARY STORAGE
792 000266' 000000 SELECT: .WORD 0 ;SOFTWARE BIT MAP OF ACTIVE DEVICES
793 000270' 000000 DEVPTR: .WORD 0 ;DEVICE SELECTION POINTER
794 000272' 000000 LURG16: .WORD 0 ;STORAGE FOR LU IBUS REG 16
795 000274' 000000 CSEL0: .WORD 0 ;STORAGE FOR INPUT COMMAND TO BE PERFORMED
796 000276' 000000 CSEL2: .WORD 0
797 000300' 000000 CSEL4: .WORD 0
798 000302' 000000 CSEL6: .WORD 0
799 000304' 000000 CSEL10: .WORD 0
800 000306' 000000 RSEL0: .WORD 0 ;STORAGE FOR CSR'S RETURNED ON OUTPUT CMND
801 000310' 000000 RSEL2: .WORD 0
802 000312' 000000 RSEL4: .WORD 0
803 000314' 000000 RSEL6: .WORD 0
804 000316' 000000 RSEL10: .WORD 0
805 000320' 000000 FLAGS: .WORD 0 ;INPUT AND OUTPUT INTERRUPT FLAG BITS
806 000322' 000000 ERRORS: .WORD 0 ;ERROR FLAG BITS
807 000324' 000000 INTIMR: .WORD 0 ;INPUT INTERRUPT TIMER
808 000326' 000000 OUTIMR: .WORD 0 ;OUTPUT INTERRUPT TIMER
809 000330' 000000 LLKCNT: .WORD 0 ;COUNT OF LOGICAL LINKS
810 ; IF P-TO-P, THIS IS SAME AS NO. OF DEVICES.
811 ; IF MULTIPPOINT, THIS IS SAME AS NO. OF TRIBUTARIES.
812 000332' 000000 ISTCNT: .WORD 0 ;COUNT OF ISTART ATTEMPTS ON LOGICAL LINK
813 000334' 000000 SCRACH: .WORD 0 ;MISCELLANEOUS STORAGE LOCATION
814 000336' 000000 TRBADR: .WORD 0 ;CURRENT TRIB ADDRESS
815 000340' 000000 TRBMAX: .WORD 0 ;MAXIMUM TRIB ADDRESS
816 000342' 000000 LNKDON: .WORD 0 ;NO. OF LOG. LNKS DONE WITH MSG
817 000344' 000000 UNKDON: .WORD 0 ;OUTPUT COUNT OF LOGICAL LINKS DONE WITH MSG
818 ; PER ITERATION
819 000346' 000000 WATCNT: .WORD 0 ;MULTIDROP CNT OF LNKS AWAITING OUTPUT
820
821 ;*****
822 ;* INTABLE - INTERRUPT STATUS TABLE - 32 (MAX.) 1-WORD ENTRIES, 1 ENTRY
823 ;* PER LOGICAL LINK.
824 ;*
825 ;* IF POINT-TO-POINT OR LPBK MODE, UP TO 16 ENTRIES CAN BE USED, WHERE AN ENTRY
826 ;* CONSISTS OF 16 INTERRUPT STATUS FLAG BITS.
827 ;* IF MULTIPPOINT MODE, UP TO 32 ENTRIES CAN BE USED, WHERE AN ENTRY CONSISTS OF
828 ;* 16 INTERRUPT STATUS FLAG BITS.
829 ;* IN ANY CASE, THE NO. OF OCCUPIED ENTRIES = THE NO. OF LOGICAL LINKS, KEPT IN
830 ;* LLKCNT.
831 ;*****
832 000350' 000040 INTABL: .BLKW 32.
833
834
835
836 ; REGISTER ADDRESS TABLE FOR ERROR REPORTS
837 000450' 160170 REGTBL: .WORD 160170
838 000452' 160172 .WORD 160172
839 000454' 160174 .WORD 160174
840 000456' 160176 .WORD 160176
841 000460' 160200 .WORD 160200
842 000462' 177777 .WORD 177777
843
844

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845
846
847 ;-----
848 ; THIS IS START OF MODULE
849 ;-----
850 000464' 016767 177324 177574 START: MOV DVID1,SELECT ;GET ACTIVE DEVICES
851 000472' 001004 BNE SETUP ;BR IF ANY ARE SELECTED
852 000474' 004767 005400 DRPMOD: JSR PC,DISABL ;DISABLE INTERRUPTS ON ALL SELECTED DEVICES
853 000500' 104410 000000' ENDS,BEGIN ;DROP THE DEVICE MODULE
854
855 ;-----
856 ;SETUP VECTORS FOR ACTIVE DEVICES, AND INITIALIZE THEM
857 ;-----
858 000504' 005067 177546 SETUP: CLR N.DEVS ;CLEAR NO. OF DEVICES
859 000510' 005067 177606 CLR ERRORS ;CLEAR ERROR FLAGS
860 000514' 012767 000010 177576 MOV #NONQD,FLAGS ;SET FLAG FOR NON-QUEUED OUTPUT INTRPTS
861 000522' 004767 005330 JSR PC,CLRCMD ;CLEAR COMMAND STORAGE AREA
862 000526' 012704 000350' MOV #INTABL,R4 ;INIT POINTER TO INTRPT STATUS TABLE
863 000532' 005024 16: CLR (R4)+ ;CLEAR A TABLE ENTRY
864 000534' 020427 000450' CMP R4,#INTABL+64. ;SEE IF ALL ENTRIES CLEARED YET
865 000540' 103774 BLO 16 ;BR IF NOT YET
866 000542' 016702 177242 MOV VECTOR,R2 ;GET INITIAL VECTOR
867 000546' 016700 177234 MOV ADDR,R0 ;GET INITIAL ADDRESS
868 000552' 012703 006352' MOV #ISR0,R3 ;INIT ISR POINTER
869 000556' 012767 000001 177504 MOV #BIT0,DEVPTR ;INIT SELECTION POINTER
870 000564' 005067 177540 CLR LLKCNT ;INIT LOGICAL LINK CNT
871
872 ;FIND A SELECTED DEVICE
873 000570' 036767 177474 177470 29: BIT DEVPTR,SELECT ;SEE IF THIS DEVICE IS SELECTED
874 000576' 001021 BNE 58 ;BR IF SELECTED
875 000600' 006367 177464 36: ASL DEVPTR ;SHIFT SELECTION POINTER
876 000604' 001002 BNE 46 ;BR IF NOT ALL DEVICES SCANNED YET
877 000606' 000167 000662 JMP STRTUP ;ALL DEVICES SCANNED - START LNKS
878 000612' 062703 000036 48: ADD #36,R3 ;POP ISR POINTER
879 000616' 062702 000010 ADD #10,R2 ;POP VECTOR
880 000622' 005767 177176 TST SR4 ; IS THIS A DMV ?
881 000626' 001402 BEQ 25$
882 000630' 062700 000010 ADD #10,R0 ;IF YES: POP ADDRESS (20)
883 000634' 062700 000010 25$: ADD #10,R0 ;IF NO: POP DMP11 ADDRESS (10)
884 000640' 000753 BR 28 ;CONTINUE
885
886 ; MASTER CLEAR THIS DEVICE
887 000642' 112760 000100 000001 58: MOV# #MCLR,BSEL1(R0) ;SET MASTER CLEAR
888 000650' 005767 177150 TST SR4 ; IS THIS A DMV ?
889 000654' 001011 BNE 23$ ; IF YES: SKIP LULOOP/RUN STUFF
890 000656' 112760 000200 000001 MOV# #RUN,BSEL1(R0) ;SET RUN BIT
891 000664' 142760 000200 000001 BICB #RUN,BSEL1(R0) ;CLEAR RUN
892 000672' 152760 000010 000001 BISS #LULOOP,BSEL1(R0) ;SET LULOOP
893 000700' 004767 004664 238: JSR PC,READ16 ;READ LU REG 16 FOR SWITCHES
894 000704' 004767 004500 JSR PC,INIDEV ;MASTER CLEAR DEVICE
895 000710' 132767 000004 177404 BITB #BADINI,ERRORS ;SEE IF INIT WAS BAD
896 000716' 001071 BNE 88 ;IF INIT BAD, GO DROP MODULE
897
898 ; LOAD INPUT AND OUTPUT VECTORS FOR THIS DEVICE
899 000720' 010312 MOV R3,(R2) ;LOAD INPUT ISR VECTOR
900 000722' 116762 177064 000002 MOV# BR1,2(R2) ;LOAD PRIORITY
901 000730' 105062 000003 CLR# 3(R2) ;CLEAR HI BYTE OF PROGRAM STATUS WORD
902 000734' 010063 000034 MOV R0,34(R3) ;LOAD DMP11 ADDRESS
903 000740' 010362 000004 MOV R3,4(R2) ;LOAD OUTPUT ISR VECTOR

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DEC/X11-1 SYSTEM EXERCISER MACRO DEFINITION MODULE

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901 000744' 062762 000016 000004 ADD #16,4(R2) ;POINT TO OUTPUT ISR
902 000752' 116762 177034 000006 MOVB BR1,6(R2) ;LOAD PRIORITY
903 000760' 105062 000007 CLR 7(R2) ;CLEAR HI BYTE OF STATUS WORD
904 ;IF DMV/INTERNAL-LOOP SELECTED THEN SETUP DEVICE FOR IT HERE
905 000764' 005767 177034 TST SR4 ;IF DMV RUNNING...
906 000770' 001412 BEQ 246 ; (BR IF NOT)
907 000772' 026727 177020 000000 CMP SR1,#0 ;AND WITH LOOPBACK...
908 001000' 001006 BNE 246 ; (BR IF NOT)
909 001002' 026727 177012 000000 CMP SR2,#0 ;AND WITH INTERNAL LOOPBACK...
910 001010' 001002 BNE 246 ; (BR IF NOT)
911 001012' 004767 004172 JSR PC,DMVLUP ; THEN SET TLOOP W/56K BPS INTERNAL
912 ;DEFINE THE MODE OF THIS DEVICE (FDX, HDX, P-P, M-P CTL)
913 001016' 112767 000002 177252 24$: MOV #MODDEF,CSEL2 ;SET MODE DEFINITION COMMAND
914 001024' 152760 000010 000001 BISB #LULOOP,BSEL1(RO) ;SET LULOOP TO FORCE MODE
915 001032' 126727 176760 000000 CMPB SR1,#0 ;SEE IF GRP OF LOOPED-BACK DEVICES
916 001040' 001025 BNE 9$ ;BR IF NOT
917 001042' 112767 000003 177232 6$: MOV #FDPP,CSEL6 ;SET FDX P-TO-P MODE
918 001050' 004767 004634 7$: JSR PC,INCMND ;PERFORM MODE DEF'N CMND
919 001054' 104407 000000' BREAKS,BEGIN ;TEMPORARY RETURN TO MONITOR....
920 001060' 104407 000000' BREAKS,BEGIN ;THEN CONTINUE AT NEXT INSTRUCTION.
921 001064' 142760 000010 000001 BIC #LULOOP,BSEL1(RO) ;CLEAR LU LOOP
922 001072' 032767 000001 177222 BIT #INTIMO,ERRORS ;SEE IF INPUT INTRPT TIMED-OUT
923 001100' 001464 BEQ 16$ ;BR IF NO TIME-OUT
924 001102' 142760 000200 000001 8$: BIC #KUN,BSEL1(RO) ;CLEAR RUN ON THIS DEVICE
925 001110' 000167 177360 JMP DRPMUD ;GO DRUP MODULE
926 001114' 126727 176676 000001 9$: CMPB SR1,#1 ;SEE IF GRP OF P-TO-P DEVICES
927 001122' 001015 BNE 12$ ;BR IF NOT
928 001124' 132767 000200 177140 BITB #ENABSW,LURG16 ;SEE IF LU SWITCHES ENABLED
929 001132' 001004 BNE 11$ ;BR IF YES
930 001134' 112767 000002 177140 10$: MOVB #HDPP,CSEL6 ;SET HALF-DPX P-TO-P MODE
931 001142' 000742 BR 7$ ;GO SET MODE
932 001144' 132767 000020 177120 11$: BITB #MODSW0,LURG16 ;SEE IF HDX IN SWITCHES
933 001152' 001770 BEQ 10$ ;BR IF HDX
934 001154' 000732 BR 6$
935 001156' 126727 176634 000002 12$: CMPB SR1,#2 ;SEE IF MULTI-POINT CTL STATION
936 001164' 001020 BNE 15$ ;BR IF NOT
937 001166' 132767 000200 177076 BITB #ENABSW,LURG16 ;SEE IF LU SWITCHES ENABLED
938 001174' 001004 BNE 14$ ;BR IF YES
939 001176' 112767 000004 177076 13$: MOVB #HDCS,CSEL6 ;SET HDX CTL MODE
940 001204' 000721 BR 7$ ;GO SET MODE
941 001206' 132767 000020 177056 14$: BITB #MODSW0,LURG16 ;SEE IF HDX IN SWITCHES
942 001214' 001770 BEQ 13$ ;BR IF HDX
943 001216' 112767 000005 177056 MOVB #FDCS,CSEL6 ;SET FDX CTL MODE
944 001224' 000711 BR 7$ ;GO SET MODE
945 001226' 004767 004710 15$: JSR PC,GETERR ;LOAD ERROR INFORMATION FOR PRINTOUT
946 001232' 012767 000007 176646 MOV #SELEERR,ERRRTP ;SET CODE FOR SELECTION ERROR
947 ;*****
948 001240' 104405 000000' 000000 HRDERS,BEGIN,NULL ;OPERATOR SPEC'D INVALID SR VALUE
949 ;*****
950 001246' 000167 177222 JMP DRPMUD ;GO DRUP THE MODULE
951 ; IF H3254,5 LOOPBACK, SELECT MODEM INTERFACE DESIRED BY OPERATOR
952 001252' 005767 176546 16$: TST SR4 ; IS THIS A DMV ?
953 001256' 001054 BNE 20$ ; IF YES: SKIP INTERFACE SETUP.
954 001260' 126727 176532 000000 CMPB SR1,#0 ;SEE IF LOOPBACK MODE
955 001266' 001050 BNE 20$ ;BR IF NOT
956 001270' 026727 176524 000001 CMP SR2,#1 ;SEE IF H3254,5 USED

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957 001276' 001044 BNE 20$ ;BR IF NOT
958 001300' 012767 000333 176772 MOV #I422:XYZIV35:INTGRLOP:TEST,CSEL4 ;INIT MODEM INTERFACE BITS
959 001306' 016701 176510 MOV SR3,R1 ;GET DESIRED MODEM SELECTION
960 001312' 001004 BNE 17$ ;BR IF NOT DEFAULT
961 001314' 042767 000020 176756 BIC #V35,CSEL4 ;SELECT V.35 BY DEFAULT
962 001322' 000416 BR 19$
963 001324' 020127 000200 17$: CMP R1,#I422 ;SEE IF 422 DESIRED
964 001330' 001411 BEQ 18$ ;BR IF YES
965 001332' 020127 000100 CMP R1,#XYZ ;SEE IF XYZ DESIRED
966 001336' 001406 BEQ 18$ ;BR IF YES
967 001340' 020127 000020 CMP R1,#V35 ;SEE IF V.35 DESIRED
968 001344' 001403 BEQ 18$ ;BR IF YES
969 001346' 020127 000010 CMP R1,#INTGRLOP ;SEE IF INTEGRAL MODEM DESIRED
970 001352' 001325 BNE 19$ ;BR IF NOT, TO REPORT ILLEGAL SR3 VALUE
971 001354' 040167 176720 18$: BIC R1,CSEL4 ;SELECT THE INTERFACE IN WORD
972 001360' 012767 000001 176710 19$: MOV #CTLIN,CSEL2 ;SET CTL IN CMND
973 001366' 012767 000023 176706 MOV #SELMMDM,CSEL6 ;CODE FOR SELECT MODEM INTERFACE
974 001374' 004767 004310 JSR PC,INCMND ;SELECT THE INTERFACE IN REG AX3-15
975 001400' 032767 000001 176714 BIT #INTIMO,ERRORS ;SEE IF INPUT CMND TIMED-OUT
976 001406' 001235 BNE 8$ ;BR IF YES, TO DROP MODULE
977 001410' 005267 176642 20$: INC N,DEVS ;UPDATE THE NO. OF DEVICES TO RUN
978 001414' 126727 176376 000000 CMPB SR1,#0 ;SEE IF RUNNING WITH LOOPBACK
979 001422' 001007 BNE 21$ ;BR IF NOT
980 001424' 026727 176370 000000 CMP SR2,#0 ;SEE IF INTERNAL LOOPBACK DESIRED
981 001432' 001003 BNE 21$ ;BR IF NOT
982 001434' 152760 000010 000001 BISB #LULOOP,BSEL1(RO) ;SET LINE UNIT LOOP
983 ;COMPUTE NO. OF LOGICAL LINKS IN LKCNTR
984 001442' 126727 176350 000002 21$: CMPB SR1,#2 ;SEE IF MULTIPOINT
985 001450' 001406 BEQ 22$ ;BR IF MULTIPOINT
986 001452' 105060 000000 CLR 22$ ;DISABLE INTRPTS FROM THIS DEVICE
987 001456' 005267 176646 INC LKCNTR ;INCR NO. OF LOGICAL LINKS
988 001462' 000167 177112 JMP 3$ ;CONTINUE SETUP
989 001466' 016767 176326 176634 22$: MOV SR2,LLKCNTR ;STORE NO. OF LOGICAL LINKS
990
991 ;-----
992 ; ATTEMPT TO START THE PROTOCOL ON EACH LOGICAL LINK, AND IF A LOGICAL LINK
993 ; CAN'T BE STARTED, DROP THE DEVICE WHICH CONTROLS THAT LINK, FROM TESTING
994 ;-----
995
996 001474' 012767 000001 176634 STRTUP: MOV #1,TRIBADR ;SET P-TO-P TRIB ADRS = 1
997 001502' 126727 176310 000002 CMPB SR1,#2 ;SEE IF MULTIPOINT
998 001510' 001003 BNE 16$ ;BR IF NOT MULTIPOINT
999 001512' 016767 176304 176616 MOV SR3,TRIBADR ;INIT MULTIPOINT TRIB ADRS
1000 001520' 016705 176604 1$: MOV LKCNTR,R5 ;INIT LOG LNK COUNTER
1001 001524' 016700 176256 MOV ADDR,R0 ;INIT DEVICE ADRS
1002 001530' 012767 000001 176532 MOV #BIT0,DEVPTR ;INIT SELECTION POINTER
1003
1004 ;FIND A SELECTED DEVICE TO START
1005 001536' 036767 176526 176522 2$: BIT DEVPTR,SELECT ;SEE IF THIS DEVICE IS SELECTED
1006 001544' 001015 BNE 22$ ;BR IF SELECTED
1007 001546' 006367 176516 20$: ASL DEVPTR ;SHIFT SELECTION POINTER
1008 001552' 001002 BNE 21$ ;BR TO KEEP SCANNING
1009 001554' 000167 000706 JMP 19$ ;ALL DEVICES SCANNED
1010 001560' 005767 176240 21$: TST SR4 ; IS THIS A DMV ?
1011 001564' 001402 BEQ 25$
1012 001566' 062700 000010 ADD #10,R0 ;IF YES: INCREMENT DEVICE ADDRESS (20)
1013 001572' 062700 000010 ADD #10,R0 ;IF NO: INCREMENT DMP11 ADDRESS (10)

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1013 001576' 000757          BR      Z6          ;CONTINUE
1014
1015 001600' 112767 000001 176470 ;SET UP FOR CONTROL IN COMMAND
22s:  MOVB  #CFLIN,CSEL2      ;SET CONTROL IN CMND
1016 001606' 116767 176524 176463 MOVB  #TRBADR,CSEL2+1 ;SET TRIB ADRS
1017 001614' 005067 176460      CLR      CSEL4
1018 001620' 005067 176456      CLR      CSEL6
1019
;ESTABLISH A LOGICAL LINK
1020 001624' 112767 000001 176450 MOVB  #ESTRIB,CSEL6 ;SET CODE FOR ESTABLISH TRIB CMND
1021 001632' 042767 000002 176460 BIC   #OUTINT,FLAGS ;CLEAR OUTPUT INTRPT FLAG
1022 001640' 004767 004044      JSR     PC,INCMND ;ESTABLISH THIS TRIBUTARY
1023
;CHANGE THE SELECTION INTERVAL TO 5 SECONDS
1024 001644' 005767 176154      TST    SK4          ; IS THIS A DMV ?
1025 001650' 001404          BEQ    Z36          ;
1026 001652' 012767 000764 176420 MOV   #5000.,CSEL4 ;IF YES: CONSTANT = 500.
1027 001660' 000403          BR     Z46          ;
1028 001662' 012767 011610 176410 MOV   #5000.,CSEL4 ;IF NO: CONSTANT = 5000.
1029 001670' 012767 000236 176404 24s: MOV   #WRITSS+36,CSEL6 ;WRITE TSS ADDRESS 36 (SEL INTERVAL).
1030 001676' 042767 000002 176414 BIC   #OUTINT,FLAGS ;CLEAR OUTPUT INTRPT FLAG.
1031 001704' 004767 004000      JSR     PC,INCMND ;CHANGE THE INTERVAL.
1032
;ISTART THE LOGICAL LINK
1033 001710' 005067 176364      CLR     CSEL4      ;CLEAR INTERVAL VALUE.
1034 001714' 012767 000010 176410 MOV   #8.,ISTCNT   ;INIT ISTART COUNTER FOR APPROX 5 MINUTES
1035
; STARTUP (EACH COUNT = 35. SEC.)
1036 001722' 112767 000003 176352 3s:  MOVB  #ISTART,CSEL6 ;SET CODE FOR ISTART REQUEST
1037 001730' 042767 000002 176362 BIC   #OUTINT,FLAGS ;CLEAR OUTPUT INTRPT FLAG
1038 001736' 004767 003746      JSR     PC,INCMND ;PERFORM ISTART CMND
1039 001742' 012767 177777 176356 MOV   #-1,OUTIMR   ;INIT OUTPUT INTERRUPT TIMER
1040
4s:
1041 001750' 104407 000000'      BREAKS,BEGIN      ;TEMPORARY RETURN TO MONITOR....
1042 001754' 104407 000000'      BREAKS,BEGIN      ;THEN CONTINUE AT NEXT INSTRUCTION.
1043 001760' 132767 000002 176332 BITB  #OUTINT,FLAGS ;SEE IF OUTPUT INTRPT SERVICED YET
1044 001766' 001015          BNE    S5          ;BR IF YES
1045 001770' 005367 176332      DEC    OUTIMR      ;DECREMENT TIMER
1046 001774' 001365          BNE    S4          ;BR IF OUTPUT INTRPT DIDN'T TIME-OUT YET
1047 001776' 004767 004140      JSR     PC,GETERR   ;LOAD ERROR INFORMATION FOR PRINTOUT
1048 002002' 012767 000023 176076 MOV   #NOINTR,ERRTYP ;CODE FOR DEVICE FAILED TO INTRPT
1049
;*****
1050 002010' 104405 000000' 000450' HDRS$,BEGIN,REGTBL ;OUTPUT INTERRUPT TIMED-OUT
1051
;*****
1052 002016' 000167 000360      JMP    L46          ;GO DROP MODULE
1053 002022' 142767 000370 176260 5s:  BICB  #CMDMSK,RSEL2 ;MASK FOR OUTPUT COMMAND FIELD
1054 002030' 126727 176254 000001 CMPB  RSEL2,#CTLOUT ;CHK FOR CTL OUT CMND RETURNED
1055 002036' 001412          BEQ    S6          ;BR IF YES
1056 002040' 004767 004076      JSR     PC,GETERR   ;LOAD ERROR INFORMATION FOR PRINTOUT
1057 002044' 012767 000000 176034 MOV   #NOTDFN,ERRTYP ;CODE FOR ERROR NOT DEFINED (MUST INTERPRET
1058
; CSR CONTENTS)
1059
;*****
1060 002052' 104405 000000' 000450' HDRS$,BEGIN,REGTBL ;UNEXPECTED OUTPUT CMND
1061
;*****
1062 002060' 000167 000316      JMP    L46          ;GO DROP MODULE
1063
;CHECK FOR LOGICAL LINK RUNNING YET
1064 002064' 126727 176224 000024 6s:  CMPB  RSEL6,#RUNSI ;SEE IF PROTOCOL RUNNING ON THIS LOG LINK YET
1065 002072' 001002          BNE    S7          ;BR IF NOT
1066 002074' 000167 000312      JMP    L56          ;GO SELECT NEXT LOGICAL LINK
1067
;CHECK FOR TRANSMIT OR SELECT THRESHOLD ERROR
1068 002100' 126727 176210 000004 7s:  CMPB  RSEL6,#THHRER ;SEE IF TRANSMIT THRESHOLD ERROR (TRIB NOT RESPONDING)

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1069 002106' 001416          BEQ    S8          ;BR IF YES
1070 002110' 126727 176200 000006 CMPB  RSEL6,#THHRER ;SEE IF SELECT THRESHOLD ERROR
1071 002116' 001412          BEQ    S8          ;BR IF YES
1072 002120' 004767 004016      JSR     PC,GETERR   ;LOAD ERROR INFORMATION FOR PRINTOUT
1073 002124' 012767 000000 175754 MOV   #NOTDFN,ERRTYP ;CODE FOR ERROR NOT DEFINED (MUST INTERPRET
1074
; CSR CONTENTS)
1075
;*****
1076 002132' 104405 000000' 000450' HDRS$,BEGIN,REGTBL ;UNEXPECTED CTL OUT CMND
1077
;*****
1078 002140' 000167 000236      JMP    L46          ;GO DROP MODULE
1079 002144' 005367 176162      DEC    ISTART      ;DECREMENT ISTART COUNTER
1080 002150' 001012          BNE    S9          ;BR IF COUNT NOT OVERFLOWED YET
1081 002152' 004767 003764      JSR     PC,GETERR   ;LOAD ERROR INFORMATION FOR PRINTOUT
1082 002156' 012767 000000 175722 MOV   #NOTDFN,ERRTYP ;CODE FOR ERROR NOT DEFINED (MUST INTERPRET
1083
; CSR CONTENTS)
1084
;*****
1085 002164' 104405 000000' 000450' HDRS$,BEGIN,REGTBL ;TIMED-OUT ATTEMPTING TO START LINK
1086
;*****
1087 002172' 000167 000204      JMP    L46          ;GO DROP MODULE
1088
;HALT PROTOCOL PRIOR TO ISTART RETRY
1089 002176' 112767 000005 176076 9s:  MOVB  #HALTST,CSEL6 ;SET CODE FOR HALT REQUEST
1090 002204' 042767 000002 176106 BIC   #OUTINT,FLAGS ;CLEAR OUTPUT INTRPT FLAG
1091 002212' 004767 003472      JSR     PC,INCMND ;PERFORM HALT CMND TO RE-INIT LINK STATUS
1092 002216' 012767 020000 176102 MOV   #20000,OUTIMR ;INIT OUTPUT INTERRUPT TIMER
1093
10s:
1094 002224' 104407 000000'      BREAKS,BEGIN      ;TEMPORARY RETURN TO MONITOR....
1095 002230' 104407 000000'      BREAKS,BEGIN      ;THEN CONTINUE AT NEXT INSTRUCTION.
1096 002234' 032767 000002 176056 BITB  #OUTINT,FLAGS ;SEE IF OUTPUT INTRPT SERVICED YET
1097 002242' 001015          BNE    I11         ;BR IF YES
1098 002244' 005367 176056      DEC    OUTIMR      ;DECREMENT TIMER
1099 002250' 001365          BNE    I10         ;BR IF OUTPUT INTRPT DIDN'T TIME-OUT YET
1100 002252' 004767 003664      JSR     PC,GETERR   ;LOAD ERROR INFORMATION FOR PRINTOUT
1101 002256' 012767 000023 175622 MOV   #NOINTR,ERRTYP ;CODE FOR DEVICE FAILED TO INTRPT
1102
;*****
1103 002264' 104405 000000' 000450' HDRS$,BEGIN,REGTBL ;OUTPUT INTERRUPT TIMED-OUT
1104
;*****
1105 002272' 000167 000104      JMP    L46          ;GO DROP MODULE
1106 002276' 142767 000370 176004 11s: BICB  #CMDMSK,RSEL2 ;MASK FOR OUT CMND RETURNED
1107
;CHECK FOR INFO OUT WITH "BUFFERS RETURNED" CODE
1108 002304' 126727 176000 000002 CMPB  RSEL2,#INFOUT ;CHK FOR INFO OUT CMND RETURNED
1109 002312' 001022          BNE    I13         ;BR IF NOT
1110 002314' 142767 000340 175772 BICB  #RTNMSK,RSEL6 ;MASK FOR RETURN KEY
1111 002322' 126727 175766 000020 CMPB  RSEL6,#BUFRTC ;CHK FOR BUFFER RETURN COMPLETE
1112 002330' 001002          BNE    I12         ;BR IF NOT BUFFER RETURN COMPLETE
1113 002332' 000167 177364      JMP    J6          ;GO TRY TO ISTART AGAIN
1114 002336' 004767 003600      JSR     PC,GETERR   ;LOAD ERROR INFORMATION FOR PRINTOUT
1115 002342' 012767 175536      MOV   #NOTDFN,ERRTYP ;CODE FOR ERROR NOT DEFINED (MUST INTERPRET
1116
; CSR CONTENTS)
1117
;*****
1118 002350' 104405 000000' 000450' HDRS$,BEGIN,REGTBL ;UNEXPECTED INFO OUT CMND
1119
;*****
1120 002356' 000411          BR     L46          ;GO DROP MODULE
1121 002360' 042767 000002 175732 13s: BIC   #OUTINT,FLAGS ;CLEAR OUTPUT INTRPT FLAG
1122 002366' 012767 020000 175732 MOV   #20000,OUTIMR ;INIT OUTPUT INTRPT TIMER
1123 002374' 105060 000002      CLRB  BSEL2(RO)    ;RELEASE THE PORT AGAIN
1124 002400' 000711          BR     I16         ;KEEP LOOKING FOR INFO OUT CMND

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1125 002402' 005060 000000      14$: CLR      SELO(R0)      ;CLEAR RUN, DISABLE INTRPTS ON THIS DEVICE
1126 002406' 000167 176062      JMP      DRPMUD        ;GO DROP MODULE
1127                                ;SELECT NEXT LOGICAL LINK TO START
1128 002412' 105060 000002      15$: CLRB     BSEL2(R0)    ;CLEAR BSEL2 (INCLUDING RDYO)
1129 002416' 126727 175374      CMPB     SR1,#2        ;SEE IF MULTIPOINT
1130 002424' 001010                                BNE     16$           ;BR IF NOT MULTIPOINT
1131 002426' 005267 175704      INC      TRBADR        ;INCREMENT TRIB ADRS
1132 002432' 105767 175700      TSTB    TRHADR        ;SEE IF TRIB ADRS OVERFLOW
1133 002436' 001007                                BNE     17$           ;BR IF NOT
1134 002440' 005267 175672      INC      TRBADR        ;IF TRIB ADRS = 0, MAKE IT 1
1135                                ; TRIB ADRS = 0 IS ILLEGAL
1136 002444' 000404                                BR      17$           ;PROCEED
1137 002446' 105060 000000      16$: CLRB     BSEL0(R0)    ;DISABLE INTRPTS FROM THIS DEVICE
1138 002452' 000167 177070      JMP      20$           ;GO START NEXT LOGICAL LINK
1139 002456' 005305      17$: DEC      R5          ;DECREMENT LOG LNK COUNTER
1140 002460' 001402      BEQ     18$           ;BR IF NO MORE LNKS TO DO
1141 002462' 000167 177112      JMP      22$           ;GO START NEXT LOGICAL LINK
1142                                ;COMPUTE MAXIMUM TRIBUTARY ADDRESS
1143 002466' 012767 000001 175642 18$: MOV      #1,TRBADR    ;SET POINT-TO-POINT TRIB ADRS = 1
1144 002474' 126727 175316 000002  CMPB     SR1,#2        ;SEE IF MULTIPOINT
1145 002502' 001021                                BNE     19$           ;BR IF NOT MULTIPOINT
1146 002504' 016767 175312 175624  MOV      SR3,TRBADR    ;INIT MULTIPOINT TRIB ADRS
1147 002512' 016767 175620 175620  MOV      TRBADR,TRBMAX ;COMPUTE MAX TRIB NO IN TRBMAX
1148 002520' 005367 175614      DEC      TRBMAX
1149 002524' 066767 175270 175606  ADD      SR2,TRBMAX
1150 002532' 026727 175602 000400  CMP      TRBMAX,#400   ;ADJUST FOR TRIB ADRS OVERFLOW
1151 002540' 002402                                BLT     19$           ;TRIB ADRS = 0 IS ILLEGAL
1152 002542' 005267 175572      INC      TRBMAX        ;GO BEGIN TESTING ON ALL LOGICAL LINKS
1153 002546' 000411      BR      REENTR
1154                                ;-----
1155                                ; RESTART ADDRESS FOR A PASS ON ALL DEVICES
1156                                ;-----
1157 002550' 012705 000040      RESTR1: MOV     #32,,R5  ;INIT INTRPT STATUS TABLE ENTRY COUNTER
1158 002554' 012704 000350'      MOV     #INTABL,R4     ;INIT TABLE POINTER
1159 002560' 012724 000001      1$:  MOV     #MDFDON,(R4)+ ;SET MODE DEF'N DONE FLAG IN STATUS BYTE
1160 002564' 005305      DEC     R5             ;DECR TABLE ENTRY COUNTER
1161 002566' 001374      BNE     1$             ;BR IF MORE TO DO
1162 002570' 000410      BR      ENABLE
1163                                ;-----
1164                                ; REENTRY ADDRESS FOR NEXT ITERATION ON ALL DEVICES
1165                                ;-----
1166                                ;-----
1167 002572' 012705 000040      REENTR: MOV     #32,,R5  ;INIT INTRPT STATUS TABLE ENTRY COUNTER
1168 002576' 012704 000350'      MOV     #INTABL,R4     ;INIT TABLE POINTER
1169 002602' 012724 000607      1$:  MOV     #MDFDON!HLTDON!STDON!OERDON!IERDON,(R4)+ ;INIT FLAGS FOR ITERATION
1170 002606' 005305      DEC     R5             ;DECREMENT COUNTER
1171 002610' 001374      BNE     1$             ;BR IF MORE TO DO
1172                                ;-----
1173                                ;GET A WRITE BUFFER, ENABLE INTERRUPTS, REQUEST INPUTS ON ALL DEVICES
1174                                ;-----
1175                                ;-----
1176 002612' 012767 000001 175516  ENABLE: MOV     #1,TRBADR ;SET POINT-TO-POINT TRIB ADRS = 1
1177 002620' 005067 175520      CLR     QUTDUN        ;INIT OUTPUT DONE COUNT TO 0
1178 002624' 005067 175516      CLR     WATCNT        ;CLEAR NO. OF MULTIDROP LNKS AWAITING OUTPUT
1179 002630' 126727 175162 000002  CMPB     SR1,#2        ;SEE IF MULTIPOINT
1180 002636' 001005      BNE     1$             ;BR IF NOT MULTIPOINT

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1181 002640' 016767 175156 175470  MOV      SR3,TRBADR    ;INIT MULTIPOINT TRIB ADRS
1182 002646' 005367 175464      DEC      TRBADR        ;GET TRIB ADRS = 1
1183                                ;-----
1184 002652'                                1$:
1185 002652' 104414 000000'      GWHUFS, BEGIN        ;GET WRITE BUFFER INFORMATION
1186 002656' 016767 175254 175366  MOV      #WBUFEA,WBUFE ;
1187 002664' 006367 175362      ASL     WBUFE          ;GET TX BUFFER EA BITS INTO BITS 6,7
1188 002670' 006367 175356      ASL     WBUFE
1189 002674' 032767 000002 175122  BIT     #MODE22,SR4    ;* DMV IN Q22 MODE ??
1190 002702' 001406      BEQ     5$             ;* NO: DONE ADJUSTING...
1191 002704' 006367 175342      ASL     WBUFE          ;* YES: ADJUST FOR Q22 MODE
1192 002710' 006367 175336      ASL     WBUFE
1193 002714' 000367 175332      SWAB    WBUFE         ;*
1194                                ;-----
1195                                ;-----
1196 002720'                                5$:
1197 002720' 104415 000000' 000124'  GETPAS,BEGIN, RBUFEVA ;GET PHYSICAL ADDRESS FROM 16-BIT RBUFEVA
1198 002726' 016767 175176 175320  MOV      #WBUFEA,RBUFE ;
1199 002734' 006367 175314      ASL     RBUFE          ;GET RCV BUF EA BITS INTO BITS 6,7
1200 002740' 006367 175310      ASL     RBUFE
1201 002744' 032767 000002 175052  BIT     #MODE22,SR4    ;* DMV IN Q22 MODE ??
1202 002752' 001406      BEQ     6$             ;* NO: DONE ADJUSTING...
1203 002754' 006367 175274      ASL     RBUFE          ;* YES: ADJUST FOR Q22 MODE
1204 002760' 006367 175270      ASL     RBUFE
1205 002764' 000367 175264      SWAB    RBUFE         ;*
1206                                ;-----
1207 002770' 016700 175012      6$: MOV      ADDR,R0      ;INIT DEVICE ADDRESS
1208 002774' 012767 000001 175266  MOV      #BIT0,DEVPTR  ;INIT SELECTION POINTER
1209 003002' 042767 000010 175310  BIC     #NONQO,FLAGS   ;CLEAR FLAG TO GET QUEUED OUTPUT INTRPTS AGAIN
1210 003010' 036767 175254 175250  BIT     DEVPTR,SELECT  ;SEE IF THIS DEVICE IS SELECTED
1211 003016' 001403                                BEQ     7$             ;BR IF NOT SELECTED
1212 003020' 112760 000221 000000  MOVVB   #RQI!IEI!EO,BSELO(R0) ;ENABLE INTRPTS, SET RQI ON THIS DEVICE
1213 003026' 005767 174772      TST     SR4           ; IS THIS A DMV ?
1214 003032' 001402                                BEQ     4$             ;
1215 003034' 062700 000010      ADD     #10,R0         ;IF YES: INCREMENT DEVICE ADDRESS (20)
1216 003040' 062700 000010      ADD     #10,R0         ;IF NO: INCREMENT DMP11 ADDRESS (10)
1217 003044' 006367 175220      ASL     DEVPTR        ;SHIFT SELECTION POINTER
1218 003050' 001357      BNE     2$             ;BR IF NOT ALL ENABLED YET
1219 003052' 104400 000000'      RTNMON: EXITS,BEGIN   ;EXIT TO MONITOR. MODULE WAIT FOR INTERRUPT.
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;*****
;* INISR - THIS IS THE INPUT INTERRUPT SERVICE ROUTINE.
;* WHEN THE INPUT INTERRUPT OCCURS, THE PROGRAM LOADS THE APPROPRIATE
;* INPUT COMMAND INTO THE DEVICE CSR'S, AND MONITORS THE PROGRESS OF THE
;* MODULE UN THE CURRENT ITERATION.
;*****
INISR:
BICB #1E11E0,BSEL0(R0) ;DISABLE INTERRUPTS
BIT #NUNQI,FLAGS ;SEE IF NON-QUEUED INPUT OPERATION REQUESTED
BEQ 1$ ;BR FOR QUEUED OPERATION
MOV#B CSEL2+1,BSEL3(R0) ;LOAD BSEL3
MOV CSEL4,SEL4(R0) ;LOAD SEL4
MOV CSEL6,SEL6(R0) ;LOAD SEL6
BISB #ININT,FLAGS ;SET INPUT INTRPT FLAG
BICB #RQI,BSEL0(R0) ;CLEAR RQI
JMP 15$
1244 003136' 126727 174654 000002 1$: CMPB SR1,#2 ;SEE IF MULTIPOINT
1245 003144' 001406 ;BR IF MULTIPOINT
1246 003146' 010004 ;GET DEVICE ADDRESS
1247 003150' 166704 174632 SUB ADDR,R4 ;COMPUTE INTRPT STATUS TABLE INDEX
1248 003154' 006204 ASR R4
1249 003156' 006204 ASR R4
1250 003160' 000431 BR 5$
1251 003162' 005267 175150 2$: INC TRBADR ;INCR TRIB ADRS
1252 003166' 105767 175144 TSTB TRBADR ;CHK TRIB ADRS
1253 003172' 001002 BNE 3$ ;IF TRIB ADRS = 0, MAKE IT 1
1254 003174' 005267 175136 INC TRBADR ;(TRIB ADRS = 0 IS ILLEGAL)
1255 003200' 026767 175132 3$: CMP TRBADR,TRBMAX ;SEE IF MAX TRIB ADRS EXCEEDED
1256 003206' 003403 BLE 4$ ;BR IF NOT
1257 003210' 016767 174606 175120 MOV SR3,TRBADR ;RE-INIT TRIB ADRS
1258 003216' 016704 175114 4$: MOV TRBADR,R4
1259 003222' 166704 174574 SUB SM3,R4 ;COMPUTE INTRPT STATUS TABLE INDEX
1260 003226' 006304 ASL R4
1261 003230' 126767 175102 174564 CMPB TRBADR,SR3 ;SEE IF TRIB ADRS HAS OVERFLOWED
1262 003236' 103002 BHS 5$ ;BR IF NOT
1263 003240' 162704 000002 SUB #2,R4 ;ADJUST TABLE INDEX TO SKIP TRIB 0
1264 003244' 032764 100000 000350' 5$: BIT #QWAIT,INTABL(R4) ;SEE IF LOG LINK AWAITING OUTPUT
1265 003252' 001051 BNE 7$ ;BR IF YES, TO RETURN
1266 003254' 032764 000020 000350' BIT #BITDON,INTABL(R4) ;SEE IF BACCIT JUST COMPLETED
1267 003262' 001045 BNE 7$ ;BR IF YES, TO RETURN
1268 003264' 032764 000010 000350' BIT #BIRDON,INTABL(R4) ;SEE IF BACCIR JUST COMPLETED
1269 003272' 001140 BNE 11$ ;BR IF YES, TO ISSUE BACCIT
1270 003274' 032764 000004 000350' BIT #ISTDON,INTABL(R4) ;SEE IF ISTART JUST COMPLETED
1271 003302' 001077 BNE 10$ ;BR IF YES, TO ISSUE BACCIR
1272 003304' 032764 000002 000350' BIT #HLTDON,INTABL(R4) ;SEE IF HALT JUST COMPLETED
1273 003312' 001053 BNE 9$ ;BR IF YES, TO ISSUE ISTART
1274 003314' 032764 000400 000350' BIT #IERDON,INTABL(R4) ;SEE IF DATA ERRORS INBOUND REQUESTED
1275 003322' 001027 BNE 8$ ;BR IF YES, TO ISSUE HALT
1276 003324' 032764 000200 000350' BIT #OERDON,INTABL(R4) ;SEE IF DATA ERRORS OUTBOUND REQUESTED
1277 003332' 001177 BNE 13$ ;BR IF YES, TO REQUEST DATA ERRORS INBOUND
1278 003334' 032764 000001 000350' BIT #MDFDON,INTABL(R4) ;SEE IF MODE DEFINITION JUST COMPLETED

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1279 003342' 001153 BNE 12$ ;BR IF YES, TO REQUEST DATA ERRORS OUTBOUND
1280 003344' 004767 002572 JSR PC,GETERR ;LOAD ERROR INFORMATION FOR PRINTOUT
1281 003350' 012767 000011 174530 MOV #ILGINT,ERRTYP ;CODE FOR ILLEGAL INTRPT OCCURRED
1282 ;*****
1283 003356' 104405 000000' 000450' HRDERS,BEGIN,RECTBL ;UNSOLICITED INPUT INTERRUPT OCCURRED
1284 ;*****
1285 003364' 142760 000201 000000 6$: BICB #RQI1IEI,BSEL0(R0) ;UNSOLICITED INPUT INTRPT - CLR RQI,IEI
1286 003372' 000167 174544 JMP RTRNMON ;RETURN TO DEC/X11 MONITOR TO AWAIT INTERRUPT
1287 003376' 000167 000402 7$: JMP CHMKRU ;GO SCAN FOR INPUT ON NEXT LOGICAL LINK
1288 ;ISSUE HALT STATE CTL IN
1289 003402' 112767 000001 174666 8$: MOV#B #CTLIN,CSEL2 ;SET UP CONTROL IN COMMAND
1290 003410' 005060 000004 CLR SEL4(R0) ;CLEAR SEL4
1291 003414' 005060 000006 CLR SEL6(R0) ;CLEAR SEL6
1292 003420' 112760 000005 000006 MOV#B #HALTST,BSEL6(R0) ;SET HALT STATE
1293 003426' 052764 000002 000350' BIS #HLTDON,INTABL(R4) ;SET HALT DONE IN STATUS WORD
1294 003434' 004767 002616 JSR PC,CLRQI ;GO SEE IF RQI SHOULD BE CLEARED
1295 003440' 000553 BR 14$
1296 ;ISSUE ISTART CTL IN
1297 003442' 112767 000001 174626 9$: MOV#B #CTLIN,CSEL2 ;SET UP CTL IN COMMAND
1298 003450' 005060 000004 CLR SEL4(R0) ;CLEAR SEL4
1299 003454' 005060 000006 CLR SEL6(R0) ;CLEAR SEL6
1300 003460' 112760 000003 000006 MOV#B #ISTART,BSEL6(R0) ;SET ISTART STATE
1301 003466' 052764 000004 000350' BIS #ISTDON,INTABL(R4) ;SET ISTART DONE IN STATUS WORD
1302 003474' 004767 002556 JSR PC,CLRQI ;GO SEE IF RQI SHOULD BE CLEARED
1303 003500' 000533 BR 14$
1304 ;ISSUE BACCIR
1305 003502' 112767 000000 174566 10$: MOV#B #BACCIR,CSEL2 ;SET UP BACCIR COMMAND
1306 003510' 016760 174412 000004 MOV RBUFFA,SEL4(R0) ;LOAD RCV BUF ADRS LO BITS
1307 003516' 012760 002000 000006 MOV #1024,SEL6(R0) ;SET RCV CHAR CNT = 1024.
1308 003524' 156760 174524 000007 BISB RBUFFEX,BSEL7(R0) ;LOAD RCV BUF EA BITS
1309 003532' 032767 000002 174264 BIT #MODE22,SR4 ;* IS THIS A DMV IN Q22 MODE
1310 003540' 001411 BEQ 16$ ;* IF YES:
1311 003542' 052767 000010 174526 BIS #Q22BIT,CSEL2 ;* SET Q22 MODE BIT
1312 003550' 012760 002000 000010 MOV #1024,SEL10(R0) ;* SET RX CHAR CNT=1024.
1313 003556' 016760 174472 000006 MOV RBUFFEX,SEL6(R0) ;* LOAD RCV BUF EA BITS
1314 ;* IF NO: LEAVE ALONE.
1315 003564' 052764 000010 000350' 16$: BIS #BIRDON,INTABL(R4) ;SET BACCIR DONE IN STATUS WORD
1316 003572' 000476 BR 14$
1317 ;ISSUE BACCIT
1318 003574' 112767 000004 174474 11$: MOV#B #BACCIT,CSEL2 ;SET UP BACCIT COMMAND
1319 003602' 016760 174326 000004 MOV WBUFFA,SEL4(R0) ;LOAD TX BUFFER ADDRESS LO BITS
1320 003610' 012760 002000 000006 MOV #1024,SEL6(R0) ;SET TX CHAR CNT = 1024.
1321 003616' 156760 174430 000007 BISB WBUFFEX,BSEL7(R0) ;LOAD TX BUF EA BITS
1322 003624' 032767 000002 174172 BIT #MODE22,SR4 ;* IS THIS A DMV IN Q22 MODE
1323 003632' 001411 BEQ 17$ ;* IF YES:
1324 003634' 052767 000010 174434 BIS #Q22BIT,CSEL2 ;* SET Q22 MODE BIT
1325 003642' 012760 002000 000010 MOV #1024,SEL10(R0) ;* SET RX CHAR CNT=1024.
1326 003650' 016760 174376 000006 MOV WBUFFEX,SEL6(R0) ;* LOAD RCV BUF EA BITS
1327 003656' ;* IF NO: LEAVE ALONE.
1328 003656' 052764 000020 000350' 17$: BIS #BITDON,INTABL(R4) ;SET BACCIT DONE IN STATUS WORD
1329 003664' 004767 002366 JSR PC,CLRQI ;GO SEE IF RQI SHOULD BE CLEARED
1330 003670' 000437 BR 14$
1331 ;REQUEST DATA ERRORS OUTBOUND
1332 003672' 112767 000001 174376 12$: MOV#B #CTLIN,CSEL2 ;SET UP CTL IN CMND
1333 003700' 005060 000004 CLR SEL4(R0) ;CLEAR SEL4
1334 003704' 005060 000006 CLR SEL6(R0) ;CLEAR SEL6

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DEC/X11-1 SYSTEM EXERCISER MACRO DEFINITION MODULE

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1335 003710' 112760 000112 000006      MOVVB #RDCTSS:DEROTB,BSEL6(R0) ;SET REQUEST TO READ DATA ERRORS OUTBOUND
1336 003716' 052764 000200 000350'    BIS #DERDON,INTABL(R4) ;SET REQUEST DONE FLAG
1337 003724' 004767 002326              JSR PC,CLRROI ;GO SEE IF RQ1 SHOULD BE CLEARED
1338 003730' 000417                      BR 145
1339                                     ;REQUEST DATA ERRORS INBOUND
1340 003732' 112767 000001 174336 13s:  MOVVB #CTLIN,CSEL2 ;SET UP CTL IN CMND
1341 003740' 005060 000004              CLR SEL4(R0) ;CLEAR SEL4
1342 003744' 005060 000006              CLR SEL6(R0) ;CLEAR SEL6
1343 003750' 112760 000113 000006      MOVVB #RDCTSS:DERINB,BSEL6(R0) ;SET REQUEST TO READ DATA ERRORS INBOUND
1344 003756' 052764 000400 000350'    BIS #1ERDON,INTABL(R4) ;SET REQUEST DONE FLAG
1345 003764' 004767 002266              JSR PC,CLRROI ;GO SEE IF RQ1 SHOULD BE CLEARED
1346                                     ;LOAD TRIB ADRS AND COMMAND INTO CSR'S
1347 003770' 116760 174342 000003 14s:  MOVVB TRBADK,BSEL3(R0) ;LOAD TRIB ADRS
1348 003776' 116760 174274 000002 15s:  MOVVB CSEL2,BSEL2(R0) ;LOAD COMMAND, CLEAR RDYI
1349
1350 004004' 132760 000020 000002      CHKMOR: BITB #RDYI,BSEL2(R0) ;SEE IF RDYI SET AGAIN
1351 004012' 001402 000000              BEQ 15 ;BR IF RDYI NOT SET AGAIN
1352 004014' 000167 177036              JMP INISR ;GO HANDLE NEXT INPUT
1353 004020' 132760 000200 000002 1s:   BITB #RDYU,BSEL2(R0) ;SEE IF AN OUTPUT IS PENDING
1354 004026' 001402 000000              BEQ RTNINT ;BR IF NOT
1355 004030' 000167 000012              JMP OUTISR ;GO HANDLE OUTPUT COMMAND
1356 004034' 152760 000021 000000      RTNINT: BISH #IEI:IEU,BSEL0(R0) ;SET IEI AND IEO AGAIN
1357 004042' 000167 177004              JMP RTNMON ;RETURN TO DEC/X11 TO AWAIT INTERRUPT
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1364                                     ;*****
1365                                     ;* OUTISR - THIS IS THE OUTPUT INTERRUPT SERVICE ROUTINE. WHEN THE OUTPUT
1366                                     ;* INTERRUPT OCCURS, THIS ROUTINE READS THE DEVICE CSR'S,
1367                                     ;* CHECKS THE OUTPUT, AND MONITORS THE PROGRESS OF THE
1368                                     ;* MODULE ON THE CURRENT ITERATION. IF SUFFICIENT ITERATIONS HAVE BEEN
1369                                     ;* PERFORMED, AN END OF PASS IS INDICATED, AND A NEW PASS IS BEGUN.
1370                                     ;*****
1371 004046' 142760 000021 000000      OUTISR: BICB #IEI:IEU,BSEL0(R0) ;CLEAR IEI, IEO
1372 004054' 032767 000010 174236      BIT #NONQU,FLAGS ;SEE IF NON-QUEUED OUTPUT INTRPT MODE
1373 004062' 001427 000000              BEQ 15 ;BR IF NOT
1374 004064' 016067 000000 174214      MOV SEL0(R0),RSEL0 ;READ AND STORE CSR'S
1375 004072' 016067 000002 174210      MOV SEL2(R0),RSEL2
1376 004100' 016067 000004 174204      MOV SEL4(R0),RSEL4
1377 004106' 016067 000006 174200      MOV SEL6(R0),RSEL6
1378 004114' 005767 173704              TST SR4 ; IS THIS A DMV?
1379 004120' 001403 000000              BEQ 26s ; IF NO: SKIP READ (DMP HAS NO SEL10!)
1380 004122' 016067 000010 174166      MOV SEL10(R0),RSEL10 ; IF YES: READ SEL10
1381 004130' 052767 000002 174162 26s:  BIS #OUTINT,FLAGS ;SET OUTPUT INTERRUPT FLAG
1382 004136' 000167 177672              JMP RTNINT ;GO RETURN TO DEC/X11 MONITOR
1383 004142' 126727 173650 000002 1s:   CMPB SR1,#2 ;SEE IF MULTIPOINT
1384 004150' 001406 000000              BEQ 25 ;BR IF MULTIPOINT
1385 004152' 010004 000000              MOV R0,R4 ;GET DEVICE ADDRESS
1386 004154' 166704 173626              SUB ADDR,R4 ;COMPUTE INTRPT STATUS TABLE INDEX
1387 004160' 006204 000000              ASR R4
1388 004162' 006204 000000              ASR R4
1389 004164' 000415 000000              BR 45 ;PROCEED
1390 004166' 116004 000003 2s:   MOVVB BSEL3(R0),R4 ;GET TRIB ADRS WHICH WAS RETURNED

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DEC/X11-1 SYSTEM EXERCISER MACRO DEFINITION MODULE

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1391 004172' 042704 177400              BIC #177400,R4 ;CLEAR UNUSED BITS
1392 004176' 120467 173620              CMPB R4,SR3 ;COMPUTE INTRPT STATUS TABLE INDEX
1393 004202' 103003 000000              BHIS 35
1394 004204' 062704 000400              ADD #400,R4 ;ADJUST FOR TRIB ADRS OVERFLOW
1395 004210' 005304 000000              DEC R4
1396 004212' 166704 173604 3s:   SUB SR3,R4
1397 004216' 006304 000000              ASL R4
1398 004220' 116001 000002 4s:   MOVVB BSEL2(R0),R1 ;GET BSEL2 CONTENTS
1399 004224' 142701 000370              BICB #CMDSK,R1 ;MASK OFF ALL BUT CMND BITS
1400 004230' 120127 000004              CMPB R1,#BACCOT ;SEE IF BACCOT CMND JUST COMPLETED
1401 004234' 001432 000000              BEQ 7s ;BR IF YES
1402 004236' 120127 000000              CMPB R1,#BACCOR ;SEE IF BACCOR CMND JUST COMPLETED
1403 004242' 001523 000000              BEQ 11s ;BR IF YES
1404 004244' 120127 000001              CMPB R1,#CTLOUT ;SEE IF CONTROL OUT CMND JUST COMPLETED
1405 004250' 001002 000000              BNE 5s ;BR IF NOT
1406 004252' 000167 000522              JMP 18s ;GO HANDLE CONTROL OUT COMMAND
1407 004256' 120127 000002 5s:   CMPB R1,#INFOUT ;SEE IF INFORMATION OUT CMND JUST COMPLETED
1408 004262' 001002 000000              BNE 6s ;BR IF NOT
1409 004264' 000167 000550              JMP 20s ;GO HANDLE INFO OUT COMMAND
1410 004270' 004767 001646 6s:   JSR PC,GETERR ;LOAD ERROR INFORMATION FOR PRINTOUT
1411 004274' 012767 000011 173604      MOV #ILGINT,ERRTYP ;SET CODE FOR ILLEGAL INTRPT OCCURRED
1412                                     ;*****
1413 004302' 104405 000000' 000450'    HRDERS,BEGIN,REGTBL ;UNSOLICITED OUTPUT INTRPT OCCURRED
1414                                     ;*****
1415 004310' 142760 000020 000000      BICB #IEU,BSEL0(R0) ;UNSOLICITED OUTPUT INTRPT - CLEAR IEO
1416 004316' 000167 176530              JMP RTNMON ;RETURN TO DEC/X11 MONITOR TO AWAIT INTRPT
1417                                     ;HANDLE BACCOT
1418 004322' 026067 000004 173604 7s:   CMP SEL4(R0),#BUFPA ;CHK FOR CORRECT TX BUFFER LO BITS RETURNED
1419 004330' 001411 000000              BEQ 9s ;BR IF CORRECT
1420 004332' 004767 001604              JSR PC,GETERR ;LOAD ERROR INFORMATION FOR PRINTOUT
1421 004336' 012767 000037 173542      MOV #BADWRT,ERRTYP ;CODE FOR UNABLE TO EXEC WRITE FUNCTION
1422                                     ;*****
1423 004344' 104405 000000' 000450'    HRDERS,BEGIN,REGTBL ;INCORRECT TX BA BITS RETURNED
1424                                     ;*****
1425 004352' 000453 000000              BR 10s
1426 004354' 116001 000007 8s:   MOVVB BSEL7(R0),R1 ;GET BSEL7 CONTENTS
1427 004360' 142701 000077              BICB #EAMSK,R1 ;MASK FOR BUS ADRS EA BITS
1428 004364' 032767 000002 173432      BIT #MODE22,SR4 ;* IF THIS IS A DMV IN Q22 MODE...
1429 004372' 001402 000000              BEQ 30s ;* THEN GET EXTENDED ADDRESS (EA)
1430 004374' 116001 000006              MOVVB BSEL6(R0),R1 ;* BITS FROM A DIFFERENT CSR.
1431 004400' 120167 173646 30s:   CMPB R1,#BUFEA ;CHK FOR CORRECT EA BITS RETURNED
1432 004404' 001411 000000              BEQ 9s ;BR IF CORRECT
1433 004406' 004767 001530              JSR PC,GETERR ;LOAD ERROR INFORMATION FOR PRINTOUT
1434 004412' 012767 000037 173466      MOV #BADWRT,ERRTYP ;CODE FOR UNABLE TO EXEC WRITE FUNCTION
1435                                     ;*****
1436 004420' 104405 000000' 000450'    HRDERS,BEGIN,REGTBL ;INCORRECT TX EA BITS RETURNED
1437                                     ;*****
1438 004426' 000425 000000              BR 10s
1439 004430' 016001 000006 9s:   MOV SEL6(R0),R1 ;GET SEL6 CONTENTS
1440 004434' 032767 000002 173362      BIT #MODE22,SR4 ;* IF THIS IS A DMV IN Q22 MODE...
1441 004442' 001402 000000              BEQ 27s ;* THEN GET CHARACTER COUNT
1442 004444' 016001 000010              MOV SEL10(R0),R1 ;* BITS FROM A DIFFERENT CSR.
1443 004450' 042701 140000 27s:   BIC #CCMSK,R1 ;MASK FOR CHAR COUNT BITS
1444 004454' 020127 002000              CMP R1,#1024. ;CHK FOR CORRECT CHAR CNT RETURNED
1445 004460' 001410 000000              BEQ 10s ;BR IF CORRECT
1446 004462' 004767 001454              JSR PC,GETERR ;LOAD ERROR INFORMATION FOR PRINTOUT

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1447 004466' 012767 000037 173412      MOV    #BADWRT,ERRTYP ;CODE FOR UNABLE TO EXEC WRITE FUNCTION
1448                                     ;*****
1449 004474' 104405 000000' 000450'      HDRERS,BEGIN,REGTBL ;INCORRECT TX CHAR CNT BITS RETURNED
1450                                     ;*****
1451 004502' 052764 000100 000350' 10%:  BIS    #BUTDON,INTABL(R4) ;SET BACCOT DONE IN STATUS BYTE
1452 004510' 000505                                     BR    17%
1453                                     ;HANDLE BACCOR
1454 004512' 026067 000004 173406 11%:  CMP    SEL4(R0),RBUFPA ;CHK FOR CORRECT RCY BUFFER LO BITS RETURNED
1455 004520' 001411                                     BEQ    12%
1456 004522' 004767 001414                                     JSR    PC,GETERR ;LOAD ERROR INFORMATION FOR PRINTOUT
1457 004526' 012767 000036 173352      MOV    #BADRED,ERRTYP ;CODE FOR UNABLE TO EXEC READ FUNCTION
1458                                     ;*****
1459 004534' 104405 000000' 000450'      HDRERS,BEGIN,REGTBL ;INCORRECT RCY BA BITS RETURNED
1460                                     ;*****
1461 004542' 000463                                     BR    16%
1462 004544' 116001 000007 12%:  MOVB  BSEL7(R0),R1 ;GET BSEL7 CONTENTS
1463 004550' 142701 000077                                     BICB  #EAMSK,R1 ;MASK FOR BUS ADRS EA BITS
1464 004554' 032767 000002 173242      BIT    #MODE22,SR4 ;* IF THIS IS A DMV IN Q22 MODE...
1465 004562' 001402                                     BEQ    28%
1466 004564' 116001 000006                                     MOVB  BSEL6(R0),R1 ;* THEN GET EXTENDED ADDRESS (EA)
1467 004570' 120167 173460 28%:  CMPB  #1,RBUFEX ;* BITS FROM A DIFFERENT CSR.
1468 004574' 001411                                     BEQ    28%
1469 004576' 004767 001340                                     JSR    PC,GETERR ;CHK FOR CORRECT EA BITS RETURNED
1470 004602' 012767 000036 173276      MOV    #BADRED,ERRTYP ;LOAD ERROR INFORMATION FOR PRINTOUT
1471                                     ;*****
1472 004610' 104405 000000' 000450'      HDRERS,BEGIN,REGTBL ;CODE FOR UNABLE TO EXEC READ FUNCTION
1473                                     ;*****
1474 004616' 000435                                     BR    16%
1475 004620' 016001 000006 13%:  MOV    SEL6(R0),R1 ;GET SEL6 CONTENTS
1476 004624' 032767 000002 173172      BIT    #MODE22,SR4 ;* IF THIS IS A DMV IN Q22 MODE...
1477 004632' 001402                                     BEQ    29%
1478 004634' 016001 000010                                     MOV    SEL10(R0),R1 ;* THEN GET CHARACTER COUNT
1479 004640' 042701 140000 29%:  BIC   #CCMSK,R1 ;* BITS FROM A DIFFERENT CSR.
1480 004644' 020127 002000                                     CMP   R1,#1024 ;MASK FOR CHAR COUNT BITS
1481 004650' 001411                                     BEQ   14% ;CHK FOR CORRECT CHAR CNT RETURNED
1482 004652' 004767 001264                                     JSR   PC,GETERR ;BR IF CORRECT
1483 004656' 012767 000036 173222      MOV    #BADRED,ERRTYP ;LOAD ERROR INFORMATION FOR PRINTOUT
1484                                     ;*****
1485 004664' 104405 000000' 000450'      HDRERS,BEGIN,REGTBL ;CODE FOR UNABLE TO EXEC READ FUNCTION
1486                                     ;*****
1487 004672' 000407                                     BR    16%
1488 004674' 010067 173200 14%:  MOV    #0,CSRA ;STORE CSR ADDRESS FOR POSSIBLE ERROR REPORT
1489 004700' 104412 000000' 000126'      CDATA$,BEGIN,RBUFPA ; REQUEST FOR MONITOR TO CHECK DATA
1490 004706' 004712' 15%                                     BR    15%
1491 004710' 000400                                     ; IF ERROR, RETURN AT TAG 15%
1492 004712' 15%:                                     BR    16%
1493 004712' 016700 173162 16%:  MOV    CSRA,R0 ;RESTORE CSRA (JUST IN CASE.)
1494 004716' 052764 000040 000350' 17%:  BIS    #BORDUN,INTABL(R4) ;SET BACCOR DONE IN STATUS WORD
1495 004724' 032764 000100 000350' 17%:  BIT    #BUTDON,INTABL(R4) ;SEE IF BACCOT DONE
1496 004732' 001522                                     BEQ    25% ;BR IF NOT
1497 004734' 032764 000040 000350' 17%:  BIT    #BORDUN,INTABL(R4) ;SEE IF BACCOR DONE
1498 004742' 001516                                     BEQ    25% ;BR IF NOT
1499 004744' 005267 173374                                     INC   OUTDON ;INCR CNT OF LOG LNKS DONE WITH MSG
1500 004750' 026767 173370 173352      CMP    OUTDON,LLKCNT ;INCR CNT OF LOG LNKS DONE WITH MSG
1501 004756' 002510                                     BLT   25% ;SEE IF ALL LOG LNKS DONE YET
1502 004760' 105060 000002                                     CLRB  BSEL2(R0) ;BR IF ALL NOT DONE
;CLEAR BSEL2 (INCLUDING RDYO)

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1503 004764' 004767 001110      JSR    PC,DISABL ;DISABLE INTRPTS ON ALL DEVICES
1504 004770' 104413 000000'      ENDITS,BEGIN ;SIGNAL END OF ITERATION.
1505                                     ;MONITOR SHALL TEST END OF PASS
1506 004774' 000167 175572      JMP    REENTR ;GO START NEW ITERATION
1507                                     ;HANDLE CTL OUT
1508 005000' 126027 000006 000024 18%:  CMPB  BSEL6(R0),#RUNST ;SEE IF PROTOCOL JUST ENTERED THE RUN STATE
1509 005006' 001003                                     BNE  19% ;BR IF NOT
1510 005010' 004767 001304      JSR    PC,SETRQI ;GO SET RQI
1511 005014' 000471      BR    25% ;PROCEED
1512 005016' 004767 001120 19%:  JSR    PC,GETERR ;LOAD ERROR INFORMATION FOR PRINTOUT
1513 005022' 012767 000000 173056      MOV    #NOTDFN,ERRTYP ;CODE FOR ERROR NOT DEFINED (MUST INTERPRET
; CSR CONTENTS)
1514                                     ;*****
1515                                     ;*****
1516 005030' 104405 000000' 000450'      HDRERS,BEGIN,REGTBL ;UNEXPECTED CTL OUT CMND
1517                                     ;*****
1518 005036' 000460      BR    25%
1519                                     ;HANDLE INFO OUT
1520 005040' 116001 000006 20%:  MOVB  BSEL6(R0),R1 ;GET BSEL6 CODE RETURNED
1521 005044' 132701 000100      BITB  #RDCTSS,R1 ;SEE IF RESPONSE TO READ ERROR REQUEST
1522 005050' 001433      BEQ   23% ;BR IF NOT
1523 005052' 142701 000340      BICB  #RTNMSK,R1 ;MASK FOR THE TSS NUMBER
1524 005056' 120127 000012      CMPB  R1,#EROTB ;SEE IF DATA ERRORS OUTBOUND BEING RETURNED
1525 005062' 001403      BEQ   21% ;BR IF YES
1526 005064' 120127 000013      CMPB  R1,#ERINB ;SEE IF DATA ERRORS INBOUND BEING RETURNED
1527 005070' 001033      BNE  24% ;BR IF NOT
1528 005072' 116001 000004 21%:  MOVB  BSEL4(R0),R1 ;GET BSEL4
1529 005076' 105701      TSTB  R1 ;SEE IF ANY DATA ERRORS RETURNED
1530 005100' 001414      BEQ   22% ;BR IF NOT
1531 005102' 004767 001034      JSR    PC,GETERR ;LOAD ERROR INFO FOR PRINTOUT
1532 005106' 012767 000001 172772      MOV    #DATERR,ERRTYP ;CODE FOR DATA ERROR
1533 005114' 010046      MOV    R0,-(SP) ;SAVE R0
1534 005116' 010446      MOV    R4,-(SP) ;SAVE R4
1535                                     ;*****
1536 005120' 104406 000000' 000450'      SOFERS,BEGIN,REGTBL ;DATA ERROR
1537                                     ;*****
1538 005126' 012604      MOV    (SP)+,R4 ;RESTORE R4
1539 005130' 012600      MOV    (SP)+,R0 ;RESTORE R0
1540 005132' 004767 001162 22%:  JSR    PC,SETRQ1 ;ENABLE INTERRUPTS ON THIS DEVICE
1541 005136' 000420      BR    25% ;PROCEED
1542 005140' 142701 000340 23%:  BICB  #RTNMSK,R1 ;MASK FOR RETURN CODE
1543 005144' 120127 000020      CMPB  R1,#BUFRTC ;CHECK FOR BUFFER RETURN COMPLETE
1544 005150' 001003      BNE  24% ;BR IF NOT
1545 005152' 004767 001142      JSR    PC,SETRQ1 ;ENABLE INPUTS ON THIS DEVICE
1546 005156' 000410      BR    25% ;PROCEED
1547 005160' 004767 000756 24%:  JSR    PC,GETERR ;LOAD ERROR INFORMATION FOR PRINTOUT
1548 005164' 012767 000000 172714      MOV    #NOTDFN,ERRTYP ;CODE FOR ERROR NOT DEFINED (MUST INTERPRET
; CSR CONTENTS)
1549                                     ;*****
1550                                     ;*****
1551 005172' 104405 000000' 000450'      HDRERS,BEGIN,REGTBL ;UNEXPECTED INFO OUT CMND
1552                                     ;*****
1553 005200' 105060 000002 25%:  CLRB  BSEL2(R0) ;CLEAR BSEL2 (INCLUDING RDYO)
1554 005204' 000167 176574      JMP    CHKNOR ;GO SEE IF RDYO OR RDYO STILL SET,
; AND RETURN TO DEC/X11 MONITOR IF NOT
1555
1556
1557
1558 ;*****

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1559 ;* DMVLUP - THIS SUBROUTINE ENTERS M-LOOP, SETS TTL LOOPBACK AND TIMER 1 FOR
1560 ;* 56K BPS, AND EXITS MLOOP. (DMV ONLY)
1561 ;*****
1562 005210' 112760 000301 000001 DMVLUP: MOVB #301,BSEL1(RO) ;ENTER MAINTENANCE LOOP
1563 005216'
1564 005216' 104407 000000' 1$: BREAKS,BEGIN ;TEMPORARY RETURN TO MONITOR....
1565 005222' 104407 000000' BREAKS,BEGIN ;THEN CONTINUE AT NEXT INSTRUCTION.
1566 005226' 132760 000200 000002 BITB #MRDY,BSEL2(RO) ;MAKE SURE MLOOP COMPLETE
1567 005234' 001770 BEQ 1$
1568 005236' 012760 000006 000002 MOV #TTL00P,BSEL2(RO) ;ISSUE TTL00P CONFIGURE COMMAND
1569 005244' 104407 000000' BREAKS,BEGIN ;TEMPORARY RETURN TO MONITOR....
1570 005250' 104407 000000' BREAKS,BEGIN ;THEN CONTINUE AT NEXT INSTRUCTION.
1571 005254' 000240 NOP
1572 005256' 000240 NOP
1573 005260' 000207 RTS PC ;RETURN
1574
1575
1576 ;*****
1577 ;* DMVRO - THIS SUBROUTINE FORCES THE DMV WHOSE ADRS IS PASSED IN RO ON ENTRY
1578 ;* TO ENTER THE M-LOOP AND READ THE INTERNAL DMV LOCATION PASSED IN THE
1579 ;* WORD FOLLOWING THE CALL.
1580 ;*****
1581 005262' 112760 000301 000001 DMVRD: MOVB #301,BSEL1(RO) ;ENTER MAINTENANCE LOOP
1582 005270'
1583 005270' 104407 000000' 1$: BREAKS,BEGIN ;TEMPORARY RETURN TO MONITOR....
1584 005274' 104407 000000' BREAKS,BEGIN ;THEN CONTINUE AT NEXT INSTRUCTION.
1585 005300' 132760 000200 000002 BITB #MRDY,BSEL2(RO) ;MAKE SURE MLOOP COMPLETE
1586 005306' 001770 BEQ 1$
1587 005310' 017660 000000 000004 MOV @(SP),SEL4(RO) ;PASS DMV INTERNAL ADDRESS
1588 005316' 012760 000001 000002 MOV #REDLOC,BSEL2(RO) ;ISSUE READ COMMAND
1589 005324'
1590 005324' 104407 000000' 2$: BREAKS,BEGIN ;TEMPORARY RETURN TO MONITOR....
1591 005330' 104407 000000' BREAKS,BEGIN ;THEN CONTINUE AT NEXT INSTRUCTION.
1592 005334' 132760 000200 000002 BITB #MRDY,BSEL2(RO) ;MAKE SURE MLOOP READ IS COMPLETE
1593 005342' 001770 BEQ 2$
1594 005344' 062716 000002 ADD #2,(SP) ;FIX UP RETURN PC
1595 005350' 000207 RTS PC ;RETURN
1596
1597
1598 ;*****
1599 ;* EXECUT - THIS SUBROUTINE FORCES THE MICROPROCESSOR WHOSE ADRS IS PASSED
1600 ;* IN RO ON ENTRY TO EXECUTE AN INSTRUCTION WHICH IS PASSED IN THE WORD
1601 ;* FOLLOWING THE CALL (DMP ONLY).
1602 ;*****
1603 005352' 152760 000006 000001 EXECUT: BLSB #ROM0:ROM1,BSEL1(RO) ;SET ROM0, ROM1 BITS IN BSEL1
1604 005360' 017660 000000 000006 MOV @(SP),SEL6(RO) ;PUT INSTRUCTION INTO SEL6
1605 005366' 152760 000007 000001 BLSB #ROM0:ROM1:STEPMP,BSEL1(RO) ;SET ROM0, ROM1, STEPMP IN BSEL1
1606 005374' 142760 000007 000001 BLSB #ROM0:ROM1:STEPMP,BSEL1(RO) ;CLEAR ROM0, ROM1, STEPMP IN BSEL1
1607 005402' 062716 000002 ADD #2,(SP) ;FIX UP RETURN PC
1608 005406' 000207 RTS PC ;RETURN
1609
1610
1611 ;*****
1612 ;* INIDEV - THIS SUBROUTINE ISSUES A MASTER CLEAR AND CLEARS THE CSR'S,
1613 ;* FOR THE DEVICE WHOSE ADDRESS IS PASSED IN RO ON ENTRY. (DMP AND DMV).
1614

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1615 ;*****
1616 005410' 010146 INIDEV: MOV R1,-(SP) ;SAVE R1
1617 005412' 004767 000236 JSR PC,CLRCSR ;CLEAR BSEL0, SEL2,4,6(,10)
1618 005416' 112760 000100 000001 MOVB #MCLR,BSEL1(RO) ;SET MASTER CLEAR BIT
1619 005424' 005767 172374 TST SR4 ; IF DMV ...
1620 005430' 001003 BNE 6$ ; THEN DON'T SET RUN.
1621
1622 ;-----
1623 005432' 000240 ;;; MOVB #RUN,BSEL1(RO) ;PATCH TO SET RUN BIT IF USING M8206
1624 005434' 000240 ; 112760
1625 005436' 000240 ; 200
1626 ;-----
1627 005440' 042767 000004 172654 6$: BIC #BADINI,ERRORS ;CLEAR BAD INIT ERROR FLAG
1628 005446' 012701 020000 MOV #2000,R1 ;INITIALIZE INIT TIMER
1629 005452' 132760 000200 000001 1$: BITB #RUN,BSEL1(RO) ;SEE IF RUN IS SET AGAIN
1630 005460' 001020 BNE 3$ ;BR IF RUN IS SET
1631 005462' 005301 DEC R1 ;DECR TIMER
1632 005464' 001405 BEQ 2$ ;BR IF INIT TIMED-OUT
1633 005466' 104407 000000' BREAKS,BEGIN ;TEMPORARY RETURN TO MONITOR....
1634 005472' 104407 000000' BREAKS,BEGIN ;THEN CONTINUE AT NEXT INSTRUCTION.
1635 005476' 000765 BR 1$ ;GO CHECK AGAIN
1636 005500' 004767 000436 2$: JSR PC,GETERR ;LOAD ERROR INFORMATION FOR PRINTOUT
1637 005504' 012767 000034 172374 MOV #N0INIT,ERRTYP ;CODE FOR DEVICE WILL NOT INIT
1638 ;*****
1639 ;*****
1640 ;*****
1641 005512' 104405 000000' 000450' HRDERS,BEGIN,REGTBL ;TIMED-OUT WAITING FOR RUN TO SET
1642 ;*****
1643 005520' 000414 BR 4$
1644 005522' 126027 000006 000305 3$: CMPB BSEL6(RO),#GOODIN ;SEE IF INIT COMPLETE CODE SET BY MICRO-CPU
1645 005530' 001413 BEQ 5$ ;BR IF YES
1646 005532' 004767 000404 JSR PC,GETERR ;LOAD ERROR INFORMATION FOR PRINTOUT
1647 005536' 012767 000034 172342 MOV #N0INIT,ERRTYP ;CODE FOR DEVICE WILL NOT INIT
1648 ;*****
1649 ;*****
1650 005544' 104405 000000' 000450' HRDERS,BEGIN,REGTBL ;INIT COMPLETE CODE NOT SET BY MICRO-CPU
1651 ;*****
1652 005552' 052767 000004 172542 4$: BIS #BADINI,ERRORS ;SET BAD INIT ERROR FLAG
1653 005560' 004767 000070 5$: JSR PC,CLRCSR ;CLEAR BSEL0, SEL2,4,6
1654 005564' 012601 MOV (SP)+,R1 ;RESTORE R1
1655 005566' 000207 RTS PC ;RETURN
1656
1657 ;*****
1658 ;* READ16 - THIS SUBROUTINE FORCES THE MICROPROCESSOR WHOSE ADDRESS IS
1659 ;* PASSED IN RO ON ENTRY, TO EXECUTE AN INSTRUCTION WHICH READS DMP LINE
1660 ;* UNIT IBUS REG 16 INTO BSEL4, AND THEN IT PLACES THE CONTENTS INTO LOC.
1661 ;* LURG16 (IF DMV: EXECUTE MLOOP, "SWPBOT" => LURG16, AND ADJUST).
1662 ;*****
1663 005570' 005767 172230 READ16: TST SR4 ;IS THIS A DMV ?
1664 005574' 001007 BNE 1$ ;BRANCH IF YES
1665 005576' 004767 177550 JSR PC,EXECUT ;DMP: EXECUTE MOVE INSTRUCTION
1666 005602' 021344 RDLU16
1667 005604' 116067 000004 172460 MOVB BSEL4(RO),LURG16 ;GET REG 16 CONTENTS INTO LURG16
1668 005612' 000415 BR 2$
1669
1670 005614' 004767 177442 1$: JSR PC,DMVRD ;DMV: READ "SWPBOT" SWITCHES

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1671 005620' 121000          .WORD SWPBUT
1672 005622' 116067          MOVB BSEL6(R0),LURG16 ;PUT IT IN LURG16
1673 005630' 000241          CLC          ;ADJUST *SWPBOT* BITS
1674 005632' 106267          ASRB LURG16  ; TO LOOK LIKE THE DMP
1675 005636' 103003          BCC 2$      ; REG 16 SWITCHES
1676 005640' 152767          BISB #ENABSW,LURG16
1677
1678 005646' 105067          CLR B LURG16+1 ;CLEAR HI BYTE
1679 005652' 000207          RTS PC      ;RETURN
1680
1681
1682
1683
1684
1685
1686
1687
1688 005654' 105060          000000
1689 005660' 005060          000002
1690 005664' 005060          000004
1691 005670' 005060          000006
1692 005674' 005767          172124
1693 005700' 001402          TST SR4
1694 005702' 005060          BEQ 1$
1695 005706' 000207          CLR SEL10(R0) ;IF DMV: CLEAR SEL10
1696
1697
1698
1699
1700
1701
1702
1703
1704
1705
1706
1707
1708 005710' 042767          000001 172404
1709 005716' 052767          000004 172374
1710 005724' 042767          000001 172366
1711 005732' 005067          172350
1712 005736' 005067          172346
1713 005742' 005067          172344
1714 005746' 005067          172342
1715 005752' 005067          172340
1716 005756' 012767          030000 172340
1717 005764' 112760          000221 000000
1718
1719 005772' 104407          000000'
1720 005776' 104407          000000'
1721 006002' 132767          000001 172310
1722 006010' 001016          000000
1723 006012' 005367          172306
1724 006016' 001365          000000
1725 006020' 004767          000116
1726 006024' 012767          000023 172054

```

```

1727
1728 006032' 104405          000000' 000450'
1729
1730 006040' 052767          000001 172254
1731 006046' 042767          000004 172244
1732 006054' 000207          000000
1733
1734
1735
1736
1737
1738
1739 006056' 005067          172214
1740 006062' 005067          172212
1741 006066' 005067          172210
1742 006072' 005067          172206
1743 006076' 000207          000000
1744
1745
1746
1747
1748
1749
1750 006100' 016700          171702
1751 006104' 012767          000001 172156
1752 006112' 036767          172152 172146
1753 006120' 001402          000000
1754 006122' 105060          000000
1755 006126' 062700          000010
1756 006132' 006367          172132
1757 006136' 001365          000000
1758 006140' 000207          000000
1759
1760
1761
1762
1763
1764
1765
1766
1767 006142' 010067          171732
1768 006146' 016067          000000 171726
1769 006154' 016067          000002 171722
1770 006162' 010067          172262
1771 006166' 010067          172260
1772 006172' 062767          000002 172252
1773 006200' 010067          172250
1774 006204' 062767          000004 172242
1775 006212' 010067          172240
1776 006216' 062767          000006 172232
1777 006224' 005767          171574
1778 006230' 001004          000000
1779 006232' 012767          177777 172220
1780 006240' 000405          000000
1781 006242' 010067          172212
1782 006246' 062767          000010 172204

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1783 006254' 000207          2S:  RTS      PC          ;RETURN
1784
1785
1786
1787
1788
1789
1790
1791
1792
1793
1794
1795
1796 006256' 052764 100000 000350' CLRRQI: BIS      #UWAIT,INTABL(R4) ;SET UWAIT BIT FOR AWAITING OUTPUT
1797 006264' 126727 171526 000002      CMPB     SR1,#2          ;SEE IF MULTIDROP
1798 006272' 001006          BNE     1S          ;BR IF NOT
1799 006274' 005267 172046          INC     #WATCNT      ;INCR CNT OF LNKS AWAITING OUTPUT
1800 006300' 026767 172042 172022      CMP     #WATCNT,LLKCNT ;SEE IF ALL LINKS WAITING
1801 006306' 002403          BLT     2S          ;BR IF NOT
1802 006310' 142760 000200 000000 1S:  BICB     #RQI,BSEL0(R0) ;CLEAR RQI ON DEVICE
1803 006316' 000207          2S:  RTS      PC          ;RETURN
1804
1805
1806
1807
1808
1809
1810
1811
1812
1813
1814
1815
1816 006320' 042764 100000 000350' SETRQI: BIC      #UWAIT,INTABL(R4) ;CLEAR AWAITING OUTPUT BIT
1817 006326' 126727 171464 000002      CMPB     SR1,#2          ;SEE IF MULTIDROP
1818 006334' 001002          BNE     1S          ;BR IF NOT
1819 006336' 005367 172004          DEC     #WATCNT      ;DECR CNT OF LNKS AWAITING OUTPUT
1820 006342' 152760 000200 000000 1S:  BLSB     #RQI,BSEL0(R0) ;SET RQI ON DEVICE
1821 006350' 000207          RTS      PC          ;RETURN
1822
1823
1824
1825
1826
1827
1828
1829
1830
1831
1832
1833
1834 006352'
1835
1836 006352' 000004 000000' 006360' IISR0:
1837
1838 006360' 016700 000022 1S:  MOV     ADDR0,R0      ;PUT DEVICE 0 CSR ADDRESS IN R0

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CXDMDC.P11 25-MAR-81 08:25

DEC/X11-1 SYSTEM EXERCISER MACRO DEFINITION MODULE

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1839 006364' 000167 174466          JMP     INISR        ;GO SERVICE INPUT INTERRUPT ON DEVICE 0
1840
1841 006370'
1842
1843 006370' 000004 000000' 006376' DISK0:
1844
1845 006376' 016700 000004 1S:  MOV     ADDR0,R0      ;PUT DEVICE 0 CSR ADDRESS IN R0
1846 006402' 000167 175440          JMP     OUTISR       ;GO SERVICE OUTPUT INTERRUPT ON DEVICE 0
1847
1848 006406' 000000          ADDR0: .WORD 0      ;DEVICE CSR ADDRESS STORED HERE
1849
1850
1851
1852 006410'
1853
1854 006410' 000004 000000' 006416' IISR1:
1855
1856 006416' 016700 000022 1S:  MOV     ADDR1,R0      ;PUT DEVICE 1 CSR ADDRESS IN R0
1857 006422' 000167 174430          JMP     INISR        ;GO SERVICE INPUT INTERRUPT ON DEVICE 1
1858
1859 006426'
1860
1861 006426' 000004 000000' 006434' OISR1:
1862
1863 006434' 016700 000004 1S:  MOV     ADDR1,R0      ;PUT DEVICE 1 CSR ADDRESS IN R0
1864 006440' 000167 175402          JMP     OUTISR       ;GO SERVICE OUTPUT INTERRUPT ON DEVICE 1
1865
1866 006444' 000000          ADDR1: .WORD 0      ;DEVICE CSR ADDRESS STORED HERE
1867
1868
1869
1870 006446'
1871
1872 006446' 000004 000000' 006454' IISR2:
1873
1874 006454' 016700 000022 1S:  MOV     ADDR2,R0      ;PUT DEVICE 2 CSR ADDRESS IN R0
1875 006460' 000167 174372          JMP     INISR        ;GO SERVICE INPUT INTERRUPT ON DEVICE 2
1876
1877 006464'
1878
1879 006464' 000004 000000' 006472' OISR2:
1880
1881 006472' 016700 000004 1S:  MOV     ADDR2,R0      ;PUT DEVICE 2 CSR ADDRESS IN R0
1882 006476' 000167 175344          JMP     OUTISR       ;GO SERVICE OUTPUT INTERRUPT ON DEVICE 2
1883
1884 006502' 000000          ADDR2: .WORD 0      ;DEVICE CSR ADDRESS STORED HERE
1885
1886
1887
1888 006504'
1889
1890 006504' 000004 000000' 006512' IISR3:
1891
1892 006512' 016700 000022 1S:  MOV     ADDR3,R0      ;PUT DEVICE 3 CSR ADDRESS IN R0
1893 006516' 000167 174334          JMP     INISR        ;GO SERVICE INPUT INTERRUPT ON DEVICE 3
1894

```

```

1895 006522' OISR3: ;-----
1896 ; PIRQ$,BEGIN,1$ ; QUEUE UP TO CONTINUE AT 1$ AND RTI
1897 006522' 000004 000000' 006530' ;-----
1898 ;
1899 006530' 016700 000004 1$: MOV ADDR3,R0 ;PUT DEVICE 3 CSR ADDRESS IN R0
1900 006534' 000167 175306 JMP OUTISR ;GO SERVICE OUTPUT INTERRUPT ON DEVICE 3
1901 ;
1902 006540' 000000 ADDR3: .WORD 0 ;DEVICE CSR ADDRESS STORED HERE
1903 ;
1904 ;
1905 ;
1906 006542' IISR4: ;-----
1907 ; PIRQ$,BEGIN,1$ ; QUEUE UP TO CONTINUE AT 1$ AND RTI
1908 006542' 000004 000000' 006550' ;-----
1909 ;
1910 006550' 016700 000022 1$: MOV ADDR4,R0 ;PUT DEVICE 4 CSR ADDRESS IN R0
1911 006554' 000167 174276 JMP INISR ;GO SERVICE INPUT INTERRUPT ON DEVICE 4
1912 ;
1913 006560' OISR4: ;-----
1914 ; PIRQ$,BEGIN,1$ ; QUEUE UP TO CONTINUE AT 1$ AND RTI
1915 006560' 000004 000000' 006566' ;-----
1916 ;
1917 006566' 016700 000004 1$: MOV ADDR4,R0 ;PUT DEVICE 4 CSR ADDRESS IN R0
1918 006572' 000167 175250 JMP OUTISR ;GO SERVICE OUTPUT INTERRUPT ON DEVICE 4
1919 ;
1920 006576' 000000 ADDR4: .WORD 0 ;DEVICE CSR ADDRESS STORED HERE
1921 ;
1922 ;
1923 ;
1924 006600' IISR5: ;-----
1925 ; PIRQ$,BEGIN,1$ ; QUEUE UP TO CONTINUE AT 1$ AND RTI
1926 006600' 000004 000000' 006606' ;-----
1927 ;
1928 006606' 016700 000022 1$: MOV ADDR5,R0 ;PUT DEVICE 5 CSR ADDRESS IN R0
1929 006612' 000167 174240 JMP INISR ;GO SERVICE INPUT INTERRUPT ON DEVICE 5
1930 ;
1931 006616' OISR5: ;-----
1932 ; PIRQ$,BEGIN,1$ ; QUEUE UP TO CONTINUE AT 1$ AND RTI
1933 006616' 000004 000000' 006624' ;-----
1934 ;
1935 006624' 016700 000004 1$: MOV ADDR5,R0 ;PUT DEVICE 5 CSR ADDRESS IN R0
1936 006630' 000167 175212 JMP OUTISR ;GO SERVICE OUTPUT INTERRUPT ON DEVICE 5
1937 ;
1938 006634' 000000 ADDR5: .WORD 0 ;DEVICE CSR ADDRESS STORED HERE
1939 ;
1940 ;
1941 ;
1942 006636' IISR6: ;-----
1943 ; PIRQ$,BEGIN,1$ ; QUEUE UP TO CONTINUE AT 1$ AND RTI
1944 006636' 000004 000000' 006644' ;-----
1945 ;
1946 006644' 016700 000022 1$: MOV ADDR6,R0 ;PUT DEVICE 6 CSR ADDRESS IN R0
1947 006650' 000167 174202 JMP INISR ;GO SERVICE INPUT INTERRUPT ON DEVICE 6
1948 ;
1949 006654' OISR6: ;-----
1950 ;

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1951 006654' 000004 000000' 006662' PIRQ$,BEGIN,1$ ; QUEUE UP TO CONTINUE AT 1$ AND RTI
1952 ;-----
1953 006662' 016700 000004 1$: MOV ADDR6,R0 ;PUT DEVICE 6 CSR ADDRESS IN R0
1954 006666' 000167 175154 JMP OUTISR ;GO SERVICE OUTPUT INTERRUPT ON DEVICE 6
1955 ;
1956 006672' 000000 ADDR6: .WORD 0 ;DEVICE CSR ADDRESS STORED HERE
1957 ;
1958 ;
1959 ;
1960 006674' IISR7: ;-----
1961 ; PIRQ$,BEGIN,1$ ; QUEUE UP TO CONTINUE AT 1$ AND RTI
1962 006674' 000004 000000' 006702' ;-----
1963 ;
1964 006702' 016700 000022 1$: MOV ADDR7,R0 ;PUT DEVICE 7 CSR ADDRESS IN R0
1965 006706' 000167 174144 JMP INISR ;GO SERVICE INPUT INTERRUPT ON DEVICE 7
1966 ;
1967 006712' OISR7: ;-----
1968 ; PIRQ$,BEGIN,1$ ; QUEUE UP TO CONTINUE AT 1$ AND RTI
1969 006712' 000004 000000' 006720' ;-----
1970 ;
1971 006720' 016700 000004 1$: MOV ADDR7,R0 ;PUT DEVICE 7 CSR ADDRESS IN R0
1972 006724' 000167 175116 JMP OUTISR ;GO SERVICE OUTPUT INTERRUPT ON DEVICE 7
1973 ;
1974 006730' 000000 ADDR7: .WORD 0 ;DEVICE CSR ADDRESS STORED HERE
1975 ;
1976 ;
1977 ;
1978 006732' IISR10: ;-----
1979 ; PIRQ$,BEGIN,1$ ; QUEUE UP TO CONTINUE AT 1$ AND RTI
1980 006732' 000004 000000' 006740' ;-----
1981 ;
1982 006740' 016700 000022 1$: MOV ADDR10,R0 ;PUT DEVICE 10 CSR ADDRESS IN R0
1983 006744' 000167 174106 JMP INISR ;GO SERVICE INPUT INTERRUPT ON DEVICE 10
1984 ;
1985 006750' OISR10: ;-----
1986 ; PIRQ$,BEGIN,1$ ; QUEUE UP TO CONTINUE AT 1$ AND RTI
1987 006750' 000004 000000' 006756' ;-----
1988 ;
1989 006756' 016700 000004 1$: MOV ADDR10,R0 ;PUT DEVICE 10 CSR ADDRESS IN R0
1990 006762' 000167 175060 JMP OUTISR ;GO SERVICE OUTPUT INTERRUPT ON DEVICE 10
1991 ;
1992 006766' 000000 ADDR10: .WORD 0 ;DEVICE CSR ADDRESS STORED HERE
1993 ;
1994 ;
1995 ;
1996 006770' IISR11: ;-----
1997 ; PIRQ$,BEGIN,1$ ; QUEUE UP TO CONTINUE AT 1$ AND RTI
1998 006770' 000004 000000' 006776' ;-----
1999 ;
2000 006776' 016700 000022 1$: MOV ADDR11,R0 ;PUT DEVICE 11 CSR ADDRESS IN R0
2001 007002' 000167 174050 JMP INISR ;GO SERVICE INPUT INTERRUPT ON DEVICE 11
2002 ;
2003 007006' OISR11: ;-----
2004 ; PIRQ$,BEGIN,1$ ; QUEUE UP TO CONTINUE AT 1$ AND RTI
2005 007006' 000004 000000' 007014' ;-----
2006 ;

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2007 007014' 016700 000004 1s:  MOV  ADDR11,R0      ;PUT DEVICE 11 CSR ADDRESS IN R0
2008 007020' 000167 175022      JMP  OUTISR        ;GO SERVICE OUTPUT INTERRUPT ON DEVICE 11
2009
2010 007024' 000000      ADDR11: .WORD 0    ;DEVICE CSR ADDRESS STORED HERE
2011
2012
2013
2014 007026'      IISR12:
2015      ;-----
2016 007026' 000004 000000' 007034'  PIRQS,BEGIN,1$    ; QUEUE UP TO CONTINUE AT 1$ AND RTI
2017      ;-----
2018 007034' 016700 000022 1s:  MOV  ADDR12,R0      ;PUT DEVICE 12 CSR ADDRESS IN R0
2019 007040' 000167 174012      JMP  INISR        ;GO SERVICE INPUT INTERRUPT ON DEVICE 12
2020
2021 007044'      OISR12:
2022      ;-----
2023 007044' 000004 000000' 007052'  PIRQS,BEGIN,1$    ; QUEUE UP TO CONTINUE AT 1$ AND RTI
2024      ;-----
2025 007052' 016700 000004 1s:  MOV  ADDR12,R0      ;PUT DEVICE 12 CSR ADDRESS IN R0
2026 007056' 000167 174764      JMP  OUTISR        ;GO SERVICE OUTPUT INTERRUPT ON DEVICE 12
2027
2028 007062' 000000      ADDR12: .WORD 0    ;DEVICE CSR ADDRESS STORED HERE
2029
2030
2031
2032 007064'      IISR13:
2033      ;-----
2034 007064' 000004 000000' 007072'  PIRQS,BEGIN,1$    ; QUEUE UP TO CONTINUE AT 1$ AND RTI
2035      ;-----
2036 007072' 016700 000022 1s:  MOV  ADDR13,R0      ;PUT DEVICE 13 CSR ADDRESS IN R0
2037 007076' 000167 173754      JMP  INISR        ;GO SERVICE INPUT INTERRUPT ON DEVICE 13
2038
2039 007102'      OISR13:
2040      ;-----
2041 007102' 000004 000000' 007110'  PIRQS,BEGIN,1$    ; QUEUE UP TO CONTINUE AT 1$ AND RTI
2042      ;-----
2043 007110' 016700 000004 1s:  MOV  ADDR13,R0      ;PUT DEVICE 13 CSR ADDRESS IN R0
2044 007114' 000167 174726      JMP  OUTISR        ;GO SERVICE OUTPUT INTERRUPT ON DEVICE 13
2045
2046 007120' 000000      ADDR13: .WORD 0    ;DEVICE CSR ADDRESS STORED HERE
2047
2048
2049
2050 007122'      IISR14:
2051      ;-----
2052 007122' 000004 000000' 007130'  PIRQS,BEGIN,1$    ; QUEUE UP TO CONTINUE AT 1$ AND RTI
2053      ;-----
2054 007130' 016700 000022 1s:  MOV  ADDR14,R0      ;PUT DEVICE 14 CSR ADDRESS IN R0
2055 007134' 000167 173716      JMP  INISR        ;GO SERVICE INPUT INTERRUPT ON DEVICE 14
2056
2057 007140'      OISR14:
2058      ;-----
2059 007140' 000004 000000' 007146'  PIRQS,BEGIN,1$    ; QUEUE UP TO CONTINUE AT 1$ AND RTI
2060      ;-----
2061 007146' 016700 000004 1s:  MOV  ADDR14,R0      ;PUT DEVICE 14 CSR ADDRESS IN R0
2062 007152' 000167 174670      JMP  OUTISR        ;GO SERVICE OUTPUT INTERRUPT ON DEVICE 14

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2063
2064 007156' 000000      ADDR14: .WORD 0    ;DEVICE CSR ADDRESS STORED HERE
2065
2066
2067
2068 007160'      IISR15:
2069      ;-----
2070 007160' 000004 000000' 007166'  PIRQS,BEGIN,1$    ; QUEUE UP TO CONTINUE AT 1$ AND RTI
2071      ;-----
2072 007166' 016700 000022 1s:  MOV  ADDR15,R0      ;PUT DEVICE 15 CSR ADDRESS IN R0
2073 007172' 000167 173660      JMP  INISR        ;GO SERVICE INPUT INTERRUPT ON DEVICE 15
2074
2075 007176'      OISR15:
2076      ;-----
2077 007176' 000004 000000' 007204'  PIRQS,BEGIN,1$    ; QUEUE UP TO CONTINUE AT 1$ AND RTI
2078      ;-----
2079 007204' 016700 000004 1s:  MOV  ADDR15,R0      ;PUT DEVICE 15 CSR ADDRESS IN R0
2080 007210' 000167 174632      JMP  OUTISR        ;GO SERVICE OUTPUT INTERRUPT ON DEVICE 15
2081
2082 007214' 000000      ADDR15: .WORD 0    ;DEVICE CSR ADDRESS STORED HERE
2083
2084
2085
2086 007216'      IISR16:
2087      ;-----
2088 007216' 000004 000000' 007224'  PIRQS,BEGIN,1$    ; QUEUE UP TO CONTINUE AT 1$ AND RTI
2089      ;-----
2090 007224' 016700 000022 1s:  MOV  ADDR16,R0      ;PUT DEVICE 16 CSR ADDRESS IN R0
2091 007230' 000167 173622      JMP  INISR        ;GO SERVICE INPUT INTERRUPT ON DEVICE 16
2092
2093 007234'      OISR16:
2094      ;-----
2095 007234' 000004 000000' 007242'  PIRQS,BEGIN,1$    ; QUEUE UP TO CONTINUE AT 1$ AND RTI
2096      ;-----
2097 007242' 016700 000004 1s:  MOV  ADDR16,R0      ;PUT DEVICE 16 CSR ADDRESS IN R0
2098 007246' 000167 174574      JMP  OUTISR        ;GO SERVICE OUTPUT INTERRUPT ON DEVICE 16
2099
2100 007252' 000000      ADDR16: .WORD 0    ;DEVICE CSR ADDRESS STORED HERE
2101
2102
2103
2104 007254'      IISR17:
2105      ;-----
2106 007254' 000004 000000' 007262'  PIRQS,BEGIN,1$    ; QUEUE UP TO CONTINUE AT 1$ AND RTI
2107      ;-----
2108 007262' 016700 000022 1s:  MOV  ADDR17,R0      ;PUT DEVICE 17 CSR ADDRESS IN R0
2109 007266' 000167 173564      JMP  INISR        ;GO SERVICE INPUT INTERRUPT ON DEVICE 17
2110
2111 007272'      OISR17:
2112      ;-----
2113 007272' 000004 000000' 007300'  PIRQS,BEGIN,1$    ; QUEUE UP TO CONTINUE AT 1$ AND RTI
2114      ;-----
2115 007300' 016700 000004 1s:  MOV  ADDR17,R0      ;PUT DEVICE 17 CSR ADDRESS IN R0
2116 007304' 000167 174536      JMP  OUTISR        ;GO SERVICE OUTPUT INTERRUPT ON DEVICE 17
2117
2118 007310' 000000      ADDR17: .WORD 0    ;DEVICE CSR ADDRESS STORED HERE

```

2119  
2120  
2121

2122  
2123  
2124  
2125  
2126  
2127 007312' 002000  
2128  
2129  
2130  
2131  
2132  
2133  
2134 011312' 000036  
2135  
2136  
2137 000001

BUFIN: .BLKB 1024. ;INPUT BUFFER (1024 BYTES)

\*\*\*\*\* PATCH AREA FOR DEBUG \*\*\*\*\*  
PATCH: .BLKW 30.  
\*\*\*\*\*  
.END





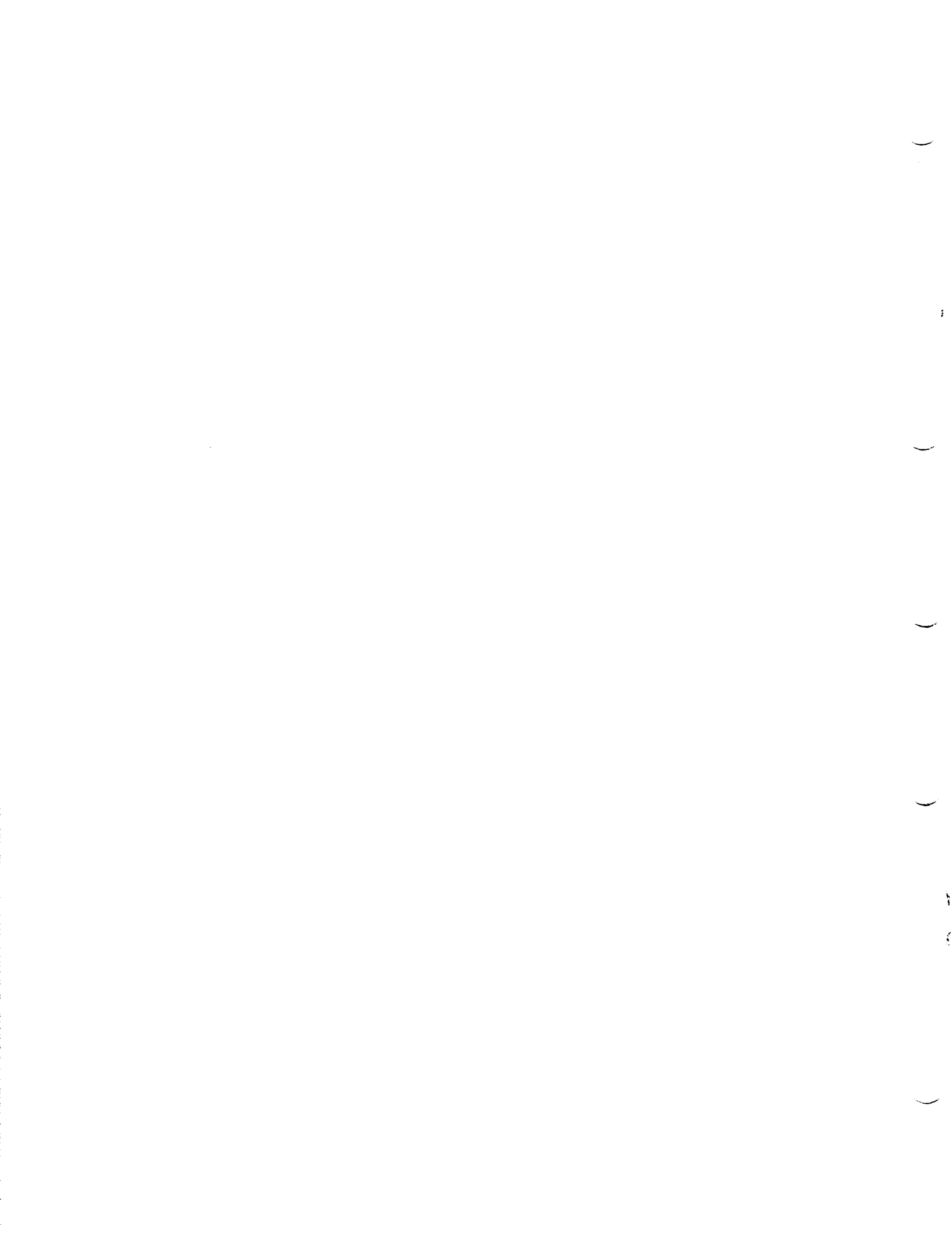
IISR1	006410H	1852#																
IISR10	006732R	1978#																
IISR11	006770R	1996#																
IISR12	007026R	2014#																
IISR13	007064H	2032#																
IISR14	007122R	2050#																
IISR15	007160R	2068#																
IISR16	007216R	2086#																
IISR17	007254R	2104#																
IISR2	006446H	1870#																
IISR3	006504R	1888#																
IISR4	006542R	1906#																
IISR5	006600R	1924#																
IISR6	006636R	1942#																
IISR7	006674H	1960#																
ILGINT=	000011	388#	1281	1411														
IMODX.=	000000	339#	1186															
INCMND	005710R	918	974	1022	1031	1038	1091	1708#										
INFOUT=	000002	435#	1108	1407														
INIDEV	005410R	892	1616#															
ININT =	000001	753#	1241	1710	1721													
INISR	003056H	1234#	1352	1839	1857	1875	1893	1911	1929	1947	1965	1983	2001	2019				
		2037	2055	2073	2091	2109												
INIT	000030R	294#																
INTABL	000350R	832#	862	864	1158	1168	1264	1266	1268	1270	1272	1274	1276	1278				
		1293*	1301*	1315*	1328*	1336*	1344*	1451*	1494*	1495	1497	1796*	1816*					
INTFDG=	000022	508#																
INTGRL=	000010	744#	958	969														
INTIMO=	000001	779#	922	975	1708	1730												
INTIMR	000324R	807#	1716*	1723*														
INTR	000120R	326#																
IRDY =	000020	685#																
ISTART=	000003	503#	1036	1300														
ISTCNT	000332R	812#	1034*	1079*														
ISTDON=	000004	765#	1169	1270	1301													
I422 =	000200	740#	958	963														
KILLTR=	000002	502#																
LATPS =	000040	520#																
LLKCNT	000330R	809#	870*	987*	989*	1000	1500	1800										
LNKDON	000342R	816#																
LULUOP=	000010	474#	890	914	921	982												
LULP =	000040	631#	684#															
LURG16	000272R	794#	928	932	937	941	1667*	1672*	1674*	1676*	1678*							
MAINT =	000004	504#																
MAINT1=	000010	640#																
MAINT2=	000004	641#																
MAP22S=	104416	341#																
MCLK =	000002	732#																
MCLR =	000100	472#	885	1618														
MDFDON=	000001	763#	1159	1169	1278													
MNTHLI=	000014	579#																
MNTRUN=	000012	578#																
MUDDDF=	000002	419#	913															
MODESW=	000361	448#																
MODE22=	000002	380#	1189	1200	1309	1322	1428	1440	1464	1476								
MODMSK=	000370	443#																

MODNAM	000000R	281#																	
MODR =	000010	698#																	
MUDSP	000252R	295	339#																
MODSW0=	000020	718#	932	941															
MODSW1=	000040	719#																	
MODSW2=	000100	720#																	
MROY =	000200	367#	1566	1585	1592														
MSGNS =	104403	341#																	
MSGSS =	104402	341#																	
MSG\$ =	104401	341#																	
NOINIT=	000034	390#	1637	1645															
NOINTK=	000023	389#	1048	1101	1726														
NUMEXM=	000302	586#																	
NUMLUP=	000001	374#																	
NUMQ1 =	000004	755#	1236	1709	1731														
NUMQ0 =	000010	756#	860	1208	1372														
NORET =	000000	597#																	
NUMDFN=	000000	385#	1057	1073	1082	1115	1513	1548											
NULL =	000000	341#	948																
N.DEVS	000256R	788#	858*	977*															
OC =	000200	620#																	
OCOR =	000020	729#																	
QERDUN=	000200	770#	1169	1276	1336														
QISR0	006370R	1841#																	
QISR1	006426R	1859#																	
QISR10	006750R	1985#																	
QISR11	007006R	2003#																	
QISR12	007044R	2021#																	
QISR13	007102R	2039#																	
QISR14	007140R	2057#																	
QISR15	007176R	2075#																	
QISR16	007234R	2093#																	
QISR17	007272R	2111#																	
QISR2	006464R	1877#																	
QISR3	006522R	1895#																	
QISR4	006560R	1913#																	
QISR5	006616R	1931#																	
QISR6	006654R	1949#																	
QISR7	006712R	1967#																	
OP =	000002	746#	958																
OPEN =	000000	282	288	289	290	291	308	309	310	311	312	313	314	315					
		317	319	321	322	324	325	326	329	330	332	333	335	336					
		337	338	341#															
OTOAS =	104420	341#																	
OUTDON	000344R	817#	1177*	1499*	1500														
OUTIMO=	000002	780#																	
OUTIMR	000326R	808#	1039*	1045*	1092*	1098*	1122*												
OUTINT=	000002	754#	1021	1030	1037	1043	1090	1096	1121	1381									
OUTISR	004046R	1355	1370#	1846	1864	1882	1900	1918	1936	1954	1972	1990	2008	2026					
		2044	2062	2080	2098	2116													
UVRK =	000010	686#																	
OWAIT =	100000	772#	1264	1796	1816														
PASCNT	000034R	296#																	
PATCH	011312R	2134#																	
PIRQ\$ =	000004	341#	1836	1843	1854	1861	1872	1879	1890	1897	1908	1915	1926	1933					
		1944	1951	1962	1969	1980	1987	1998	2005	2016	2023	2034	2041	2052					









.REM !

IDENTIFICATION

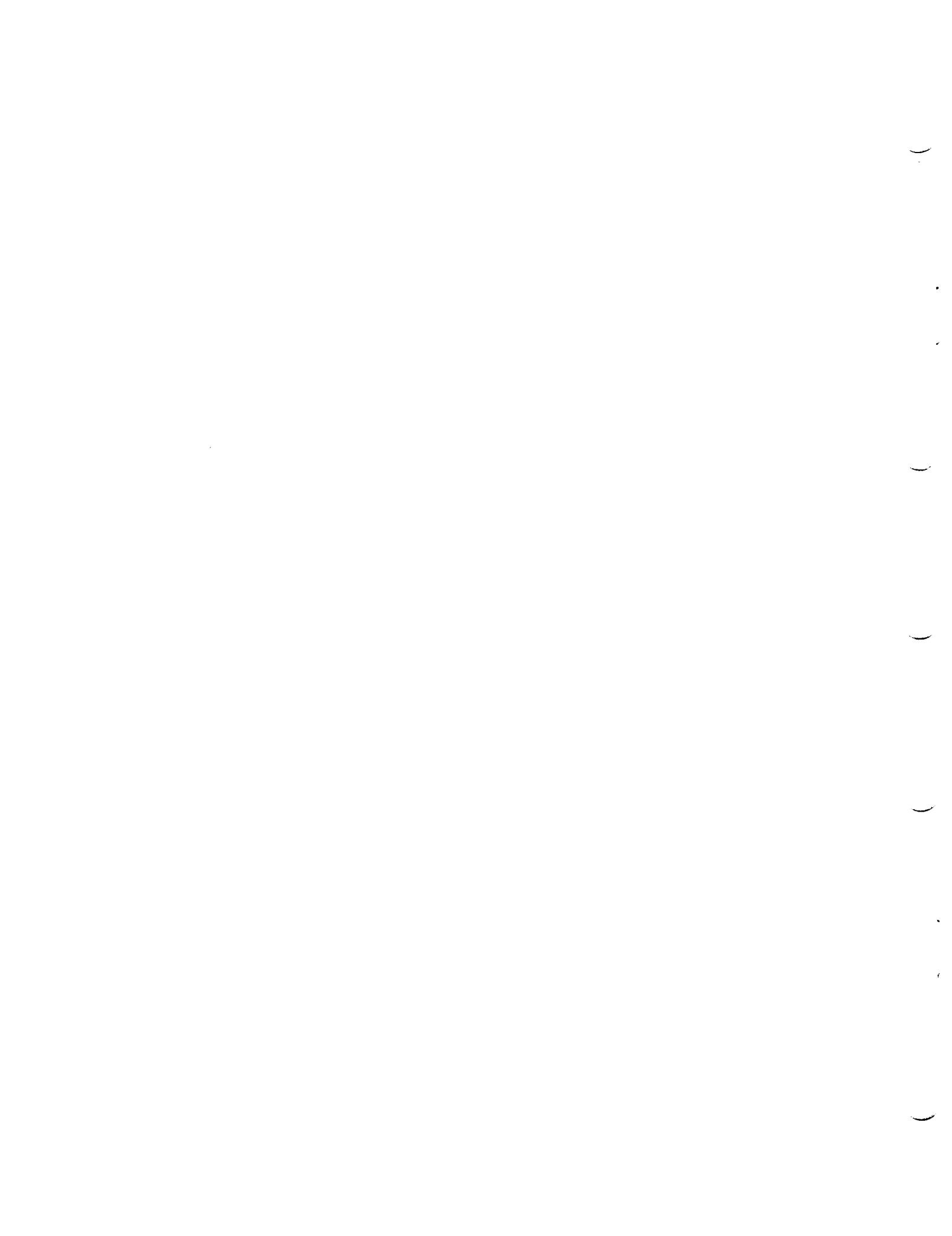
PRODUCT CODE: AC-F0729-MC  
PRODUCT NAME: CYBMHR0 M9312 MODULE  
PRODUCT DATE: SEPTEMBER 1978  
MAINTAINER: DEC/X11 SUPPORT GROUP

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## 1.0 ABSTRACT

THIS MODULE PERFORMS CHECKSUM VERIFICATION OF THE M9312 BOOTSTRAP TERMINATOR. IT COMPARES THE CHECKSUM FOUND IN THE LAST LOCATION OF THE ROMS TO ONE IT CALCULATES FROM ALL OTHER LOCATIONS OTHER THAN THE EXCEPTION LOCATION. LOCATION SRI IS USED TO SELECT WHICH ROMS TO TEST.

## 2.0 REQUIREMENTS

HARDWARE: ANY PDP-11 PROCESSOR WITH A M9312 BOOTSTRAP TERMINATOR AND AT LEAST ONE ROM PLUGGED INTO THE TERMINATOR.

STORAGE:: BMH REQUIRES:

1. DECIMAL WORDS: 237
2. OCTAL WORDS: 0355
3. OCTAL BYTES: 732

## 3.0 PASS DEFINITION

ONE PASS CONSISTS OF DOING A CHECKSUM ON EACH ROM 30 (8) TIMES.

## 4.0 EXECUTION TIME

BMH TAKES APPROXIMATELY 35 SECONDS TO COMPLETE A PASS WHEN RUNNING ALONE.

## 5.0 CONFIGURATION REQUIREMENTS

SET THE CORRESPONDING BITS IN SRI TO A"1" FOR THE DESIRED ROMS:

BIT 0 = 1	- DIAG. ROM
BIT 1 = 1	- BOOT ROM IN E-35 (173000-173177)
BIT 2 = 1	- BOOT ROM IN E-33 (173200-173377)
BIT 3 = 1	- BOOT ROM IN E-34 (173400-173577)
BIT 4 = 1	- BOOT ROM IN E-32 (173600-173777)

## 6.0 DEVICE/OPTION SETUP

BMH8 DEC/X11 SYSTEM EXERCISER MODULE  
XBMH80.P11 12-OCT-78 11:52

MACV11 30A(1052) 12-OCT-78 16:22 PAGE 4

NONE

SEQ 0003

( ( ( ( (

## 7.0 MODULE OPERATION

THIS MODULE FIRST CHECKS FOR SR1 RD BE NON-ZERO. IF IT IS ZERO THE MODULE TYPES THE MESSAGE:

NO ROMS SELECTED SR 1 = 0

AND THEN NOTIFIS THE MONITOR TO DROP IT. IF SR1 IS NON-ZERO THE CONTROL ROUTINE THEN CALLS THE "GETROM" SUBROUTINE TO LOCATE THE FIRST ROM. IF THE FIRST ROM IS NOT SELECTED THE "GETROM" SUBROUTINE WILL RETURN WITH LOCATION "FIRSTA"=0. IF THE FIRST ROM IS SELECTED "GETROM" WILL RETURN WITH "FIRSTA" CONTAINING THE FIRST ADDRESS TO BE SUMMED, LOCATION "EXCADR" CONTAINING THE EXCEPTION ADDRESS, LOCATION "LASTA" CONTAINING THE LAST ADDRESS TO BE SUMMED AND LOCATION "GOODA" CONTAINING THE ADDRESS OF THE ROMS CHECKSUM.

THE CONTROL ROUTINE THEN CALLS THE SUB-ROUTINE "CHECKR" TO CHECK THE ROM. IF LOCATION "FIRSTA" EQUALS ZERO "CHECKR" WILL JUST RETURN TO THE CONTROL ROUTINE. BUT IF "FIRSTA" IS NON-ZERO "CHECKR" WILL CALL THE ROUTINE "CALSUM". "CALSUM" CALCULATES THE CRC16 OF THE ROM FROM "FIRSTA" TO "LASTA" WITH THE EXCEPTION OF "EXCADR". "CALSUM" RETURNS WITH THE CALCULATED CHECKSUM IN LOCATION "BAD". THE ROUTINE "CHECKR" THEN COMPARES THE GOOD CHECKSUM FROM THE BOARD WITH THE "BAD" SUM AND IF THEY DON'T COMPARE REPORTS AN ERROR:

CHECKSUM ERROR ON M9312 BOOTSTRAP.

THE ROUTINE "CHECKR" THEN RETURNS TO THE CONTROL ROUTINE AND THE PROCESS IS REPEATED FOR EACH ROM.

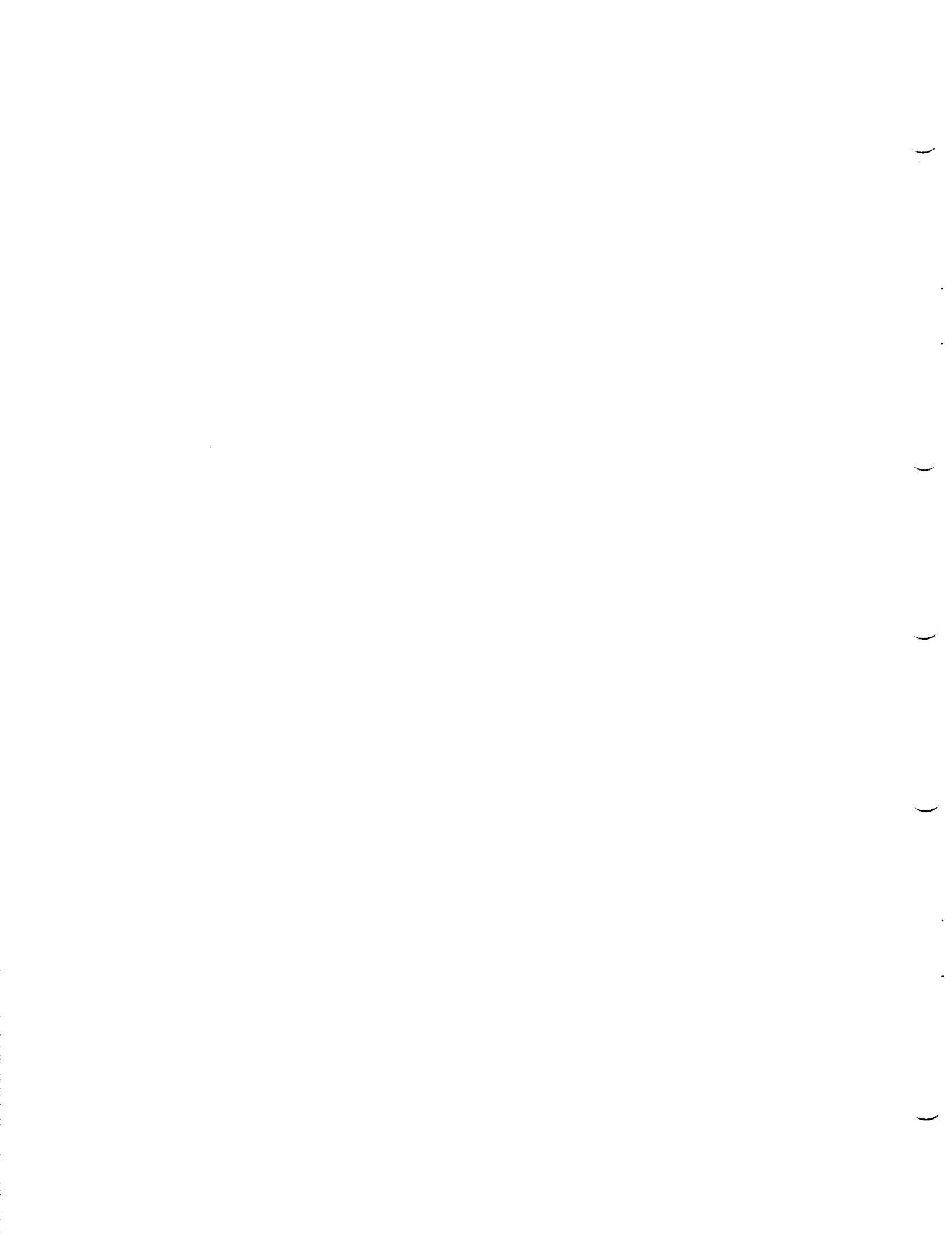
## 8.0 OPERATING OPTIONS

NONE

## 9.0 NON-STANDARD PRINTOUTS

NONE

!





```

000000* BKMOD <BMH8 > 0,0,0,0,0,30,164
000000* MODDLE 40020, BMH8 0,0,0,0,0,30,164
; TITLE BMH8 DEC/111 SYSTEM EXERCISER MODULE
; DDRCOM VERSION 6 23-NOV-78
*****LIST*****
000000* 041110 040 BEGIN:
000000* 046502 MODNAM: .ASCII /BMH8 / ;MODULE NAME
000005* 000000 XFLAG: .BYTE OPEN ;USED TO KEEP TRACK OF WBOFF USAGE
000006* 000000 ADDR: 0+0 ;1ST DEVICE ADDR
000010* 000000 VECTOR: 0+0 ;1ST DEVICE VECTOR.
000012* 000 BR1: .BYTE PRTY0+0 ;1ST RR LEVEL.
000013* 000 BR2: .BYTE PRTY0+0 ;2ND RR LEVEL.
000014* 000001 DVID1: .BYTE ;DEVICE INDICATOR 1.
000016* 000000 SR1: OPEN ;SWITCH REGISTER 1.
000020* 000000 SR2: OPEN ;SWITCH REGISTER 2.
000022* 000000 SR3: OPEN ;SWITCH REGISTER 3.
000024* 000000 SR4: OPEN ;SWITCH REGISTER 4.
*****LIST*****
000026* 040020 STAT: 40020 ;STATUS WORD.
000030* 000752 INT: START ;MODULE START ADDR.
000032* 000234 SPPOINT: MODSP ;MODULE STACK POINTER.
000034* 000000 PASCNT: 0 ;PASS COUNTER.
000036* 000030 ICONT: 30 ;# OF ITERATIONS PER PASS=30
000040* 000000 SOFCNT: 0 ;LOC TO COUNT ITERATIONS
000042* 000000 HRDCNT: 0 ;LOC TO SAVE TOTAL HARD ERRORS
000044* 000000 SOFPAS: 0 ;LOC TO SAVE TOTAL SOFT ERRORS
000046* 000000 HRDPAS: 0 ;LOC TO SAVE TOTAL HARD ERRORS PER PASS
000050* 000000 SYSCNT: 0 ;# OF SYS ERRORS ACCUMULATED
000052* 000000 RAMNUM: 0 ;HOLDS RANDOM # WHEN RAND MACRO IS CALLED
000056* 000000 CONFIG: ;RESERVED FOR MONITOR USE
000058* 000000 RES1: 0 ;RESERVED FOR MONITOR USE
000060* 000000 RES2: 0 ;RESERVED FOR MONITOR USE
000062* 000000 SVR0: OPEN ;LOC TO SAVE R0.
000064* 000000 SVR1: OPEN ;LOC TO SAVE R1.
000066* 000000 SVR2: OPEN ;LOC TO SAVE R2.
000070* 000000 SVR3: OPEN ;LOC TO SAVE R3.
000072* 000000 SVR4: OPEN ;LOC TO SAVE R4.
000074* 000000 SVR5: OPEN ;LOC TO SAVE R5.
000076* 000000 SVR6: OPEN ;LOC TO SAVE R6.
00100* 000000 CSRA: OPEN ;ADDR OF CURRENT CSR.
00102* 000000 CSRB: OPEN ;ADDR OF GOOD DATA, OR
00104* 000000 ACSRA: OPEN ;CONTENTS OF CSR.
00106* 000000 WASADR: OPEN ;ADDR OF BAD DATA, OR
00108* 000000 ASADR: OPEN ;STATUS REG CONTENTS.
00110* 000000 ERATYP: ;TYPE OF ERROR
00112* 000252 ASB: OPEN ;EXPECTED DATA.
00114* 000000 AWAS: OPEN ;ACTUAL DATA.
00116* 000000 RSTRT: RSTRT ;RESTART ADDRFS AFTER END OF PASS
00118* 000000 WDRS: OPEN ;WORDS TO MEMORY PER ITERATION
00120* 000000 WDRP: OPEN ;WORDS FROM MEMORY PER ITERATION
00122* 000164 INTR: OPEN ;# OF INTERRUPTS PER ITERATION
;MODULE IDENTIFICATION NUMBER=164
;MODULE STACK STARTS HERE.
IDNUM: 164
-RPT SPSTZ
-WLIST

```

```

;WORD 0
;LIST
;ENDR
000224* MODSP: *****
200 FIRSTA: 0
201 000224* 000000 EXCADR: 0
202 000226* 000000 LASTA: 0
203 000230* 000000 CRMP: 0
204 000232* 000000 GOODA: 0
205 000234* 000000 BAD: 0
206 000236* 000000 COUNT: 0
207 000240* 000000 #BAD ;+ADDRESS POINTER FOR ERROR CALL
208 000242* 000236* #GOODA ;+ADDRESS OF EXPECTED CHECKSUM
209 000244* 000234* #BADADR ;+ADDRESS OF ACTUAL CHECKSUM
210 000246* 000242* -1 ;+MESSAGE TERMINATOR
211 000250* 177777

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212 000252* 005767 177540 START: TST SRI
213 000252* 005767 177540 RESTR: BNE
214 000260* 104403 000000* 000622* MSGNS,BEGIN,MSG1
215 000260* 104403 000000* 000622* EWD5,BEGIN
216 000260* 104403 000000* 000622*
217 000272* 012767 000001 177732 2S: MOV #1, CROMP
218 000272* 012767 000001 177732 3S: CMP #40, CROMP
219 000306* 001402 000040 177724 BRD
220 000310* 004767 000014 JSR PC, GETROM
221 000314* 004767 000146 JSR PC, CHECKR
222 000322* 104413 000000* 4S: ENDITS,BEGIN
223 000326* 000761 BR 2S
224
225
226
227
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229
230 000330* 005067 177670 GETROM: CLR FIRSTA
231 000334* 005767 177672 177454 BIT CROMP, SRI
232 000342* 001446 MOV #45, CROMP
233 000344* 016700 177662 MOV CROMP, R0
234 000350* 000241 CLC
235 000352* 006000 ROR
236 000354* 103184 BVC
237 000356* 012767 165000 177640 BVC #165000, FIRSTA
238 000364* 005067 177636 CLR EXCADR
239 000370* 012767 165775 177632 MOV #165775, LASTA
240 000400* 000425 177630 MOV #165776, GOODA
241 000406* 012701 173000 1S: MOV #173000, R1
242 000410* 006000 ROR
243 000412* 006701 000200 2S: BRD
244 000416* 006701 000200 ADD #200, R1
245 000422* 006773 177574 3S: MOV R1, FIRSTA
246 000424* 010167 177524 ADD #24, R1
247 000430* 010167 177524 MOV R1, EXCADR
248 000434* 010167 177524 ADD #51, R1
249 000440* 010167 177560 MOV R1, LASTA
250 000444* 010167 177560 ADD #1, R1
251 000446* 010167 177544 MOV R1, GOODA
252 000450* 006167 177546 4S: ROL CROMP
253 000464* 000207 RTS
254
255
256
257
258 000468* 005767 177532 CHECKR: TST FIRSTA
259 000472* 001419 JSR #5, CALSUM
260 000500* 027767 177530 177530 CMP #GOODA, BAD
261 000506* 001410 BNE
262 000510* 104403 000000* 000622* MSGNS,BEGIN,MSG2
263 000516* 005067 177364 CLR CROMP
264
265
266 000522* 104405 000000* 000244*
267

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268 000530* 000207 1S: RTS PC
269
270
271 000532* 016700 177466 CALSUM: MOV FIRSTA, R0
272 000536* 005005 CLR R5
273 000540* 012003 LOOP: MOV (R0)+, R3
274 000542* 012702 000020 CRCLOP: MOV #16, R2
275 000546* 002481 CLC
276 000550* 006005 ROR
277 000552* 006003 ROR
278 000554* 102006 BVC
279 000556* 010201 120001 MOV #120001, R1
280 000562* 010201 120001 BIC #120001, R5
281 000564* 042705 120001 BIC #120001, R5
282 000570* 050105 1S: BIC R1, R5
283 000574* 003367 DEC R2
284 000576* 020067 177424 CRCLOP
285 000602* 001001 ROR EXCADR
286 000602* 001001 BNE
287 000604* 007720 TST (R0)+
288 000606* 020067 177416 2S: CMP R0, LASTA
289 000612* 101752 LOOP
290 000614* 010567 177416 MOV R5, BAD
291 000620* 000207 PC
292
293
294 000622* 000632* MSG1: NOROMS
295 000624* 177777* MSG2:
296 000626* 000654* CLR CROMP
297 000630* 177777
298
299 000632* 020045 047516 051040 NOROMS: .ASCIZ "% NO ROMS SELECTED SRI=0%"
300 000640* 045217 020143 042523
301 000646* 045214 020143 042105
302 000654* 051440 030522 030075
303 000662* 000045
304 000672* 051513 044103 041505 CRCERR: .ASCIZ "% CHECKSUM ERROR ON M9312 BOOTSTRAP%"
305 000672* 051513 046525 042440
306 000700* 051172 051117 047440
307 000706* 020116 034815 030463
308 000712* 020067 047502 052117
309 000722* 052123 040522 022520
310 000730* 000
311 000732* .EVEN
312 000001 .END

```



SVR2	000066R	175#
SVR3	000070R	176#
SVR4	000072R	177#
SVR5	000074R	178#
SVR6	000076R	179#
STSCNT	000052R	188#
TRPDFD=	000022	200#
VECTDR	000010R	149#
WISADR	000104R	183#
WDFR	000116R	190#
WOTO	000114R	189#
XPLAG	000005R	147#
.	= 000732R	311#

. A9S. 000000 000  
000732 001

ERRORS DETECTED: 0  
DEFAULT GLOBALS GENERATED: 0

XBH80, XBH80/SOL/CRF:SYN=DDXCOM, XBH80  
RUN-TIME: 11.2 SECONDS  
RUN-TIME RATIO: 24/2=8.6  
CORE USED: 7K (13 PAGES)

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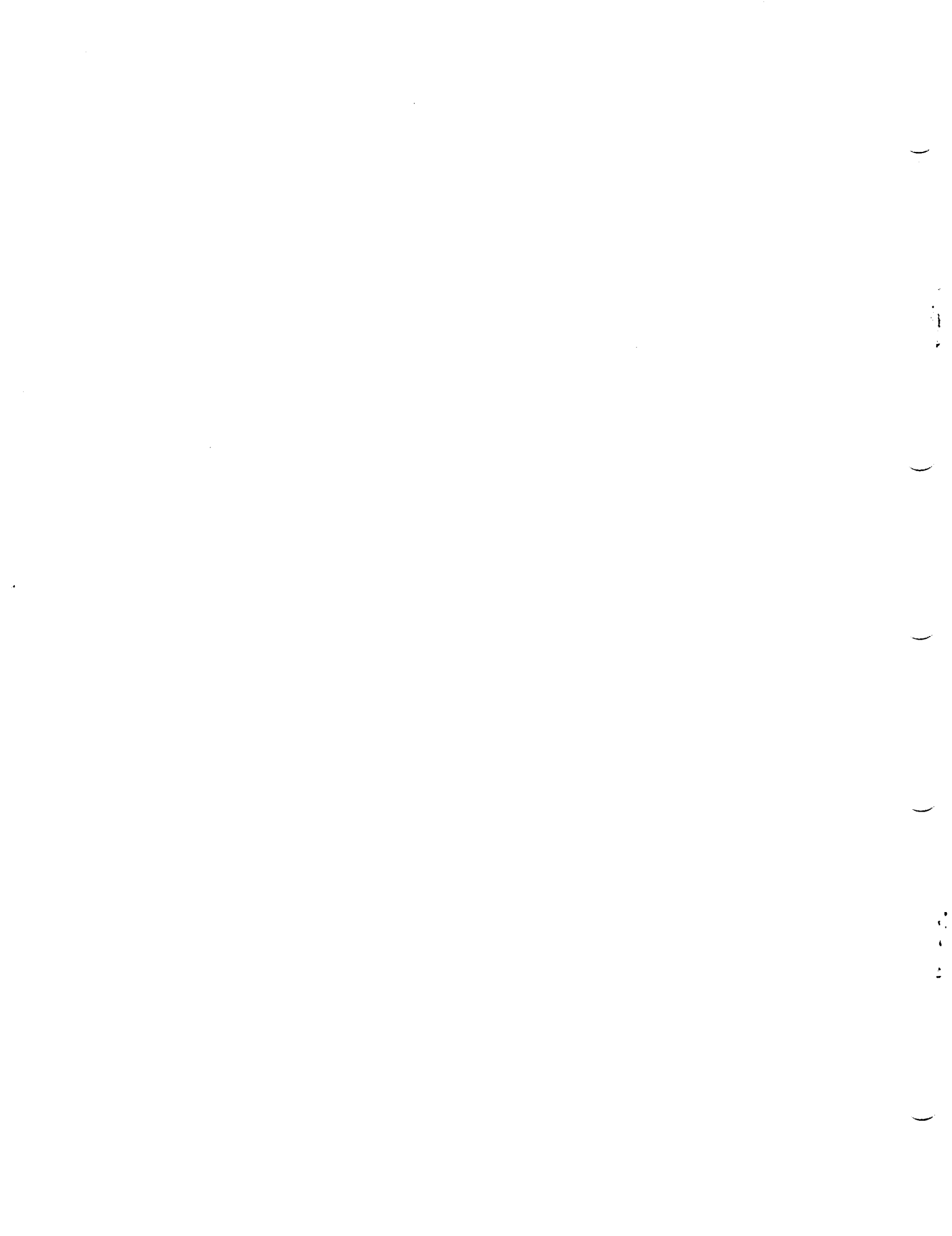
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1 000000 .REP1 0

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IDENTIFICATION  
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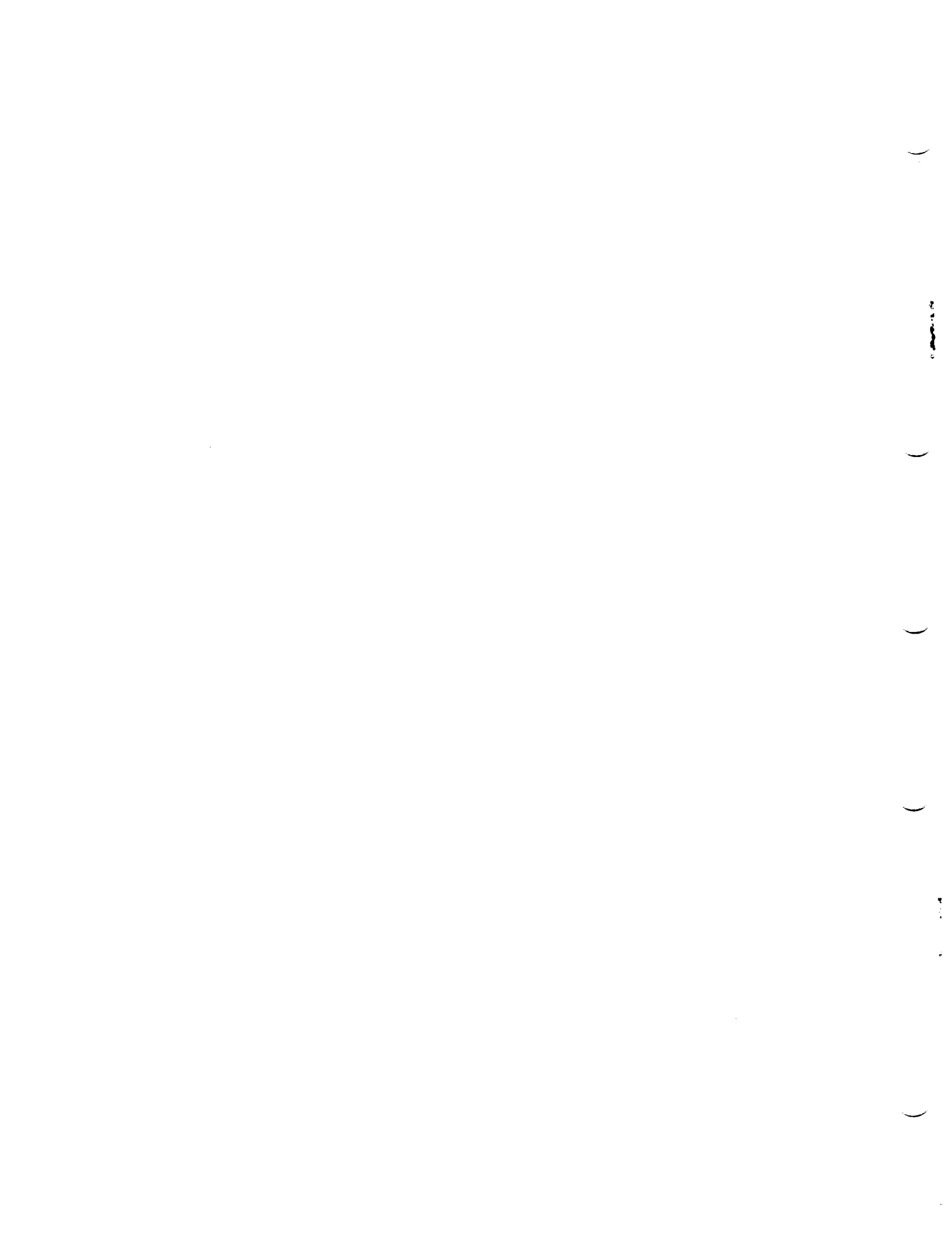
PRODUCT CODE: AC-F084B-MC  
PRODUCT NAME: CXPLAB0 PCL11 MODULE  
PRODUCT DATE: FEB 1979  
MAINTAINER: DAVE WIENS, CSS KANATA

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1. ABSTRACT

-----  
PLA IS AN IUMODX ROUTINE THAT EXERCISES 1 (ONE) PCL11 ATTACHED TO THE PDP-11 SYSTEM UNIBUS. IT EXERCISES PCL11 BY CAUSING THE TRANSMITTER IN THE UNIT TO TRANSMIT A FULL SILO (64. WORDS) TO THE RECEIVER IN THE SAME UNIT. DATA IS OBTAINED FOR THE TRANSMISSION VIA NPR AND READ BACK VIA NPR. DATA IS CHECKED IN CORE BY THE DEC/X11 MONITOR. ALL ERRORS ARE REPORTED ON THE CONSOLE PRINT DEVICE.

2. REQUIREMENTS

-----  
HARDWARE: PDP-11 WITH PCL11 ON THE UNIBUS  
TDM BUS CABLE DETACHED FROM UNIT  
UNDER TEST.

STORAGE:: PLA REQUIRES:

1. DECIMAL WORDS: 810
2. OCTAL WORDS: 1452
3. OCTAL BYTES: 3124

OTHER: THIS MODULE IS MEANT TO BE CONFIGURED  
AND RUN WITH DEC/X11 ONLY.

3. PASS DEFINITION

-----  
ONE PASS OF THE PLA MODULE CONSISTS OF 3072 CYCLES OF THE BASIC TEST SEQUENCE: TRANSMIT 64 WORDS TO THE RECEIVER, CHECK RCV'D WORDS FOR DATA ERRORS, TRAP ALL HARDWARE ERRORS. ONE PASS TAKES APPROXIMATELY 30 SECONDS.

4. CONFIGURATION REQUIREMENTS

-----  
DEFAULT PARAMETERS:  
DEVICE ADDRESS: 164200  
VECTOR: 170  
PRIORITY (BR1) 5

REQUIRED PARAMETERS:  
SR1: MUST CONTAIN PCL11 RCVR  
TDM-BUS ADDRESS.

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5. DEVICE / OPTION SETUP  
-----

SINCE PCL11 IS AN INTER-PROCESSOR COMMUNICATIONS DEVICE,  
MAKE CERTAIN THAT THE UNIT IS DISCONNECTED FROM THE TDM BUS  
TO OTHER PROCESSORS.

ALSO, IT IS VITAL THAT THE RECEIVERS TDM-BUS ADDRESS BE  
KNOWN SO THAT IT MAY BE ENTERED INTO "SR1" AT CONFIGURE TIME.

7. MODULE OPERATION  
-----

TEST SEQUENCE:

- A. SET UP DEVICE REGISTER ADDRESSES AND MODULE VARIABLES
- B. PERFORM VALIDITY CHECK ON HARDWARE TO INSURE THAT THE  
MODULE WILL NOT GET "HUNG UP"
- C. GET WRITE AND READ INFORMATION FOR NPR
- D. ENABLE XMTR AND RCVR NPR - START TRANSMISSION.
- E. CHECK FOR XMTR & RCVR ERRORS - REPORT ANY AND RETRY  
UP TO RETRY LIMIT. MODULE GETS DROPPED IF 10 (OCTAL)  
ERRORS OCCUR IN ONE CYCLE AND SR1 IS POSITIVE (BIT 15 CLEAR).
- F. IF END OF PASS, REPORT AND GO TO C

7. OPERATION OPTIONS  
-----

- SR1 BIT15 SET (1)  
IF ERROR RETRY LIMIT IS EXCEEDED, RESET RETRY  
LIMIT AND CONTINUE.
- SR1 BIT15 CLEAR (0)  
IF ERROR RETRY LIMIT IS EXCEEDED, A HARD ERROR  
IS ASSUMED AND THE MODULE IS DROPPED.

NOTE  
----

THE RETRY COUNT IS CLEARED EVERY CYCLE UNLESS  
ERRORS OCCUR.

- SR1 BITS<7:0>  
MUST CONTAIN THE CORRECT RECEIVER TDM BUS ADDRESS.  
(COMMONLY CALLED RECEIVER NUMBER)  
THIS IS AN OCTAL NUMBER BETWEEN 1 AND 37.

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8. ERROR MESSAGES

-----

A. THE STANDARD DEC/X11 ERROR ROUTINES ARE USED IN THIS MODULE.

B. FURTHER PRINTOUTS ARE SELF- EXPLANATORY.

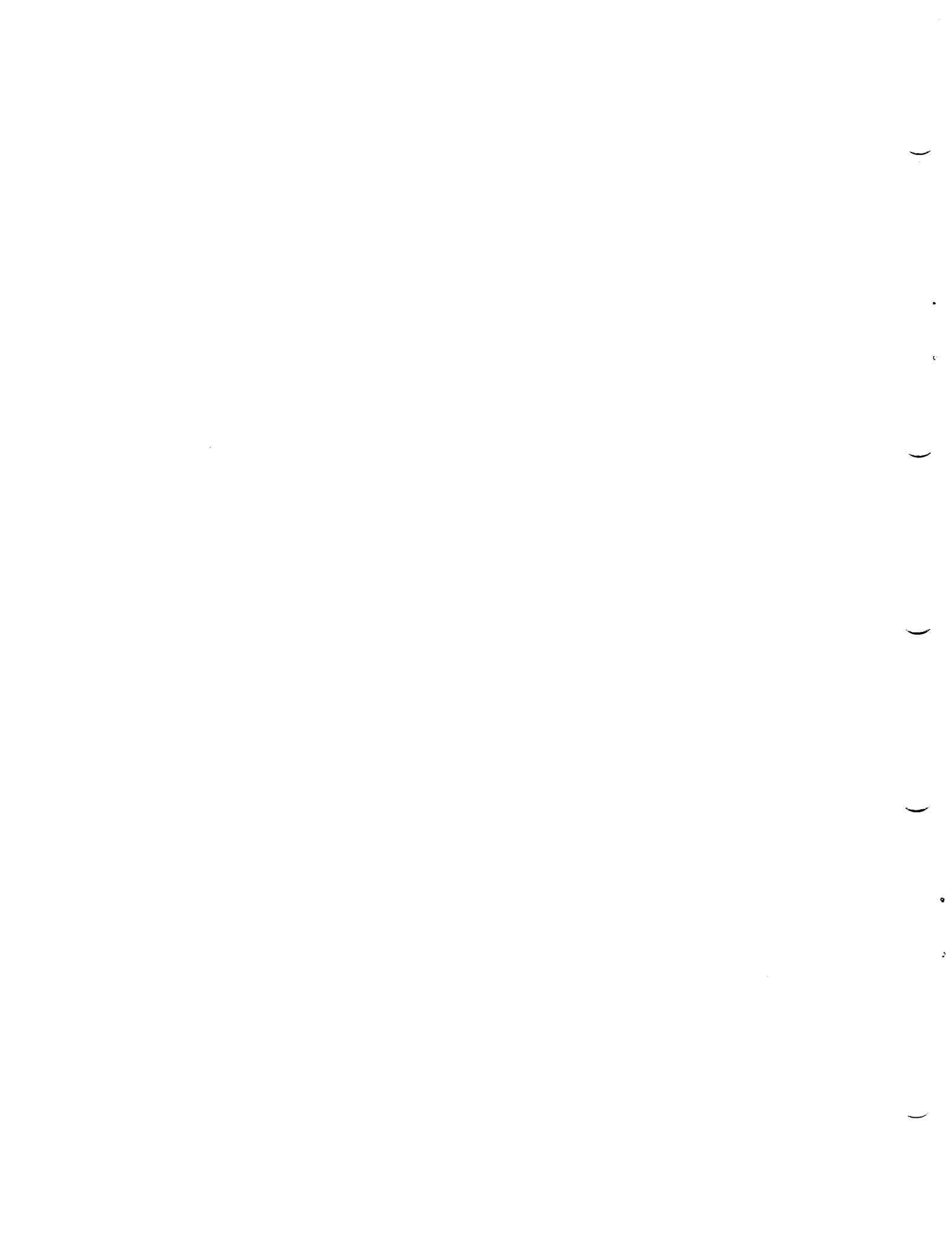
C. ERROR MESSAGES DUMP THE CONTENTS OF THE DEVICE REGISTERS IN THE FOLLOWING ORDER:

RECEIVER:

RCR RSR RDDB RDBC RDBA RDCRC

TRANSMITTER:

TCR TSR TSDB TSBC TSBA TMMR TSCRC



```

150          .ENDR
151          ;PCL11 - DEC/X11 SYSTEM EXERCISER MODULE
152
153          IUMODX <PLAB >,164200,170,5,0,0,3072,,0,BUFIN,64,,64.
154          MODULE 1:0000,PLAB ,164200,170,5,0,0,3072,,0,BUFIN,64,,64.
155          .TITLE PLAB DEC/X11 SYSTEM EXERCISER MODULE
156          ; DDXCOM VERSION 6 23-MAY-78
157          .LIST BIN
158          ;*****
159          BEGIN:
160          MODNAM: .ASCII /PLAB / ;MODULE NAME.
161          XFLAG: .BYTE OPEN ;USED TO KEEP TRACK OF WBUFF USAGE
162          ADDR: 164200+0 ;1ST DEVICE ADDR.
163          VECTOR: 170+0 ;1ST DEVICE VECTOR.
164          BR1: .BYTE PRTY5+0 ;1ST BR LEVEL.
165          BR2: .BYTE PRTY0+0 ;2ND BR LEVEL.
166          DVID1: 0+1 ;DEVICE INDICATOR 1.
167          SR1: OPEN ;SWITCH REGISTER 1
168          SR2: OPEN ;SWITCH REGISTER 2
169          SR3: OPEN ;SWITCH REGISTER 3
170          SR4: OPEN ;SWITCH REGISTER 4
171          ;*****
172          STAT: 150000 ;STATUS WORD.
173          INIT: STAKT ;MODULE START ADDR.
174          SPUINT: MUDSP ;MODULE STACK POINTER.
175          PASCNT: 0 ;PASS COUNTER.
176          ICUNT: 3072. ;# OF ITERATIONS PER PASS=3072.
177          ICOUNT: 0 ;LOC TO COUNT ITERATIONS
178          SOFCNT: 0 ;LOC TO SAVE TOTAL SOFT ERRORS
179          HRDCNT: 0 ;LOC TO SAVE TOTAL HARD ERRORS
180          SOFPAS: 0 ;LOC TO SAVE SOFT ERRORS PER PASS
181          HRDPAS: 0 ;LOC TO SAVE HARD ERRORS PER PASS
182          SYSCNT: 0 ;# OF SYS ERRORS ACCUMULATED
183          RANNUM: 0 ;HOLDS RANDOM # WHEN RAND MACRO IS CALLED
184          CONFIG: ;RESERVED FOR MONITOR USE
185          RES1: 0 ;RESERVED FOR MONITOR USE
186          RES2: 0 ;RESERVED FOR MONITOR USE
187          SVR0: OPEN ;LOC TO SAVE R0.
188          SVR1: OPEN ;LOC TO SAVE R1.
189          SVR2: OPEN ;LOC TO SAVE R2.
190          SVR3: OPEN ;LOC TO SAVE R3.
191          SVR4: OPEN ;LOC TO SAVE R4.
192          SVR5: OPEN ;LOC TO SAVE R5.
193          SVR6: OPEN ;LOC TO SAVE R6.
194          CSRA: OPEN ;ADDR OF CURRENT CSR.
195          SBADR: ;ADDR OF GOOD DATA, OR
196          ACSR: OPEN ;CONTENTS OF CSR.
197          WASADR: ;ADDR OF BAD DATA, OR
198          ASTAT: OPEN ;STATUS REG CONTENTS.
199          ERRTYP: ;TYPE OF ERROR
200          ASB: OPEN ;EXPECTED DATA.
201          AWAS: OPEN ;ACTUAL DATA.
202          RSTRT: RSTRT ;RESTART ADDRESS AFTER END OF PASS
203          WDTU: OPEN ;WORDS TO MEMORY PER ITERATION
204          WDFR: OPEN ;WORDS FROM MEMORY PER ITERATION
205          INTR: OPEN ;# OF INTERRUPTS PER ITERATION

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206          IDNUM: 0 ;MODULE IDENTIFICATION NUMBER=0
207          RBUFVA: BUFIN ;READ BUFFER VIRTUAL ADDRESS
208          RBUFPA: OPEN ;READ BUFFER PHYSICAL ADDRESS
209          RBUFEA: OPEN ;READ BUFFER EA BITS
210          RBUFSZ: 64. ;SIZE OF THE READ BUFFER
211          WBUFPA: OPEN ;WRITE BUFFER PHYSICAL ADDRESS
212          WBUFEA: OPEN ;WRITE BUFFER EA BITS
213          WBUFSQ: 64. ;WRITE BUFFER SIZE REQUESTED
214          WBUFSZ: OPEN ;WRITE BUFFER SIZE AVAILABLE
215          CDRECT: OPEN ;CDATA/DATCK ERROR COUNT
216          CDWDCT: OPEN ;CDATA/DATCK WORD COUNT
217          FREE: OPEN ;RESERVED FOR FUTURE USE
218          .REPT SPSIZ ;MODULE STACK STARTS HERE.
219          .NLIST
220          .WORD 0
221          .LIST
222          .ENDR
223          MODSP:
224          ;*****
225          ;SOME EXTRA DEFINITIONS:
226
227          ;TRANSMIT START FUNCTION:
228
229          TXMSTR = 60101
230
231          ;RECEIVE START FUNCTION:
232
233          RCVSTR = 60001

```

```

234 000252' 004767 001126 START: JSR PC, SETUP ;GENERATE DEVICE ADDRESSES
235 000256' 004767 001322 JSR PC, TSTDRP ;SET MASTER, DROP MODULE?
236 000262' 105767 001734 TSTB DEVICE ;IF SET, DROP MODULE
237 000266' 001045 BNE FINI
238
239 000270'
240 000270' 104415 000000' 000124' RESTRT: GETPAS, BEGIN, RBUFVA ;GET PHYSICAL ADDRESS FROM 16-BIT RBUFVA
241 000276' 016767 177630 001702 MOV #RBUFSZ, RCBC ;GET RCVR BUFF SIZE
242 000304' 006367 001676 ASL RCBC ;DOUBLE IT FOR BYTE COUNT
243 000310' 005467 001672 NEG RCBC ;NEGATE BYTE COUNT
244
245 000314'
246 000314' 104414 000000' BCGW: GWBUFS, BEGIN ;GET WRITE BUFFER INFORMATION
247 000320' 016767 177616 001662 MOV #RBUFSZ, TXBC ;GET ALLOCATED BUFFER SIZE
248 000326' 006367 001656 ASL TXBC ;DOUBLE IT FOR BYTE COUNT
249 000332' 005467 001652 NEG TXBC ;NEGATE BYTE COUNT
250
251 000336' 105067 001662 HCHK: CLR B ERFLG ;CLEAR ERRORS FLAG
252 000342' 004767 000122 JSR PC, TRNSFR ;SEND SOME DATA & RCV IT
253 000346' 105767 001652 TSTB ERFLG ;IF ERRORS, RETRY UP TO 10 TIMES
254 000352' 001023 BNE RETRY
255 000354' 004767 001006 JSR PC, ERSUB2 ;LOAD ERROR INFORMATION
256
257 000360' 104412 000000' 000126' CDATAS, BEGIN, RBUFPA ; REQUEST FOR MONITOR TO CHECK DATA
258 000366' 000370' .+2 ; IF ERROR, CONTINUE
259
260 000370' 105067 001627 CLR B TRY ;REFRESH RETRY COUNT
261 000374'
262 000374' 104413 000000' PASS: ENDITS, BEGIN ;SIGNAL END OF ITERATION.
263 ;MONITOR SHALL TEST END OF PASS
264 000400' 000745 BK BCGW
265
266 000402' 052777 000002 002042 FINI: BIS #BIT1, @RCR ;CLEAR RCVR HDWARE
267 000410' 052777 000002 002012 BIS #BIT1, @TCR ;CLEAR XMTR HDWARE
268 000416' 104410 000000' ENDS, BEGIN ;
  
```

```

269 000422' RETRY: ;ERROR RETRY RIN ENTRY POINT
270 000422' 105267 001575 INCB TRY ;COUNT THE RETRYS
271 000426' 122767 000010 001567 CMPB #10, TRY ;LIMIT EXCEEDED?
272 000434' 001340 BNE BCHK ;NO, GO TRY AGAIN
273 000436' 104403 000000' 002474' MSGNS, BEGIN, EXCED ;ASCII MESSAGE CALL WITH COMMON HEADER
274 000444' 012777 000002 002000 MOV #BIT1, @RCR ;CLEAR RCVR HDWARE
275 000452' 012777 000002 001750 MOV #BIT1, @TCR ;CLEAR XMTR HDWARE
276 000460' 005767 177332 TST SRI ;DROP THE MODULE?
277 000464' 100724 BMI BCHK ;NOT IF SRI BIT15=1
278 000466' 000745 BK FINI ;YES, IF SRI BIT15=0
279
280 ;PCL DRIVE SUBROUTINE
281 ;CALLED BY: JSR PC, TRNSFR
282
283 ;TRANSFER 64 WORDS TO XMTR VIA NPR
284 ;TRANSMIT IT TO THE RECEIVER VIA TDM BUS
285 ;TRANSFER RCVR DATA TO MODULE (BUFIN) VIA NPR
286 ;WAIT FOR COMPLETION FROM XMTR & RCVR
287
288 000470' TRNSFR: ;DRIVER SUBROUTINE ENTRY
289 000470' 012777 000002 001754 MOV #BIT1, @RCR ;CLR RCVR HDWARE
290 000476' 012777 000002 001724 MOV #BIT1, @TCR ;CLR XMTR HDWARE
291 000504' 016777 001476 001746 MOV RCBC, @RDBC ;LOAD RCVR BYTE COUNT
292 000512' 016777 001472 001716 MOV TXBC, @TSBC ;LOAD XMTR BYTE COUNT
293 000520' 016777 177402 001734 MOV #RBUFPA, @RDBA ;LOAD RCVR NPR DEST ADDRESS
294 000526' 016767 177376 001462 MOV #RBUFEA, RFUNCT ;AND GET EXT ADDR BITS
295 000534' 016777 177374 001676 MOV #RBUFLA, @TSBA ;LOAD XMTR NPR SOURCE ADDRESS
296 000542' 016767 177370 001450 MOV #RBUFLA, XFUNCT ;AND GET EXT ADDR BITS
297 000550' 116767 177242 001443 MOV B SRI, XFUNCT+1 ;GET RCVR ADDRESS FOR XMTR
298 000556' 052767 060001 001432 BIS #RCVSTR, RFUNCT ;LOAD RCVR FUNCTION AND GO
299 000564' 052767 060101 001426 BIS #TXMSTR, XFUNCT ;LOAD XMTR FUNCTION AND GO
300 000572' 012777 000620' 001672 MOV #TXINT, @TXVECT ;SET XMTR INTR ENTRY POINT
301 000600' 016777 001412 001644 MOV RFUNCT, @RCH ;CONNECT RCVR FIRST
302 000606' 016777 001406 001614 MOV XFUNCT, @TCR ;NOW CONNECT XMTR
303 000614' 104400 000000' EXIT$, BEGIN ;EXIT TO MONITOR, MODULE WAIT FOR INTERRUPT.
  
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304 ;TRANSMITTER INTERRUPT ENTRY POINT
305
306 000620' TXINT: ;XMTR INTR ENTRY POINT
307 000620' 042777 000100 001602 BIC #BIT6,@TCR ;CLEAR XMTR INTR ENABLE
308 ;-----
309 000626' 000004 000000' 000634' PIRQS,BEGIN,11$ ; QUEUE UP TO CONTINUE AT 11$ AND RTI
310 ;-----
311
312 000634' 004767 000056 11$: JSR PC,TXEH$ ;GO CHECK FOR XMTR ERRORS
313 000640' 105767 001362 TSTB TXMEH$ ;IF ANY ERRORS, SET ERFLG
314 000644' 001015 BNE 24$
315 000646' 004767 000314 12$: JSR PC,RCERS ;GO CHECK FOR RCVR ERRORS
316 000652' 105767 001347 TSTB RCVERS ;IF ERRORS, SET ERFLG
317 000656' 001001 BNE 23$
318 000660' 000207 13$: RTS PC ;RETURN
319
320 000662' 105067 001337 23$: CLRB RCVERS ;CLR RCVR ERR FLG
321 000666' 112767 177777 001330 MOVB #-1,ERFLG ;SET ERROR FLAG
322 000674' 000167 177760 JMP 13$
323 000700' 105067 001322 24$: CLRB TXMERS ;CLR XMTR ERR FLG
324 000704' 112767 177777 001312 MOVB #-1,ERFLG ;SET ERROR FLAG
325 000712' 000167 177730 JMP 12$
  
```

```

326 ;CHECK FOR TRANSMITTER ERRORS AND REPORT, IF ANY
327
328 000716' TXERS: ;ENTRY POINT FOR XMTR ERR CHK
329 000716' 112767 177777 001302 MOVB #-1,TXERS ;SET XMTR EROR FLAG
330 000724' 032777 100000 001500 BIT #BIT15,@TSR ;HARDWARE ERROR?
331 000732' 001430 BEQ 10$ ;NO, CHECK FURTHER
332 000734' 032777 004000 001470 BIT #BIT11,@TSR ;WAS IT MASTER DOWN?
333 000742' 001412 BEQ 2$ ;NO, REPORT WHAT IT WAS
334 000744' 104403 000000' 002500' MSGNS,BEGIN,MSTDWN ;ASCII MESSAGE CALL WITH COMMON HEADER
335 000752' 004767 000626 JSR PC,TSTDRP ;GO SEE IF WE CAN KEEP MODULE
336 000756' 105767 001240 TSTB DEVICE ;OK?
337 000762' 001474 BEQ TXRRTN ;YES, TRY AGAIN
338 000764' 000167 177412 JMP FINI ;NO, DROP THE MODULE BECAUSE
339 ;THE HARDWARE IS NON-RUNABLE
340 000770' 004767 000354 2$: JSR PC,ERSUB1 ;LOAD ERROR INFORMATION
341 000774' 012767 000020 177104 MOV #20,ERRTYP ;UNKNOWN XMTR ERROR
342 ;*****
343 001002' 104405 000000' 002430' HDRERS,BEGIN,TABLE1 ;XMTR HARDWARE ERROR
344 ;*****
345 001010' 000167 000140 JMP TXRRTN ;TRY AGAIN
346 001014' 032777 000020 001410 10$: BIT #BIT4,@TSR ;WAS RCVR BUSY OR NOT THERE?
347 001022' 001415 BEQ 3$ ;NO, CONTINUE CHECK
348 001024' 104403 000000' 002504' MSGNS,BEGIN,WRCAD ;ASCII MESSAGE CALL WITH COMMON HEADER
349 001032' 004767 000312 JSR PC,ERSUB1 ;GET ERROR INFORMATION
350 001036' 012767 000007 177042 MOV #7,ERRTYP ;SELECTION ERROR
351 ;*****
352 001044' 104406 000000' 002430' SOfERS,BEGIN,TABLE1 ;RCVR BUSY OR WRONG ADDR
353 ;*****
354 001052' 000167 000076 JMP TXRRTN ;TRY AGAIN
355 001056' 132777 000004 001364 3$: BITB #BIT2,@TMMRH ;IS NOW MASTER CAUSING INTERRUPT?
356 001064' 001410 BEQ 4$ ;NO, CONTINUE CHECK
357 001066' 142777 000004 001354 BICB #BIT2,@TMMRH ;YES,CLEAR IT
358 001074' 104403 000000' 002510' MSGNS,BEGIN,NMSINT ;ASCII MESSAGE CALL WITH COMMON HEADER
359 001102' 000167 000046 JMP TXRRTN ;TRY AGAIN
360 001106' 032777 000040 001316 4$: BIT #BIT5,@TSR ;WAS MESSAGE REJECTED?
361 001114' 001405 BEQ 5$ ;NO, CONTINUE CHECK
362 001116' 104403 000000' 002514' MSGNS,BEGIN,MRJCT ;ASCII MESSAGE CALL WITH COMMON HEADER
363 001124' 000167 000024 JMP TXRRTN ;TRY AGAIN
364 001130' 032777 000200 001274 5$: BIT #BIT7,@TSR ;SUCCESSFUL TRANSFER
365 001136' 001007 BNE TXUK ;YES, OK LEAVE
366 001140' 012767 000011 176740 MOV #11,ERRTYP ;ILLEGAL INTR OR DONE CLM
367 ;*****
368 001146' 104406 000000' 002430' SOfERS,BEGIN,TABLE1 ;UNKNOWN INTERRUPT
369 ;*****
370 001154' 000207 TXRRTN: RTS PC ;RETURN TO CALLER
371
372 001156' 105067 001044 TXUK: CLRB TXMERS ;NO ERRORS, SKIP RETRY
373 001162' 000167 177766 JMP TXRRTN
  
```

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374                                     ;CHECK FOR RECEIVER ERRORS AND REPORT, IF ANY.
375
376 001166'                                RCERS:                                ;ENTRY POINT FOR RCVR ERR CHK
377 001166' 112767 177777 001031          MOV#   #-1,RCVERS                            ;SET RCVR ERROR FLAG
378 001174' 032777 100000 001252          BIT   #BIT15,@RSR                          ;HARDWARE ERROR?
379 001202' 001412                                BEQ   10$                                    ;NO,CHECK FURTHER
380 001204' 004767 000156                                JSR   PC,ERSUB2                            ;LOAD ERROR INFORMATION
381 001210' 012767 000017 176670          MOV   #17,ERRTYP                          ;UNKNOWN RECEIVER ERROR
382                                     ;*****
383 001216' 104405 000000' 002452'        HRDRS,BEGIN,TABLE2                        ;RCVR HARDWARE ERROR
384                                     ;*****
385 001224' 000167 000106                                JMP   RCRRTN                                ;ERROR RETURN
386 001230' 032777 000400 001216 10$:    BIT   #BIT8,@RSR                          ;DATA OUTPUT RDY SET?
387 001236' 001415                                BEQ   3$                                    ;NO,CONTINUE CHECK
388 001240' 104404 000000' 002522'        MSGNS,BEGIN,ERDOPR                        ;ASCII MESSAGE CALL WITH COMMON HEADER
389 001246' 004767 000114                                JSR   PC,ERSUB2                            ;LOAD ERROR INFORMATION
390 001252' 012767 000044 176626          MOV   #44,ERRTYP                          ;FLAG SHOULD NOT BE SET
391                                     ;*****
392 001260' 104406 000000' 002452'        SOFRS,BEGIN,TABLE2                        ;DAT OUT RDY SET
393                                     ;*****
394 001266' 000167 000044                                JMP   RCRRTN                                ;ERROR RETURN
395 001272' 032777 000040 001154 3$:    BIT   #BITS,@RSR                          ;REJECT COMPLETED INTERRUPT?
396 001300' 001415                                BEQ   4$                                    ;NO, CONTINUE CHECK
397 001302' 104403 000000' 002530'        MSGNS,BEGIN,MRJTD                         ;ASCII MESSAGE CALL WITH COMMON HEADER
398 001310' 004767 000052                                JSR   PC,ERSUB2                            ;LOAD ERROR INFORMATION
399 001314' 012767 000044 176564          MOV   #44,ERRTYP                          ;FLAG SHOULD NOT BE SET
400                                     ;*****
401 001322' 104406 000000' 002452'        SOFRS,BEGIN,TABLE2                        ;REJ COMPL SET
402                                     ;*****
403 001330' 000167 000002                                JMP   RCRRTN                                ;ERROR RETURN
404 001334' 000401                                4$:   BR   RCOK                             ;SHOULDN'T BE HERE, LEAVE.
405 001336' 000207                                RCRRTN: RTS   PC                           ;RETURN TO CALLER
406
407 001340' 105067 000661                                RCOK:  CLRB   RCVERS                        ;NO ERRORS, SKIP RETRY
408 001344' 000167 177766                                JMP   RCRRTN
  
```

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409 001350'                                ERSUB1:                                ;RTN TO LD XMTR ERR INFO
410 001350' 016767 001056 176522          MOV   TSR,CSRA                            ;LOAD ADDR OF XMTR STATUS REG
411 001356' 017767 001050 176516          MOV   @TSR,ACSR                          ;LOAD CONTENTS OF XMTR STAT REG
412 001364' 000207                                RTS   PC                                    ;RETURN
413
414 001366'                                ERSUB2:                                ;ROUTINE TO LD RCVR ERR INFO
415 001366' 016767 001062 176504          MOV   RSK,CSHA                            ;LOAD ADDR OF RCVR STAT REG
416 001374' 017767 001054 176500          MOV   @RSK,ACSR                          ;LD CONTENTS OF RCVR STAT REG
417 001402' 000207                                RTS   PC                                    ;RETURN
418
419                                     ;ROUTINE TO GENERATE DEVICE ADDRESSES AND VECTORS
420
421
422 001404'                                SETUP:                                ;SETUP ROUTINE ENTRY POINT
423 001404' 016700 176376                                MOV   ADDR,R0                             ;GET DEVICE BASIC ADDRESS
424 001410' 010067 001014                                MOV   R0,ICR                             ;ICR ADDRESS
425 001414' 005720                                TST   (R0)+
426 001416' 010067 001010                                MOV   R0,TSR                             ;TSR ADDRESS
427 001422' 005720                                TST   (R0)+
428 001424' 010067 001004                                MOV   R0,TSDB                            ;TSDB ADDRESS
429 001430' 005720                                TST   (R0)+
430 001432' 010067 001000                                MOV   R0,TSBC                            ;TSHC ADDRESS
431 001436' 005720                                TST   (R0)+
432 001440' 010067 000774                                MOV   R0,TSBA                            ;TSBA ADDRESS
433 001444' 005720                                TST   (R0)+
434 001446' 010067 000770                                MOV   R0,TMMH                            ;TMMR ADDRESS
435 001452' 005200                                INC   R0
436 001454' 010067 000770                                MOV   R0,TMMRH                          ;TMMR HIGH BYTE
437 001460' 005200                                INC   R0
438 001462' 010067 000756                                MOV   R0,TSCRC                          ;TSCRC ADDRESS
439 001466' 016700 176316                                MOV   VECTOR,R0                          ;GET BASIC VECTOR
440 001472' 010067 000774                                MOV   R0,TVECT                          ;SAVE IT
441 001476' 012720 000620'                                MOV   #TXINT,(R0)+                       ;SET INTR POINTER IN CASE.
442 001502' 116710 176304                                MOV#  B#1,(R0)                          ;SET XMTR PRIORITY
443 001506' 005720                                TST   (R0)+
444 001510' 010067 000754                                MOV   R0,RCVECT                          ;SAVE RCVR VECTOR
445 001514' 062700 000002                                ADD   #2,R0
446 001520' 116710 176266                                MOV#  R#1,(R0)
  
```



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447 001524' 016700 176256      MOV   ADDR,R0          ;GET DEVICE BASIC ADDRESS
448 001530' 062700 000020      ADD   #20,R0          ;ADD OFFSET FOR RCVR ADDR
449 001534' 010067 000712      MOV   R0,RCR          ;RCR ADDRESS
450 001540' 005720              TST   (R0)+
451 001542' 010067 000706      MOV   R0,HSR          ;HSR ADDRESS
452 001546' 005720              TST   (R0)+
453 001550' 010067 000702      MOV   R0,RDDB         ;RDDB ADDRESS
454 001554' 005720              TST   (R0)+
455 001556' 010067 000676      MOV   R0,RDBC         ;RDBC ADDRESS
456 001562' 005720              TST   (R0)+
457 001564' 010067 000672      MOV   R0,RDBA         ;RDBA ADDRESS
458 001570' 022020              CMP   (R0)+,(R0)+
459 001572' 010067 000666      MOV   R0,RDCRC        ;RDCRC ADDRESS
460 001576' 105067 000421      CLR  TRY              ;CLEAR RETRY COUNTER
461 001602' 000207              RTS   PC              ;RETURN
    
```

```

462                                     ;THIS ROUTINE TESTS IF THE DEVICE IS WORTHY OF RUNNING WITH
463                                     ;DEC/X11 MODULE.
464                                     ; IT CHECKS THAT MASTER IS SET, (IF NOT, THAT IT CAN BE SET)
465                                     ;THEN DOES A SIMPLE TEST TO SEE IF THE DEVICE CAN INTERRUPT
466                                     ;AND GO
467                                     ;IF THIS TEST FAILS, THE MODULE WILL BE DROPPED.
468
469 001604'                                TSTDRP:                ;DROP TEST ROUTINE ENTRY
470 001604' 052777 000002 000616          BIS   #BIT1,@TCM      ;CLEAR XMTR HDWARE
471 001612' 052777 000002 000632          BIS   #BIT1,@RCR      ;CLEAR RCVR HDWARE
472 001620' 105067 000376                  CLR  DEVICE           ;CLEAR HARD ERROR FLAG
473 001624' 105077 000620                  CLR  @TMMRH           ;CLEAR MASTER, AUTO ADDR ETC.
474 001630' 112777 000021 000612          MOV  #21,@TMMRH       ;SET MASTER & AUTO ADDR
475 001636' 132777 000021 000604          BIT  #21,@TMMRH       ;ARE THEY SET?
476 001644' 001016                          RNE   1$              ;YES, NEXT TEST
477 001646' 112767 177777 000346          MOV  #-1,DEVICE       ;NO,SET DEVICE TO DROP MODULE
478 001654' 104403 000000' 002536'        MSGN,BEGIN,MSHDR      ;ASCII MESSAGE CALL WITH COMMON HEADER
479 001662' 012767 000042 176216          MOV  #42,ERRTYP       ;ACTIVE BIT SHD BE SET.
480                                     ;*****
481 001670' 104405 000000' 002430'        HDRS,BEGIN,TABLE1    ;MASTER OR AUTO ADDR CLR
482                                     ;*****
483 001676' 000167 000266                  JMP   TSTRTN          ;EXIT
484 001702' 012777 120000 000520 1$:      MOV  #120000,@TCR     ;SET RIB & SND WD
485 001710' 012777 177777 000516          MOV  #-1,@TSD0        ;PUT A WORD INTO SILO
486 001716' 012767 177600 000270          MOV  #-200,CLK        ;SET UP TO WAIT A BIT
487 001724'                                2$:
488 001724' 104407 000000'                BREAKS,BEGIN          ;TEMPORARY RETURN TO MONITOR....
489 001730' 104407 000000'                BREAKS,BEGIN          ;THEN CONTINUE AT NEXT INSTRUCTION.
490 001734' 032777 000020 000470          BIT  #BIT4,@TSR       ;TDM BUS BUSY SET?
491 001742' 001016                          BNE   3$              ;YES, NEXT TEST
492 001744' 005267 000244                  INC   CLK              ;NO, WAIT A WHILE
493 001750' 001365                          HNE   2$
494 001752' 112767 177777 000242          MOV  #-1,DEVICE       ;SET DEVICE TO DROP MODULE
495 001760' 012767 000006 176120          MOV  #6,ERRTYP        ;DEVICE WON'T GO.
496                                     ;*****
497 001766' 104405 000000' 002430'        HDRS,BEGIN,TABLE1    ;XMTR WON'T GO
498                                     ;*****
499 001774' 000167 000170                  JMP   TSTRTN          ;EXIT
500 002000' 052777 010000 000424 3$:      BIS   #BIT12,@TSR     ;CAUSE FORCED TXM ERR
501 002006' 012767 177600 000200          MOV  #-200,CLK        ;SET UP TO WAIT A BIT
502 002014'                                4$:
503 002014' 104407 000000'                BREAKS,BEGIN          ;TEMPORARY RETURN TO MONITOR....
504 002020' 104407 000000'                BREAKS,BEGIN          ;THEN CONTINUE AT NEXT INSTRUCTION.
505 002024' 032777 020000 000376          BIT  #BIT13,@TCM     ;IS SND WD CLEAR NOW?
506 002032' 001416                          BEQ   5$              ;YES, XMTR LOOKS OK
507 002034' 005267 000154                  INC   CLK              ;WAIT & GIVE IT A CHANCE
508 002040' 001365                          BNE   4$
509 002042' 112767 177777 000152          MOV  #-1,DEVICE       ;SET DEVICE TO DROP MODULE
510 002050' 012767 000023 176030          MOV  #23,ERRTYP       ;DEVICE FAILED IO INTERRUPT
511                                     ;*****
512 002056' 104405 000000' 002430'        HDRS,BEGIN,TABLE1    ;XMTR WON'T INTERRUPT
513                                     ;*****
514 002064' 000167 000100                  JMP   TSTRTN          ;EXIT
    
```

```

515 002070' 012777 000002 000332 5S:   MOV   #BIT1,@TCR           ;CLEAR XMTH HDWARE
516 002076' 052777 020000 000346       BIS   #BIT13,@RCR        ;SET RCY WD IN RCVR
517 002104' 052777 010000 000342       BIS   #BIT12,@RSR        ;CAUSE FAKE TXM ERR IN RCVR
518 002112' 012767 177600 000074       MOV   #-200,CLK         ;SET UP TO WAIT A BIT
519 002120'                                6S:
520 002120' 104407 000000'                BREAKS,BEGIN           ;TEMPORARY RETURN TO MONITOR...
521 002124' 104407 000000'                BREAKS,BEGIN           ;THEN CONTINUE AT NEXT INSTRUCTION.
522 002130' 032777 020000 000314       BIF   #BIT13,@RCR        ;IS RCY WD CLEAR NOW?
523 002136' 001414                        BEQ   TSTRTN            ;YES, RCVR LOOKS GOOD TOO
524 002140' 005267 000050                INC   CLK               ;NO, WAIT & GIVE IT A CHANCE
525 002144' 001365                        BNE   6S
526 002146' 112767 177777 000046       MOVB  #-1,DEVICE        ;SET DEVICE TO DROP MODULE
527 002154' 012767 000023 175724       MOV   #23,ERRTYP        ;DEVICE FAILED TO INTERRUPT
528                                ;*****
529 002162' 104405 000000' 002452'      HRDENS,BEGIN,TABLE2    ;RCVR WON'T INTERRUPT
530                                ;*****
531 002170' 052777 000002 000254 TSTRTN: BIS   #BIT1,@RCR        ;CLEAR RCYR HDWARE
532 002176' 052777 000002 000224       BIS   #BIT1,@TCR        ;CLEAR XMTH HDWARE
533 002204' 000207                        RTS   PC                ;RETURN
  
```

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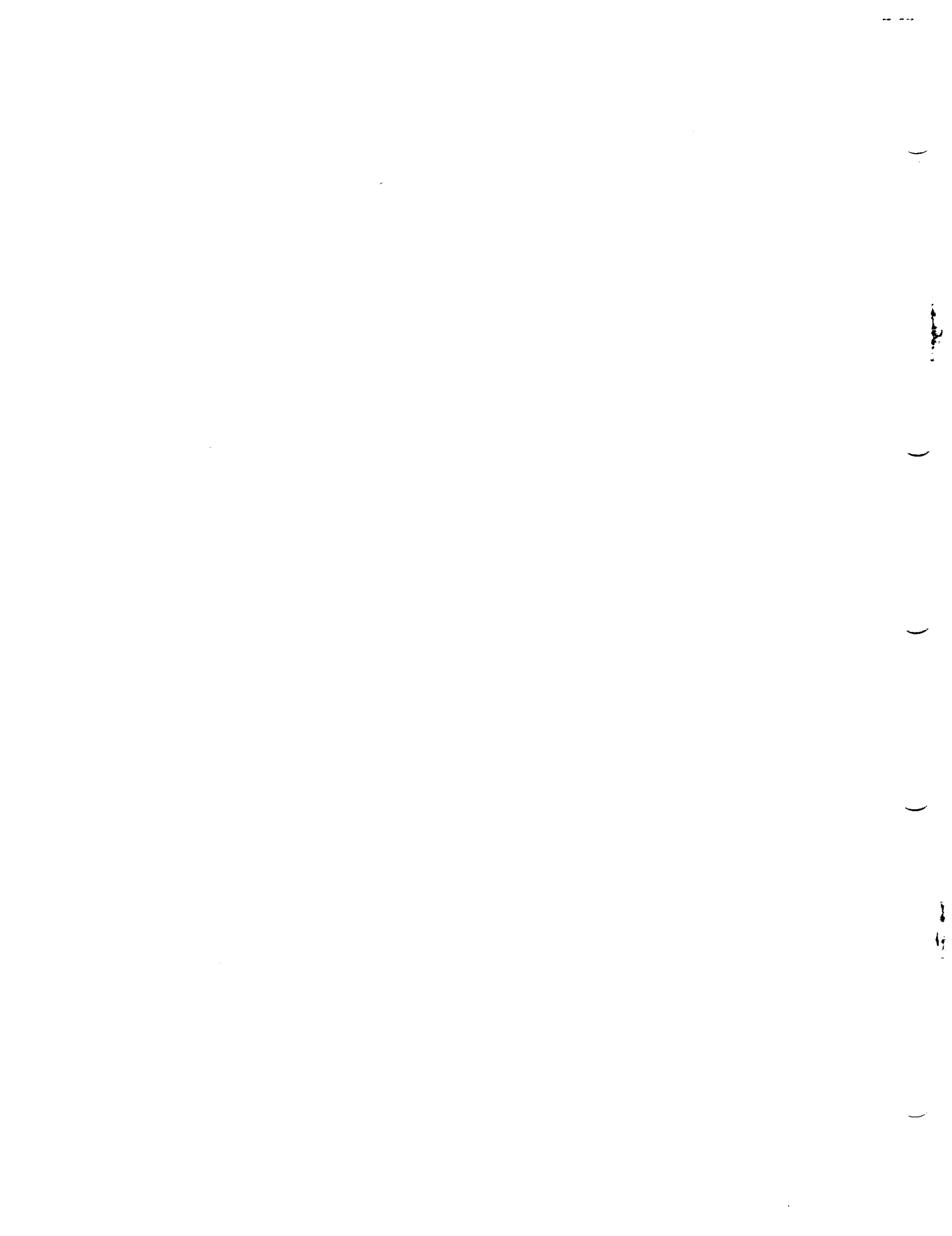
534                                ;CONSTANT AND VARIABLE STORAGE
535
536 002206' 000000                RCBC:  .WORD  0
537 002210' 000000                TXBC:  .WORD  0
538 002212' 000000                CNT:   .WORD  0
539 002214' 000000                CLK:   .WORD  0
540 002216' 000000                RFUNCT: .WORD  0
541 002220' 000000                XFUNCT: .WORD  0
542 002222' 000                DEVICE: .BYTE  0
543 002223' 000                TRY:   .BYTE  0
544 002224' 000                ERFLG: .BYTE  0
545 002225' 000                RCVER: .BYTE  0
546 002226' 000                TXMERS: .BYTE  0
547                                .EVEN
548 002230' 000100                BUFIN: .BLKW  64.
549
550 002430'                                TABLE1:
551 002430' 000000                TCR:   .WORD  0
552 002432' 000000                TSR:   .WORD  0
553 002434' 000000                TSDB:  .WORD  0
554 002436' 000000                TSBC:  .WORD  0
555 002440' 000000                TSBA:  .WORD  0
556 002442' 000000                TMMR:  .WORD  0
557 002444' 000000                TSCRC: .WORD  0
558 002446' 177777                .WORD  177777
559
560 002450' 000000                TMMRH: .WORD  0
561
562 002452'                                TABLE2:
563 002452' 000000                KCR:   .WORD  0
564 002454' 000000                RSR:   .WORD  0
565 002456' 000000                RDBB:  .WORD  0
566 002460' 000000                RDBC:  .WORD  0
567 002462' 000000                RDBA:  .WORD  0
568 002464' 000000                RDCRC: .WORD  0
569 002466' 177777                .WORD  177777
570 002470' 000000                RCVECT: .WORD  0
571 002472' 000000                IXVECT: .WORD  0
  
```

```
572 ;ASCII MESSAGE STORAGE
573
574 002474' 002544' EXCED: MSG1
575 002476' 177777 177777
576 002500' 002612' MSTDWN: MSG2
577 002502' 177777 177777
578 002504' 002644' WRCAD: MSG3
579 002506' 177777 177777
580 002510' 002707' NMSINT: MSG4
581 002512' 177777 177777
582 002514' 003067' MRJCT: MSG11
583 002516' 002753' MSG5
584 002520' 177777 177777
585 002522' 003077' ERDOPR: MSG12
586 002524' 003012' MSG7
587 002526' 177777 177777
588 002530' 003077' MRJTD: MSG12
589 002532' 002753' MSG5
590 002534' 177777 177777
591 002536' 003107' MSHDER: MSG13
592 002540' 003042' MSG8
593 002542' 177777 177777
```

```
594 002544' 042445 051122 051117 MSG1: .ASCIZ '%ERROR RETRY FOR THIS CYCLE EXCEEDED%'
595 002552' 051040 052105 054522
596 002560' 043040 051117 052040
597 002566' 044510 020123 054503
598 002574' 046103 020105 054105
599 002602' 042503 042105 042105
600 002610' 000045
601 002612' 050045 046103 046440 MSG2: .ASCIZ '%PCL MASTER WENT DOWN !!%'
602 002620' 051501 042524 020122
603 002626' 042527 052116 042040
604 002634' 053517 020116 020441
605 002642' 000045
606 002644' 051045 053103 020122 MSG3: .ASCIZ '%RCVR BUSY, OK WRONG RCVR ADDRESS%'
607 002652' 052502 054523 020054
608 002660' 051117 053440 047522
609 002666' 043516 051040 053103
610 002674' 020122 042101 051104
611 002702' 051505 022523 000
612 002707' 045 040515 052123 MSG4: .ASCIZ '%MASTER HAS JUST SET ON THIS PCL11%'
613 002714' 051105 044040 051501
614 002722' 045040 051525 020124
615 002730' 042523 020124 047117
616 002736' 052040 044510 020123
617 002744' 041520 030514 022461
618 002752' 000
619 002753' 040 042515 051523 MSG5: .ASCIZ ' MESSAGE WAS REJECTED BY RCVR%'
620 002760' 043501 020105 040527
621 002766' 020123 042522 042512
622 002774' 052103 042105 041040
623 003002' 020131 041522 051126
624 003010' 000045
625 003012' 042040 052101 020101 MSG7: .ASCIZ ' DATA OUTPUT READY SET%'
626 003020' 052517 050124 052125
627 003026' 051040 040505 054504
628 003034' 051440 052105 000045
629 003042' 040515 052123 051105 MSG8: .ASCIZ '%MASTER WILL NOT SET%'
630 003050' 053440 046111 020114
631 003056' 047516 020124 042523
632 003064' 022524 000
633 003067' 045 054055 052115 MSG11: .ASCIZ '%-XMTR-'
634 003074' 026522 000
635 003077' 045 051055 053103 MSG12: .ASCIZ '%-RCVR-'
636 003104' 026522 000
637 003107' 045 040510 042122 MSG13: .ASCIZ '%HARD ERROR%'
638 003114' 042440 051122 051117
639 003122' 000045
640 000001 .END
```







.REM

IDENTIFICATION

PRODUCT CODE: AC-F0698-MC  
PRODUCT NAME: CX8MG80 BM873-YJ MODULE  
PRODUCT DATE: SEPTEMBER 1978  
MAINTAINER: DEC/X11 SUPPORT GROUP

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1.0 ABSTRACT

BMG IS A BACKGROUND MODULE THAT EXERCISES A SINGLE 8M873-YJ BOOTSTRAP ROM OPTION. IT COMPARES THE CONTENTS OF EACH OF THE 256(10) LOCATIONS STORED IN THE ROM WITH THE CONTENTS OF A 256(10) WORD CORE MEMORY BUFFER TO VERIFY THAT EACH LOCATION IN THE ROM CAN BE UNIQUELY ADDRESSED AND CONTAINS THE CORRECT DATA. ALL ERRORS ARE REPORTED VIA THE CONSOLE DEVICE.

2.0 REQUIREMENTS

HARDWARE: A PDP11 COMPUTER WITH A 8M873-YJ OPTION

STORAGE:: BMG REQUIRES:

1. DECIMAL WORDS: 368
2. OCTAL WORDS: 0560
3. OCTAL BYTES: 1340

3.0 PASS DEFINITION

THE INITIAL PASS CONSISTS OF EXECUTING THE BASIC TEST SEQUENCE ONE TIME BEFORE REPORTING END OF PASS. SUBSEQUENT PASSES OF THE BMGB MODULE CONSISTS OF 100(8) ITERATIONS OF THE BASIC TEST SEQUENCE DESCRIBED IN PARA. 7 BELOW.

4.0 EXECUTION TIME

PASS TIME VARIES DEPENDENT UPON CPU TYPE AND THE CONFIGURATION BEING EXERCISED.

5.0 CONFIGURATION OPTIONS

DEFAULT PARAMETERS:

DVA: 173000

REQUIRED PARAMETERS:

NONE

6.0 DEVICE OPTION SETUP

NONE REQUIRED

7.0 MODULE OPERATION

TEST SEQUENCE:



1. R1 IS SET UP TO POINT TO THE FIRST WORD IN THE ROM
2. R2 IS SET UP TO POINT TO THE CORRESPONDING WORD IN THE CORE MEMORY BUFFER.

THE ADDRESS IN R1 IS CHECKED FOR EQUALITY TO EITHER 173024 OR 173224 AND IF FOUND EQUAL GOES TO STEP (5) - IF NOT IT PROCEEDS WITH STEP (3). THESE TWO ADDRESSES ARE NOT CHECKED BECAUSE THEIR CONTENTS AS READ ON THE BUS WILL VARY DEPENDENT UPON WHICH PARTICULAR "LOAD" BUTTON HAD BEEN INITIALLY DEPRESSED TO LOAD THE PROGRAM.

3. R1 AND R2 ARE USED TO COMPARE A ROM WORD WITH ITS CORE IMAGE COUNTERPART. IF THE WORDS DON'T COMPARE A SUB-ROUTINE IS CALLED TO SET UP THE ERROR INFORMATION AND REPORT IT VIA A "DATER" CALL TO THE MONITOR.
4. STEP (3) IS REPEATED.
5. R1 AND R2 ARE UPDATED TO POINT TO THE NEXT WORD AND A TEST MADE ON R2 TO SEE IF 256(10) WORDS HAVE BEEN CHECKED. IF YES, GO TO STEP (6) IF NOT REPEAT (3) THRU (5).
6. A PASS COUNTER IS DECREMENTED AND TESTED TO SEE IF 100(8) ITERATIONS OF STEPS (1) THRU (5) HAVE OCCURRED - IF YES GO TO STEP (7) IF NOT REPEAT (1) THRU (5).
7. REPORT END OF PASS AND REPEAT (1) THRU (6).

8.0 OPERATOR OPTIONS

-----  
(NONE)

9.0 NON-STANDARD PRINTOUTS

-----  
(NONE)

```

000000* 046502 041107 040
000000* 000
000005* 173000
000010* 000000
000012* 000
000013* 000
000014* 000001
000016* 000000
000020* 000000
000022* 000000
000024* 000000

000026* 040020
000030* 000224
000032* 000224
000034* 000000
000036* 000100
000040* 000000
000042* 000000
000044* 000000
000046* 000000
000050* 000000
000052* 000000
000054* 000000
000056* 000000
000060* 000000
000062* 000000
000064* 000000
000066* 000000
000070* 000000
000072* 000000
000074* 000000
000076* 000000
000100* 000000
000102*
000104* 000000
000106* 000000
000108* 000000
000112* 000230
000114* 000000
000116* 000000
000120* 000000
000122* 000156 000040

      <BMGB > 173000,0,0,0,0,100,156
      MODULE 40020, BMGB, 173000,0,0,0,0,100,156
      -TITLE BMGB DEC/X11 SYSTEM EXERCISER MODULE
      DDRCOM VERSION 6 23-MAY-78
      .LIST BIN
*****
BEGIN:
MODNAME: .ASCII /BMGB / ;MODULE NAME
IPLAC: .BYTE OPEN ;USED TO KEEP TRACK OF WBUFF USAGE
ADDR: 173000+0 ;1ST DEVICE ADDR.
VECTOR: 0+0 ;1ST DEVICE VECTOR.
BR1: .BYTE PRTO+0 ;1ST BR LEVEL.
BR2: .BYTE PRTO+0 ;2ND BR LEVEL.
DVID1: 0+1 ;DEVICE INDICATOR 1.
SR1: OPEN ;SWITCH REGISTER 1.
SR2: OPEN ;SWITCH REGISTER 2.
SR3: OPEN ;SWITCH REGISTER 3.
SR4: OPEN ;SWITCH REGISTER 4.
*****
STAT: 40020 ;STATUS WORD.
INIT: START ;MODULE START ADDR.
SPOINT: MODSP ;MODULE STACK POINTER.
PASCNT: 0 ;PASS COUNTER.
ICOUNT: 100 ;# OF ITERATIONS PER PASS=100
ICOUNT: 0 ;LOC TO COUNT ITERATIONS
SOFCNT: 0 ;LOC TO SAVE TOTAL SOFT ERRORS
HRDCNT: 0 ;LOC TO SAVE TOTAL HARD ERRORS
SOFPAS: 0 ;LOC TO SAVE SOFT ERRORS PER PASS
HRDPAS: 0 ;LOC TO SAVE HARD ERRORS PER PASS
SYSCHT: 0 ;# OF SYS ERRORS ACCUMULATED
RANNUM: 0 ;HOLDS RANDOM # WHEN RAND MACRO IS CALLED
RES1: 0 ;RESERVED FOR MONITOR USE
RES2: 0 ;RESERVED FOR MONITOR USE
SVR0: OPEN ;LOC TO SAVE R0.
SVR1: OPEN ;LOC TO SAVE R1.
SVR2: OPEN ;LOC TO SAVE R2.
SVR3: OPEN ;LOC TO SAVE R3.
SVR4: OPEN ;LOC TO SAVE R4.
SVR5: OPEN ;LOC TO SAVE R5.
SVR6: OPEN ;LOC TO SAVE R6.
CSRA: OPEN ;ADDR OF CURRENT CSR.
SADR: ;ADDR OF GOOD DATA, OR
ACSR: OPEN ;CONTENTS OF CSR.
#ASADR: ;ADDR OF BAD DATA, OR
ASTAT: OPEN ;STATUS REG CONTENTS.
ERRTYP: ;TYPE OF ERROR
ASB: OPEN ;EXPECTED DATA.
AVAS: OPEN ;ACTUAL DATA.
RSTRT: RSTRT ;RESTART ADDRESS AFTER END OF PASS
WDTO: OPEN ;WORDS TO MEMORY PER ITERATION
WDFR: OPEN ;WORDS FROM MEMORY PER ITERATION
INTR: OPEN ;# OF INTERRUPTS PER ITERATION
IDNUM: 156 ;MODULE IDENTIFICATION NUMBER=156
      .REPT SPSTZ
      .MLIST
*****

```

```

000224*
      .WORD 0
      .LIST
      .ENDR
MODSP:
*****

```

```

199
200 000224* 016705 177556
201
202 000230*
203 000230* 010501
204 000232* 012702 000336*
205 000236* 022701 173024
206 000244* 001413
207 000244* 021112 173224
208 000250* 001410
209 000252* 021112
210 000254* 001404
211 000256* 004767 000026
212 000262* 021112
213 000264* 001404
214 000266* 004767 000016
215 000272* 022122
216 000274* 022702 001336*
217 000300* 001356
218 000302*
219
220
221 000306* 104413 000000*
222 000306* 000750
223
224
225
226 000310* 010267 177566
227 000312* 010167 177564
228 000320* 011167 177564
229 000324* 011267 177556
230
231 000330* 104404 000000*
232
233 000334* 000207
234
235
236
237
238
239
240
241 000336*
242 000336* 010037
243 000340* 000040
244 000342* 013700
245 000344* 177570
246 000346* 032700
247 000350* 001001
248 000354* 000510
249
250 000356* 005000
251 000360* 000404
252
253 000362* 173000
254 000364* 000340

```

```

START: MOV ADDR,R5 ;GET FIRST ROM ADDRESS INTO R5
RESTART:
AGAIN: MOV R5,R1 ;R1 POINTS TO ROM WORD
MOV #BTAB,R2 ;R2 POINTS TO ROM IMAGE (W CORF)
1$: CMP #173024,R1 ;ROM ADDRESS = 173024 ??
BEQ $+1 ;
MOV #173224,R1 ;ROM ADDRESS = 173224 ??
BEQ $+1 ;
CMP (R1),(R2) ;CHECK ONE LOCATION
BEQ $+1 ;
PC, #BERR ;R1 IF [ROM] = [CORE]
;GO SETUP AND REPORT ERROR
2$: CMP (R1),(R2) ;CHECK IT AGAIN
BEQ $+1 ;
PC, #BERR ;R1 IF [ROM] = [CORE]
;GO SETUP AND REPORT ERROR
3$: CMP (R1)+(R2)+ ;ADD +1 TO BOTH POINTERS
CMP #BTAB,R2 ;DONE LAST WORD ??
BNE $+1 ;
4$: ENDTAB,BEGIN ;SIGNAL END OF ITERATION.
BR AGAIN ;MONITOR SHALL TEST END OF PASS.

;THIS ROUTINE SETS UP AND REPORTS ALL DATA COMPARE ERRORS
BERR: MOV R2,#BADR ;SAVE THE ADDR. OF GOOD DATA
MOV R1,#WASADR ;SAVE ADDR. OF THE BAD DATA
MOV (R1),#WAS ;GET WAS DATA
MOV (R2),#ASB ;GET THE S/R DATA
;*****
;*****
;*****
RTS PC ;CONTINUE CHECKING

;256(10) WORD TABLE THAT STORES A CORE IMAGE OF THE CONTENTS OF THE ROM
BTAB:
010037 ;173000 010037 BUTON1: MOV R0,#SSGSV+0 ;SAVE R0 IN LOCA
000040 ;173000 000040 013700 MOV #BWR,R0 ;GET SWITCH REGI
013700 ;173004 013700 MOV #BIT0,R0 ;IS LOW-ORDR BIT
177570 ;173006 177570 032700 BIT #BIT0,R0 ;IS LOW-ORDR BI
032700 ;173010 032700 BIT #BIT0,R0 ;IS LOW-ORDR BI
000001 ;173012 000001 001007 BNE BUTONX ;YES-- LOOK AT C
001007 ;173014 001007 BR RECSAV ;NO-- SAVE R1-R7
000510 ;173016 000510 BUTON3: CLR R0 ;SAVE LOAD FROM P
005000 ;173020 005000 BR BUTONX ;GO TO COMMON CO
000404 ;173022 000404 FILLTO 24 ;WORD
173000 ;173024 173000*
000340 ;173026 000340

```

```

255 000366* 012700
256 000370* 000200
257
258
259 000372* 010005
260 000374* 106300
261 000376* 122700
262 000400* 000000
263 000402* 001001
264 000404* 005000
265
266 000406* 000300
267 000410* 042700
268 000412* 177770
269 000414* 105705
270 000416* 100551
271 000420* 012737
272 000422* 132774
273 000424* 000004
274 000426* 005037
275 000430* 000006
276
277 000432* 012706
278 000434* 000020
279 000436* 012701
280 000440* 177170
281 000442* 005711
282
283 000444* 005705
284 000446* 100402
285 000450* 005306
286 000452* 002531
287
288 000454* 000005
289
290 000456* 032711
291 000460* 000040
292 000462* 001775
293 000464* 111704
294 000466* 010002
295 000470* 001402
296 000472* 001705
297 000474* 000020
298
299 000476* 052702
300 000500* 000007
301 000502* 010103
302 000504* 010223
303
304 000506* 105711
305 000510* 100376
306 000512* 100376
307 000514* 000001
308 000516* 005304
309 000520* 001372
310

```

```

012700 ;173030 012700 BUTON2: MOV #BIT7,R0 ;BIT 7 MEANS LOA
000200 ;173032 000200 BUTONX: MOV R0,R5 ;SAVE PARAMETER
;LEFT-ALIGN SPEE
;IS SPEED 0, 1.
010005 ;173034 010005 ASLR R0,R5 ;SAVE PARAMETER
106300 ;173036 106300 CMPR #3*BIT4,R0 ;IS SPEED 0, 1.
122700 ;173040 122700 BRL 101001 ;YES-- UNIT IS 1
000060 ;173042 000060 CLR R0 ;NO-- USE UNIT #
101001 ;173044 101001 10S: SWAB R0 ;GET UNIT # IN L
;RTRM TO 3 BITS
005000 ;173046 005000 BIC #C7,R0 ;WHERE SHOULD WE
;BIT 7 = 1 -- SO
;BOCT FR
000300 ;173050 000300 TSTB R5 ;WHERE SHOULD WE
042700 ;173052 042700 BIC #C7,R0 ;WHERE SHOULD WE
105705 ;173054 105705 BIC #C7,R0 ;WHERE SHOULD WE
100551 ;173056 100551 BIC #C7,R0 ;WHERE SHOULD WE
012737 ;173058 012737 000004 BIC #C7,R0 ;WHERE SHOULD WE
173274 ;173060 173274 BIC #C7,R0 ;WHERE SHOULD WE
000004 ;173062 000004 BIC #C7,R0 ;WHERE SHOULD WE
005037 ;173064 005037 CLR #6 ;SET PS OF TINED
000006 ;173066 000006 RXBOOT: MOV #RSSTRY,SP ;SET RETRY COUNY
012706 ;173068 012706 MOV #RSSX11+RXCS,R1 ;ADDRESS CONTROL
000020 ;173070 000020 TST (R1) ;RX11 EXIST?
177170 ;173072 177170 RXRTRY: TST R5 ;INDEFINITE RETR
100402 ;173074 100402 BNE RXRSET ;YES-- TRY FAITH
005306 ;173076 005306 DEC SP ;NO-- DECREMENT
002531 ;173078 002531 BLT RXEHLT ;GIVE UP IF RON
000005 ;173080 000005 RXRSET: RESET ;CLEAR THE WORLD
032711 ;173082 032711 20$: BIT #RXDONE,(R1) ;WAIT UNTIL READ
000040 ;173084 000040 BEQ 20$ ;NOT YET-- WAIT
001775 ;173086 001775 MOVB (PC),R4 ;SET TRACK/SECTO
111704 ;173088 111704 MOV R0,R2 ;SET UNIT #
010002 ;173090 010002 BEQ 30$ ;ZERO-- USE ZERO
001402 ;173092 001402 MOV #RXUNIT,R2 ;NO--ZERO-- ASSD
000020 ;173094 000020 30$: BIS #RXREAD+RXGO,P2 ;SET READ FUNCTI
052702 ;173096 052702 MOV R1,R3 ;COPY ADDRESS OF
000007 ;173098 000007 MOV R2,(R3)+ ;START READ FUNC
010103 ;173100 010103 40$: TSTR (R1) ;READY?
010223 ;173102 010223 SPL #1 ;NO-- WAIT
;SET SECTOR #, +
105711 ;173104 105711 MOV #1,(R3) ;SET SECTOR #, +
100376 ;173106 100376 DEC R4 ;COUNT DOWN SECT
000001 ;173108 000001 BNE 40$ ;TRACK TO SET ST
005304 ;173110 005304
001372 ;173112 001372
;173114

```

311	000522	032711	032711	173164	032711	BIT	#RXERRIRXDONE,(R1) ;DONE OR ERRO
312	000524	100040	100040	173166	100040	BEG	50S ;NO-- WAIT
313	000526	001775	001775	173168	001775	BPL	RXRTRY ;YES-- ERROR IN
314	000530	100745	100745	173172	100745	MOV	#RXEMPT+RXGO,(R1) ;START EMPTY
315	000532	012711	012711	173174	012711		
316	000534	000003	000003	173176	000003		
317				173178			
318	000536	132711	132711	173180	132711	60S:	BITB #RXTREQIRXDONE,(R1) ;READY FOR W
319	000540	000240	000240	173202	000240		
320	000542	001775	001775	173204	001775	BEG	60S ;NOT READY-- WAI
321	000544	100147	100147	173206	100147	BPL	CLRPC ;DONE-- GO TO LO
322	000546	111324	111324	173210	111324	MOV	CLRPC,(R4)+ ;WAIT DOWN-- GET
323	000550	000772	000772	173214	000772	BR	50S ;WAIT FOR NEXT B
324				173216			
325	000552	005000	005000	173214	005000	BUTOM5:	CLR R0 ;HERE TO START W
326	000554	000425	000425	173216	000425	BR	TCBOTO ;BOOT FROM TAPE
327				173220			
328	000556	000000	000000	173220	000000	BUTOM0:	HALT ;HALT NOW
329	000560	000666	000666	173224	000666	BR	BUTOM1 ;BUT LOOK AT SWR
330				173224			
331	000562	173230	173230	173224	173230	FILLTO	BUTOM1 ;BUT LOOK AT SWR
332	000564	000340	000340	173226	000340	WORD	BW873,PR7
333				173230			
334	000566	010037	010037	173232	010037	BUTOM4:	MOV R0,RSSGSV+0 ;SAVE R0 IN 40
335	000570	000040	000040	173232	000040		
336	000572	012700	012700	173234	012700		
337	000574	173622	173622	173236	173622		
338				173240			
339	000576	010037	010037	173240	010037	REGSAV:	MOV R0,RSSGSV+16 ;SAVE R0 AS PC
340	000600	000056	000056	173240	000056		
341	000602	012700	012700	173244	012700		
342	000604	000056	000056	173246	000056		
343	000606	010640	010640	173250	010640	MOV	SP,-(R0) ;SAVE SP IN 54
344	000610	010540	010540	173252	010540	MOV	R5,-(R0) ;SAVE R5 IN 52
345	000612	010440	010440	173252	010440	MOV	R4,-(R0) ;SAVE R4 IN 50
346	000614	010340	010340	173256	010340	MOV	R3,-(R0) ;SAVE R3 IN 48
347	000616	010240	010240	173260	010240	MOV	R2,-(R0) ;SAVE R2 IN 46
348	000620	010140	010140	173262	010140	MOV	R1,-(R0) ;SAVE R1 IN 44
349	000622	014000	014000	173264	014000	MOV	-C(R0) ;SAVE R1 IN 42
350	000624	000177	000177	173266	000177	MOV	R0,RSSGSV+16 ;RESTORE R0 FROM
351	000626	004564	004564	173270	004564	JMP	GO TO SAVED PC
352				173272			
353	000630	005005	005005	173272	005005	TCBOTO:	CLR R5 ;SET SWR PARAMET
354				173274			
355	000632	012737	012737	173274	012737	TCBOOT:	MOV #DLBOOT,R#4 ;IN CASE
356	000634	173530	173530	173276	173530		
357	000636	000004	000004	173300	000004		
358	000640	012706	012706	173302	012706		
359	000642	000020	000020	173304	000020		
360	000644	012701	012701	173306	012701		
361	000646	177342	177342	173310	177342		
362				173312			
363	000650	105011	105011	173312	105011	TCRTRY:	CLRB (R1) ;STOP ALL TAPE M
364	000652	005705	005705	173314	005705	TST	R5 ;INDEFINITE RETR
365	000654	100402	100402	173316	100402	BMI	10S ;YES-- TRV HARDF
366	000656	005306	005306	173320	005306	DEC	SP ;NO-- DECREMENT

367	000660	002426	002426	173322	002426	BLT	TCERLT ;TOO MANY-- GIVF
368				173324			
369	000662	000005	000005	173324	000005	10S:	RESET ;CLEAR TC11
370	000664	110061	110061	173326	110061	MOV	R0,1(R1) ;SELECT PROPER II
371	000666	000001	000001	173330	000001		
372	000670	052711	052711	173334	052711	BIS	#TCREV+TCRNUM+TCGO,(R1) ;START T
373	000672	004003	004003	173334	004003		
374				173336			
375	000674	005711	005711	173336	005711	20S:	TST (R1) ;ERROR?
376	000676	100776	100776	173340	100776	TST	30S ;NO-- WAIT FOR F
377	000700	005761	005761	173340	005761	TST	TCST-TCCM(R1) ;END-ZONE UP YET
378	000702	177776	177776	173344	177776		
379	000704	100361	100361	173346	100361		
380	000706	012761	012761	173350	012761	BPL	TCRTRY ;NO-- MUST RE ↑
381	000710	177400	177400	173350	177400	MOV	#-256,TCMC-TCCM(R1) ;SFT WORT
382	000712	000002	000002	173354	000002		
383	000714	042711	042711	173356	042711		
384	000716	004000	004000	173360	004000	BIC	#TCREV,(R1) ;SET FORWARD MOD
385	000720	112711	112711	173362	112711		
386	000722	000005	000005	173364	000005	MOV	#TCREAD+TCGO,(R1) ;START READ, F
387				173366			
388	000724	105711	105711	173366	105711	30S:	TSTB (R1) ;TRANSFER DONE?
389	000726	100376	100376	173370	100376	BPL	30S ;NO-- WAIT SOME
390	000730	005711	005711	173372	005711	TST	(R1) ;YES-- ERROR?
391	000732	100746	100746	173374	100746	BMI	TCRTRY ;YES-- RETRY
392	000734	005007	005007	173376	005007	CLR	PC ;NO-- DONE-- GOT
393				173400			
394				173400			
395				173400			
396				173400			
397				173400			
398	000736	000000	000000	173400	000000	-IIF DF	RSSP04, RPEHLT:
399	000740	000776	000776	173402	000776	-IIF DF	RSSX11, RPEHLT:
400				173404		-IIF DF	TSSC11, TCERLT:
401	000742	012706	012706	173404	012706	-IIF DF	DSSL11, DLEHLT:
402	000744	000020	000020	173406	000020	HALTED:	HALT ;DIE
403	000746	012701	012701	173410	012701	BR	STAY DEAD
404	000750	176700	176700	173414	176700	RPBOOT:	MOV #RSSTRY,SP ;RETRY RETRY TIM
405	000752	012702	012702	173414	012702		
406	000754	004000	004000	173416	004000		
407				173420			
408	000756	005705	005705	173420	005705	RPRTY:	TST R5 ;INFINITE RETRY?
409	000760	100402	100402	173422	100402	BMI	RPRSET ;YES-- TRV AGAIN
410	000762	005306	005306	173424	005306	DEC	SP ;RETRY COUNT FXH
411	000764	002764	002764	173426	002764	BLT	RPEHLT ;YES-- GIVE UP
412				173430			
413	000766	000005	000005	173430	000005	RPRSET:	RESET ;ZAPII
414	000770	110061	110061	173434	110061	MOV	R0,RPCS2(R1) ;SELECT PROPER II
415	000772	000010	000010	173434	000010		
416	000774	012711	012711	173436	012711	MOV	#RPPRST+RPGO,(R1) ;DO *READ-IN P
417	000776	000021	000021	173440	000021		
418	001000	005061	005061	173442	005061	CLR	RPDC(R1) ;SET CYLINDER 0
419	001002	000034	000034	173444	000034		
420	001004	005061	005061	173446	005061	CLR	RPDA(R1) ;TRACK 0, SECT0
421	001006	000006	000006	173448	000006		
422	001010	050261	050261	173452	050261	BIS	R2,RPOP(R1) ;SET INHIBIT ECC

423	001012	000032	000032	173454	000032				
424	001014	012761	012761	173456		012761	MOV	R-256,RPWC(R1)	SET UP WORD COD
425	001016	177400	177400	173460	177400				
426	001020	000002	000002	173464	000002				
427	001022	000071	000071	173466	000071	012711	MOV	#RPPREAD+RPGO,(R1)	START READ PH
428	001024	000071	000071	173470					
429				173470					
430	001026	105711	105711	173470	105711	20S:	TSTB	(R1)	READY?
431	001028	105711	105711	173470	105711		BPL	#R1	WAIT UNTIL
432	001030	032761	032761	173474	032761		BIT	#RPFER,RPER1(R1)	FORMAT ERROR?
433	001034	000020	000020	173476	000020				
434	001036	000014	000014	173476	000014				
435	001040	001402	001402	173502	001402		REQ	30S	NO-- TRY AGAIN
436	001042	052702	052702	173504	052702		BTS	#RPFM22,R2	YES-- TRY FOR 2
437	001044	010000	010000	173506	010000				
438				173510					
439	001046	032711	032711	173510	032711	30S:	BIT	#RPTREIRPMCPE,(R1)	TRANSFER OR
440	001050	060000	060000	173512	060000				
441	001052	001341	001341	173512	001341		BNE	RPRTRY	YES-- ERROR-- T
442	001054	032761	032761	173512	032761		BIT	#RPTAIRPERR,FPDS(R1)	ATTN OR 0
443	001056	140000	140000	173520	140000				
444	001060	000012	000012	173522	000012				
445	001062	001335	001335	173522	001335		BNE	RPRTRY	YES-- ERROR-- T
446				173526			CLRPC:	CLR	PC
447	001064	005007	005007	173526	005007				JMP 0
448				173530			BUTOM6:		
449				173530			DLBOOT:		
450	001066	012701	012701	173530	012701		MOV	#DSSL11+DLRCSW,R1	GET DL11 EXTE
451	001070	177560	177560	173534	177560		MOV	(PC),SP	SET TEMP STACK
452	001072	005004	005004	173534	005004		CLR	R4	RESET MEMORY AD
453	001074	005004	005004	173534	005004		MOV	#DLCHAR,R2	SET ADN
454	001076	012702	012702	173540	012702				
455	001100	173610	173610	173542	173610				
456				173544					
457				173544					
458	001102	004712	004712	173544	004712	10S:	CALL	(R2)	GET A CHARACTER
459	001104	124427	124427	173544	124427		JSR	PC,(R2)	
460	001106	000220	000220	173546	000220		CMPB	-(R4),#220	DLR?
461	001110	001374	001374	173550	001374		BNE	10S	NO-- KEEP ON LO
462				173552			CALL	(R2)	GET TWO BYTE
463	001112	004742	004742	173554	004742		JSR	PC,(R2)	
464	001114	014403	014403	173556	014403		MOV	-(R4),R3	GET BYTE COUNT
465	001116	042703	042703	173556	042703		BTC	#BIT15BIT14,R3	CLEAR QSYNCR AND
466	001120	140000	140000	173562	140000				
467				173564					
468	001122	004712	004712	173564	004712		CALL	(R2)	SKIP TWO WORD R
469				173566			JSR	PC,(R2)	
470	001124	004712	004712	173566	004712		CALL	(R2)	AND TWO WORD
471	001126	005722	005722	173570	005722		JSR	PC,(R2)	
472				173572			TST	(R2)	SKIP THE CALL (
473	001130	004712	004712	173574	004712		CALL	(R2)	+1 MAKES 5 RYT
474	001132	005004	005004	173574	005004		JSR	PC,(R2)	
475				173576			CLR	R4	RESET RACK TO 0
476				173576					
477	001134	004712	004712	173578	004712	20S:	CALL	(R2)	GET A CHARACTER
478	001136	005303	005303	173600	005303		JSR	PC,(R2)	
				173600			DEC	(R3)	REDUCE COUNT

479	001140	003375	003375	173602	003375		BGT	20S	BRACK IF NOR
480	001142	005007	005007	173604	005007		CLR	PC	ELSE GO TO LOAD
481				173606			CALL	(PC)	GET A BYTE, THF
482	001144	004717	004717	173610	004717		JSR	PC,(PC)	
483				173610			DLCHAR:		
484	001146	105711	105711	173610	105711		TSTB	(R1)	READY WITH A CH
485	001150	100376	100376	173612	100376		BPL	DLCHAR	NO-- WAIT SOME
486	001152	116124	116124	173614	116124		MOVB	DLRBUF+DLRCSW(R1),(R4)+	YES-- S
487	001154	000002	000002	173616	000002				
488				173618					
489	001156	000207	000207	173620	000207		RETURN		AND RETURN FROM
490				173622			RFS	PC	
491	001160	005005	005005	173624	005005		OTRDMP:		
492	001162	012500	012500	173624	012500		CLR	R5	POINT TO LOCATI
493	001164	012501	012501	173624	012500		MOV	(R5)+,R0	SAVE LOCATION 0
494	001166	011502	011502	173626	011501		MOV	(R5)+,R1	AND LOCATION 2
495	001170	012725	012725	173628	012725		MOV	(R5)+,R2	SAVE 4 IN R2
496	001172	173646	173646	173634	173646		MOV	#215,(R5)+	SET NXM
497	001174	011503	011503	173636	011503		MOV	(R5),R3	SAVE 6 IN R3
498	001176	005015	005015	173640	005015		CLR	(R5)	SET PS FOR TRAP
499				173640					
500	001200	012704	012704	173644	012704	20S:	MOV	#DSS120+DLVCNT-DTESIZ,R4	POINT
501	001202	174340	174340	173644	174340				
502				173646					
503	001204	012706	012706	173646	012706	21S:	MOV	#SSDTE+10,SP	SET STACK TO S
504	001206	000140	000140	173650	000140				
505				173652					
506	001210	062704	062704	173652	062704	22S:	ADD	#DTESIZ,R4	RUMP TO NEXT DT
507	001212	000040	000040	173654	000040				
508	001214	105704	105704	173656	105704		TSTB	R4	IS THIS THE END
509	001216	100770	100770	173656	100770		BMI	20S	YES-- START ALL
510	001220	032764	032764	173660	032764		BIT	#T011DB,STAT+DLVCNT(R4)	DOORREL
511	001222	004000	004000	173664	004000				
512	001224	000034	000034	173666	000034				
513	001226	001770	001770	173670	001770		BEQ	22S	NO-- TRY NEXT D
514	001230	026417	026417	173674	026417		CMP	T0108C+DLVCNT(R4),(PC)	DOES THI
515	001232	000014	000014	173678	000014				
516	001234	001365	001365	173676	001365		BNE	22S	NO-- TRY ANOTHE
517	001236	010315	010315	173700	010315		MOV	R3,(R5)	RESTORE LOCATI
518	001240	010245	010245	173702	010245		MOV	R2,-(R5)	4
519	001242	010145	010145	173704	010145		MOV	R1,-(R5)	RESTORE 2
520	001244	010045	010045	173706	010045		MOV	R0,-(R5)	AND 0
521	001246	012700	012700	173708	012700		MOV	#SSDTE,R0	POINT TO SAVE
522	001250	000130	000130	173712	000130				
523				173714					
524	001252	012420	012420	173714	012420	29S:	MOV	(R4)+,(R0)+	SAVE A REGISTER
525	001254	022700	022700	173716	022700		CMP	#T011DT+DLVCNT+SSDTE,R0	FINISH
526	001256	000156	000156	173718	000156				
527	001260	103374	103374	173722	103374		BHIS	29S	NO-- SAVE SOME
528				173724			ADDX	DIAG-<T011DT+2>,R4	R4 POINTS +
529	001262	005724	005724	173724	005724		TST	(R4)+	
530	001264	010401	010401	173724	010401		MOV	R4,R1	SO DOES R1
531	001270	000100	000100	173726	000100		MOV	#RESET,R0	SETUP R0 FOR "D
532				173728					
533	001272	010021	010021	173734	010021		MOV	R0,(R1)+	R1 POINTS TO ST
534	001274	005061	005061	173736	005061		CLR	DLVCNT+STAT(R1)	SET DTE20 FOR 4

535 001276\* 177744  
536 001300\* 005061  
537 001302\* 177764  
538  
539 001304\* 032711  
540 001306\* 004000  
541 001310\* 001775  
542 001312\* 010014  
543 001314\* 005061  
544 001316\* 177766  
545 001320\* 012761  
546 001322\* 007400  
547 001324\* 177762  
548  
549 001326\* 105711  
550 001330\* 100376  
551 001332\* 005007  
552  
553 001334\* 000000  
554  
555 001336\* 177777  
556  
557 000001

177744 173740 177744  
005061 173742 005061  
177764 173744 177764  
032711 173746 032711  
004000 173748 004000  
001775 173750 001775  
010014 173752 010014  
005061 173754 005061  
177766 173756 177766  
007400 173758 007400  
177762 173760 177762  
173770  
105711 173772 105711  
100376 173774 100376  
005007 173776 005007  
000000 173778 000000  
173779  
173777

TABEND: 177777

.END

30\$: CLR TO10AD-STAT(R1) ;START DUMPING -  
BIT #TO11DB,(R1) ;I- DOORBELL RIM  
BEQ 305 ;NO-- WAIT FOR D  
MOV #0,(R4) ;YES-- CLEAR D00  
CLR TO11AD-STAT(R1) ;START INPUT TO  
MOV #IFLOP1<<-256.>&BCOUNT>,TO11BC-S  
40\$: TSTB (R1) ;TRANSFER COMPLE  
SPL 305 ;NO-- WAIT SOME  
CLR PC ;GO TO LOADED CO  
FILLIO 1000  
-BYTE 0  
-BYTE 0

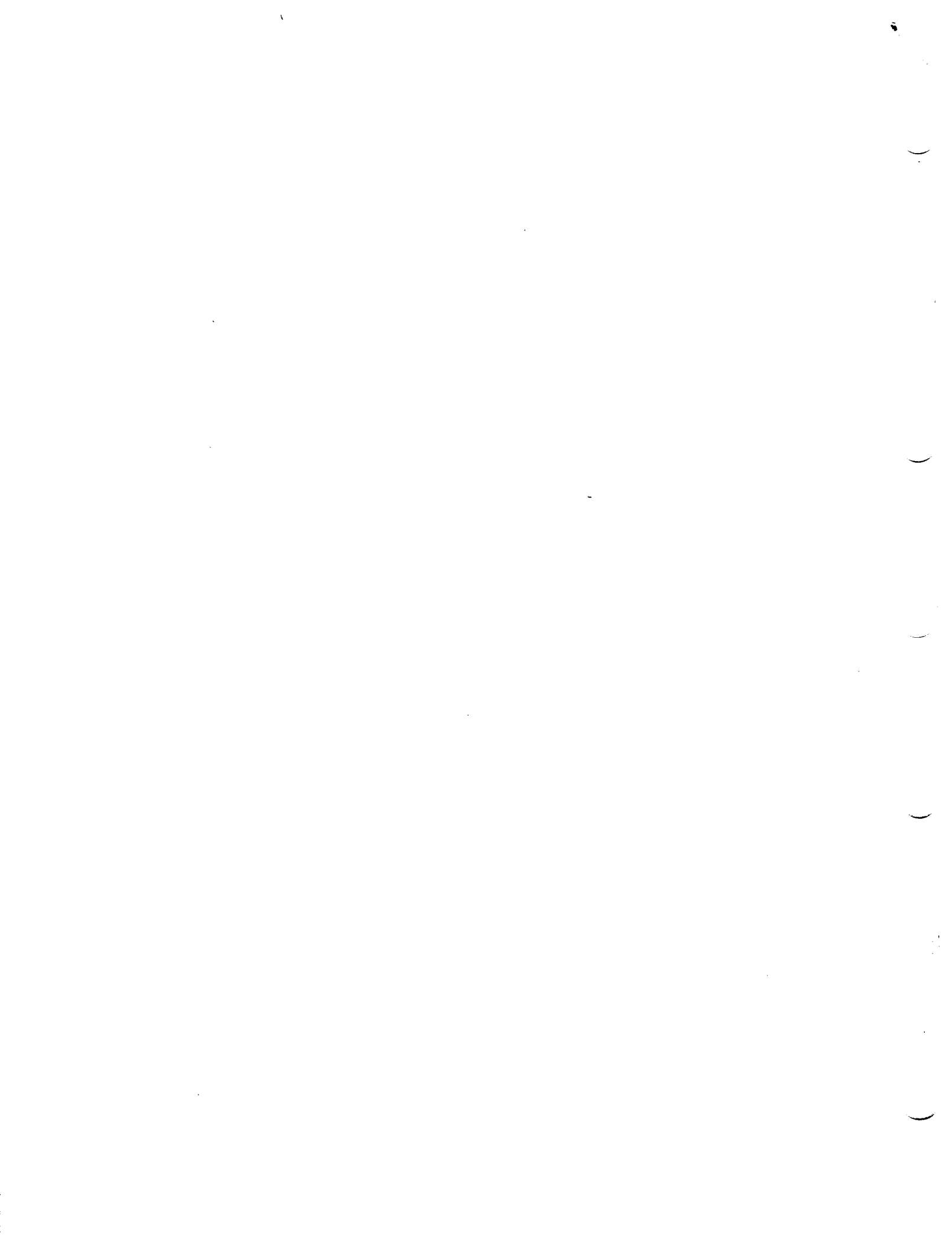
ACSR 000102R 181#  
ADDR 000006R 147#  
ADDR22= 001000 199#  
AGAIN 000230R 203#  
ASB 00106R 225#  
ASTAT 000104R 183#  
AWAS 000110R 186#  
CGGIN 000000R 144#  
BIT0 = 000001 199#  
BIT1 = 000002 199#  
BIT2 = 002000 199#  
BIT3 = 004000 199#  
BIT4 = 010000 199#  
BIT5 = 020000 199#  
BIT6 = 040000 199#  
BIT7 = 080000 199#  
BIT8 = 100000 199#  
BIT9 = 000004 199#  
BIT10 = 000010 199#  
BIT11 = 000020 199#  
BIT12 = 000040 199#  
BIT13 = 000100 199#  
BIT14 = 000200 199#  
BIT15 = 000400 199#  
BIT16 = 000800 199#  
BIT17 = 001000 199#  
BIT18 = 000100R 211#  
BIT19 = 001000R 214#  
BERN 000310R 204#  
BTAB 000336R 199#  
BREAKS= 104407 199#  
B21 000012R 149#  
B22 000013R 150#  
BTONS = 104421 199#  
CDATAS = 104412 199#  
CONPIC = 000056R 189#  
CTRA 000102R 180#  
DATCKS= 104411 199#  
DATERS= 104404 199#  
DVT01 = 000014R 151#  
ENDITS = 104413 199#  
ENDS = 104410 199#  
ERRTYP = 000166R 184#  
EXITS = 104400 199#  
GETPAS = 104415 199#  
GWBHPS = 104414 199#  
HDOCHT = 000044R 199#  
HDOERS= 104405 199#  
HRDPAS = 000053R 166#  
ICDNT 000036R 161#  
ICDNT 000040R 162#  
IDNOM 000122R 161#  
INIR 000030R 156#  
INT 000120R 190#  
MAP22S = 104416 199#  
MORNAM = 000090R 145#  
NDSP 000224R 159#  
NSGS = 104408 199#  
NSGS = 104402 199#  
NSGS = 104401 199#

200  
225#  
228#  
219#  
231  
214  
240#  
226#  
231  
219  
197#

NULL = 000000	199#																			
OPEN = 000000	146#																			
DTAS = 104420	181#	152	153	154	155	172	173	174	175	176	177	178	179							
PASCHT = 00034R	183#	183	185	186	188	189	190	199#												
PTRS = 00004	199#																			
POPS = 005726	199#																			
POPS2 = 022626	199#																			
PRTY = 000000	199#																			
PRTY0 = 000000	149#	150	199#																	
PRTY1 = 00040	199#																			
PRTY2 = 000100	199#																			
PRTY3 = 000140	199#																			
PRTY4 = 000200	199#																			
PRTY5 = 000240	199#																			
PRTY6 = 000300	199#																			
PRTY7 = 000340	199#																			
PS = 177776	199#																			
PSW = 177776	199#																			
PUSH = 005746	199#																			
PUSH2 = 074646	199#																			
RANOS = 104417	199#																			
RANUM = 000054R	168#																			
RFSRT = 000230R	187#	202#																		
RES1 = 000056R	170#																			
RES2 = 000060R	171#																			
RSTRT = 000112R	184#																			
SBADK = 000102R	180#	226*																		
SDFCNT = 000042R	163#																			
SDFRS = 104406	199#																			
SDFPAS = 000045R	165#																			
SPDINT = 000072R	159#																			
SPSIZ = 000040	157#	192																		
SRI = 000016H	152#																			
SR2 = 000020R	153#																			
SR3 = 000022R	154#																			
SR4 = 000024R	155#																			
START = 000224H	159#	200#																		
STAT = 000026R	158#																			
SVR0 = 000062R	172#																			
SVR1 = 000064R	173#																			
SVR2 = 000066R	174#																			
SVR3 = 000070R	175#																			
SVR4 = 000072R	175#																			
SVR5 = 000074R	174#																			
SVR6 = 000076R	178#																			
SVSCNT = 000052R	167#																			
TABEND = 001336R	210#																			
TRPFD = 000025R	180#	555#																		
VECTOR = 000010R	148#																			
WASADR = 000104R	182#	227*																		
WDFR = 000116R	189#																			
WDTN = 000114R	188#																			
XFLAG = 000005R	146#																			

. ARS. 000000 000  
 001340 001

ERRORS DETECTED: 0  
 DEFAULT GLOBALS GENERATED: 0  
 XRMGR0,XRMGR0/SOL/CRF:SYM=DOXCOM,XRMGR0  
 RUN-TIME: 11.1 SECONDS  
 RUN-TIME RATIO: 38/3=10.2  
 CORE USED: 7K (13 PAGES)





.REM -

IDENTIFICATION

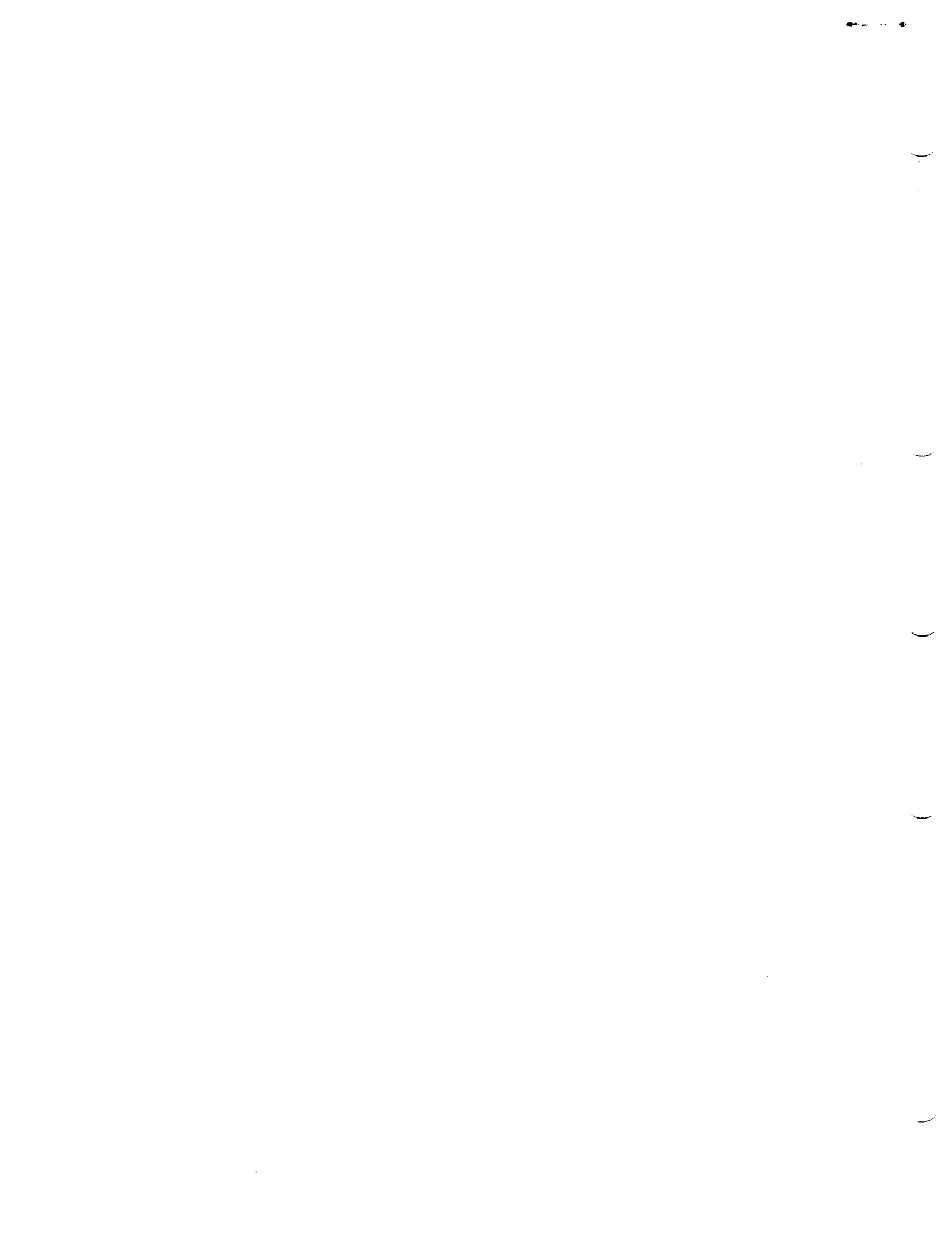
PRODUCT CODE: AC-F066B-MC  
PRODUCT NAME: CXBMFRO BM873-YH MODULE  
PRODUCT DATE: SEPTEMBER 1978  
MAINTAINER: DEC/X11 SUPPORT GROUP

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1.0 ABSTRACT

BMF IS A BACKGROUND MODULE THAT EXERCISES A SINGLE BM873-YH BOOTSTRAP ROM OPTION. IT COMPARES THE CONTENTS OF EACH OF THE 256(10) LOCATIONS STORED IN THE ROM WITH THE CONTENTS OF A 256(10) WORD CORE MEMORY BUFFER TO VERIFY THAT EACH LOCATION IN THE ROM CAN BE UNIQUELY ADDRESSED AND CONTAINS THE CORRECT DATA. ALL ERRORS ARE REPORTED VIA THE CONSOLE DEVICE.

2.0 REQUIREMENTS

HARDWARE: A PDP11 COMPUTER WITH A BM873-YH OPTION

STORAGE:: BMF REQUIRES:

1. DECIMAL WORDS: 368
2. OCTAL WORDS: 0560
3. OCTAL BYTES: 1340

3.0 PASS DEFINITION

THE INITIAL PASS CONSISTS OF EXECUTING THE BASIC TEST SEQUENCE ONE TIME BEFORE REPORTING END OF PASS. SUBSEQUENT PASSES OF THE BMFB MODULE CONSISTS OF 100(8) ITERATIONS OF THE BASIC TEST SEQUENCE DESCRIBED IN PARA. 7 BELOW.

4.0 EXECUTION TIME

PASS TIME VARIES DEPENDENT UPON CPU TYPE AND THE CONFIGURATION BEING EXERCISED. WHEN RUNNING ALONE ON A PDP11/40 THE FIRST PASS SHOULD TAKE LESS THAN 10 SECONDS AND SUBSEQUENT PASSES LESS THAN ONE MINUTE.

5.0 CONFIGURATION OPTIONS

DEFAULT PARAMETERS:

DVA: 173000

REQUIRED PARAMETERS:

NONE

6.0 DEVICE OPTION SETUP

NONE REQUIRED

7.0 MODULE OPERATION

TEST SEQUENCE:

1. R1 IS SET UP TO POINT TO THE FIRST WORD IN THE ROM
2. R2 IS SET UP TO POINT TO THE CORRESPONDING WORD IN THE CORE MEMORY BUFFER.

THE ADDRESS IN R1 IS CHECKED FOR EQUALITY TO EITHER 173024 OR 173224 AND IF FOUND EQUAL GOES TO STEP (5) - IF NOT IT PROCEEDS WITH STEP (3). THESE TWO ADDRESSES ARE NOT CHECKED BECAUSE THEIR CONTENTS AS READ ON THE BUS WILL VARY DEPENDENT UPON WHICH PARTICULAR "LOAD" BUTTON HAD BEEN INITIALLY DEPRESSED TO LOAD THE PROGRAM.

3. R1 AND R2 ARE USED TO COMPARE A ROM WORD WITH ITS CORE IMAGE COUNTERPART. IF THE WORDS DON'T COMPARE A SUB-ROUTINE IS CALLED TO SET UP THE ERROR INFORMATION AND REPORT IT VIA A "DATER" CALL TO THE MONITOR.
4. STEP (3) IS REPEATED.
5. R1 AND R2 ARE UPDATED TO POINT TO THE NEXT WORD AND A TEST MADE ON R2 TO SEE IF 256(10) WORDS HAVE BEEN CHECKED. IF YES, GO TO STEP (6) IF NOT REPEAT (3) THRU (5).
6. A PASS COUNTER IS DECREMENTED AND TESTED TO SEE IF 100(8) ITERATIONS OF STEPS (1) THRU (5) HAVE OCCURRED - IF YES GO TO STEP (7) IF NOT REPEAT (1) THRU (5).
7. REPORT END OF PASS AND REPEAT (1) THRU (6).

8.0 OPERATOR OPTIONS  
-----

(NONE)

9.0 NON-STANDARD PRINTOUTS  
-----

(NONE)

```

000000*          RKM0D <RMPB > 173000,100,155
000000*          MODULE 40020,173000,100,155
;          TITLE BMFB DEC/X11 SYSTEM EXERCISER MODULE
          DDACON VERSION 6 23-MAY-78
          .LIST BIN
*****
000000*          SCIN:
000000*          046502 041106 040 MODNAM: .ASCIT /RMPB / ;MODULE NAME
000000*          000000*          000 XPLAG: .BYTE OPEN ;USED TO KEEP TRACK OF WBMFB USAGE
000000*          173000 ADDR: 173000+0 ;1ST DEVICE ADDR.
000010*          000000 VECTOR: +0 ;1ST DEVICE VECTOR.
000010*          000 BR1: .BYTE PRTV+0 ;1ST RR LEVEL.
000010*          000 BR2: .BYTE PRTV+0 ;2ND RR LEVEL.
000010*          000001 DVID1: +1 ;DEVICE INDICATOR 1.
000010*          000009 SR1: OPEN ;SWITCH REGISTER 1
000020*          000000 SR2: OPEN ;SWITCH REGISTER 2
000020*          000000 SR3: OPEN ;SWITCH REGISTER 3
000020*          000000 SR4: OPEN ;SWITCH REGISTER 4
*****
000026*          040020 STAT: 40020 ;STATUS WORD.
000030*          000224*          000 INIT: START ;MODULE START ADDR.
000030*          000224*          000 SPOINT: MODSP ;MODULE STACK POINTFR.
000030*          000000 PASCNT: 0 ;PASS COUNTER.
000030*          000100 ICOUNT: 100 ;# OF ITERATIONS PER PASS=100
000040*          000000 ICOUNT: 0 ;LOC TO COUNT ITERATIONS
000040*          000000 SOFCNT: 0 ;LOC TO SAVE TOTAL SOFT ERRORS
000040*          000000 HRDCNT: 0 ;LOC TO SAVE TOTAL HARD ERRORS
000040*          000000 SUPPAS: 0 ;LOC TO SAVE SOFT ERRORS PER PASS
000040*          000000 HRPDAS: 0 ;LOC TO SAVE HARD ERRORS PER PASS
000050*          000000 SYSCNT: 0 ;# OF SYS ERRORS ACCUMULATED
000050*          000000 RANWUN: 0 ;HOLDS RANDOM # WHEN RAND MACRO IS CALLED
000050*          000000 CMPTG: ;RESERVED FOR MONITOR USE
000050*          000000 RES1: 0 ;RESERVED FOR MONITOR USE
000060*          000000 RES2: 0 ;RESERVED FOR MONITOR USE
000060*          000000 SVR0: OPEN ;LOC TO SAVE R0.
000060*          000000 SVR1: OPEN ;LOC TO SAVE R1.
000060*          000000 SVR2: OPEN ;LOC TO SAVE R2.
000070*          000000 SVR3: OPEN ;LOC TO SAVE R3.
000070*          000000 SVR4: OPEN ;LOC TO SAVE R4.
000070*          000000 SVR5: OPEN ;LOC TO SAVE R5.
000070*          000000 SVR6: OPEN ;LOC TO SAVE R6.
000100*          000000 CSRA: OPEN ;ADDR OF CURRENT CSR.
000100*          000000 SRADR: OPEN ;ADDR OF GOOD DATA, OR
000100*          000000 ACSR: OPEN ;CONTENTS OF CSR.
000100*          000000 WSADR: OPEN ;ADDR OF BAD DATA, OR
000100*          000000 ASTAT: OPEN ;STATUS HPG CONTENTS.
000100*          000000 ERRTYP: OPEN ;TYPE OF ERORR
000100*          000000 ASB: OPEN ;EXPECTED DATA.
000110*          000000 ANAS: OPEN ;ACTUAL DATA.
000110*          000230*          000 RSTRT: RSTRT ;RESTART ADDRESS AFTER END OF PASS
000110*          000000 WOTO: OPEN ;WORDS TO MEMORY PER ITERATION
000110*          000000 WOPR: OPEN ;WORDS FROM MEMORY PER ITERATION
000120*          000000 INTR: OPEN ;# OF INTERRUPTS PER ITERATION
000120*          000155 IDNUM: 155 ;MODULE IDENTIFICATION NUMBER=155
000040*          000040 .REPT SPSTZ ;MODULE STACK STARTS HERE.
          .NLIST

```

```

          .WORD 0
          .LIST
          .ENDD
000224*
MODSP:
;*****

```

203  
204 000224\* 016705 177556  
205  
206 000230\*  
207 000230\* 010501  
208 000232\* 012702 000336\*  
209 000236\* 031701 173024  
210 000242\* 031413  
211 000244\* 022701 173224  
212 000250\* 001410  
213 000252\* 031455  
214 000254\* 031455  
215 000256\* 004767 000026  
216 000262\* 021112  
217 000264\* 001767  
218 000266\* 004767 000016  
219 000272\* 022122  
220 000274\* 022702 001336\*  
221 000300\* 001356  
222 000302\*  
223 000302\* 104413 000000\*  
224  
225 000306\* 000750  
226  
227  
228  
229  
230  
231 000310\* 010267 177556  
232 000314\* 010167 177564  
233 000320\* 011167 177564  
234 000324\* 011267 177556  
235  
236 000330\* 104404 000000\*  
237  
238 000334\* 000207  
239  
240  
241  
242  
243  
244  
245 000336\*  
246 000336\* 010037  
247 000336\* 000040  
248 000342\* 013700  
249 000344\* 177570  
250 000346\* 032700  
251 000350\* 005001  
252 000352\* 001007  
253 000354\* 000513  
254  
255 000356\* 005000  
256 000360\* 000404  
257  
258 000362\* 173000

START: MOV ADDR,R5 ;GET FIRST ROM ADDRESS INTO R5  
RESTR: AGATN: MOV R5,R1 ;R1 POINTS TO ROM WORD  
MOV #BMTAB,R2 ;R2 POINTS TO ROM IMAGE IN CORE  
1S: CMP #173024,R1 ;ROM ADDRESS = 173024 ??  
BEQ #173224,R1 ;BR IF YES  
CMP #173224,R1 ;ROM ADDRESS = 173224 ??  
BFG #173224,R1 ;BR IF YES  
CMP (R1),(R2) ;CHECK ONE LOCATION  
BFG #173224,R1 ;BR IF (ROM) = (CORE)  
JSR PC,WMERR ;GO SETUP AND REPORT ERROR  
2S: CMP (R1),(R2) ;CHECK IT AGAIN  
BFG #173224,R1 ;BR IF (ROM) = (CORE)  
JSR PC,WMERR ;GO SETUP AND REPORT ERROR  
3S: CMP (R1)+(R2)+ ;ADD +2 TO BOTH POINTERS  
CMP #BMTABEND,R2 ;DONE LAST WORD ??  
BNE #173224,R1 ;BR IF NOT  
4S: ENDDITS,REGIN ;SIGNAL END OF ITERATION.  
BR AGATN ;MONITOR SHALL TEST END OF PASS

;THIS ROUTINE SETS UP AND REPORTS ALL DATA COMPARE ERRORS  
BMERR: MOV R2,SBADR ;SAVE THE ADDR. OF GOOD DATA  
MOV R1,WASADR ;SAVE ADDR. OF THE BAD DATA  
MOV (R1),AWAS ;GET WAS DATA  
MOV #BMTABEND,R2 ;SET UP DATA  
DATAERS,REGIN ;DATA ERROR!!!  
RTS PC ;CONTINUE CHECKING

;256(10) WORD TABLE THAT STORES A CORE IMAGE OF THE CONTENTS OF THE ROM

BMTAB: 010037 173000 010037 BUTON1: MOV R0,POTOR7+0 ;SAVE R0 IN LOCATION 40  
000040 173002 000040 ;GET SWITCH REGISTER  
013700 173004 013700 MOV SWR,R0  
032700 173006 032700 BIT #R10,R0 ;IS LOW-ORDER BIT SET?  
000001 173010 000001 BNE BUTONX ;YES-- LOOK AT CONTENTS  
001007 173014 001007 BR RFGSAV ;NO-- SAVE R1-R7 IN 42-5  
000513 173016 000513 BUTON3: CLR R0 ;SAVE LOAD FROM FLOPPY, 0  
005000 173020 005000 BR BUTONX ;GO TO COMMON CODE FOR 3  
000404 173022 000404 FILLTO 24  
173000 173024 173000 WORD RDMORG,PR7

259 000364\* 000340  
260 000366\* 012700  
261 000370\* 000200  
262  
263 000372\* 010005  
264 000374\* 106300  
265 000376\* 122700  
266 000378\* 000060  
267 000400\* 101001  
268 000402\* 101001 177556  
269 000404\* 005000  
270  
271 000406\* 000300  
272 000410\* 042700  
273 000412\* 177700  
274 000414\* 105705  
275 000416\* 100553  
276 000420\* 012737  
277 000422\* 173044  
278 000424\* 000004  
279 000426\* 005037  
280 000430\* 000006  
281  
282 000432\* 012706  
283 000434\* 000014  
284 000436\* 012701  
285 000440\* 173100  
286 000442\* 010003  
287  
288 000444\* 005705  
289 000446\* 100402  
290 000450\* 005306  
291 000452\* 002445  
292  
293 000454\* 000005  
294  
295 000456\* 032711  
296 000460\* 000040  
297 000462\* 001775  
298  
299 000464\* 010300  
300 000466\* 001402  
301 000470\* 012700  
302 000472\* 000020  
303  
304 000474\* 052700  
305 000476\* 000007  
306 000500\* 010102  
307 000502\* 010022  
308  
309 000504\* 105711  
310 000510\* 012712  
311 000512\* 000001  
312  
313 000514\* 105711  
314

000340 173026 000340 BUTON2: MOV #RIT7,R0 ;RIT 7 MEANS LOAD FROM R  
012700 173030 012700 BUTONX: MOV R0,R5 ;SAVE PARAMETER FOR ROOT  
000200 173032 000200 ASLR R0 ;LEFT-ALIGN SPEED FIELD  
100005 173034 010005 CMPR #3\*RIT4,R0 ;IS SPEED 0, 1, OR 2?  
106300 173036 106300 BHI 10S ;YES-- UNIT IS UNIT TO 0  
122700 173040 122700 CLR R0 ;NO-- USE UNIT #0  
000060 173042 000060 10S: SWAB R0 ;GET UNIT # IN LOW BYTE  
101001 173044 101001 BIC #C7,R0 ;TRIM TO 3 BITS 2, 1, 0  
005000 173046 005000 TSTR R5 ;WHERE SHOULD WE ROOT PP  
000300 173050 000300 BMI RPBOOT ;RIT 7 = 1 -- BOOT FROM  
042700 173052 042700 MOV #TCROOT,4 ;SET TIMEOUT TRAP TO TRY  
05705 173054 05705 CLR 6 ; - -  
100402 173056 100402 RXBOOT: MOV #RETRY,SP ;SET RETRY COUNT  
005306 173060 005306 MOV #RXEPA+RXCS,R1 ;ADDRESS CONTROL STATUS  
002445 173104 002445 MOV R0,R3 ;COPY UNIT #  
000005 173106 000005 RXRTRY: TST R5 ;INDEFINITE RETRY?  
100402 173110 100402 BMI RXRSET ;YES-- TRY FAITHFULLY  
005306 173112 005306 DEC SP ;NO-- DECREMENT RETRY CO  
002445 173114 002445 BLT RXEHLT ;GIVE UP IF RUN OUT  
000005 173116 000005 RXRSET: RESET ;CLEAR THE WORLD  
032711 173120 032711 20S: BIT #RXDONE,(R1) ;WAIT UNTIL READY FOR FU  
000040 173122 000040 BEQ 20S ;NOT YET-- WAIT  
001775 173124 001775 RXPERF: MOV R3,P0 ;GET UNIT #  
010300 173126 010300 BFG #5 ;ZERO-- USE ZERO  
001402 173130 001402 MOV #RXUNIT,R0 ;NON-ZERO-- ASSUME UNIT  
012700 173134 000020 5S: BIS #PXREAD+RXGO,R0 ;SET READ FUNCTION  
052700 173136 052700 MOV R1,R2 ;COPY ADDRESS OF RXCS  
000007 173140 000007 MOV R0,(R2)+ ;START READ FUNCTION, R2  
105711 173146 105711 10S: TSTR (R1) ;READY?  
100402 173150 100402 BPL 10S ;NO-- WAIT  
012712 173152 012712 MOV #1,(R2) ;SET SECTOR #  
000001 173154 000001 20S: TSTR (R1) ;READY FOR TRACK?



427	001010*	008021	000021	173452	000021	CLR	RPDC(R1)	;	SET CYLINDER 0	
428	001010*	008021	005061	173454	005061	CLR	RPDA(R1)	;	TRACK 0, SECTOR 0	
429	001014*	000034	000034	173456	000034	CLR	RPDA(R1)	;	TRACK 0, SECTOR 0	
430	001016*	005061	005061	173460	005061	CLR	RPDA(R1)	;	TRACK 0, SECTOR 0	
431	001020*	000006	000006	173462	000006	CLS	R2,RPDC(R1)	;	SET INHIBIT ECC, 22-SEC	
432	001024*	000032	000032	173464	000032	BTS	R2,RPDC(R1)	;	SET INHIBIT ECC, 22-SEC	
433	001024*	000032	000032	173466	000032	BTS	R2,RPDC(R1)	;	SET INHIBIT ECC, 22-SEC	
434	001024*	014761	014761	173470	014761	MOV	#-256.,RPWC(R1)	;	SET UP WORD COUNT TO PR	
435	001030*	000002	000002	173472	000002	MOV	#-256.,RPWC(R1)	;	SET UP WORD COUNT TO PR	
436	001030*	000002	000002	173474	000002	MOV	#-256.,RPWC(R1)	;	SET UP WORD COUNT TO PR	
437	001034*	012711	012711	173476	012711	MOV	#RPREAD+RPGD,(R1)	;	START READ FUNCTION	
438	001036*	000071	000071	173478	000071	MOV	#RPREAD+RPGD,(R1)	;	START READ FUNCTION	
439	001040*	105711	105711	173502	105711	20S:	TSTR	(R1)	;	READY?
440	001042*	100376	100376	173504	100376	BPL	(R1)	;	NO-- WAIT UNTIL IT IS	
441	001042*	100376	100376	173504	100376	BPL	(R1)	;	NO-- WAIT UNTIL IT IS	
442	001044*	032761	032761	173506	032761	BIT	#RPFER,RPER1(R1)	;	FORMAT ERROR?	
443	001046*	000020	000020	173510	000020	BIT	#RPFER,RPER1(R1)	;	FORMAT ERROR?	
444	001048*	000014	000014	173514	000014	BIT	#RPFER,RPER1(R1)	;	FORMAT ERROR?	
445	001052*	001403	001403	173514	001403	BEQ	30S	;	NO-- TRY AGAIN	
446	001054*	052702	052702	173516	052702	BTS	#RPFM22,R2	;	YES-- TRY FOR 22 SECTOR	
447	001056*	010000	010000	173522	010000	BR	RPRTRY	;	TRY AGAIN	
448	001060*	000740	000740	173524	000740	BR	RPRTRY	;	TRY AGAIN	
449	001062*	032711	032711	173524	032711	BIT	#RPTREIRPMCPPE,(R1)	;	TRANSFER OR MRC PART	
450	001064*	060000	060000	173524	060000	BIT	#RPTREIRPMCPPE,(R1)	;	TRANSFER OR MRC PART	
451	001064*	032711	032711	173524	032711	BIT	#RPTREIRPMCPPE,(R1)	;	TRANSFER OR MRC PART	
452	001066*	000000	000000	173524	000000	BNE	RPRTRY	;	YES-- ERROR-- TRY AGAIN	
453	001066*	000000	000000	173524	000000	BNE	RPRTRY	;	YES-- ERROR-- TRY AGAIN	
454	001072*	140000	140000	173532	140000	BIT	#RPATAIRPERR,RPOS(R1)	;	ATTN OR OTHER PRR	
455	001074*	000012	000012	173534	000012	BIT	#RPATAIRPERR,RPOS(R1)	;	ATTN OR OTHER PRR	
456	001076*	001331	001331	173534	001331	BIT	#RPATAIRPERR,RPOS(R1)	;	ATTN OR OTHER PRR	
457	001100*	005007	005007	173542	005007	CLR	PC	;	JMP 0	
458	001102*	016100	016100	173544	016100	RPEHLT:	MOV	RPDS(R1),R0	;	DISPLAY DRIVE STATUS
459	001102*	016100	016100	173544	016100	RPEHLT:	MOV	RPDS(R1),R0	;	DISPLAY DRIVE STATUS
460	001104*	000012	000012	173546	000012	HALTED:	HALT		;	DTE
461	001106*	000000	000000	173550	000000	HALT	BR		;	STAY DEAD
462	001110*	000776	000776	173552	000776	BR	RO,ROTUR7+0	;	SAVE RO IN 40	
463	001110*	000776	000776	173552	000776	BR	RO,ROTUR7+0	;	SAVE RO IN 40	
464	001112*	010037	010037	173554	010037	MOV	#10S,R0	;	SET RETURN ADDRESS IN R	
465	001114*	000040	000040	173556	000040	MOV	REGSAV	;	SAVE R1-R7	
466	001116*	000040	000040	173556	000040	MOV	REGSAV	;	SAVE R1-R7	
467	001118*	014566	014566	173562	014566	BR	R5	;	ADDRESS LOCATION ZERO	
468	001122*	000630	000630	173564	000630	BR	(R5)+,R0	;	SAVE 0 IN R0	
469	001124*	005005	005005	173566	005005	MOV	(R5)+,R1	;	SAVE 2 IN R1	
470	001126*	012500	012500	173566	012500	MOV	(R5)+,R2	;	SAVE 4 IN R2	
471	001130*	012501	012501	173572	012501	MOV	(R5)+,R3	;	SAVE 4 IN R3	
472	001130*	012501	012501	173572	012501	MOV	(R5)+,R4	;	SAVE 4 IN R4	
473	001132*	011502	011502	173574	011502	MOV	#21S,(R5)+	;	SET XRM TRAP ADDRESS IN	
474	001134*	014762	014762	173576	014762	MOV	(R5),R3	;	SAVE 6 IN R3	
475	001134*	014762	014762	173576	014762	MOV	(R5),R3	;	SAVE 6 IN R3	
476	001140*	011503	011503	173602	011503	CLR	(R5)	;	SET PS FOR TRAP	
477	001140*	005015	005015	173604	005015	CLR	(R5)	;	SET PS FOR TRAP	
478	001144*	012704	012704	173606	012704	20S:	MOV	#DLVNT-DTESIZ,R4	;	POINT TO DTE # -1'S D
479	001144*	005015	005015	173606	005015	20S:	MOV	#DLVNT-DTESIZ,R4	;	POINT TO DTE # -1'S D
480	001144*	012704	012704	173610	012704	20S:	MOV	#DLVNT-DTESIZ,R4	;	POINT TO DTE # -1'S D
481	001144*	012704	012704	173610	012704	20S:	MOV	#DLVNT-DTESIZ,R4	;	POINT TO DTE # -1'S D
482	001146*	174340	174340	173610	174340	20S:	MOV	#DLVNT-DTESIZ,R4	;	POINT TO DTE # -1'S D

483	001150*	012706	012706	173612	012706	21S:	MOV	#4,SP	;	SET SP TO 4, STACK IS L
484	001152*	000004	000004	173614	000004	21S:	MOV	#4,SP	;	SET SP TO 4, STACK IS L
485	001152*	000004	000004	173614	000004	21S:	MOV	#4,SP	;	SET SP TO 4, STACK IS L
486	001154*	062704	062704	173616	062704	22S:	ADD	#DTEISZ,R4	;	RUMP TO NEXT DTE'S EXT
487	001156*	000040	000040	173618	000040	22S:	ADD	#DTEISZ,R4	;	RUMP TO NEXT DTE'S EXT
488	001156*	000040	000040	173618	000040	22S:	ADD	#DTEISZ,R4	;	RUMP TO NEXT DTE'S EXT
489	001162*	100770	100770	173622	100770	TSTR	R4	;	IS THIS THE END OF THE	
490	001162*	100770	100770	173622	100770	20S:	BMI	#T011DB,STAT-DLVNT(R4)	;	DOORBELL RINGIN
491	001164*	032764	032764	173626	032764	BIT	#T011DB,STAT-DLVNT(R4)	;	DOORBELL RINGIN	
492	001166*	004000	004000	173628	004000	BIT	#T011DB,STAT-DLVNT(R4)	;	DOORBELL RINGIN	
493	001168*	000034	000034	173632	000034	BIT	#T011DB,STAT-DLVNT(R4)	;	DOORBELL RINGIN	
494	001172*	001770	001770	173634	001770	BFQ	22S	;	NO-- TRY NEXT DTE	
495	001174*	026417	026417	173636	026417	CMP	T010BC-DLVNT(R4),(PC)	;	DOES THIS ONE HA	
496	001176*	000014	000014	173640	000014	MOV	R3,(R5)	;	NO-- TRY ANOTHER DTE	
497	001178*	001364	001364	173642	001365	MOV	R2,(R5)	;	RESTORE LOCATION 6	
498	001202*	010315	010315	173644	010315	MOV	R1,(R5)	;	4	
499	001204*	010245	010245	173646	010245	MOV	R0,(R5)	;	2	
500	001206*	010145	010145	173650	010145	MOV	R0,(R5)	;	0	
501	001210*	010045	010045	173652	010045	MOV	#DTESAV,R0	;	POINT TO SAVE AREA	
502	001212*	012700	012700	173654	012700	MOV	#DTESAV,R0	;	POINT TO SAVE AREA	
503	001214*	000130	000130	173656	000130	MOV	#DTESAV,R0	;	POINT TO SAVE AREA	
504	001216*	012420	012420	173660	012420	29S:	MOV	(R4)+(R0)+	;	SAVE A REGISTER
505	001220*	022700	022700	173662	022700	29S:	CMP	#T011DB-DLVNT+DTESAV,R0	;	FINISHED?
506	001222*	000156	000156	173664	000156	BRIS	29S	;	NO-- SAVE SOME MORE	
507	001222*	000156	000156	173664	000156	BRIS	29S	;	NO-- SAVE SOME MORE	
508	001224*	103374	103374	173666	103374	ADDX	DIAC2-T011DB-2,R4	;	R4 POINTS TO DIAC2 RE	
509	001226*	005724	005724	173670	005724	TST	(R4)+	;	SO DOES R1	
510	001230*	010401	010401	173672	010401	MOV	R4,R1	;	SETUP R0 FOR "DIAGNOSTI	
511	001230*	010401	010401	173672	010401	MOV	#DRESET,R0	;	SETUP R0 FOR "DIAGNOSTI	
512	001232*	012700	012700	173674	012700	MOV	R0,(R1)+	;	R1 POINTS TO STATUS REC	
513	001234*	000100	000100	173676	000100	CLR	DLVNT-STAT(R1)	;	SET DTEZ FOR MAXIMUM D	
514	001236*	000100	000100	173678	000100	CLR	DLVNT-STAT(R1)	;	SET DTEZ FOR MAXIMUM D	
515	001240*	005061	005061	173700	005061	MOV	R0,(R1)+	;	R1 POINTS TO STATUS REC	
516	001242*	177744	177744	173702	005061	CLR	DLVNT-STAT(R1)	;	SET DTEZ FOR MAXIMUM D	
517	001244*	005061	005061	173704	177744	CLR	T010AD-STAT(R1)	;	START DUMPING -11 MEMO	
518	001246*	177744	177744	173706	005061	CLR	T010AD-STAT(R1)	;	START DUMPING -11 MEMO	
519	001250*	032711	032711	173712	032711	30S:	BIT	#T011DB,(R1)	;	IS DOORBELL RINGING (TR
520	001252*	004000	004000	173714	032711	30S:	BIT	#T011DB,(R1)	;	IS DOORBELL RINGING (TR
521	001254*	001775	001775	173716	001775	BEQ	30S	;	NO-- WAIT FOR DOORBELL	
522	001256*	010014	010014	173720	001775	MOV	R0,(R4)	;	YES-- CLEAR DOORBELL AN	
523	001260*	005061	005061	173722	005061	CLR	T011AD-STAT(R1)	;	START INPUT TO LOCATION	
524	001260*	005061	005061	173722	005061	CLR	T011AD-STAT(R1)	;	START INPUT TO LOCATION	
525	001264*	177766	177766	173726	177766	MOV	#FLOPI<<-256.>&7777>,T011BC-STAT(R1)	;	2	
526	001264*	177766	177766	173726	177766	MOV	#FLOPI<<-256.>&7777>,T011BC-STAT(R1)	;	2	
527	001268*	107400	107400	173730	107400	MOV	#FLOPI<<-256.>&7777>,T011BC-STAT(R1)	;	2	
528	001270*	177762	177762	173732	177762	MOV	#FLOPI<<-256.>&7777>,T011BC-STAT(R1)	;	2	
529	001272*	032711	032711	173734	032711	40S:	BIT	#T011DB,(R1)	;	TRANSFER COMPLETE?
530	001274*	000200	000200	173736	000200	40S:	BIT	#T011DB,(R1)	;	TRANSFER COMPLETE?
531	001276*	001775	001775	173740	001775	BEQ	40S	;	NO-- WAIT UNTIL DONE	
532	001276*	001775	001775	173740	001775	BEQ	40S	;	NO-- WAIT UNTIL DONE	
533	001300*	005007	005007	173742	005007	FILET	1000	;	GO TO LOADED CODE, STAR	
534	001302*	000000	000000	173744	000	FILET	0	;	GO TO LOADED CODE, STAR	
535	001302*	000000	000000	173744	000	FILET	0	;	GO TO LOADED CODE, STAR	
536	001304*	000000	000000	173746	000	FILET	0	;	GO TO LOADED CODE, STAR	
537	001304*	000000	000000	173746	000	FILET	0	;	GO TO LOADED CODE, STAR	
538	001304*	000000	000000	173746	000	FILET	0	;	GO TO LOADED CODE, STAR	



539	001306"	000000	000000	173750	000	-BYT	0
540				173751	000	-BYT	0
541	001310"	000000	000000	173752	000	-BYT	0
542				173753	000	-BYT	0
543	001312"	000000	000000	173754	000	-BYT	0
544				173755	000	-BYT	0
545	001314"	000000	000000	173756	000	-BYT	0
546				173757	000	-BYT	0
547	001316"	000000	000000	173760	000	-BYT	0
548				173761	000	-BYT	0
549	001320"	000000	000000	173763	000	-BYT	0
550				173764	000	-BYT	0
551	001322"	000000	000000	173765	000	-BYT	0
552				173766	000	-BYT	0
553	001324"	000000	000000	173767	000	-BYT	0
554				173768	000	-BYT	0
555	001326"	000000	000000	173770	000	-BYT	0
556				173771	000	-BYT	0
557	001330"	000000	000000	173772	000	-BYT	0
558				173773	000	-BYT	0
559	001332"	000000	000000	173774	000	-BYT	0
560				173775	000	-BYT	0
561	001334"	000000	000000	173776	000	-BYT	0
562				173777	000	-BYT	0
563	001336"	177777	TABEND: 177777	174000			
564							
565	000001		-END				

ACSP	000102R	185#	
ADDR	00005R	151#	204
ADDR22=	00100	203#	
ACATN	000230R	207#	225
ASB	000106R	189#	233*
ASTAT	000104R	187#	
AWAS	00010R	190#	232*
BECTN	000000R	148#	223
BIT0	= 000001	203#	235
BIT1	= 000002	203#	
BIT10	= 002000	203#	
BIT11	= 004000	203#	
BIT12	= 010000	203#	
BIT13	= 020000	203#	
BIT14	= 040000	203#	
BIT15	= 100000	203#	
BIT2	= 000004	203#	
BIT3	= 000010	203#	
BIT4	= 000020	203#	
BIT5	= 000040	203#	
BIT6	= 000100	203#	
BIT7	= 000200	203#	
BIT8	= 000400	203#	
BIT9	= 001000	203#	
BWERK	000310R	215	230#
BMTAB	000336R	208	245#
BREAKS=	104407	203#	
BR1	000012R	153#	
BR2	000013R	154#	
BTODS	= 104421	203#	
CDATAS	= 104413	203#	
CONFIC	000056R	173#	
CSRA	000100R	193#	
DATCKS	= 104411	203#	
DATRHS	= 104408	203#	235
DVID1	000014R	155#	
ENDITS	= 104413	203#	223
EVDS	= 104410	203#	
EQRTYP	000106R	188#	
EXITS	= 104400	203#	
GETPAS	= 104415	203#	
GWBWFS	= 104414	203#	
HRDCMT	000044R	168#	
HRDRS=	104405	203#	
HRDPA=	000050R	170#	
ICUMT	000036R	165#	
ICUMT	000040R	166#	
IDNUM	000122R	195#	
INT	000030R	162#	
INTD	000120R	194#	
NAP22S	= 104416	203#	
MODNAM	000000R	149#	
MODSP	000224R	163	201#
MSGNS	= 104403	203#	
MSGSS	= 104402	203#	
MSG	= 104401	203#	

NOLL = 000000	203#																		
OPEN = 000000	150#	156	157	158	159	176	177	178	179	180	181	182	183						
OTDAS = 104420	187#																		
PASCMT = 000034R	164#																		
PTWOS = 000004	203#																		
PDSP = 002622	203#																		
PDSP2 = 022622	203#																		
PRTV = 000000	153#	154	203#																
PRTV0 = 000000	203#																		
PRTV1 = 000040	203#																		
PRTV2 = 000140	203#																		
PRTV3 = 000140	203#																		
PRTV4 = 000200	203#																		
PRTV5 = 000240	203#																		
PRTV6 = 000300	203#																		
PRTV7 = 000340	203#																		
PS = 177776	203#																		
PSM = 177776	203#																		
PUSH = 005746	203#																		
PUSH2 = 024646	203#																		
RANDS = 104417	203#																		
RANNUM = 000054R	172#																		
RESRPT = 000730R	193#	206#																	
RES1 = 000056R	174#																		
RES2 = 000060R	175#																		
RSTRT = 000112R	191#																		
SADR = 000102R	188#	230*																	
SOPCNT = 000042R	167#																		
SOPFRS = 104405	203#																		
SOPPAS = 000045R	169#																		
SPTMT = 000030R	163#																		
PSIZ = 000040	161#	196																	
SR1 = 000016R	156#																		
SR2 = 000020R	157#																		
SR3 = 000022R	158#																		
SR4 = 000024R	159#																		
START = 000224R	162#	204#																	
STA1 = 000026R	161#																		
SVR0 = 000062R	176#																		
SVR1 = 000064R	177#																		
SVR2 = 000066R	178#																		
SVR3 = 000070R	179#																		
SVR4 = 000072R	180#																		
SVR5 = 000074R	181#																		
SVR6 = 000076R	182#																		
SYSGMT = 000052R	171#	563#																	
TBRND = 001336R	220#																		
TOPEND = 000025R	203#																		
VECTOR = 000010R	152#																		
WASADR = 000104R	186#	231*																	
WDFP = 000110R	193#																		
WDT0 = 000116R	192#																		
XFLAG = 000005R	150#																		

. ARS. 000000 000  
 001340 001

ERRORS DETECTED: 0  
 DEFAULT GLOBALS GENERATED: 0

XMPFB0,XMPFB0/SOL/CRP:SYM=DDXCOM,XMPFB0  
 RUN-TIME: 1 1 . 2 SECONDS  
 RUN-TIME RATIO: 21/3=5  
 CORE USED: 7K (13 PAGES)