

IDENTIFICATION

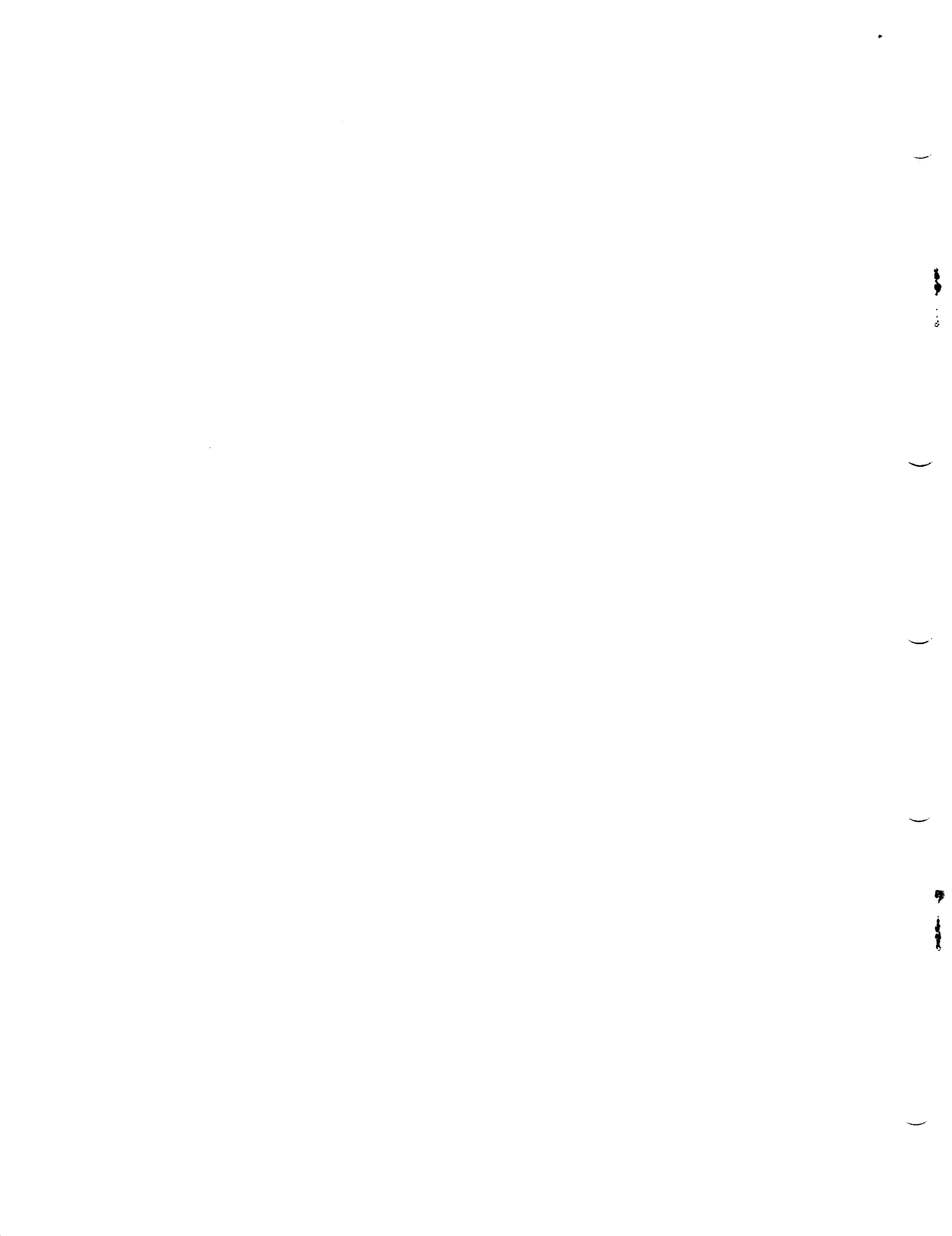
PRODUCT CODE: AC-F858B-MC
PRODUCT NAME: CXDMRBO DMR-11 MODULE
PRODUCT DATE: JUNE 1980
MAINTAINER: DEC/X11 SUPPORT GROUP

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1. ABSTRACT

DMR IS AN IUMOD THAT EXERCISES UP TO AND INCLUDING TWO CONSECUTIVELY ADDRESSED AND CONSECUTIVELY VECTORED DMR11 SYNCHRONOUS INTERFACES. THIS MODULE EXERCISE MAY BE RUN WITH LINE UNIT LOOPBACK (ITL LOOPBACK). ADDITIONALLY IF TWO DMRS ARE TO BE EXERCISED, THE OPERATOR MAY SELECT TO PERFORM AN ACTUAL LINK EXERCISE WITH ACTUAL CABLE CONNECTION.

A SET DATA PATTERN IS USED FOR DATA TRANSMISSION. THE RECEIVER AND TRANSMITTER ISR ARE ALWAYS AT PRIORITY FIVE (BR1, BR2). DATA CHECKING IS PERFORMED AT LEVEL 0. THIS CHECKING IS DONE OUTSIDE THE ISRS AND ONLY ON THE LAST BUFFER RECEIVED BECAUSE A COMMON IS USED FOR ALL RECEIVED BUFFERS.

CXDMRB IS A REVISION THAT SOLVES 4 PROBLEMS IN REV A (CXDMRA) THESE PROBLEMS WERE

- A. A PASS IN INTERNAL LOOPBACK TOOK MORE THAN 1 MINUTE
- B. AN ILLEGAL INSTRUCTION FOR AN 11/20 (SOB) WAS USED.
- C. WHEN 2 DMRS WERE EXERCISED IN DMR MODE, A FLAG CONTENTION COULD OCCUR.
- D. A HUNG CONDITION COULD OCCUR UNDER CERTAIN CONDITIONS IF THE MODULE WERE RELOCATED BETWEEN ITERATIONS.

2. REQUIREMENTS

HARDWARE:

1. M8207-RA (MICROPROCESSOR)
2. M8203 (LINE UNIT)

STORAGE:: DMR REQUIRES:

1. DECIMAL WORDS: 2190.
2. OCTAL WORDS: 4216
3. OCTAL BYTES: 10434

3. PASS DEFINITION

ONE PASS OF THE DMR MODULE CONSISTS OF TRANSMITTING AND RECEIVING ALL BUFFERS 25 ITERATIONS FOR EACH SELECTED DEVICE. THE BUFFER NUMBER IS 7 WHEN IN DMC MODE AND 64. WHEN IN DMR MODE. BUFFER SIZE IS VARIED FROM 1 TO 512. BYTES BY THE PROGRAM.

4. EXECUTION TIME

RUNNING ALONE ON AN 11/40, ONE PASS TAKES APPROXIMATELY ONE MINUTE IN INTERNAL LOOPBACK

5. CONFIGURATION PARAMETERS.

DEFAULT PARAMETERS:

ADDR: 1, VECTOR: 1, BR1: 5, BR2: 5, DVID1: 1, SR1:0
DMR WILL RUN UP TO TWO CONSECUTIVELY ADDRESSED AND CONSECUTIVELY VECTORED DMR11'S.

SR1:
BIT 0 = 1 - TURN AROUND CONNECTOR
 = 0 - LINE UNIT LOOPBACK (TTL LOOPBACK)

BIT 1 = 1 - DMR MODE
 = 0 - DMC MODE

BIT 2 = 1 - LINK MODE (BIT 0 IGNORED)
 = 0 - NON-LINK

SR4: (VVALID ONLY IN DMR MODE)
BIT 0 = 1 - MODEM WRITE
BIT 1 = 1 - ENABLE EXTENDED ERROR
BIT 2 = 1 - DISABLE EXTENDED ERROR
BIT 3 = 1 - DESELECT DMC LINE
BIT 4 = 1 - REQUEST BASE TABLE UPDATE
BIT 5 = 1 - SET REP/SEL TIMER
BIT 6 = 1 - SET THRESHOLD VALUES
BIT 7 = 1 - READ M8207 RAM

CAUTION - UNDERSTAND THE EFFECT OF THIS
 COMMAND BEFORE USING IT.

BIT 8 = 1 - WRITE AX3-15 INTERFACE

BIT 9 = 1 - NOP1
BIT 10 = 1 - READ MODEM (NOP)

IF ANY OF THE FOLLOWING DMR COMMANDS ARE USED COMMAND VALUES ARE
GIVEN AT LOCATION 300

6. DEVICE/OPTION SETUP

CONFIGURE MODULE WITH CORRECT PARAMETERS.

NOTE: SR1 CAN BE SET UP AT CONFIGURATION TIME OR
 AT RUN TIME WITH A MOD COMMAND.

THE INTERRELATION OF CERTAIN PARAMETERS SHOULD BE UNDERSTOOD.

1. THERE MUST BE 2 DEVICES FOR A LINK EXERCISE
2. IN A LINK EXERCISE BIT 0 OF SR1 (TURN AROUND) IS
 IGNORED (AN ACTUAL CONNECTION IS ASSUMED).
3. DMR COMMANDS SPECIFIED IN SR4 CAN ONLY BE PERFORMED
 WHEN THE DMR11 IS IN DMR MODE.
4. VALUES USED FOR CERTAIN DMR COMMANDS ARE LOCATED AT
 LOCATION 300. BEFORE CHANGING THOSE VALUES REALIZE
 THE EFFECT IT MAY HAVE ON THE EXERCISE. NOTE: THAT
 TO ACTUALLY USE THE DMR COMMAND TO WRITE THE AX3-15
 INTERFACE, UNDERSTAND THE EFFECT OF THE AX3 VARIABLE
 BEFORE USING THE COMMAND.

7. MODULE OPERATION

1. LOAD SOFTWARE POINTERS IN LINK TABLE.
2. LOAD VECTORS AND PRIORITIES IN TABLE
3. ENABLE SELECTED DEVICES.
4. IF FIRST PASS, PERFORM A BASE IN AND CONTROL IN. ALSO IF IN DMK MODE, PERFORM ANY REQUESTED DMR COMMANDS.
5. COMMENCE BUFFER TRANSACTIONS.
6. SCAN FOR ALL DEVICES TO FINISH BUFFER TRANSACTIONS.
7. IF NOT DONE GO TO 5.
IF HUNG REPORT SO AND DROP HUNG DEVICE.
8. IF NOT LAST ITERATION, GO TO 10
9. AFTER LAST ITERATION, HALT THE DMR AND WAIT FOR THE HALT TO BE COMPLETED.
10. CHECK DATA FOR ALL DEVICES SELECTED.
11. DECREMENT ITERATION COUNT
12. IF NOT = 0 GO TO 1
13. SIGNAL ENDPASS.

IISR: INPUT INTERRUPT SERVICE ROUTINE.

11. GET INTERRUPTING DMR CSR.
12. IF BASE IN WAS REQUESTED, LOAD BASE ADDRESS. IF NOT FIRST ITERATION OF THE PASS SET THE BASE IN RESUME BIT.
13. IF IN DMR MODE AND IF DMR COMMANDS REQUESTED, PERFORM ANY AND ALL DMR COMMANDS.
14. IF RECEIVE BA/CC WAS REQUESTED, LOAD REC BA/CC.
15. IF XMIT BA/CC WAS REQUESTED, LOAD XMIT BA/CC.
16. IF LAST RCV OR XMIT BA/CC IN SET THE APPROPRIATE BIT IN THE END OF PASS FLAG.
17. RTI

OISR: OUTPUT INTERRUPT SERVICE ROUTINE.

01. GET INTERRUPTING DMR CSR
02. IF ERROR, REPORT IT AND EXIT.
03. IF XMIT DONE OR REC DONE, ISSUE A HALT (PROCEDURE ERROR).
04. AFTER RECEIVING THE CONTROL OUT FROM THE HALT (PROCEDURE ERROR), SET THE BIT FOR THE ENDPASS FLAG
05. RTI

9. NON-STANDARD PRINTOUTS

IF THE MODULE "HANGS" IN WHICH NOT ALL SELECTED DEVICES HAVE FINISHED, THEN A "HUNG" MESSAGE IS PRINTED OUT. CHECK THE ENDPASS FLAGS FOR EACH SELECTED DEVICE IN THE LINK TABLE TO DETERMINE WHICH DEVICE FAILED TO FINISH AND HOW FAR IT GOT.

FOR EXAMPLE:

THE TWO ENDPASS FLAGS ARE LOCATED IN THE LINK TABLE (INTLNK) AT THE FOLLOWING LOCATIONS.

XX11: 4660
XX21: 4722

ONLY BITS 0 THRU 4 ARE USED AND ARE DEFINED AS FOLLOWS:

BIT0 = 1	THE BASE ADDRESS WAS LOADED.
BIT1 = 1	ALL BA/CC IN RECEIVE BUFFERS LOADED
BIT2 = 1	ALL BA/CC IN TRANSMIT BUFFERS LOADED.
BIT3 = 1	ALL BA/CC OUT TRANSMITS WERE RECEIVED.
BIT4 = 1	ALL BA/CC OUT RECEIVES WERE RECEIVED.
BIT5 = 1	HALT DONE (OR END OF ITERATION)

A CORRECT END PASS FLAG = 77, WHEN THE ENDPASS FLAGS = 77 FOR THE SELECTED DEVICES, THE DATA IS CHECKED. IF A "HUNG" MESSAGE IS TYPED IT IS BECAUSE ONE OR BOTH DEVICES DID NOT FINISH. TO FIND WHICH ONE, CHECK THE END PASS FLAGS, ANY THAT ARE NOT EQUAL TO 77 ARE THE HUNG DEVICES. CHECK WHICH BITS OF THE ENDPASS FLAG ARE CLEAR TO SEE WHAT IT WAS TRYING TO DO.

SOFT ERROR

IF THE DMR'S PROTOCOL CHECKERS DETECT AN ERROR IN THE TRANSMISSION OF A MESSAGE, IT WILL RETRANSMIT THE ENTIRE MESSAGE, UPDATING AN ERROR COUNTER IN ITS RAM. IF THIS COUNTER EXCEEDS 7 ON ANY GIVEN MESSAGE, IT WILL DECLARE A HARD ERROR. HOWEVER, IF FEWER THAN 7 OCCUR, IT WILL TAKE NO NOTICE OF THE CONDITION. NOTE: THIS THRESHOLD OF 7 MAY BE CHANGED IN DMR MODE BY CHANGING THE NAKS THRESHOLD. FOR DEC/X11 PURPOSES, HOWEVER, THE DMR MODULE WILL CHECK THE ERROR COUNTER AFTER EACH MESSAGE; IF IT HAS BEEN INCREMENTED AT ALL, I.E., IF AT LEAST ONE RE-TRANSMISSION WAS MADE, DMR WILL DECLARE A SOFT ERROR.

THE SOFT ERROR MESSAGE MAY INDICATE AN INTERMITTANT DEVICE FAILURE OR OTHER HARDWARE PROBLEM; HOWEVER, IF THE MESSAGE OCCURS IN A HEAVILY LOADED SYSTEM, IT MAY BE THAT THE PROBLEM IS DUE TO BUS LATENCY (THE DMR-11 DOES NOT RECOGNISE A DISTINCT "DATA LATE" ERROR--IT CONSIDERS THE CONDITION MERELY ANOTHER TRANSMISSION PROBLEM). ESPECIALLY IF THERE ARE OTHER FAST DIRECT MEMORY ACCESS DEVICES SELECTED, IT COULD BE THAT THE DMR-11'S NPR'S ARE NOT BEING HONORED QUICKLY ENOUGH TO PREVENT BIT-DROPPING.

TO VERIFY WHETHER THIS IS THE CONDITION, RUN A SINGLE DMR MODULE, WITH A SINGLE DMR-11 DEVICE SELECTED. THE SOFT ERROR MESSAGE SHOULD NOT OCCUR UNDER THESE CONDITIONS. IF IT DOES OCCUR, THE PROBLEM IS PROBABLY IN DMR-11 HARDWARE OR A

DMRB DEC/X11 SYSTEM EXERCISER M MACRO V03.01 16-JUN-80 18:23:50 PAGE 4-1
DEC/X11-1 SYSTEM EXERCISER MACRO DEFINITION MODULE

SEQ 6

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DMRB DEC/X11 SYSTEM EXERCISER MACRO DEFINITION MODULE
1 000000
000000
      IUMOD <DMRB >,1,1,5,5,0,25,,127
      MODULE 140000,DMRB ,1,1,5,5,0,25,,127
      .TITLE DMRB DEC/X11 SYSTEM EXERCISER MODULE
      ;
      UDXCUM VERSION 6      23-MAY-78
      .LIST HIN
;*****
BEGIN:
000000      104      115      122      MUDNAM: .ASCII /DMRB / ;MODULE NAME.
000003      102      040
000005      000
      XFLAG: .BYTE OPEN ;USED TO KEEP TRACK OF WRUFF USAGE
000006      000001      ;1ST DEVICE ADDR.
000010      000001      VECTOR: 1+0 ;1ST DEVICE VECTOR.
000012      240      BR1: .BYTE PR1Y5+0 ;1ST BR LEVEL.
000013      240      BR2: .BYTE PR1Y5+0 ;2ND BR LEVEL.
000014      000001      DVID1: 0+1 ;DEVICE INDICATOR 1.
000016      000000      SR1: OPEN ;SWITCH REGISTER 1
000020      000000      SR2: OPEN ;SWITCH REGISTER 2
000022      000000      SR3: OPEN ;SWITCH REGISTER 3
000024      000000      SR4: OPEN ;SWITCH REGISTER 4
;*****
000026      140000      STAT: 140000 ;STATUS WORD.
000030      000346      INIT: START ;MODULE START ADDR.
000032      000224      SPOIN1: MODSP ;MODULE STACK POINTER.
000034      000000      PASCNT: 0 ;PASS COUNTER.
000036      000031      ICUNT: 25. ;# OF ITERATIONS PER PASS=25.
000040      000000      ICDUNT: 0 ;LOC TO COUNT ITERATIONS
000042      000000      SOFCNT: 0 ;LOC TO SAVE TOTAL SOFT ERRORS
000044      000000      HRDCNT: 0 ;LOC TO SAVE TOTAL HARD ERRORS
000046      000000      SUPPAS: 0 ;LOC TO SAVE SOFT ERRORS PER PASS
000050      000000      HRDPAS: 0 ;LOC TO SAVE HARD ERRORS PER PASS
000052      000000      SYSCNT: 0 ;# OF SYS ERRORS ACCUMULATED
000054      000000      RANNUM: 0 ;HOLDS RANDOM # WHEN RAND MACRO IS CALLED
000056      000000      CONFIG:
000056      000000      RES1: 0 ;RESERVED FOR MONITOR USE
000060      000000      RES2: 0 ;RESERVED FOR MONITOR USE
000062      000000      SVR0: OPEN ;LOC TO SAVE R0.
000064      000000      SVR1: OPEN ;LOC TO SAVE R1.
000066      000000      SVR2: OPEN ;LOC TO SAVE R2.
000070      000000      SVR3: OPEN ;LOC TO SAVE R3.
000072      000000      SVR4: OPEN ;LOC TO SAVE R4.
000074      000000      SVR5: OPEN ;LOC TO SAVE R5.
000076      000000      SVR6: OPEN ;LOC TO SAVE R6.
000100      000000      CSMA: OPEN ;ADDR OF CURRENT CSR.
000102      000000      SBADR: ;ADDR OF GOOD DATA, OR
000102      000000      ACSR: OPEN ;CONTENTS OF CSR.
000104      000000      WASADR: ;ADDR OF BAD DATA, OR
000104      000000      ASTAT: OPEN ;STATUS REG CONTENTS.
000106      000000      ERRTP: ;TYPE OF ERROR
000106      000000      ASB: OPEN ;EXPECTED DATA.
000110      000000      AWA: OPEN ;ACTUAL DATA.
000112      000640      RSTRT: RSTRT ;RESTART ADDRESS AFTER END OF PASS
000114      000000      WDT0: OPEN ;WORDS TO MEMORY PER ITERATION
000116      000000      WDR: OPEN ;WORDS FROM MEMORY PER ITERATION
000120      000000      INTR: OPEN ;# OF INTERRUPTS PER ITERATION
000122      000127      IDNUM: 127 ;MODULE IDENTIFICATION NUMBER=127
000122      000040      .REPT SPSIZ ;MODULE STACK STARTS HERE.
000224      MUDSP:

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DMRB DEC/X11 SYSTEM EXERCISER MACRO DEFINITION MODULE
2
;*****
;VARIABLES FOR DMR11
000224      000000      DLY1: 0 ;DELAY COUNTER 1 USED IN DELAY LOOP.
000226      000000      DLY2: 0 ;DELAY COUNTER 2 USED IN DELAY LOOP.
000230      000000      SELECT: 0 ;LOCATION TO SAVE THE # OF ACTIVE DMKS.
000232      000037      FLAGA: 37 ;MASK FOR ALL BUT HALF COMPLETE
000234      000077      FLAGB: 77 ;MASK FOR END OF PASS FLAGS (XX11 AND XX21).
;A BIT IN THE FLAG IS SET WHEN THE DMK IS
;COMPLETED A COMMAND(S) I.E. ALL HA/CC IN
;RECEIVE ACTIONS. 37 = END OF PASS.
000236      000000      FIPS1: 0 ;FLAG USED TO MARK FIRST ITERATION OF A PASS.
000240      000000      LAST: 0 ;FLAG TO MARK LAST ITERATION.
000242      000000      COUNT: 0 ;WORD USED TO DETERMINE THE LAST ITERATION.
000244      000000      MASK: 0 ;MASK USED IN "SCAN" LOOP TO MARK THE
;NUMBER OF DMKS UNDER TEST.
000246      000000      BUFNUM: 0 ;NUMBER OF RECVD AND TRANSMIT BUFFERS
000250      000000      DMREAL: 0 ;DMR EXTENDED CODE FLAG
;
; BIT 0 = MODEM WRITE
; BIT 1 = ENABLE EXT ERROR
; BIT 2 = DISABLE EXT ERROR
; BIT 3 = DESELECT DMC LINE
; BIT 4 = REQUEST BASE I. UPDATE
; BIT 5 = SET REP/SELECT TIMER
; BIT 6 = SET THRESHOLD VALUES.
; BIT 7 = READ M8207 RAM
; BIT 8 = WRITE AX3-15 ****CAUTION****
; BIT 9 = NOP
; BIT 10= READ MODEM STATUS
000252      000000      MODE: 0 ;MODE FLAG
;MODE = 0 FOR DMC MODE
; = -1 FOR DMR MODE
000254      000000      LINK: 0 ;FLAG FOR LINK TEST.
000256      000000      LULOOP: 0 ;FLAG USED TO MARK WHEN LINE UNIT
;LOOPBACK IS DESIRED
000260      000000      VA: 0 ;VIRTUAL ADDRESS
000262      000000      PA: 0 ;PHYSICAL ADDRESS
000264      000000      EA: 0 ;EXTENDED ADDRESS
000266      000000      SAR0: 0 ;SAVE R0
000270      000000      SAR1: 0 ;SAVE R1
;
; .BLKW 3
;
; PAICH AREA TO CHANGE ANY OF THE FOLLOWING COMMAND VALUES
;
;MODEM STATUS BITS IN BSEL6
;BIT0 = RTS HOLD
;BIT1 = SELECT STANDBY
;BIT2 = MAINTENANCE 1 (REMOTE MODEM LOOPBACK)
;BIT3 = MAINTENANCE 2 (LOCAL MODEM LOOPBACK)
;BIT4 = HALF DUPLEX
;BIT5 = SELECT FREQUENCY
;BIT6 = DIR
;BIT7 = PULL
000300      000000      MODEM1: 0 ;BITS TO CLEAR IN MODEM REGISTER
000302      000000      MODEM2: 0 ;BITS TO SET IN MODEM REGISTER

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000304 000036          TIME: 30.          ;REP/SELECT TIMER VALUE IN SEL6
;IN 100 MSEC INCREMENTS (I.E.
;VALUE OF 5 = 500 MSEC)
;THRESHOLD VALUES:
;LO BYTE (BSEL4) = NAKS REC.
;HI BYTE (BSEL5) = NAKS XMIT.
;LO BYTE (BSEL6) = REP/SEL SENT
;HI BYTE (BSEL7) = NO BUFFER
;MICRO RAM ADDRESS (LOADED IN SEL6)
;CONTENTS RETURNED IN BSEL3

;***** WARNING *****
;THE CODE TO WRITE AX3-15
;MAY CAUSE PROBLEMS IF YOU
;ATTEMPT TO CHANGE TO AN INTERFACE
;THAT YOU DON'T HAVE A TURNAROUND
;OR IMPROPER CONFIGURATION.
;*****

000306 003006          THKSH1: 3006
000310 003006          THRSZ2: 3006
000312 000000          MRAM: 0

;DEFAULT RS422
;THE BIT CLEARED WILL DETERMINE THE
;PATTERN WRITTEN INTO AX3-15. THIS
;VALUE IS LOADED INTO BSEL7
;I.E.
;BIT3 (10) AX3=323  INTEGRAL  = H3254
;BIT4 (20) AX3=313  V.35      = H3254
;BIT6 (100) AX3=233  EIA (XYZ) = H3255
;BIT7 (200) AX3=133  RS422    = H3255

          .BLKW 4.          ;

000326 000001 000077 000010  BUFFER: 1,63.,8.,128.,32.,257.,512.,17. ;BUFFER SIZE TABLE
000334 000200 000040 000401
000342 001000 000021
    
```

***** DMR COMMANDS *****

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000000          BACCII =0          ;BA/CC IN TRANSMIT COMMAND
000001          CNFIN  =1          ;CONTROL IN COMMAND
000002          FINI   =2          ;HALT COMMAND
000003          BASEI  =3          ;BASE IN COMMAND
000004          BACCIR =4          ;BA/CC IN RECEIVE COMMAND
000005          MW     =5          ;MODEM WRITE COMMAND
000006          EXER   =6          ;ENABLE EXTENDED ERROR COMMAND
000007          DXER   =7          ;DISABLE EXT. ERROR COMMAND
000010          DDMC   =10         ;Deselect DMC LINE MODE
000011          UPDATE =11         ;REQUEST BASE TABLE UPDATE COMMAND
000012          TIMEK  =12         ;SET REP/SELECT TIMER COMMAND
000013          THRESH =13         ;SET THRESHOLD COMMANDS
000014          MRAM   =14         ;READ M8207 RAM
000015          INTER  =15         ;WRITE AX3-15 INTERFACE
000016          NOP    =16         ;NOP
000017          RMODEM =17         ;READ MODEM STATUS COMMAND (NOP).
000522          DMR    =522        ;DMR MODE PASSWORD
001000          STOP   =1000       ;HALT (OR PROCEDURE ERROR) BIT
    
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010000          RESUME =10000      ;RESUME BIT
020040          OK     =20040      ;MASK FOR NON ERROR CONTROL OUT IN DMR MODE:
;BIT 5 = REPORT DMR START UP.
;BIT 13 = REPORT BASE TABLE UPDATE.
;DMR RUN AND BASE TABLE UPDATE
;CONTROL OUT - BIT SET FOR HALT.

001000          HALTC  =1000
000000          TERM  =0
    
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1
2
3
4
000346          ; BEGIN THE TESTS FOR THE DMC11/DMK11
000346 032767 000004 177442 STAKI: BIT #BIT2,SK1 ;IS THIS A LINK TEST
000354 001406          ;IF NOT PROCEED
000356 012767 177777 177670 MOV #=-1,LINK ;SET THE LINK TEST FLAG
000364 005067 177666 CLR LULOOP ;CLEAR THE LINE UNIT LOOP FLAG.
000370 000402 BK 26
000372          1s: CLR LINK ;NO LINK TEST - CLEAR FLAG
000372 005067 177656          2s:
000376          BIT #BIT1,SK1 ;WHAT MODE IS DESIRED?
000404 001412          BEQ JS ;BR FOR DMC MODE
000406 012767 000100 177632 MOV #64.,BUFNUM ;# OF BUFFERS IN DMR MODE.
000414 012767 177777 177630 MOV #=-1,MODE ;SET THE DMR MODE FLAG
000422 016767 177376 177620 MOV SR4,DMREXT ;SET DMR COMMAND FLAG.
000430 000407 BK 45
000432          3s: MOV #7,BUFNUM ;# OF BUFFERS IN DMC MODE
000440 005067 177606 CLR MODE ;CLEAR FLAG = DMC MODE
000444 005067 177600 CLR DMREXT ;CLEAR DMR COMMAND FLAG.

000450          4s:
000450 005767 177600 TST LINK ;IS THIS A LINK TEST?
000454 001010          BNE 5s ;IF YES - DON'T ALLOW SELECTION OF LU LOOP
000456 032767 000001 177332 BIT #BIT0,SR1 ;TURN AROUND CONNECTOR
000464 001004          BNE 5s ;YES - BR
000466 012767 177777 177562 MOV #=-1,LULOOP ;SET FLAG TO INDICATE LINE UNIT
                                ;LOOP NEEDED
                                BK 65
000474 000402          5s: CLR LULOOP ;CLEAR FLAG - USING TURN AROUND
000476 005067 177554          6s:
000502          MOV BUFNUM,INTR ;INTERRUPTS/ITERATION
000510 012767 001000 177376 MOV #512.,WDTO ;MAX. WORDS TO MEM/ITERATION
000516 012767 001000 177372 MOV #512.,WDFR ;MAX. WORDS FROM MEM/ITERATION
000524 032767 177774 177262 BIT #<C>,DVID1 ;DROP MODULE IF DEVICES OTHER
000532 001040          BNE UROP ;THAN FIRST 4 ARE SELECTED
000534 016767 177254 177466 MOV UVIU1,SELECT ;SELECT=ACTIVE DEVICES
000542 001434          BEQ DRUP ;NO DEVICES - DROP.
000544 005767 177504 TST LINK ;IS THIS A LINK TEST?
000550 001433          BEQ NESTRT ;IF NOT - START (SKIP DEVICE CHECK)
000552 022767 000003 177450 CMP #3,SELECT ;ARE THERE TWO DEVICES?
000560 001427          BEQ RESTR1 ;IF YES - PROCEED

;-----
; ERROR - MUST HAVE 2 DEVICES FOR A LINK TEST.
;-----

000562 104401 000000' 002641' MSGS,BEGIN,NEED2 ;ASCII MESSAGE CALL
000570 016767 177212 177302 MOV ADDR,CSRA ;SAVE CSR FOR DEVICE 1
000576 016767 177276 177276 MOV CSRA,ACSR ;CALCULATE CSR FOR DEVICE 2
000604 012767 000010 177270 MOV #10,ACSR ;SECOND DEVICE SHOULD BE AT THIS CSR.
000612 016767 177412 177264 MOV SELECT,ASTAT ;SAVE THE SELECT FLAG
000620 012767 000006 177260 MOV #6,ERRTYP ;DEVICE OFF LINE, NON EX OR NOT READY ERROR.
;*****
000626 104405 000000' 000000 HRDEMS,BEGIN,NULL ;MUST HAVE 2 DEVICES FOR A LINK TEST
;*****
    
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000634          DROP:
000634 104410 000000' ENDS,BEGIN ;NO DMR'S OR ILLEGAL DMR'S SELECTED
000640          RESTR1:
000640 005067 177372 CLR FIRST ;FLAG FOR 1ST PASS
000644 005067 177370 CLR LAST ;FLAG FOR LAST PASS
000650 016767 177162 177364 MOV ICOUNT,COUNT ;# OF ITERATIONS
000656 005367 177360 DEC COUNT ;COUNT IS 1 LESS
000662 001002          BNE LOOP ;AS LONG AS IT ISN'T 0, OK
000664 005267 177352 INC COUNT ;IF 0, CORRECT (UNLY WHEN ICOUNT=1)
000670          LOOP:
000670 012700 006362' MOV #RCVBUF,R0 ;GET SET TO CLEAR BUFFERS
000674          3s:
000674          CLR (R0)+ ;CLEAR BUFFER
000676 022700 007362' CMP #BASE1,R0 ;END OF BUFFERS?
000702 001374          BNE JS ;OR IF NO
000704 016700 177320 MOV SELECT,R0 ;R0=ACTIVE BITS
000710 001751          BEQ DRUP ;DROP MODULE IF NO DEVICES ARE SELECTED
000712 016701 177070 MOV ADDR,R1 ;R1=DEVICE CSR
000716 016702 177066 MOV VECTOR,R2 ;R2=VECTOR
000722 012703 004646' MOV #INTLNK,R3 ;R3=POINTNER TO INTERRUPT LINKAGE
000726 016767 177302 003724 MOV FLAGB,XX11 ;SET END PASS FLAG FOR DEVICE #1
000734 016767 177274 003760 MOV FLAGB,AX21 ;SET END PASS FLAG FOR DEVICE #2
000742 012767 005752' MOV #PIRINQ,INQIN ;SET UP ALL QUEUES
000750 012767 005752' MOV #PIRINQ,INQOUT
000756 012767 006152' MOV #PIROUTQ,OUTQIN
000764 012767 006152' MOV #PIROUTQ,OUTQOUT
000772          4s:
000772 006200          ASR R0 ;ACTIVE?
000774 103410          HCS 65 ;BR IF ACTIVE
000776 001455          BEQ SETUP2 ;BR IF DONE
001000          5s:
001000 062701 000010 ADD #10,R1 ;UPDATE CSR
001004 062702 000010 ADD #10,R2 ;UPDATE VECTOR
001010 062703 000042 ADD #42,R3 ;UPDATE LINK
001014 000766          BR 4s ;CONTINUE
001016          6s:
001020 116762 176766 000002 MOV R3,(R2) ;LOAD VECTOR
001026 010163 000010 MOV R1,10(R4) ;LOAD INTERRUPT LEVEL
001032 010362 000004 MOV R3,4(R2) ;LOAD CSR TO LINKAGE
001036 062762 000004 ADD #4,4(R2) ;LOAD LINKAGE ADDRESS IN VECTOK
001044 116762 176742 000006 MOV B1,(R2) ;ADJUST I1
001052 042763 177776 000012 BIC #<C1>,12(R3) ;LOAD INTERRUPT LEVEL
                                ;CLEAR ALL BUT BIT0 IN THE END OF PASS FLAG.
                                ;THIS BIT WILL ONLY BE SET ON THE FIRST
                                ;ITERATION AFTER A BASE IN.
001060 005063 000020 CLR 20(R3) ;CLEAR INPUT COUNT LOCATION
001064 005063 000022 CLR 22(R3) ;CLEAR OUTPUT COUNT LOCATION
001070 005767 177142 TST FIRST ;FIRST PASS?
001074 001341          BNE 5s ;BR IF NOT
001076 005063 000012 CLR 12(R3) ;CLEAR ENTIRE END OF PASS FLAG ON FIRST ITER.
001102 005063 000024 CLR 24(R3) ;CLEAR AREA TO SAVE
001106 005063 000026 CLR 26(R3) ;BASE TABLE ERROR
001112 005063 000030 CLR 30(R3) ;COUNTS FOR COMPARISON
001116 005063 000032 CLR 32(R3) ;
001122 016763 177122 000034 MOV DMREXT,34(R3) ;INITIALIZE DMR EXTENDED COMMANDS
001130 000723          BK 55 ;CONTINUE
    
```

```

SETUP2:
001132 016701 176650      MOV     ADDR,R1      ;R1=DEVICE CSR
001136 016700 177066      MOV     SELECT,R0   ;R0=ACTIVE BITS
001142                                1S:
001142 006200              ASK     K0           ;ACTIVE?
001144 103404              RCS     JS           ;BR IF YES
001146 001523              DEQ    SCAN         ;BR IF UJNE
001150                                2S:
001150 062701 000010      ADD     #10,R1      ;UPDATE CSR
001154 000772              BK     1S          ;CONTINUE
001156                                3S:
001156 005767 177054      TST    FIRST        ;FIRST PASS?
001162 001012              BNE   4S           ;MASTER CLEAR FIRST TIME ONLY
001164 105061 000003      CLRB   3(R1)       ;CLEAR BSEL3 - VALUE RETURNED AFTER
                                                ;INIT, IF DEVICE IS A DMR.
001170 012711 040000      MOV     #BIT14,(R1) ;MASTER CLEAR
001174 000240              NOP
001176 000240              NOP
001200 000240              NOP
001202 000240              NOP
001204 000240              NOP
001206 000402              BR     5S
001210                                4S:
001210 052711 100000      BIS     #BIT15,(R1) ;SET THE RUN BIT.
001214                                5S:
001214 005711              TST    (R1)        ;RUN SET?
001216 100415              BNI   6S           ;BR IF YES
001220 010067 177042      MOV     R0,SAR0    ;SAVE R0
001224 010167 177040      MOV     R1,SAR1    ;SAVE R1
001230 104407 000000'    DBREAKS,BEGIN     ;TEMPORARY RETURN TO MONITOR....
001234 104407 000000'    DBREAKS,BEGIN     ;THEN CONTINUE AT NEXT INSTRUCTION.
001240 016700 177022      MOV     SAR0,R0    ;RESTORE R0
001244 016701 177020      MOV     SAR1,R1    ;RESTORE R1
001250 000761              BR     5S          ;WAIT FOR RUN
001252                                6S:
001252 005767 176760      TST    FIRST        ;IS THIS THE FIRST PASS?
001256 001033              BNE   15S         ;IF NOT, MODE SHOULD BE DETERMINED.
001260 005767 176766      TST    MODE         ;MODE?
001264 001430              BEQ   15S         ;BR IF DMC MODE.
001266 105761 000003      TSTB   3(R1)       ;IS THIS A DMR?
001272 001025              BNE   15S         ;IF DMR - OK (NOTE: NOTHING
                                                ;RETURNED IN BSEL3 IF DEVICE IS DMR)
    
```

```

;-----
; ERROR - CAN'T BE IN DMR MODE UNLESS THE DEVICE IS ACTUALLY A DMR.
; (NOTE: DMR CAN BE IN DMC MODE; HOWEVER DMC CAN ONLY BE IN
; DMC MODE).
;-----
    
```

```

001274 010146      MOV     R1,*(SP)    ;SAVE R1 - DURING THE CALL TO THE MSG
                                                ;MACRO, INTERRUPTS MAY OCCUR IF ANOTHER
                                                ;DEVICE HAS BEEN SETUP. IF THIS OCCURS
                                                ;R1 MAY RETURN FROM THE INTERRUPT WITH
                                                ;THE ADDRESS OF THE 1ST DEVICE.
001276 104401 000000' 002556'    MSG$,BEGIN,MIXED  ;ASCII MESSAGE CALL
001304 012601              MOV     (SP)+,R1   ;RESTORE THE ADDRESS IN R1
001306 010167 176566      MOV     R1,CSRA    ;SAVE CSR
001312 016167 000002 176562      MOV     2(R1),ACSK ;SAVE CONTENTS OF SEL2
    
```

```

001320 016767 176726 176556      MOV     MODE,ASTAT ;SAVE THE MODE FLAG
001326 012767 000006 176552      MOV     #6,ERRIYP ;DEVICE NOT READY.
001334 104405 000000' 000000      HDR$,BEGIN,NULL   ;DMR MODE ILLEGAL WHEN DEVICE IS A DMC
                                                ;*****
001342 104410 000000'    ENDS,BEGIN        ;DROP IT
001346                                15S:
001346 005767 176704      TST    LULOOP       ;IS LINE UNIT REQUESTED?
001352 001403              BEQ   16S         ;BR IF NO (CONNECTOR USED)
001354 052711 004000      BIS     #4000,(R1) ;OTHERWISE SET LU LOOP
001360 000402              BR     17S         ;CONTINUE
001362                                16S:
001362 042711 004000      BIC     #4000,(R1) ;TURNAROUND OR LINK CONNECTOR
001366                                17S:
001366 052761 000100 000002      BIS     #100,2(R1) ;SET IEO
001374 005767 176636      TST    FIRST        ;IS THIS THE FIRST ITERATION
001400 001003              BNE   18S         ;IF NOT - SKIP BASE IN
001402 052711 000143      BIS     #143,(R1)  ;SET IEI,RQI,BASE1
001406 000660              BR     2S          ;CONTINUE NEXT DEVICE
001410                                18S:
001410 052711 000144      BIS     #144,(R1)  ;SET IEI,RQI,BA/CC IN
001414 000655              BR     2S          ;CONTINUE NEXT DEVICE
    
```

```
001416  
001416 012767 000003 176620  
001424 012767 000010 176572  
001432 005067 176570  
001436  
001436 026767 176572 003214  
001444 001003  
001446 042767 000001 176570  
001454  
001454 026767 176554 003240  
001462 001003  
001464 042767 000002 176552  
001472  
001472 005767 176546  
001476 001440  
  
001500  
  
001500 104407 000000'  
001504 104407 000000'  
001510 005367 176512  
001514 001402  
001516 000167 177714  
001522  
001522 005367 176476  
001526 001402  
001530 000167 177702  
001534  
001534 016700 176504  
001540 040067 176464  
001544 006000  
001546 103004  
001550 004367 000446  
001554 004656'  
001556 000001  
001560  
001560 006000  
001562 103004  
001564 004367 000432  
001570 004720'  
001572 000002  
001574  
001574 000167 177070  
  
;-----  
;SCAN ALL ENDPASS FLAGS UNTIL  
;ALL ACTIVE DMRIIS ARE FINISHED  
; (ENDPASS FLAG = 37)  
;-----  
SCAN:  
MOV #3,MASK ;SET BIT FOR ALL DEVICES  
MOV #10,DL11 ;DELAY COUNT  
CLR DLY2 ;DELAY  
  
18: CMP FLAGB,XX11 ;DEVICE 1 DONE?  
BNE 25 ;BR IF NO  
BIC #1,MASK ;DEVICE 1 IS DONE SO CLEAR BIT0  
  
25: CMP FLAGB,XX21 ;DEVICE 2 DONE?  
BNE 35 ;BR IF NO  
BIC #2,MASK ;DEVICE 2 IS DONE SO CLEAR BIT1  
  
35: TSI MASK ;ARE ALL DEVICES FINISHED?  
BEQ CHECK ;IF YES - CHECK DATA  
  
20s:  
;-----  
; DELAY LOOP  
;-----  
BREAKS,BEGIN ;TEMPORARY RETURN TO MONITOR....  
BREAKS,BEGIN ;THEN CONTINUE AT NEXT INSTRUCTION.  
DEC DLY2 ;DEC DELAY COUNT  
BEQ 25S ;  
JMP 1S ;BR IF NOT DONE  
  
25s: DEC DLY1 ;DEC DELAY COUNT  
HEQ 30S ;  
JMP 1S ;BR IF NOT DONE  
  
30s: MOV MASK,R0 ;PUT BITS OF HUNG DEVICES IN R0  
BIC R0,SELECT ;DROP ANY HUNG DEVICES  
RDR R0 ;WAS DEVICE 1 HUNG?  
BCC 35S ;BR IF NOT  
JSR R3,XERR ;TYPE ERROR MESSAGE  
CSR1 ;POINTER TO DEVICE #1 CSR  
1 ;DEVICE NUMBER FOR TYPEOUT  
  
35s: RDR R0 ;WAS DEVICE 2 HUNG?  
BCC 40S ;BR IF NOT  
JSR R3,XERR ;TYPE ERROR MESSAGE  
CSR2 ;POINTER TO DEVICE #2 CSR  
2 ;DEVICE NUMBER FOR TYPEOUT  
  
40s: JMP LOOP ;RESTAKE MODULE  
  
;-----  
;CHECK EACH CHARACTER OF THE LAST RECEIVE BUFFER, AND THE BASE  
;TABLE ERROR COUNTS.  
;-----  
CHECK:  
MOV #INTLNK+10,R1 ;R1 IS POINTER TO CSR
```

```
001604 016700 176420  
001610  
001610 006200  
001612 103462  
001614 001403  
001616  
001616 062701 000042  
001622 000772  
001624  
001624 012767 177777 176404  
001632 005367 176404  
001636 001003  
001640 012767 177777 176372  
001646  
  
001646 104413 000000'  
  
001652 032767 000001 176350  
001660 001416  
001662 012767 007362' 176370  
001670 004767 002322  
001674 026767 003004 176360  
001702 001022  
001704 026767 002776 176352  
001712 001016  
001714 000417  
001716  
001716 012767 007762' 176334  
001724 004767 002266  
001730 026767 003012 176324  
001736 001004  
001740 026767 003004 176316  
001746 001402  
001750  
001750 005067 176262  
001754  
001754 000167 176710  
001760  
  
001760 012703 001000  
001764 012704 006362'  
001770 012705 004752'  
001774  
001774 121514  
001776 001414  
002000 011167 176074  
002004 010567 176072  
002010 010467 176070  
002014 111567 176066  
002020 111467 176064  
  
002024 104404 000000'  
  
MOV SELECT,R0 ;R0 CONTAINS BITS FOR ACTIVE DEVICES  
  
4s: ASR R0 ;ACTIVE?  
BCS 12S ;BR IF YES  
BEQ 6S ;BR IF DONE  
  
5s: ADD #42,R1 ;UPDATE R1 TO NEXT DEVICE CSR  
BR 4S ;CONTINUE  
  
6s: MOV #1,FIRST ;SET FIRST FLAG TO -1  
DEC COUNT ;DECREMENT LAST ITERATION COUNT  
BNE 7S ;IF NOT 0, NOT LAST ITERATION.  
MOV #-1,LAST ;SET LAST PASS FLAG  
  
7s: ENDS,BEGIN ;SIGNAL END OF ITERATION.  
;MONITOR SHALL TEST END OF PASS  
;THIS CODE WAS ADDED TO SEE IF RELOCATION  
;TOOK PLACE. IF IT DID, MAKE SURE THE DMR  
;IS RESET (BY USING THE FIRST FLAG).  
;WAS THERE A DEVICE 1?  
;IF NOT CHECK BASE2 ADDRESS  
;GET BASE TABLE 1 ADDRESS.  
;GET THE PHYSICAL ADDRESS OF THE TABLE  
;IS THE ADDRESS THE SAME AS ON LAST BASE IN?  
;IF NOT, CLEAR THE FIRST FLAG  
;CHECK THE EXTENDED ADDRESS  
;IF NOT, CLEAR THE FIRST FLAG  
  
8s: MOV #BASE1,VA ;GET BASE TABLE 2 ADDRESS.  
JSR PC,EABITS ;GET THE PHYSICAL ADDRESS OF THE TABLE  
CMP XX15,PA ;IS THE ADDRESS THE SAME AS ON LAST BASE IN?  
BNE 9S ;IF NOT, CLEAR THE FIRST FLAG  
CMP XX16,EA ;CHECK THE EXTENDED ADDRESS  
BNE 10S ;IF NOT, CLEAR THE FIRST FLAG  
BK 10S ;IF YES, PROCEED.  
  
9s: CLR FIRST ;CLEAR FIRST FLAG TO FORCE A MASTER CLEAR.  
  
10s: JMP LOOP ;LOOP MODULE  
  
12s: ;ONLY THE LAST BUFFER IS CHECKED BECAUSE  
;WE OVERLAY THE BUFFER AREA.  
;R3 IS THE MAX. BUFFER SIZE  
;R4 POINTS TO FIRST REC BUFFER  
;R5 POINTS TO GOOD DATA  
  
14s: CMPB (R5),(R4) ;COMPARE DATA  
BEQ 15S ;BR IF GOOD  
MOV (R1),CSRA ;LOAD CSR  
MOV R5,SBADR ;LOAD GOOD ADDRESS  
MOV R4,WASADR ;LOAD BAD ADDRESS  
MOVB (R5),ASB ;LOAD GOOD DATA  
MOVB (R4),WAS ;LOAD BAD DATA  
  
;*****  
DATERS,BEGIN ;DATA END!!  
;*****
```

```

002030          15$:
002030 122524      CMPB (R5)+(R4)+      ;POP DATA POINTERS
002032 005303      JEC R3              ;CONTINUE UNTIL DONE
002034 001357      BNE 14$              ;
002036 005767 176176  TSI LAST          ;IS THIS THE LAST PASS?
002042 001665      REG 5$              ;IF NOT - SKIP BASE TABLE ERROR CHECK.
002044 012705 000003  MOV #3,R5          ;NOW LETS CHECK BASE TABLE ERROR COUNTS
002050 016104 000006  MOV 6(R1),R4       ;GET BASE TABLE ADDRESS
002054 060504      ADD R5,R4          ;ADD OFFSET TO ERROR COUNTS
002056 010102      MOV R1,R2          ;GET POINTER TO CSR
002060 062702 000014  ADD #14,R2         ;MAKE IT POINT TO SAVED ERROR COUNTS
002064          16$:
002064 122224      CMPB (R2)+(R4)+      ;COMPARE BASE TABLE ERROR COUNTS
002066 001005      BNE 17$              ;TO SAVED ERROR COUNTS BR IF NOT SAME
002070 005205      INC R5              ;BUMP TO NEXT ERROR COUNT
002072 022705 000013  CNP #13,R5       ;ALL DONE YET?
002076 001372      BNE 16$              ;BR IF NO
002100 000646      BK 5$              ;CONTINUE NEXT DEVICE
002102          17$:
002102 104403 000000' 002154' MSGNS,BEGIN,SUFT    ;ASCII MESSAGE CALL WITH COMMON HEADER
002110 012705 000003  MOV #3,R5          ;BASE OFFSET TO ERROR COUNTS
002114 012702 002360'  MOV #ESAV1,R2       ;LOAD COUNTS FOR TYPEOUT
002120 016104 000006  MOV 6(R1),R4       ;GET BASE ADDRESS
002124 060504      ADD R5,R4          ;ADD IN OFFSET
002126          18$:
002126 112422      MOVb (R4)+(R2)+      ;OK LOAD TABLE FOR TYPEOUT
002130 005205      INC R5              ;INCREMENT COUNTER
002132 022705 000013  CNP #13,R5       ;DONE YET?
002136 001373      BNE 18$              ;BR IF NOT
;SAVE THESE BASE TABLE ERROR COUNTS
002140 010102      MOV R1,R2          ;GET POINTER TO CSR
002142 062702 000014  ADD #14,R2         ;MAKE IT POINT TO SAVED ERROR COUNTS
002146 012704 002360'  MOV #ESAV1,R4       ;R4 POINTS TO NEW COUNT VALUES
002152          19$:
002152 012422      MOV (R4)+(R2)+      ;STORE BASE TABLE COUNTS
002154 022704 002370'  CNP #ESAV4+2,R4     ;DONE?
002160 001374      BNE 19$              ;BR IF NOT
002162 011167 175712  MOV (R1),CSRA      ;LOAD CSR
002166 011105      MOV (R1),R5          ;
002170 011567 175706  MOV (R5),ACSR      ;SAVE CONTENTS OF SEL0
002174 016167 000006 175702  MOV 6(R1),ASIA1    ;SAVE BASE ADDRESS
002202 012767 000001 175676  MOV #1,ERRTYP     ;DATA ERROR
;*****
002210 104406 000000' 002404' SUPERS,BEGIN,FTABLE ;BASE TABLE DDCMP ERROR COUNTERS
;*****
002216 000167 177374  JMP 5$              ;CONTINUE NEXT DEVICE

002222 012302      XERR: MOV (R3)+,R2      ;GET POINTER TO CSR
002224 012367 000112  MOV (R3)+,DEV     ;GET DEVICE NUMBER
002230 052767 000060 000104  BIS #60,DEV      ;MAKE IT ASCII
002236 104403 000000' 002344' MSGNS,BEGIN,DROP1  ;ASCII MESSAGE CALL WITH COMMON HEADER
002244 011201      MOV (R2),R1          ;GET CSR ADDRESS
002246 010167 175626  MOV R1,CSRA      ;SAVE CSR
002252 011167 175624  MOV (R1),ACSR    ;SAVE CONTENTS OF SEL0
002256 016167 000002 175620  MOV 2(R1),ASIA1  ;SAVE CONTENTS OF SEL2
002264 016167 000004 175732  MOV 4(R1),DLI1   ;SAVE CONTENTS OF SEL4
    
```

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002272 016167 000006 175726  MOV 6(R1),DLI2    ;SAVE CONTENTS OF SEL6
002300 016267 000002 000052  MOV 2(R2),ESAV1  ;END PASS FLAG
002306 016267 000010 000046  MOV 10(R2),ESAV2 ;RECEIVE BUFFER OFFSET
002314 016267 000012 000042  MOV 12(R2),ESAV3 ;REC/XMIT COUNTEKS
002322 012767 000023 175556  MOV #23,ERRTYP  ;NO INTERRUPT
;*****
002330 104405 000000' 002370' HDRS,BEGIN,ETABLE ;DUMP DMC CSR'S AND STATUS FLAGS
;*****
002336 005011      CLR (R1)          ;SHUT OFF HUNG DMC11
002340 000203      RTS R3              ;RETURN
002342 000000      DEV: 0
002344          DROP1:
002344 002416'      XDROP1
002346 002342'      DEV
002350 002437'      XDROP2
002352 177777      -1
002354          SUFT:
002354 002475'      SUFT1
002356 177777      -1
002360 000000      ESAV1: 0              ;EXTENDED ERROR PRINTOUT LOCATIONS
002362 000000      ESAV2: 0
002364 000000      ESAV3: 0
002366 000000      ESAV4: 0
002370 000224'      ETABLE: DLI1          ;TABLE OF ADDRESSES FOR EXTENDED ERROR PRINTOUT
002372 000226'      DLI2
002374 002360'      ESAV1
002376 002362'      ESAV2
002400 002364'      ESAV3
002402 177777      -1
002404 002360'      FTABLE: ESAV1
002406 002362'      ESAV2
002410 002364'      ESAV3
002412 002366'      ESAV4
002414 177777      -1
002416 045 104 115 XDROP1: .ASCIZ /%DMK11 DEVICE # /
002421 122 061 061
002424 040 104 105
002427 126 111 103
002432 105 040 043
002435 040 000
002437 040 111 123 XDROP2: .ASCIZ / IS HUNG AND HAS BEEN DROPPED/
002442 040 110 125
002445 116 107 040
002450 101 116 104
002453 040 110 101
002456 123 040 102
002461 105 105 116
002464 040 104 122
002467 117 120 120
    
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002472 105 104 000
002475 045 123 117 SOFT1: .ASCIZ /%SOFT ERROR - DDCMP ERROR COUNTERS ARE NON ZERO%/
002500 106 124 040
002503 105 122 122
002506 117 122 040
002511 055 040 104
002514 104 101 115
002517 120 040 105
002522 122 122 117
002525 122 040 103
002530 117 125 116
002533 124 105 122
002536 123 040 101
002541 122 105 040
002544 116 117 116
002547 040 132 105
002552 122 117 045
002555 000
002556 045 111 114 MIXED: .ASCIZ /%ILLEGAL TO BE IN DMR MODE IF THE DEVICE IS A DMC%/
002561 114 105 107
002564 101 114 040
002567 124 117 040
002572 102 105 040
002575 111 116 040
002600 104 115 122
002603 040 115 117
002606 104 105 040
002611 111 106 040
002614 124 110 105
002617 040 104 105
002622 126 111 103
002625 105 040 111
002630 123 040 101
002633 040 104 115
002636 103 045 000
002641 045 115 125 NEED2: .ASCIZ /%MUST HAVE TWO DEVICES SELECTED FOR LINK TEST%/
002644 123 124 040
002647 110 101 126
002652 105 040 124
002655 127 117 040
002660 104 105 126
002663 111 103 105
002666 123 040 123
002671 105 114 105
002674 103 124 105
002677 104 040 106
002702 117 122 040
002705 114 111 116
002710 113 040 124
002713 105 123 124
002716 045 000
    .EVEN
    
```

```

0 002720 11SR:
002720 010577 003426 MOV R5, @INQIN ;STORE LINK POINTER IN QUEUE
002724 062767 000002 003420 ADD #2, INQIN ;UPDATE QUEUE
002732 022767 006152' 003412 CMP #PIRING+200, INQIN ;END OF QUEUE?
002740 001003 BNE IS ;BR IF NO
002742 012767 005752' 003402 MOV #PIRING, INQIN ;RESET QUEUE POINTER
002750 012605 1S:
002752 000004 000000' 002760' MOV (SP)+, R5 ;RESTORE R5
;-----
PIRQS, BEGIN, 2S ; QUEUE UP TO CONTINUE AT 2S AND MTI
;-----
002760 2S:
002760 017705 003370 MOV @INQOUT, R5 ;GET LINK POINTER IN R5
002764 062767 000002 003362 ADD #2, INQOUT ;UPDATE QUEUE
002772 022767 006152' 003354 CMP #PIRING+200, INQOUT ;END OF QUEUE?
003000 001003 BNE 3S ;BR IF NO
003002 012767 005752' 003344 MOV #PIRING, INQOUT ;RESET QUEUE POINTER
003010 3S:
003010 016501 000004 MOV 4(R5), R1 ;LOAD CSR ADDRESS
003014 011100 MOV @R1, R0 ;SAVE SEL0
003016 042700 177760 BIC #17760, R0 ;KEEP ONLY THE COMMAND BITS (0-3)
003022 020027 000000 CMP #0, #BACCIT ;IS THE COMMAND BA/CC XMIT ?
003026 001503 BEQ XMIT ;BR IF YES
003030 020027 000004 CMP R0, #BACCIP ;IS THE COMMAND BA/CC REC'V ?
003034 001501 BEQ REC ;BRANCH IF YES
003036 020027 000001 CMP R0, #CNTIN ;IS THE COMMAND A CONTROL IN?
003042 001466 BEQ CTRL ;BRANCH IF YES
003044 020027 000003 CMP R0, #BASEI ;IS THE COMMAND A BASE IN?
003050 001413 BEQ BASEIN ;BR IF YES
003052 020027 000002 CMP R0, #FINI ;IS THIS THE HALT COMMAND
003056 001402 BEQ DONE ;BR IF YES.
003060 000167 000422 JMP DMRIN ;IF NOT ONE OF THE ABOVE MUST BE
;AN EXTENDED DMR COMMAND.
;JUMP TO PROCESS EXTENDED COMMAND.

003064 DONE:
003064 052711 000100 BIS #100, (R1) ;CLEAR INTERRUPT ENABLE
003070 004767 001110 JSR PC, ENDCLR ;CLEAR R01
003074 104400 000000' EXITS, BEGIN ;EXIT TO MONITOR. MODULE WAIT FOR INTERRUPT.

003100 BASEIN:
003100 016567 000012 175152 MOV 12(R5), VA ;BASE IN
003106 004767 001104 JSR PC, LAHITS ;GET PHYSICAL ADDRESS
003112 016761 175144 000004 MOV PA, 4(R1) ;LOAD PHYSICAL BASE ADDRESS
003120 016761 175140 000006 MOV EA, 6(R1) ;LOAD EA BITS FOR BASE ADDRESS
003126 016765 175130 000032 MOV PA, 32(R5) ;SAVE THE PHYSICAL ADDRESS.
003134 016765 175124 000034 MOV EA, 34(R5) ;SAVE THE EXTENDED ADDRESS.
003142 005767 175104 IST MODE ;IS THIS DMC MODE?
003146 001403 BEQ BS ;IF NOT, NO NEED TO SET MODE.
003150 052761 000522 000006 BIS #DMR, 6(R1) ;SET DMR MODE.
003156 004767 001022 6S:
003162 052765 000001 000006 JSR PC, ENDCLR ;CLEAR R01
BIS #1, 6(R5) ;SET BIT0 IN END PASS FLAG TO
;SHOW THAT A BASE ADDRESS WAS LOADED

003170 005767 175056 IST MODE ;WHAT IS THE MODE
003174 001405 BEQ 7S ;BR IF IN DMC MODE
003176 005765 000030 IST 30(R5) ;ARE ANY DMR MODE EXTENDED COMMANDS
;REQUESTED ?
    
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003202 001402          BEQ      7$          ;BR IF NONE
003204 000167 000422    JMP      DMNRXT          ;ISSUE THE DMR COMMAND
                                7$:
003210 152711 000041    BISB   #41,(R1)        ;ASK FOR CNTL 1
003214 104400 000000'   EXITS,BEGIN          ;EXIT TO MONITOR. MODULE WAIT FOR INTERRUPT.
                                CNTRL:
003220 005061 000006    CLR      6(R1)        ;SET FULL DUPLEX
003224 004767 000754    JSR     PC,ENDCLR    ;CLEAR RQ1
003230 152711 000044    BISB   #44,(R1)        ;ASK FOR REC BA/CC 1
003234 104400 000000'   EXITS,BEGIN          ;EXIT TO MONITOR. MODULE WAIT FOR INTERRUPT.
                                REC:
003240 012767 006362' 175012    MOV     #RCVBUF,VA    ;VA GETS REC BUFFER VIRTUAL ADDRESS
003246 004767 000744    JSR     PC,EABITS    ;GET PHYSICAL ADDRESS
003252 016761 175004 000004    MOV     PA,4(R1)     ;LOAD PHYSICAL RBUF
003260 016761 175000 000006    MOV     EA,6(R1)     ;LOAD RBUF EA BITS
003266 052761 001000 000006    BIS    #512,,6(R1)   ;MAX BUFFER SIZE POSSIBLE
003274 004767 000704    JSR     PC,ENDCLR    ;CLEAR RQ1
003300 105265 000014    INCB   14(R5)        ;UPDATE COUNTER.
003304 126765 174736 000014    CMPB  BUFNUM,14(R5) ;LOADED ALL BUFFERS YET?
003312 001404          BEQ     1$           ;BR IF YES
003314 152711 000044    BISB   #44,(R1)        ;REQUEST REC BA/CC
003320 104400 000000'   EXITS,BEGIN          ;EXIT TO MONITOR. MODULE WAIT FOR INTERRUPT.
                                1$:
003324 052765 000002 000006    BIS    #2,6(R5)      ;SET BIT1 IN END PASS FLAG TO
                                ;SHOW THAT ALL 7 REC BA/CC WERE LOADED
003332 005767 174716    TST    LINK          ;IS THIS A LINK TEST?
003336 001003          BNE    2$           ;IF YES - BR
003340 152711 000040    BISB   #40,(R1)        ;ASK FOR XMIT BA/CC 1
003344 000412          BR     3$           ;EXIT
                                2$:
003346 032767 000002 001346    BIT    #2,XX21       ;IS THE 2ND DEVICE RCV. DONE?
003354 001406          BEQ    3$           ;IF NOT, DON'T ASK FOR XMIT.
003356 152777 000040 001272    BISB   #40,#CSR1     ;BA/CC IN XMIT FOR DEVICE 1.
003364 152777 000040 001326    BISB   #40,#CSR2     ;BA/CC IN XMIT FOR DEVICE 2.
                                3$:
003372 104400 000000'   EXITS,BEGIN          ;EXIT TO MONITOR. MODULE WAIT FOR INTERRUPT.
                                XMIT:
003376 012767 004752' 174654    MOV     #XBUF,VA     ;LOAD XBUF VIRTUAL ADDRESS
003404 004767 000606    JSR     PC,EABITS    ;GET PHYSICAL ADDRESS
003410 016761 174646 000004    MOV     PA,4(R1)     ;LOAD PHYSICAL XBUF
003416 016761 174642 000006    MOV     EA,6(R1)     ;LOAD EA BITS
003424 116500 000015    MOVB   15(R5),R0     ;SEE WHAT BUFFER NUMBER THIS WILL BE
003430 042706 177770    BIC    #17770,R0     ;SAVE ONLY THE LAST DIGIT (0-7)
003434 006300          ASL    R0            ;GET LOCATION IN BUFFER TABLE
003436 056061 000326' 000006    BIS    BUFFER(R0),6(R1);LOAD TRANSMIT COUNT
                                ;ACCORDING TO THE BUFFER TABLE
                                ;+OFTSET IN ORDER TO VARY BUFFER SIZE.
003444 004767 000534    JSR     PC,ENDCLR    ;CLEAR RQ1
003450 105265 000015    INCB   15(R5)        ;COUNT HOW MANY XMIT BA/CC ARE DONE.
003454 126765 174566 000015    CMPB  BUFNUM,15(R5) ;DO WE HAVE ALL BUFFERS?
003462 001404          BEQ    1$           ;BR IF YES
003464 152711 000040    BISB   #40,(R1)        ;ASK FOR XMIT BA/CC
003470 104400 000000'   EXITS,BEGIN          ;EXIT TO MONITOR. MODULE WAIT FOR INTERRUPT.
                                1$:
003474 052765 000004 000006    BIS    #4,6(R5)      ;SET BIT2 IN END PASS FLAG TO
                                ;SHOW THAT ALL 7 XMIT BA/CC WERE LOADED
003502 104400 000000'   EXITS,BEGIN          ;EXIT TO MONITOR. MODULE WAIT FOR INTERRUPT.
    
```

```

003506 020027 000005    DMRIN: CMP     R0,#MW        ;IS THIS A MODEM WRITE?
003512 001007          BNE    1$           ;BR IF NOT
003514 046761 174560 000006    BIC    MODEM1,6(R1)  ;CLEAR NECESSARY BITS
003522 056761 174554 000006    BIS    MODEM2,6(R1) ;SET NECESSARY BITS
003530 000436          BR     1$           ;
                                1$:
003532 020027 000012    CMP     R0,#TIMER    ;IS THIS TO SET REP/SELECT TIMER VALUE ?
003536 001004          BNE    2$           ;BR IF NOT
003540 016761 174540 000006    MOV     TIME,6(R1)   ;LOAD VALUE
003546 000427          BR     2$           ;
                                2$:
003550 020027 000013    CMP     R0,#THRESH   ;IS THIS TO SET THE THRESHOLD VALUES?
003554 001007          BNE    3$           ;BR IF NOT
003556 016761 174524 000004    MOV     THRS1,4(R1)  ;LOAD VALUE FOR NAKS
                                ;BSEL4 = NAKS REC BSELS = NAKS SENT
003564 016761 174520 000006    MOV     THRS2,6(R1)  ;LOAD VALUE FOR REP/SEL AND NO BUFFER
                                ;BSEL6 = REP/SEL BSEL7 = NO BUFFER
                                ;
003572 000415          BR     3$           ;
                                3$:
003574 020027 000014    CMP     R0,#RRAM     ;IS THIS THE READ M8207 RAM?
003600 001004          BNE    4$           ;BR IF NOT
003602 116761 174504 000006    MOVB   MKAM,6(R1)   ;ADDRESS OF RAM LOADED IN SEL6
                                ;CURRENTS RETURNED IN BSEL3.
                                ;
003610 000406          BR     4$           ;
                                4$:
003612 020027 000015    CMP     R0,#INTER    ;IS THIS THE AX3-15 WRITE?
003616 001003          BNE    5$           ;BR IF NOT
                                ;***** WARNING *****
                                ;MAKE SURE THAT AX3 HAS THE CORRECT
                                ;VALUE FOR THE DESIRED INTERFACE. SEE
                                ;THE COMMENTS ON AX3 IN THE VARIABLE
                                ;LISTINGS.
                                ;*****
003620 116761 174470 000007    MOVB   AX3,7(R1)    ;LOAD AX3-15 VALUE
003626 004767 000352    DMRNXT: JSR     PC,ENDCLR    ;CLEAR RQ1.
003632 005765 000030    TST    30(R5)        ;SEE IF THERE ARE ANY DMB MODE COMMANDS
003636 001556          BEQ    20$         ;
                                ;
                                ; DMRNXT IS A FLAG THAT ALLOWS THE USER TO SELECT ANY COMBINATION OF THE
                                ; DMB COMMANDS TO BE PERFORMED (IF SET)
                                ;
                                ; BIT 0 = MODEM WRITE          BIT 1 = ENABLE EXT ERROR
                                ; BIT 2 = DISABLE EXT ERROR    BIT 3 = DESELECT DMC LINE
                                ; BIT 4 = REQUEST BASE T. UPDATE BIT 5 = SET REP/SELECT TIMER
                                ; BIT 6 = SET THRESHOLD VALUES BIT 7 = READ M8207 RAM
                                ; BIT 8 = WRITE AX3-15          BIT 9 = NOP
                                ; BIT 10 = READ MODEM
                                ;
003640 032765 000001 000030    BIT    #BIT0,30(R5) ;MODEM WRITE?
003646 001406          BEQ    1$           ;BR IF NOT
003650 042765 000001 000030    BIC    #BIT0,30(R5) ;CLEAR THE REQUEST
003656 152711 000045    BISB   #40,MW,(R1)  ;ASK FOR A MODEM WRITE
003662 000546          BR     25$         ;
                                1$:
003664          BR     25$         ;
    
```



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DEC/X11-1 SYSTEM EXERCISER MACRO DEFINITION MODULE
003664 032765 000002 000030 BIT #BIT1,30(R5) ;ENABLE EXT ERROR?
003672 001406 BEQ 2S ;BR IF NOT
003674 042765 000002 000030 BIC #BIT1,30(R5) ;CLEAR THE REQUEST
003702 152711 000046 B1SB #40:EXER,(R1) ;ASK FOR ENABLE EXT ERROR.
003706 000534 BR 25S
003710 032765 000004 000030 2S: BIT #BIT2,30(R5) ;DISABLE EXT ERROR?
003716 001406 BEQ 3S ;BR IF NOT
003720 042765 000004 000030 BIC #BIT2,30(R5) ;CLEAR THE REQUEST
003726 152711 000047 B1SB #40:DXER,(R1) ;ASK FOR DISABLE EXT ERROR.
003732 000522 BR 25S
003734 032765 000010 000030 3S: BIT #BIT3,30(R5) ;DESELECT DMC LINE MODE?
003742 001406 BEQ 4S ;BR IF NOT
003744 042765 000010 000030 BIC #BIT3,30(R5) ;CLEAR THE REQUEST
003752 152711 000050 B1SB #40:DMC,(R1) ;ASK FOR DESELT DMC.
003756 000510 BR 25S
003760 032765 000020 000030 4S: BIT #BIT4,30(R5) ;REQUEST BASE TABLE UPDATE?
003766 001406 BEQ 5S ;BR IF NOT
003770 042765 000020 000030 BIC #BIT4,30(R5) ;CLEAR THE REQUEST
003776 152711 000051 B1SB #40:UPDATE,(R1) ;ASK FOR UPDATE.
004002 000476 BR 25S
004004 032765 000040 000030 5S: BIT #BIT5,30(R5) ;SET REP/SELECT TIMER VALUE?
004012 001406 BEQ 6S ;BR IF NOT
004014 042765 000040 000030 BIC #BIT5,30(R5) ;CLEAR THE REQUEST
004022 152711 000052 B1SB #40:TIMER,(R1) ;SET TIMER
004026 000464 BR 25S
004030 032765 000100 000030 6S: BIT #BIT6,30(R5) ;SET THRESHOLD VALUES?
004036 001406 BEQ 7S ;BR IF NOT
004040 042765 000100 000030 BIC #BIT6,30(R5) ;CLEAR THE REQUEST
004046 152711 000053 B1SB #40:THRESH,(R1) ;SET THRESHOLD.
004052 000452 BR 25S
004054 032765 000200 000030 7S: BIT #BIT7,30(R5) ;READ M8207 RAM?
004062 001406 BEQ 8S ;BR IF NOT
004064 042765 000200 000030 BIC #BIT7,30(R5) ;CLEAR THE REQUEST
004072 152711 000054 B1SB #40:RRAM,(R1) ;READ M8207 RAM.
004076 000440 BR 25S
004100 032765 000400 000030 8S: BIT #BIT8,30(R5) ;WRITE AX3-15?
004106 001406 BEQ 9S ;BR IF NOT
004110 042765 000400 000030 BIC #BIT8,30(R5) ;CLEAR THE REQUEST
004116 152711 000055 B1SB #40:INTER,(R1) ;WRITE AX3-15
004122 000426 BR 25S
004124 032765 001000 000030 9S: BIT #BIT9,30(R5) ;NOP?
004132 001406 BEQ 10S ;BR IF NOT
004134 042765 001000 000030 BIC #BIT9,30(R5) ;CLEAR THE REQUEST
004142 152711 000056 B1SB #40:NOPI,(R1) ;NOP
004146 000414 BR 25S
004150 032765 002000 000030 10S: BIT #BIT10,30(R5) ;READ MODEM?
004156 001406 BEQ 20S ;IF NOT, ISSUE A CONTROL IN
004160 042765 002000 000030 BIC #BIT10,30(R5) ;CLEAR THE REQUEST
    
```

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DEC/X11-1 SYSTEM EXERCISER MACRO DEFINITION MODULE
004166 152711 000057 B1SB #40:RMODEM,(R1) ;READ MODEM.
004172 000402 BR 25S
004174 04174 20S: B1SB #41,(R1) ;CONTROL IN
004200 004200 25S: EXIT$,BEGIN ;EXIT TO MONITOR. MODULE WAIT FOR INTERRUPT.
004200 104400 000000' ;
004204 ENDCLR:
004204 142711 000040 B1CB #40,(R1) ;CLEAR ROI
004210 1S: TSTB (R1) ;IS ROI GONE?
004210 105711 BMI 1S ;BR IF NO
004212 100776 RTS PC ;RETURN
004214 000207
004216 EABITS:
004216 104415 000000' 000260' GETPAS,BEGIN,VA ;GET PHYSICAL ADDRESS FROM 16-BIT VA
004224 000367 174034 SWAB EA ;BITS 4+5 TO 13+12
004230 006167 174030 ROL EA ;NOW 14+13
004234 006167 174024 ROL EA ;NOW 15+14
004240 042767 037776 174016 BIC #37776,EA ;CLEAR ALL BUT 14 & 15
004246 000207 RTS PC ;RETURN
    
```

```

0 004250          UISR:
004250 010577 002102      MOV  R5,#OUTQIN      ;MOVE LINK POINTER TO QUEUE
004254 062767 000002 002074  ADD  #2,OUTQIN      ;UPDATE QUEUE
004262 022767 006352' 002066  CMP  #PIROUTQ+200,OUTQIN ;END OF QUEUE?
004270 001003          HNE  1$             ;BR IF NO
004272 012767 006152' 002056  MOV  #PIROUTQ,OUTQIN ;RESET QUEUE POINTER
004300          1$:
004300 012605          MOV  (SP)+,R5       ;RESTORE R5
-----
004302 000004 000000' 004310'  PIRQS,BEGIN,2$     ; QUEUE UP TO CONTINUE AT 2$ AND RTI
-----
004310          2$:
004310 017705 002044      MOV  @OUTQOUT,R5     ;GET LINK POINTER FROM QUEUE
004314 062767 000002 002036  ADD  #2,OUTQOUT     ;UPDATE QUEUE
004322 022767 006352' 002030  CMP  #PIROUTQ+200,OUTQOUT ;END OF QUEUE?
004330 001003          HNE  3$             ;BR IF NO
004332 012767 006152' 002020  MOV  #PIROUTQ,OUTQOUT ;RESET QUEUE POINTER
004340          3$:
004340 011501          MOV  (R5),R1        ;LOAD CSR ADDRESS
004342 032761 000001 000002  BIT  #BIT0,2(R1)    ;CONTROL OUT?
004350 001451          BEQ  15$            ;BR IF NO
004352 032761 001000 000006  HIT  #HALTC,6(R1)   ;HALT?
004360 001407          BEQ  5$             ;BR IF NOT
004362 052765 000040 000002  BIS  #40,2(R5)      ;SET THE END OF PASS FLAG.
004370 042761 000100 000002  BIC  #100,2(R1)     ;CLEAR OIE IF YES
004376 000407          BR   8$
004400          5$:
004400 005767 173646      IS1  MODE           ;ARE WE IN DMC MODE?
004404 001411          BEQ  10$            ;IF YES, CAN ONLY BE AN ERROR
004406 032761 020040 000006  RIT  #OK,6(R1)      ;IF DMR, IS THIS DMR RUN OR
                                ;BASE TABLE UPDATE
                                ;IF NOT, MUST BE AN ERROR.
004414 001405          BEQ  10$
004416          8$:
004416 142761 000207 000002  BICB #207,2(R1)     ;CLEAR RDU (NOT AN ERROR)
004424 104400 000000'     EXITS,BEGIN        ;EXIT TO MONITOR. MODULE WAIT FOR INTERRUPT.
004430          10$:
004430 010167 173444      MOV  R1,CSRA        ;LOAD DEVICE CSR
004434 016167 000004 173440  MOV  4(R1),ACSP     ;LOAD CONTENTS OF DEVICE CSR
004442 016167 000006 173434  MOV  6(R1),ASTAT    ;LOAD ERROR BITS
004450 005067 173432      CLR  ERRTP          ;UNKNOWN ERROR
                                ;*****
004454 104405 000000' 000000  HKDEKS,BEGIN,NULL  ;A CNTL 0 WAS RECEIVED, ASTAT=ERROR BITS
                                ;*****
004462 142761 000207 000002  BICB #207,2(R1)     ;CLEAR RDU
004470 104400 000000'     EXITS,BEGIN        ;EXIT TO MONITOR. MODULE WAIT FOR INTERRUPT.
004474          15$:
004474 032761 000004 000002  BIT  #BIT2,2(R1)    ;RECEIVER DONE?
004502 001017          HNE  17$            ;BR IF YES
004504 142761 000207 000002  BICB #207,2(R1)     ;CLEAR RDU
004512 105265 000013      INCB 13(R5)         ;HI BYTE IS XMIT DONE COUNT
004516 126765 173524 000013  CMPB BUFNUM,13(R5)  ;DO WE HAVE ALL XMIT DONES YET?
004524 001004          HNE  16$            ;BR IF NO
004526 052765 000010 000002  BIS  #10,2(R5)      ;SET BIT3 IN ENDPASS FLAG TO
                                ;SHOW THAT WE GOT 7 XMIT DONES
004534 000416          BR   16$
004536          16$:
004536 104400 000000'     EXITS,BEGIN        ;EXIT TO MONITOR. MODULE WAIT FOR INTERRUPT.
    
```

```

004542          17$:
004542 142761 000207 000002  BICB #207,2(R1)     ;CLEAR RDU
004550 105265 000012      INCB 12(R5)         ;LO BYTE IS REC DONE COUNT
004554 126765 173466 000012  CMPB BUFNUM,12(R5)  ;DO WE HAVE ALL REC DONES YET?
004562 001027          HNE  20$            ;BR IF NO
004564 052765 000020 000002  BIS  #20,2(R5)      ;SET BIT4 IN ENDPASS FLAG TO
                                ;SHOW THAT WE GOT ALL 7 REC DONES
004572          18$:
004572 026765 173434 000002  CMP  FLAGA,2(R5)    ;ALL BUT HALT DONE?
004600 001020          HNE  20$            ;BR IF NOT
004602 005767 173432      TST  LAST           ;IS THIS THE LAST PASS?
004606 001403          BEQ  19$            ;IF NOT - DON'T HALT
004610 052711 000042      BIS  #42,(R1)       ;ISSUE A HALT.
004614 000412          BR   20$
004616          19$:
004616 052765 000040 000002  BIS  #40,2(R5)      ;SET THE END OF PASS FLAG.
004624 042711 000100      BIC  #100,(R1)      ;CLEAR OIE IF YES
004630 042761 000100 000002  BIC  #100,2(R1)     ;CLEAR OIE IF YES
004636 042711 100000      BIC  #BIT15,(R1)    ;TURN OFF THE RUN BIT.
004642          20$:
004642 104400 000000'     EXITS,BEGIN        ;EXIT TO MONITOR. MODULE WAIT FOR INTERRUPT.
    
```

```

0                               ;LINK TABLE TO INTERRUPT SERVICE ROUTINES
                                ;-----
                                INTLNK:
004646                          JSR      R5,IISR
004646 004567 176046            JSR      R5,OISR
004652 004567 177372
004656 000000                  CSR1:   .WORD
004660 000000                  XX11:  .WORD
004662 006362'                 RCVBUF
004664 007362'                 BASE1
004666 000000                  XX12:  .WORD
004670 000000                  XX13:  .WORD
004672 000000                  .WORD
004674 000000                  .WORD
004676 000000                  .WORD
004700 000000                  .WORD
004702 000000                  XX14:  .WORD
004704 000000                  XX15:  .WORD
004706 000000                  XX16:  .WORD

                                ;DMR CSR FOR DEVICE 1
                                ;END PASS FLAG FOR DEVICE 1
                                ;RECEIVE BUFFER 1
                                ;BASE ADDRESS FOR DEVICE 1
                                ;BACCI XMIT/REC COUNTERS
                                ;BACCO XMIT/REC COUNTERS
                                ;THESE NEXT 8 BYTES ARE FOR
                                ;THE DDCMP ERROR COUNTS
                                ;IN THE BASE TABLE TO BE
                                ;SAVED FOR COMPARISION.
                                ;DMR EXT FLAG FOR THE DEVICE 1
                                ;THIS FLAG CUNTROLS DMR COMMANDS.
                                ;PHYSICAL BASE ADDR FOR DEVICE 1
                                ;EXTENDED BASE ADDR FOR DEVICE 1

004710 004567 176004            JSR      R5,IISR
004714 004567 177330            JSR      R5,OISR
004720 000000                  CSR2:   .WORD
004722 000000                  XX21:  .WORD
004724 006362'                 RCVBUF
004726 007362'                 BASE2
004730 000000                  XX22:  .WORD
004732 000000                  XX23:  .WORD
004734 000000                  .WORD
004736 000000                  .WORD
004740 000000                  .WORD
004742 000000                  .WORD
004744 000000                  XX24:  .WORD
004746 000000                  XX25:  .WORD
004750 000000                  XX26:  .WORD

                                ;DMR CSR FOR DEVICE 2
                                ;END PASS FLAG FOR DEVICE 2
                                ;RECEIVE BUFFER 2
                                ;BASE ADDRESS FOR DEVICE 2
                                ;BACCI XMIT/REC COUNTERS
                                ;BACCO XMIT/REC COUNTERS
                                ;THESE NEXT 8 BYTES ARE FOR
                                ;THE DDCMP ERROR COUNTS
                                ;IN THE BASE TABLE TO BE
                                ;SAVED FOR COMPARISION.
                                ;DMR EXT FLAG FOR THE DEVICE 2
                                ;THIS FLAG CUNTROLS DMR COMMANDS.
                                ;PHYSICAL BASE ADDR FOR DEVICE 2
                                ;EXTENDED BASE ADDR FOR DEVICE 2
    
```

```

0                               ;BUFFERS & QUEUES
                                ;-----
004752                          XBUF:   ;TRANSMIT BUFFER (512. BYTES)
                                ; ** CCIIT PSEUDO-RANDOM TEST PATTERN **
                                ; THE FOLLOWING 32 WORDS TRANSLATE INTO A 512.
                                ; BIT PATTERN THAT WAS GENERATED ACCORDING TO CCIIT
                                ; RECOMMENDATION V.52. NOTE: THE RECOMMENDED PATIEMN
                                ; IS 511 BITS - THIS WAS EXTENDED BY 1 BIT TO END
                                ; ON AN EVEN WORD BOUNDARY.

                                .HEPT  H.
004752 177603 157427 031011    .WORD  177603,157427,031011
004760 047321 163715 105221    .WORD  047321,163715,105221
004766 143325 142304 040041    .WORD  143325,142304,040041
004774 014116 052606 172334    .WORD  014116,052606,172334
005002 105025 123754 111337    .WORD  105025,123754,111337
005010 111523 030030 145064    .WORD  111523,030030,145064
005016 137642 143531 063617    .WORD  137642,143531,063617
005024 135015 066730 026575    .WORD  135015,066730,026575
005032 052012 053627 070071    .WORD  052012,053627,070071
005040 151172 165044 031605    .WORD  151172,165044,031605
005046 166632 016741          .WORD  166632,016741

                                .LIST  ME

005752                          PIRING: .BLKW 100      ;QUEUE FOR 64. IN INTERRUPTS
006152                          PIROUT: .BLKW 100      ;QUEUE FOR 64. OUT INTERRUPTS
006352 000000                  INQIN:  0
006354 000000                  INQUOT: 0
006356 000000                  OUTQIN: 0
006360 000000                  OUTQUOT:0
006362                          RCVBUF: .BLKB  512.    ;RECEIVE BUFFER IS 512. BYTES

007362                          BASE1:  .BLKB  256.    ;BASE TABLE FOR DEVICE 1

007762                          BASE2:  .BLKB  256.    ;BASE TABLE FOR DEVICE 2

                                ;PATCH AREA
                                ;-----
010362 000240                  PATCH:  NUP
                                .BLKW  20.                ;PATCH AREA

1                               .END
    
```


.REM -

IDENTIFICATION

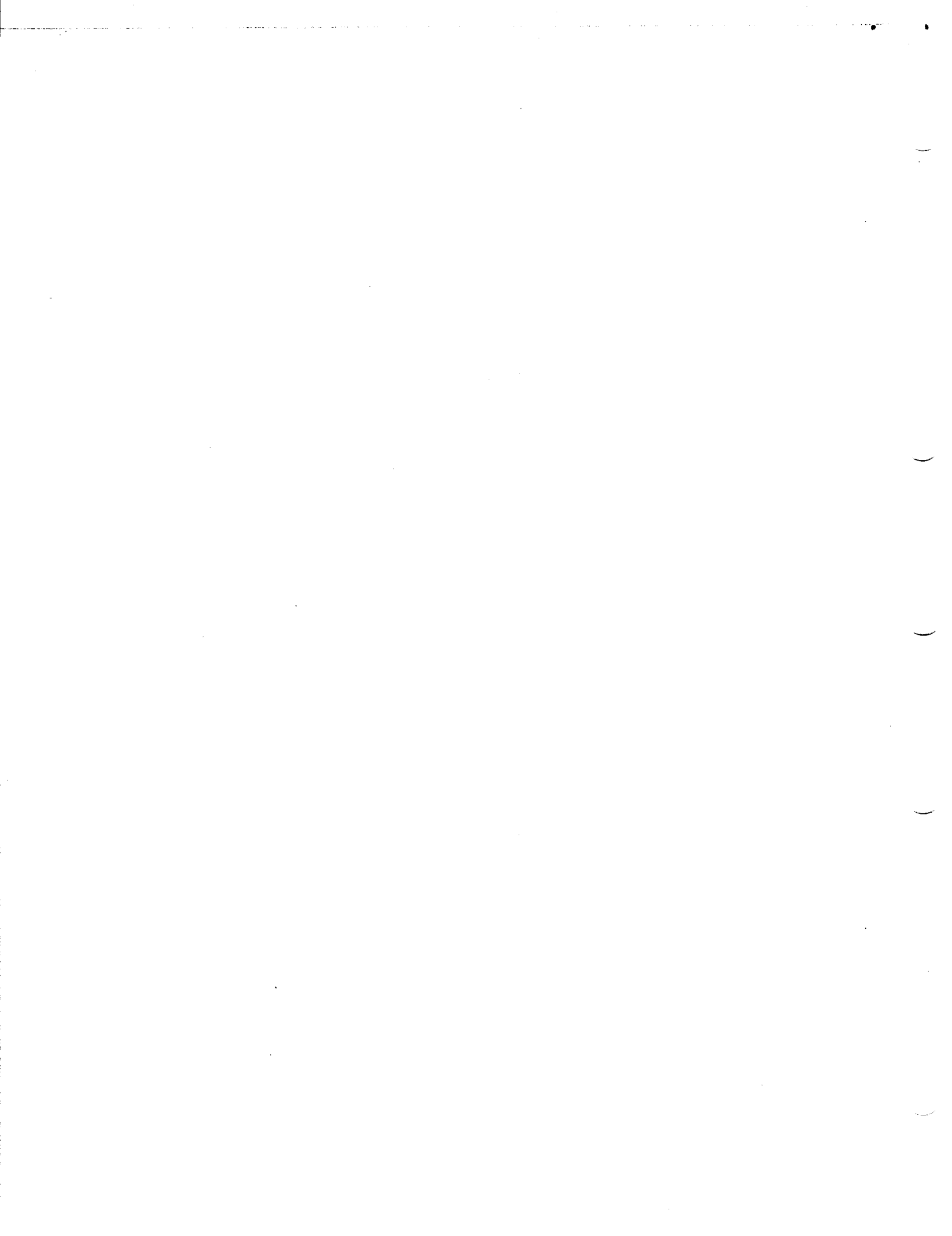
PRODUCT CODE: AC-FB18A-MC
PRODUCT NAME: CXDRJAO DRV11-J MODULE
PRODUCT DATE: JANUARY 1981
MAINTAINER: DEC/X11 SUPPORT GROUP

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1. ABSTRACT

DRJA IS AN IOMOD THAT EXERCISES ONE DRV11-J. THE MODULE USES A LOOP BACK CABLE TO CHECK DATA TRANSFERS TO AND FROM THE DRV11-J. IT TRANSMITS AND RECEIVES A SERIES OF WORST-CASE BUS PATTERNS AND ALSO TESTS THE ABILITY OF THE DRV11J TO GENERATE BOTH SEND AND RECEIVE INTERRUPTS.

2. REQUIREMENTS

HARDWARE: ONE DRV11-J WITH A BC05W-02 CABLE WHICH MUST BE INSTALLED WITH 1/2 TWIST BETWEEN THE 50 PIN CONNECTORS TO LOOPBACK OUTPUTS TO INPUTS.

STORAGE:: DRJA REQUIRES:

1. DECIMAL WORDS: 458
2. OCTAL WORDS: 712
3. OCTAL BYTES: 1624

3. PASS DEFINITION

ONE ITERATION OF THE DRJA MODULE CONSISTS OF SENDING AND RECEIVING 128 WORDS AND GENERATING 2 SEND AND 2 RECEIVE INTERRUPTS.

ONE PASS IS EQUAL TO 900. ITERATIONS.

4. EXECUTION TIME

ONE PASS OF DRJA RUNNING ALONE ON A PDP11/03 PROCESSOR TAKES APPROXIMATELY 25 SECONDS.

5. CONFIGURATION REQUIREMENTS

DEFAULT PARAMETERS:

DEVADR: 164160, VECTOR: 300, BK1: 4, DEVCN1: 1

REQUIRED PARAMETERS:

AT CONFIGURATION TIME MODIFY "VECTOR" IF SYSTEM SPECIFIES OTHER THAN 300

THE DRV11-J HAS PROGRAMMABLE VECTORS AND AT RUN TIME THE DRJA MODULE WILL SELECT THE CONFIGURED VECTOR AND SETUP 4 VECTORS IN INCREMENTS ABOVE THE SELECTED VECTOR.

EA: 300,304,310,314

6. DEVICE/OPTION SET-UP

CONNECT THE BC05W-02 CABLE WITH 1/2 TWIST BETWEEN THE
50 PIN CONNECTORS TO TIE OUTPUT BACK TO INPUT.

7. MODULE OPERATION

TEST SEQUENCE:

- A. SET UP VECTORS AND ADDRESS POINTER
- B. OUTPUT TEST DATA TO OUTPUT BUFFER
- C. COMPARE OUTPUT BUFFER WITH TEST DATA-REPORT ANY DATA
ERROR
- D. COMPARE INPUT BUFFER WITH TEST DATA-REPORT ANY DATA ERROR
- E. IF NOT 128 TRANSFERS, NEXT TEST DATUM--REPEAT B-D
- F. IF FINISHED GENERATE AND TEST INPUT/OUTPUT INTERRUPTS.
- G. IF NO INTERRUPT - DO NOT REPORT END PASS
- H. IF INTERRUPT - REPORT END PASS RESTART AT A

8. OPERATION OPTIONS

NONE

9. NON-STANDARD PRINTOUTS

NONE: ALL PRINTOUTS HAVE THE STANDARD FORMATS DESCRIBED IN THE
DEC/X11 DOCUMENT

```
DRV11-J DEC/X11 EXERCISER MODULE
000000' IOMOD <DRJA >,164160,300,4,,,900,,0
000000' MODULE 140000,DRJA ,164160,300,4,,,900,,0
; .TITLE DRJA DEC/X11 SYSTEM EXERCISER MODULE
; DDXCOM VERSION 6 23-MAY-78
; .LIST BIN
;*****
000000' BEGIN:
000000' 051104 040512 040 MODNAM: .ASCII /DRJA / ;MODULE NAME.
000005' 000 XFLAG: .BYTE OPEN ;USED TO KEEP TRACK OF WBUFF USAGE
000006' 164160 ADDR: 164160+0 ;1ST DEVICE ADDR.
000010' 000300 VECTOR: 300+0 ;1ST DEVICE VECTOR.
000012' 200 BR1: .BYTE PRTY4+0 ;1ST BR LEVEL.
000013' 000 BR2: .BYTE PRTY+0 ;2ND BR LEVEL.
000014' 000001 DVID1: +1 ;DEVICE INDICATOR 1.
000016' 000000 SR1: OPEN ;SWITCH REGISTER 1
000020' 000000 SR2: OPEN ;SWITCH REGISTER 2
000022' 000000 SR3: OPEN ;SWITCH REGISTER 3
000024' 000000 SR4: OPEN ;SWITCH REGISTER 4
;*****
000026' 140000 STAT: 140000 ;STATUS WORD.
000030' 000262' INIT: START ;MODULE START ADDR.
000032' 000224' SPOINT: MODSP ;MODULE STACK POINTER.
000034' 000000 PASCNT: 0 ;PASS COUNTER.
000036' 001604 ICONT: 900. ;# OF ITERATIONS PER PASS=900.
000040' 000000 ICOUNT: 0 ;LOC TO COUNT ITERATIONS
000042' 000000 SOFCNT: 0 ;LOC TO SAVE TOTAL SOFT ERRORS
000044' 000000 HRDCNT: 0 ;LOC TO SAVE TOTAL HARD ERRORS
000046' 000000 SOFPAS: 0 ;LOC TO SAVE SOFT ERRORS PER PASS
000050' 000000 HRDPAS: 0 ;LOC TO SAVE HARD ERRORS PER PASS
000052' 000000 SYSCNT: 0 ;# OF SYS ERRORS ACCUMULATED
000054' 000000 RANNUM: 0 ;HOLDS RANDOM # WHEN RAND MACRO IS CALLED
000056' 000000 CONFIG: ;RESERVED FOR MONITOR USE
000056' 000000 RES1: 0 ;RESERVED FOR MONITOR USE
000060' 000000 RES2: 0 ;RESERVED FOR MONITOR USE
000062' 000000 SVR0: OPEN ;LOC TO SAVE R0.
000064' 000000 SVR1: OPEN ;LOC TO SAVE R1.
000066' 000000 SVR2: OPEN ;LOC TO SAVE R2.
000070' 000000 SVR3: OPEN ;LOC TO SAVE R3.
000072' 000000 SVR4: OPEN ;LOC TO SAVE R4.
000074' 000000 SVR5: OPEN ;LOC TO SAVE R5.
000076' 000000 SVR6: OPEN ;LOC TO SAVE R6.
000100' 000000 CSRA: OPEN ;ADDR OF CURRENT CSR.
000102' SBADR: ;ADDR OF GOOD DATA, OR
000102' 000000 ACSR: OPEN ;CONTENTS OF CSR.
000104' WASADR: ;ADDR OF BAD DATA, OR
000104' 000000 ASTAT: OPEN ;STATUS REG CONTENTS.
000106' ERRTYP: ;TYPE OF ERROR
000106' 000000 ASB: OPEN ;EXPECTED DATA.
000110' 000000 AWAS: OPEN ;ACTUAL DATA.
000112' 000304' RSTRT: RESTRT ;RESTART ADDRESS AFTER END OF PASS
000114' 000000 WDTU: OPEN ;WORDS TO MEMORY PER ITERATION
000116' 000000 WDFR: OPEN ;WORDS FROM MEMORY PER ITERATION
000120' 000000 INTR: OPEN ;# OF INTERRUPTS PER ITERATION
000122' 000000 IDNUM: 0 ;MODULE IDENTIFICATION NUMBER=0
```

```

000040          .REPT  SPSIZ          ;MODULE STACK STARTS HERE.
                .NLIST
                .WORD  0
                .LIST
                .ENDR

000224' MODSP:
;*****
;
;
;THIS MODULE TESTS THE DRV11-J GENERAL DEVICE INTERFACE
;8C05W-02 CABLE MUST BE INSTALLED WITH A 1/2 TWIST BETWEEN THE
;50 PIN CONNECTORS TO LOOPBACK OUTPUTS TO INPUTS.
;
;DRV11-J BUS REGISTER ADDRESS POINTERS
522 000224' 164160  DRVJCA: 164160
523 000226' 164162  DRVJBA: 164162
524
525 000230' 164164  DRVJCB: 164164
526 000232' 164166  DRVJBB: 164166
527
528 000234' 164170  DRVJCC: 164170
529 000236' 164172  DRVJBC: 164172
530
531 000240' 164174  DRVJCD: 164174
532 000242' 164176  DRVJBD: 164176
533
534
535          ;DRV11-J VECTOR ADDRESS POINTERS
536 000244' 000300  DRVECA: 300
537 000246' 000304  DRVECB: 304
538 000250' 000310  DRVECC: 310
539 000252' 000314  DRVECD: 314
540
541          ;LOCAL VARIABLES
542 000254' 177777  ALLON: -1
543 000256' 000000  ALLOFF: 0
544 000260' 177776  BUBBLE: 177776
545
546
547
548
549
550          ;INITIALIZATION FOR GENERAL DEVICE INTERFACE
551
552
553 000262' 012767  000004  177630  START:  MOV    #4.,INTR          ;4 INTERRUPTS PER ITERATION
554 000270' 012767  000200  177616          MOV    #128.,WDTU        ;128. WDS TO MEM PER ITERATION
555 000276' 012767  000200  177612          MOV    #128.,WDFR        ;128. WDS FROM MEM PER ITERATION
556
557 000304' 012700  000224'          RESTR1: MOV    #DRVJCA,R0      ;SET UP REGS ADDR POINTER
558 000310' 016701  177472          MOV    ADDR,R1            ;GET BASE ADDRESS
559 000314' 010120          SETUP2: MOV   R1,(R0)+        ;LOAD IHFM
560 000316' 062701  000002          ADD    #2,R1             ;GET NEXT READY
561 000322' 022700  000244'          CMP    #DRVJBD+2,R0        ;DONE?

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562 000326' 001372      BNE      SETUP2      ;BR IF NOT
563 000330' 012700 000244' MOV      #DRVECA,R0   ;SET UP VECTOR ADDRESS POINTER
564 000334' 016701 177450 MOV      VECTOR,R1   ;GET BASE VECTOR ADDRESS
565 000340' 010120      MOV      R1,(R0)+
566 000342' 062701 000004 ADD      #4,R1        ;POINT TO NEXT
567 000346' 022700 000254' CMP      #DRVECD+2,R0 ;ALL DONE?
568 000352' 001372      BNE      SETUP3      ;BR IF NOT
569 000354' 016705 177644 MOV      DRVJCA,R5   ;STORE CSRA IN R5
570 000360' 010567 177514 MOV      R5,CSRA
571 000364' 016700 177654 MOV      DRVECA,R0   ;SETUP INTERRUPT SERVICE
572 000370' 012720 001556' MOV      #DRASRV,(R0)+ ;RPLY A SERVICE ROUTINE
573 000374' 016720 177412 MOV      BR1,(R0)+   ;STORE PRIORITY
574 000400' 012720 001570' MOV      #DRBSRV,(R0)+ ;RPLY B SERVICE ROUTINE
575 000404' 016720 177402 MOV      BR1,(R0)+   ;STORE PRIORITY
576 000410' 012720 001602' MOV      #DRCSRVR,(R0)+ ;RPLY C SERVICE ROUTINE
577 000414' 016720 177372 MOV      BR1,(R0)+   ;STORE PRIORITY
578 000420' 012720 001614' MOV      #DRDSRV,(R0)+ ;RPLY D SERVICE ROUTINE
579 000424' 016720 177362 MOV      BR1,(R0)+   ;STORE PRIORITY
580 000430' 005015      CLR      (R5)        ;CLEAR CSRA AND INT. ENABLE
581 000432' 005065 000010 CLR      10(R5)      ;CLEAR CSRC,SET INPUT MODE
582 000436' 105065 000015 CLR      15(R5)      ;CLEAR CSRD,SET INPUT MODE
583 000442' 112765 000001 MOV      #BIT0,1(R5) ;SET CSRA INTO OUTPUT MODE
584 000450' 112765 000001 MOV      #BIT0,5(R5) ;SET CSRB INTO OUTPUT MODE
585 000456' 016701 177572 MOV      ALLOW,R1    ;SET UP INITIAL TEST PATTERNS; 177777
586 000462' 016702 177572 MOV      BUBBLE,R2   ; AND 177776 (0 WILL BUBBLE TO LEFT)
587 000466' 010103      MOV      R1,R3       ;R3 REMEMBERS ALLOW/ALLOFF WHEN BUBBLE IS IN USE
588 000470' 010104      MOV      R1,R4       ;C-BIT SET WORD FOR BUBBLE ROL
589
590
591
592 ;CHECK DATA TRANSFER ON DRV11-J
593 ;
594 ; TRANSMIT, RECIEVE AND CHECK (VIA BC05W-02 CABLE) A SERIES OF
595 ; 64 WORD PATTERNS FORCING 'WORST-CASE' TRANSITIONS. THIS
596 ; CONSISTS OF 177777 FOLLOWED BY 177776, 177777, 177775, 177777,
597 ; 177773, 177777, 177767, ETC (ALL ONES ALTERNATING WITH A
598 ; 0 BUBBLING LEFT THROUGH A WORD OF ONES), THEN ALL ZEROS ALTERN-
599 ; ATING WITH THE SAME BUBBLE PATTERN.
600
601 000472' 010567 177402 DRACT1: MOV      R5,CSRA ;SAVE CSR ADDRESS
602 000476' 010165 000002 MOV      R1,2(R5)    ;MOVE DATA TO OUTPUT BUFFER DBRA
603 000502' 020165 000002 CMP      R1,2(R5)    ;CHECK DATA AT DBRA
604 000506' 001421      BEQ      1$         ;BRANCH IF DATA GOOD
605 000510' 012767 177701 177364 MOV      #177701,SBADR ;GOOD DATA ADDRESS
606 000516' 062705 000002 ADD      #2, R5      ;ADD 2 TO GET DATA REG. ADDRESS
607 000522' 010567 177356 MOV      R5, WASADR  ;BAD DATA ADDRESS
608 000526' 162705 000002 SUB      #2, R5      ;RESTORE R5
609 000532' 010167 177350 MOV      R1,ASB     ;MOVE 'SHOULD BE'
610 000536' 016567 000002 177344 MOV      2(R5),A$AS ;MOVE 'WAS'
611 ;*****
612 (1) 000544' 104404 000000' DATERS,BEGIN ;DATA ERROR!!!
613 (1) ;*****
614 000550' 000506      BR      4$         ;NEXT DATA
615 000552' 016767 177452 177320 1$: MOV      DRVJCB,CSRA ;SAVE CSRB ADDRESS
616 000560' 010165 000006 MOV      R1,6(R5)   ;MOVE DATA TO OUTPUT BUFFER DBRB
617 000564' 020165 000006 CMP      R1,6(R5)   ;CHECK DATA AT DBRB

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```
616 000570' 001421      BEQ      2S          ;BRANCH IF DATA GOOD
617 000572' 012767 177701 177302  MOV      #177701,SBADR ;GOOD DATA ADDRESS
618 000600' 062705 000006      ADD      #6, R5      ;ADD 2 TO GET DATA REG. ADDRESS
619 000604' 010567 177274      MOV      R5, WASADR  ;BAD DATA ADDRESS
620 000610' 162705 000006      SUB      #6, R5      ;RESTORE R5
621 000614' 010167 177266      MOV      R1,ASB      ;MOVE 'SHOULD BE'
622 000620' 016567 000006 177262  MOV      6(R5),AWAS   ;MOVE 'WAS'
623                                     ;*****
(1) 000626' 104404 000000'      DATERS,BEGIN        ;DATA ERROR!!!
(1)                                     ;*****
624 000632' 000455      BR      4S          ;NEXT DATA
625 000634' 016767 177374 177236 2S:  MOV      DRVJCC,CSRA  ;SAVE CSRC ADDRESS
626 000642' 020165 000012      CMP      R1,12(R5)   ;CHECK RECEIVED DATA AT DBRC
627 000646' 001421      BEQ      3S          ;BRANCH IF DATA GOOD
628 000650' 012767 177701 177224  MOV      #177701,SBADR ;GOOD DATA ADDRESS
629 000656' 062705 000012      ADD      #12, R5     ;GET INPUT DATA ADDRESS
630 000662' 010567 177216      MOV      R5, WASADR  ;BAD DATA ADDRESS
631 000666' 162705 000012      SUB      #12, R5     ;RESTORE R5
632 000672' 010167 177210      MOV      R1,ASB      ;MOVE 'SHOULD BE'
633 000676' 016567 000012 177204  MOV      12(R5),AWAS  ;MOVE 'WAS'
634                                     ;*****
(1) 000704' 104404 000000'      DATERS,BEGIN        ;DATA ERROR!!!
(1)                                     ;*****
635 000710' 000426      BR      4S          ;NEXT DATA
636 000712' 016767 177322 177160 3S:  MOV      DRVJCD,CSRA  ;SAVE CSRD ADDRESS
637 000720' 020165 000016      CMP      R1,16(R5)   ;CHECK RECEIVED DATA AT DBRD
638 000724' 001420      BEQ      4S          ;BRANCH IF DATA GOOD
639 000726' 012767 177701 177146  MOV      #177701,SBADR ;GOOD DATA ADDRESS
640 000734' 062705 000016      ADD      #16, R5     ;GET INPUT DATA ADDRESS
641 000740' 010567 177140      MOV      R5, WASADR  ;BAD DATA ADDRESS
642 000744' 162705 000016      SUB      #16, R5     ;RESTORE R5
643 000750' 010167 177132      MOV      R1,ASB      ;MOVE 'SHOULD BE'
644 000754' 016567 000016 177126  MOV      16(R5),AWAS  ;MOVE 'WAS'
645                                     ;*****
(1) 000762' 104404 000000'      DATERS,BEGIN        ;DATA ERROR!!!
(1)                                     ;*****
646
647                                     ;CHECK CURRENT DATA PATTERN AND LOAD NEXT ACCORDINGLY
648 ;
649 000766' 020167 177262      4S:  CMP      R1,ALLON    ;IF JUST TESTED WITH
650 000772' 001405      BEQ      5S          ; ALL ON/OFF PATTERN,
651 000774' 020167 177256      CMP      R1,ALLOFF   ; LOAD THE CURRENT BUBBLE
652 001000' 001402      BEQ      5S          ; FOR NEXT TEST CYCLE
653 001002' 010301      MOV      R3,R1       ;NOT BUBBLE; LOAD CURRENT ALL ON/OFF
654 001004' 000632      BR      DRAC11      ;TEST WITH IT
655 001006' 010201      5S:  MOV      R2,R1       ;SET UP BUBBLE FOR THIS CYCLE
656 001010' 006304      ASL      R4          ;PRE-LOAD C-BIT FOR ROL
657 001012' 006102      ROL      R2          ;BUBBLE 0 TO LEFT ONE BIT
658 001014' 020267 177240      CMP      R2,BUBBLE   ;HAS IT CIRCLED ALL THE WAY?
659 001020' 001401      BEQ      6S          ;YES: SWITCH ALLON-ALLOFF OR END
660 001022' 000623      BR      DRAC11      ;TEST WITH THE BUBBLE
661 001024' 005703      6S:  TST      R3          ;ALLON OR ALLOFF?
662 001026' 001405      BEQ      INTES1     ;IF ALLOFF, FINISHED: TEST INTERRUPTS
663 001030' 010304      MOV      R3,R4       ;RESET C-BIT PRE-LOAD
664 001032' 016703 177220      MOV      ALLOFF,R3   ;SWITCH TO ALLOFF
665 001036' 010301      MOV      R3,R1       ;SET UP FOR TEST
```

```
666 001040' 000614 BR DRAC11 ;THEN DO IT
667
668
669 ;CHECK INTERRUPTS ON DRV11-J
670
671 001042' 016705 177156 INTST: MOV DRVJCA,R5 ;R5 = CSRA ADDRESS
672 001046' 016767 177166 177030 MOV DRVJCD,WASADR ;SAVE CSRD ADDRESS WHICH WILL BE
673 ;USED TO CHECK ISR REGISTER
674 001054' 105015 CLR B (R5) ;CHIP RESET GROUP 1
675 001056' 105065 000010 CLRB 10(R5) ;CHIP RESET GROUP 2
676 ;IMR SET TO ALL ONES
677 ;ISR,IRR,ACK AND MODE
678 ;REGISTER ALL CLEARED
679 001062' 016702 177156 MOV DRVECA,R2 ;STORE FIRST VECTOR TO
680 ;PROGRAM ON THE DRV11-J.
681 ROR R2
682 001070' 006002 ROR R2 ;JUSTIFY THE VECTOR
683 ;FOR THE DDRV11-J VECTOR MEMORY
684 001072' 012703 000344 MOV #344,R3 ;START WITH BYTE COUNT = 0
685 ;AND LEVEL 4 FOR RPLY A.
686 001076' 012701 000004 MOV #4,R1 ;COUNTER FOR 4 VECTORS.
687 001102' 110365 000010 1S: MOV R3,10(R5) ;PRESET VECTOR MEMORY IN CSRC
688 001106' 110265 000014 MOV R2,14(R5) ;WRITE VECTOR INTO VECTOR MEMORY
689 ;THROUGH CSRD
690 001112' 005203 INC R3 ;INCREMENT IRR LEVEL
691 001114' 005202 INC R2 ;INCREMENT VECTOR
692 001116' 005301 DEC R1 ;FINISHED 4 VECTORS?
693 001120' 001370 BNE 1S ;DO VECTORS FOR RPLY A,B,C,D
694 001122' 112765 000260 000010 MOV #260,10(R5) ;PRESELECT IMR FOR WRITING
695 001130' 112765 000017 000014 MOV #17,14(R5) ;WRITE GROUP2 IMR THROUGH CSRD
696 ;TO CLEAR MASK BITS FOR RPLY A,B,C,D
697 001136' 112715 000241 MOV #241,(R5) ;ARM GROUP 1 CHIP TO PASS ENABLE
698 ;TO GROUP2.
699 001142' 112765 000241 000010 MOV #241,10(R5) ;ARM GROUP2 TO ALLOW INTERRUPTS
700
701 ;WRITE DATA INTO OUTPUT BUFFER DBRA AND CAUSE INTERRUPT
702 ;AT IRR6 RPLY C
703
704 001150' 012765 177777 000002 TSINTC: MOV #-1,2(R5) ;WRITE INTO OUTPUT BUFFER
705 001156' 052715 001000 BIS #BIT9,(R5) ;SET INTERRUPT ENABLE
706 001162' 104400 000000' EXITS,BEGIN ;EXIT TO MONITOR. MODULE WAIT FOR INTERRUPT.
707 001166' 112765 000240 000010 CHECKC: MOV #240,10(R5) ;RETURN FOR RPLY C
708 ;INTERRUPT CHECK.
709 ;SETUP TO READ ISR REGISTER
710 001174' 012701 000100 MOV #100,R1 ;ISR 6 EXPECTED
711 001200' 116567 000014 176702 MOV 14(R5),AWAS ;READ ISR REGISTER THROUGH CSRD
712 001206' 020167 176676 CMP R1,AWAS ;COMPARE ISR DATA WITH EXPECTED
713 001212' 001415 BEQ TSINTA ;BRANCH IF GOOD DATA
714 001214' 016767 177014 176656 MOV DRVJCC,CSRA ;SAVE CSRC FOR ERROR PRINT
715 001222' 012767 177701 176652 MOV #177701,SBADR ;GOOD DATA ADDRESS
716 001230' 016767 177004 176646 MOV DRVJCD,WASADR ;BAD DATA ADDRESS
717 001236' 010167 176644 MOV R1,ASB ;SHOULD BE DATA
718 ;*****
(1) 001242' 104404 000000' DATERS,BEGIN ;DATA ERROR!!!
(1) ;*****
719
```

```
720 ;READ DATA FROM INPUT BUFFER DBRC AND CAUSE INTERRUPT
721 ;AT IRK4 RPLY A
722
723 001246' 012765 000160 000010 TSINTA: MOV #160,10(R5) ;CLEAR THE ISR REGISTER
724 001254' 016502 000012 MOV 12(R5),R2 ;READ DBRC INPUT BUFFER
725 001260' 104400 000000' EXITS,BEGIN ;EXIT TO MONITOR. MODULE WAIT FOR INTERRUPT.
726 001264' 112765 000240 000010 CHECKA: MOV #240,10(R5) ;RETURN FOR RPLY A
727 ;INTERRUPT CHECK.
728 ;SETUP TO READ ISR REGISTER
729 001272' 012701 000020 MOV #20,R1 ;ISR 4 EXPECTED
730 001276' 116567 000014 176604 MOV #14(R5),AWAS ;READ ISR REGISTER THROUGH CSRD
731 001304' 020167 176600 CMP R1,AWAS ;COMPARE ISR DATA WITH EXPECTED
732 001310' 001415 BEQ TSINTD ;BRANCH IF GOOD DATA
733 001312' 016767 176706 176560 MOV DRVJCA,CSRA ;SAVE CSRA FOR ERROR PRINT
734 001320' 012767 177701 176554 MOV #177701,SBADR ;GOOD DATA ADDRESS
735 001326' 016767 176706 176550 MOV DRVJCD,WASADR ;BAD DATA ADDRESS
736 001334' 010167 176546 MOV R1,ASB ;SHOULD BE DATA
737 ;*****
(1) 001340' 104404 000000' DATERS,BEGIN ;DATA ERROR!!!
(1) ;*****
738
739 ;WRITE DATA INTO OUTPUT BUFFER DBRB AND CAUSE INTERRUPT
740 ;AT IRP7 RPLY D
741
742 001344' 012765 000160 000010 TSINTD: MOV #160,10(R5) ;CLEAR THE ISR REGISTER
743 001352' 012765 177777 000006 MOV #-1,6(R5) ;WRITE INTO OUTPUT BUFFER DBRB
744 001360' 104400 000000' EXITS,BEGIN ;EXIT TO MONITOR. MODULE WAIT FOR INTERRUPT.
745 001364' 112765 000240 000010 CHECKD: MOV #240,10(R5) ;RETURN FOR RPLY D
746 ;INTERRUPT CHECK.
747 ;SETUP TO READ ISR REGISTER
748 001372' 012701 000200 MOV #200,R1 ;ISR 7 EXPECTED
749 001376' 116567 000014 176504 MOV #14(R5),AWAS ;READ ISR REGISTER THROUGH CSRD
750 001404' 020167 176500 CMP R1,AWAS ;COMPARE ISR DATA WITH EXPECTED
751 001410' 001415 BEQ TSINTB ;BRANCH IF GOOD DATA
752 001412' 016767 176622 176460 MOV DRVJCD,CSRA ;SAVE CSRD FOR ERROR PRINT
753 001420' 012767 177701 176454 MOV #177701,SBADR ;GOOD DATA ADDRESS
754 001426' 016767 176606 176450 MOV DRVJCD,WASADR ;BAD DATA ADDRESS
755 001434' 010167 176446 MOV R1,ASB ;SHOULD BE DATA
756 ;*****
(1) 001440' 104404 000000' DATERS,BEGIN ;DATA ERROR!!!
(1) ;*****
757
758 ;READ DATA FROM INPUT BUFFER DBRD AND CAUSE INTERRUPT
759 ;AT IRK5 RPLY B.
760
761 001444' 012765 000160 000010 TSINTB: MOV #160,10(R5) ;CLEAR THE ISR REGISTER
762 001452' 016503 000016 MOV 16(R5),R3 ;READ INPUT BUFFER DBRD
763 001456' 104400 000000' EXITS,BEGIN ;EXIT TO MONITOR. MODULE WAIT FOR INTERRUPT.
764 001462' 112765 000240 000010 CHECKB: MOV #240,10(R5) ;RETURN FOR RPLY B
765 ;INTERRUPT CHECK.
766 ;SETUP TO READ ISR REGISTER
767 001470' 012701 000040 MOV #40,R1 ;ISR 5 EXPECTED
768 001474' 116567 000014 176406 MOV #14(R5),AWAS ;READ ISR REGISTER THROUGH CSRD
769 001502' 020167 176402 CMP R1,AWAS ;COMPARE ISR DATA WITH EXPECTED
770 001506' 001415 BEQ ERPS ;BRANCH IF GOOD DATA
771 001510' 016767 176514 176362 MOV DRVJCB,CSRA ;SAVE CSRB FOR ERROR PRINT
```



```
772 001516' 012767 177701 176356      MOV      #177701,SBADR          ;GOOD DATA ADDRESS
773 001524' 016767 176510 176352      MOV      DRVJCD,WASADR        ;BAD DATA ADDRESS
774 001532' 010167 176350      MOV      R1,ASH              ;SHOULD BE DATA
775                                     ;*****
(1) 001536' 104404 000000'          DATERS,BEGIN                  ;DATA ERROR!!!
(1)                                     ;*****
776
777
778 001542' 042715 001000      ENPS:   BIC      #BIT9, (R5)   ;CLEAR INTERRUPT ENABLE
779 001546' 104413 000000'          ENBITS,BEGIN                  ;SIGNAL END OF ITERATION.
(1)                                     ;MONITOR SHALL TEST END OF PASS
780 001552' 000167 176526      JMP      RSTRT              ;BACK TO BEGINNING
781
782
783                                     ;INPUT/OUTPUT SERVICE ROUTINES
784
785
786 001556'          DRASRV:
(1)                                     ;-----
(1) 001556' 000004 000000' 001564'      PIRQS,BEGIN,1$              ; QUEUE UP TO CONTINUE AT 1$ AND RTI
(1)                                     ;-----
787 001564' 000167 177474      1$:   JMP      CHECKA          ;CHECK RPLY A (ISR 4) IN THE ISR REGISTER
788
789 001570'          DRBSRV:
(1)                                     ;-----
(1) 001570' 000004 000000' 001576'      PIRQS,BEGIN,1$              ; QUEUE UP TO CONTINUE AT 1$ AND RTI
(1)                                     ;-----
790 001576' 000167 177660      1$:   JMP      CHECKB          ;CHECK RPLY B (ISR 5) IN THE ISR REGISTER
791
792 001602'          DRCSRV:
(1)                                     ;-----
(1) 001602' 000004 000000' 001610'      PIRQS,BEGIN,1$              ; QUEUE UP TO CONTINUE AT 1$ AND RTI
(1)                                     ;-----
793 001610' 000167 177352      1$:   JMP      CHECKC          ;CHECK RPLY C (ISR 6) IN THE ISR REGISTER
794
795 001614'          DRDSRV:
(1)                                     ;-----
(1) 001614' 000004 000000' 001622'      PIRQS,BEGIN,1$              ; QUEUE UP TO CONTINUE AT 1$ AND RTI
(1)                                     ;-----
796 001622' 000167 177536      1$:   JMP      CHECKD          ;CHECK RPLY D (ISR 7) IN THE ISR REGISTER
797
798          000001          .END
```


DRJA DEC/X11 SYSTEM EXERCISER MODULE
XDRJA0.P11 23-JAN-81 02:42

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CROSS REFERENCE TABLE -- USER SYMBOLS

SEQ 0012

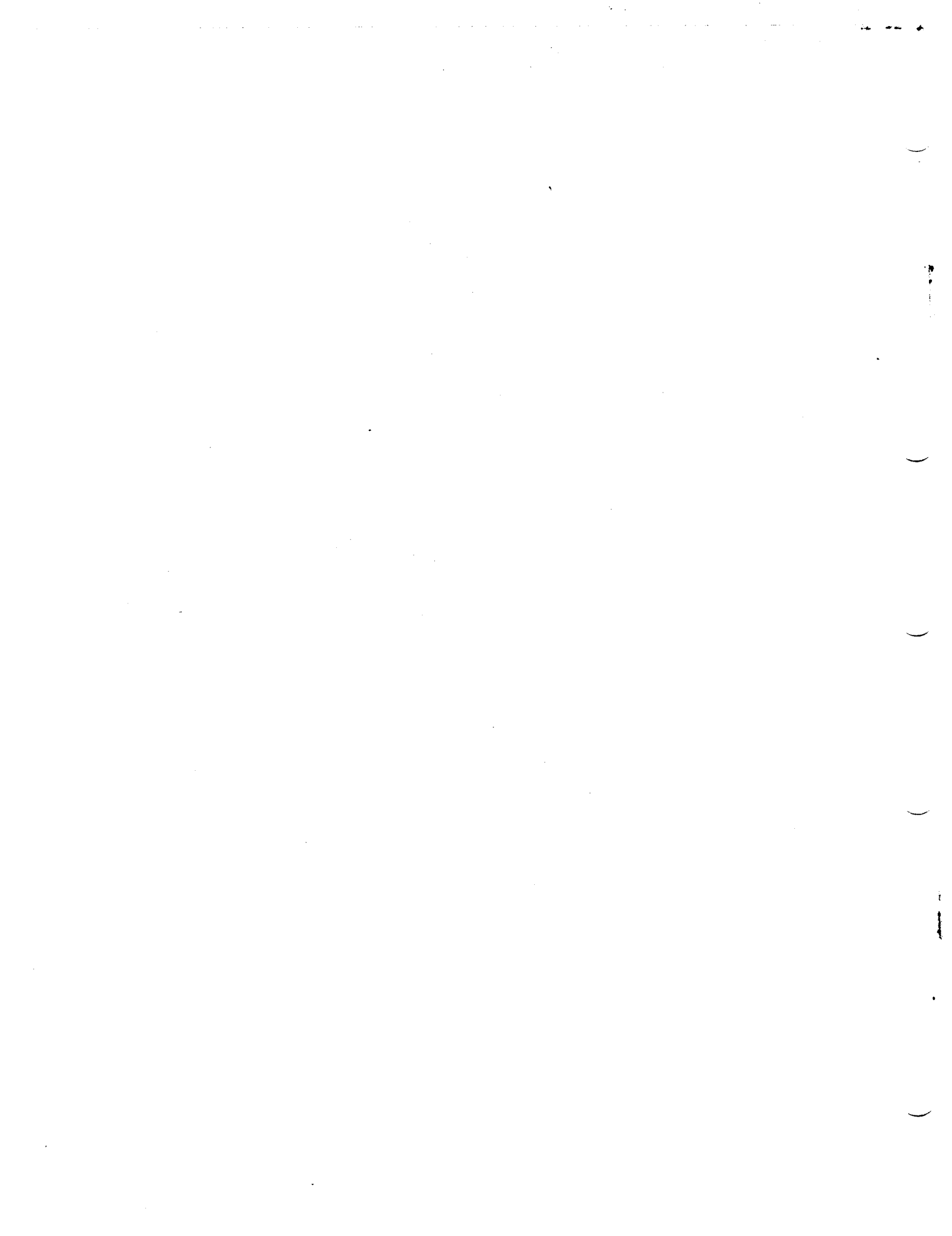
DRVJCD 000240R	531#	636	672	716	735	752	754	773
DV101 000014R	512#							
ENDITS= 104413	512#	779						
ENDS = 104410	512#							
ENPS 001542R	770	778#						
ERRTYP 000106R	512#							
EXITS = 104400	512#	706	725	744	763			
GETPAS= 104415	512#							
GWBUFFS= 104414	512#							
HRDCNT 000044R	512#							
HRDEFS= 104405	512#							
HRDPAS 000050R	512#							
ICONT 000036R	512#							
ICOUNT 000040R	512#							
IDNUM 000122R	512#							
INIT 000030R	512#							
INTEST 001042R	662	671#						
INTR 000120R	512#	553*						
MAP22S= 104416	512#							
MUDNAM 000000R	512#							
MODSP 000224R	512#							
MSGNS = 104403	512#							
MSGSS = 104402	512#							
MSG = 104401	512#							
NULL = 000000	512#							
OPEN = 000000	512#							
UTOAS = 104420	512#							
PASCNT 000034R	512#							
PIRQS = 000004	512#	786	789	792	795			
POPSP = 005726	512#							
POPSP2= 022626	512#							
PRTY = 000000	512#							
PRTY0 = 000000	512#							
PRTY1 = 000040	512#							
PRTY2 = 000100	512#							
PRTY3 = 000140	512#							
PRTY4 = 000200	512#							
PRTY5 = 000240	512#							
PRTY6 = 000300	512#							
PRTY7 = 000340	512#							
PS = 177776	512#							
PSW = 177776	512#							
PUSH = 005746	512#							
PUSH2 = 024646	512#							
RANDS = 104417	512#							
RANNUM 000054R	512#							
RESIRT 000304R	512	557#	780					
RES1 000056R	512#							
RES2 000060R	512#							
RSTRT 000112R	512#							
SBADR 000102R	512#	605*	617*	628*	639*	715*	734*	753*
SETUP2 000314R	559#	562						
SETUP3 000340R	565#	568						
SOFcnt 000042R	512#							
SOFERS= 104406	512#							
SOFPAS 000046R	512#							

BKMQD	95#								
BREAK	185#								
BTOD	204#								
CKDATA	240#								
DATAACK	249#								
DATERR	138#	611	623	634	645	718	737	756	775
DFSEVN	272#	512							
DSEVNT	282#	512							
END	175#								
ENDIT	166#	779							
ENDMOD	171#								
EQUATS	288#	512							
EXIT	120#	706	725	744	763				
GETPA	231#								
GWBUFF	219#								
HRDER	128#								
IOMOD	91#	512							
IOMODP	115#								
IOMODR	111#								
IOMODX	107#								
MAP22	235#								
MODULE	8#	512							
MSG	154#								
MSGN	158#								
MSGS	162#								
NBKMOD	103#								
OTOA	190#								
PIRQ	179#	786	789	792	795				
RAND	124#								
SBKMOD	99#								
SUFER	144#								

. ABS. 000000 000
001626 001

ERRORS DETECTED: 0
DEFAULT GLOBALS GENERATED: 0

DSKZ:XDRJAO,DSKZ:XDRJAO/CRF=DDXCOM.P11,XDRJAO.P11
RUN-TIME: 3 4 .6 SECONDS
RUN-TIME RATIO: 45/9=5.0
CORE USED: 7K (13 PAGES)



DMEB DEC/X11 SYSTEM EXERCISER MODULE MACY11 30A(1052) 25-MAR-81 08:35 PAGE 2
CXDMEB.P11 25-MAR-81 08:25 DEC/X11-1 SYSTEM EXERCISER MACRO DEFINITION MODULE

.REM_

IDENTIFICATION

PRODUCT CODE: AC-F808B-MC
PRODUCT NAME: CXDMEB0 DMP/DMV11 DECX SLV MOD
PRODUCT DATE: AUGUST 1981
MAINTAINER: DIAGNOSTIC ENGINEERING CC:38P
AUTHORS: DAVID HOFFMAN
 CHRIS BRIENEN

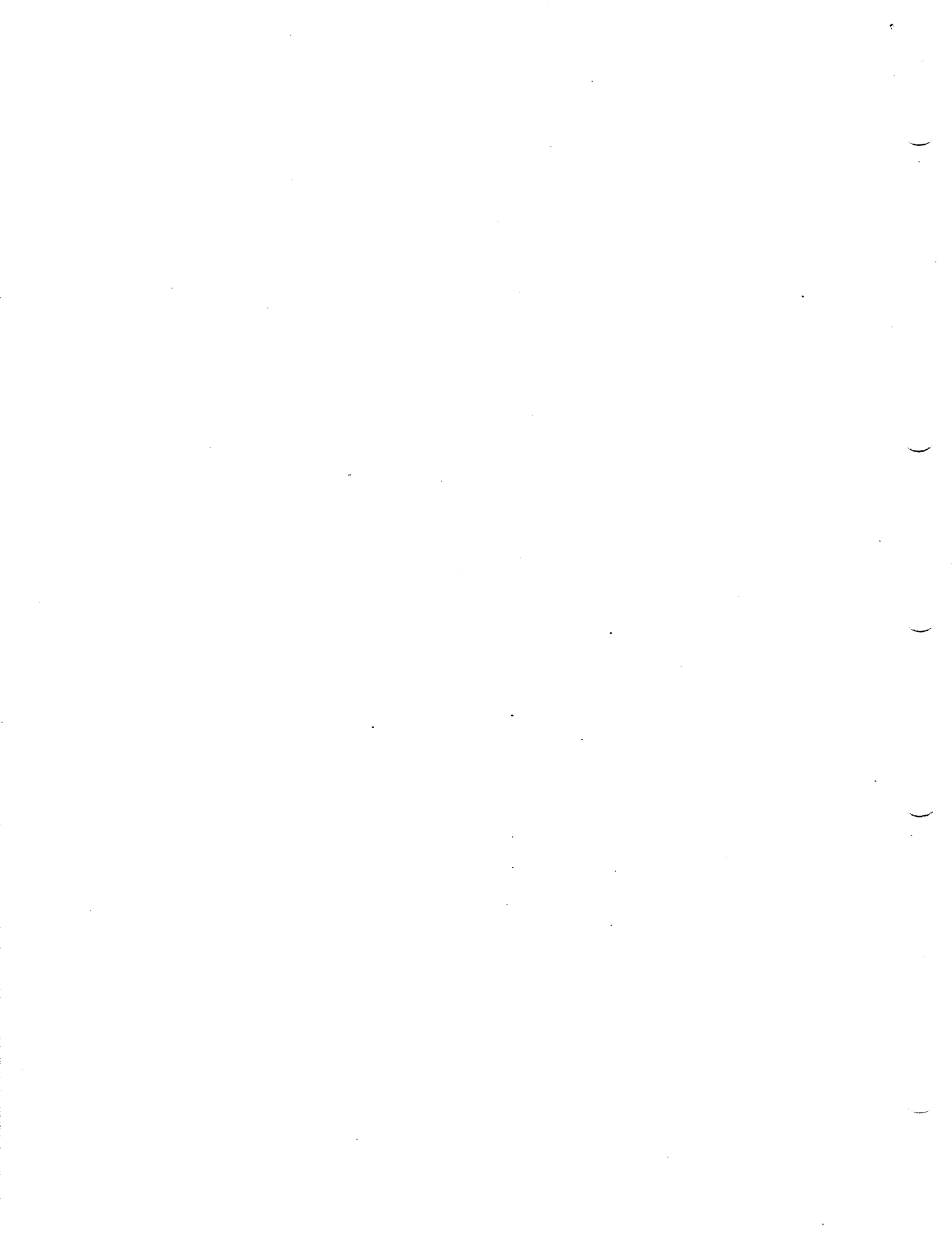
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1. ABSTRACT

THERE ARE 2 DEC/X11 IOMODX MODULES WRITTEN FOR THE DMP/DMV11. THESE ARE DMD AND DME. TOGETHER, THESE 2 MODULES CAN OPERATE UP TO 16 (DEC.) DMP11 DEVICES IN POINT-TO-POINT LINKS, OR A SINGLE DEVICE CONFIGURED AS A MULTIPPOINT CONTROL STATION COMMUNICATING WITH UP TO 32 (DEC.) TRIBUTARIES, OR UP TO 16 (DEC.) DEVICES CONFIGURED AS MULTIPPOINT TRIBUTARIES ON THE SAME PDP-11 UNIBUS (16/16 W/DMV11 QBUS). THE BASIC OPERATION IS TO TRANSMIT, RECEIVE, AND CHECK 32(DEC.) DATA MESSAGES OF 1024 (DEC.) BYTES EACH, ON A GIVEN PHYSICAL LINK. BY DEFAULT, THIS WOULD INVOLVE A SINGLE PDP-11 SYSTEM WITH 1 OR MORE DEVICES OPERATED IN INTERNAL OR EXTERNAL LOOPBACK MODE. HOWEVER, BY OPERATOR SELECTION OF NON-DEFAULT MODES, ACTUAL POINT-TO-POINT OR MULTIPPOINT OPERATION IS POSSIBLE. IN ONE SUCH MODE, THE MODULE CAN DRIVE THE OPERATION OF A CONTROL STATION WHICH SENDS AND RECEIVES BACK MESSAGES FROM A SPECIFIED LIST OF TRIBUTARIES, ON THE SAME OR DIFFERENT PDP-11 SYSTEMS. IN ANOTHER SUCH MODE, THE MODULE CAN DRIVE THE OPERATION OF A GROUP OF TRIBUTARIES WHICH RESIDE ON A SINGLE SYSTEM AND REFLECT BACK MESSAGES SENT TO THEM BY A CONTROL STATION, ON THE SAME OR DIFFERENT SYSTEM.

DMD IS THE MASTER MODULE WHICH CAN OPERATE UP TO 16 (DEC.) DEVICES IN LOOPED-BACK OR POINT-TO-POINT MASTER MODES, OR A SINGLE DEVICE IN MULTIPPOINT CONTROL MODE.

DME IS THE SLAVE MODULE WHICH WHICH CAN OPERATE UP TO 16 (DEC.) DEVICES IN POINT-TO-POINT SLAVE OR MULTIPPOINT TRIBUTARY MODES. A MASTER MODULE CAN BE SELF-SUFFICIENT (IF LOOPBACK IS USED) OR IT CAN COMMUNICATE WITH A SLAVE MODULE(S) ON THE SAME OR ANOTHER PROCESSOR. THE REST OF THIS DOCUMENT WILL DESCRIBE THE USE AND OPERATION OF THE SLAVE MODULE, DME.

IT IS REQUIRED THAT THE OPERATOR CONFIGURE THE DEC/X11 EXERCISER WITH A SEPARATE COPY OF MODULE DME FOR EACH GROUP OF POINT-TO-POINT SLAVES OR MULTIPPOINT TRIBUTARY DEVICES ON THIS PDP-11 SYSTEM. THE ACTUAL OPERATING MODE OF EACH COPY IS SELECTED BY THE SETTINGS OF SW1-SW4 (SOFTWARE SWITCHES) FOR THAT COPY. FOR MULTI-PROCESSOR CONFIGURATIONS, A DEC/X11 EXERCISER MUST BE CONFIGURED AND RUN ON EACH PDP-11 PROCESSOR, SIMULTANEOUSLY. NOTE THAT THE DME MODULE IS NEVER ALLOWED TO RELOCATE; USE A MODULE THAT DOES NOT ALLOW RELOCATION OR USE THE RUN LOCK COMMAND (RUNL).

AFTER THE MASTER MODULE (DMD) HAS BEEN STARTED ON ONE PROCESSOR, THE OPERATOR IS ALLOWED AT LEAST 5 MINUTES TO START THE SLAVE MODULE(S) (DME) ON THE OTHER PROCESSOR(S), BEFORE GETTING A PROTOCOL TRANSMIT THRESHOLD ERROR ON THE MASTER MODULE PROCESSOR.

THE M8203 LINE UNIT CONTAINS HARDWARE SWITCHES WHICH CAN DEFINE THE PHYSICAL LINK AND DMP11 OPERATING MODE, AND A TRIBUTARY ADDRESS (IF MULTIPPOINT). IF THE MODE SWITCHES ARE ENABLED, THE MODE DEFINED IN THE SWITCHES WILL BE USED, IF POSSIBLE. IF THE MODE SWITCHES ARE NOT ENABLED, THE PROGRAM WILL DEFINE THE MODE AUTOMATICALLY. IN ANY CASE, THE PROGRAM DOES NOT REQUIRE OPERATOR INTERVENTION TO CHANGE HARDWARE SWITCH SETTINGS OR CONFIGURE CABLES, ETC.

2. REQUIREMENTS

HARDWARE: 1 TO 16 (DEC.) DMP/DMV11'S AND ASSOCIATED CABLES
AND CONNECTORS.
STORAGE: DME REQUIRES ABOUT 2K WORDS OF STORAGE

3. PASS DEFINITION

ONE PASS OF THE DME MODULE CONSISTS OF TRANSMITTING AND RECEIVING
A TOTAL OF 32,768 (DEC.) 8-BIT CHARACTERS, PER DMP/DMV11.

4. EXECUTION TIME

DME RUNNING ALONE ON A PDP11/40 PROCESSOR TAKES APPROXIMATELY
1 MINUTE PER SLAVE DEVICE TO COMPLETE ONE PASS AT 9600 BAUD. THE PASS
TIME IS ALSO INVERSELY PROPORTIONAL TO THE BAUD RATE.

5. CONFIGURATION PARAMETERS

DEFAULT PARAMETERS :

DEVADR: 160170, VECTOR:300, BR1:5, BR2:UNUSED, DEVCNT:1

SOFTWARE SWITCH REGISTER OPTIONS :

THE ALLOWABLE OCTAL VALUES OF SR4 ARE:

SR4=000000 : IF TESTING DMP11
SR4=000001 : IF TESTING DMV11
SR4=000002 : IF TESTING DMV11 (AND Q22 SOFTWARE MODE IS DESIRED)

THE ALLOWABLE OCTAL VALUES OF SR1 ARE: 000000 AND 000001.
A DESCRIPTION OF SR1-SR3 (AND THEIR MEANING) ARE GIVEN BELOW:

FOR SR1 = 000000:

ALL UNITS SELECTED IN DVID1 WILL BE RUN IN POINT-TO-POINT SLAVE,
FULL OR HALF-DUPLEX MODE (DEPENDING ON LINE UNIT SWITCH SETTINGS)
WITHOUT LOOPBACK. THE MODULE COMMUNICATES WITH MASTER MODULE(S)
ON THE SAME AND/OR OTHER PDP-11 SYSTEM(S).

** THIS IS THE DEFAULT MODE OF OPERATION **.

WHEN SR1 = 000000, SR2, SR3, AND SR4 ARE UNUSED, AND
THE HEADER ENTRY DVC MUST BE IN THE RANGE 1-20 (OCTAL).

FOR SR1 = 000001:

ALL UNITS SELECTED IN DVID1 WILL BE RUN IN MULTIPOINT TRIBUTARY,
FULL OR HALF-DUPLEX MODE (DEPENDING ON LINE UNIT SWITCH SETTINGS)
WITHOUT LOOPBACK. THE MODULE COMMUNICATES WITH A MASTER MODULE ON
THE SAME OR DIFFERENT PDP-11 SYSTEM.

WHEN SR1 = 000001, THE FOLLOWING MEANING IS GIVEN TO SR2 AND SR3 :
- SR2 = THE TOTAL NUMBER OF TRIBUTARIES (OCTAL) ON THE MULTIPOINT
LINK WHICH ARE ON THIS CPU. THE ALLOWABLE RANGE IS 000001-000040
(DMP) OR 000001-000020 (DMV).

- SH3 = THE STARTING TRIBUTARY ADDRESS (OCTAL). THE ALLOWABLE RANGE IS 000001-000377. THE PROGRAM WILL USE THIS STARTING TRIB ADRS TO COMPUTE THE OTHER TRIB ADDRESSES ON THE MULTIPOINT LINK, AND THE ADDRESSES CAN "WRAPAROUND" 377 TO 001, IF NECESSARY. THE HEADER ENTRY DVC MUST BE IN THE RANGE 1-20 (OCTAL).

 NOTE:: MEMORY RELOCATION IS NOT ALLOWED IN EITHER POINT-TO-POINT OR MULTIPOINT OPERATION. USE EITHER A MODULE THAT DOES NOT ALLOW FOR RELOCATION OR USE THE RUN LOCK COMMAND (RUNL).

6. DEVICE/OPTION SETUP

DME REQUIRES NO ADDITIONAL DEVICE SETUP.

7. DME MODULE OPERATION

TEST SEQUENCE:

- A. INITIALIZE ALL SLAVE DEVICES
- B. LOAD INPUT AND OUTPUT INTERRUPT VECTORS ON ALL SLAVES
- C. PERFORM MODE DEFINITION ON ALL SLAVES
- D. VERIFY OPERATION OF INPUT AND OUTPUT INTERRUPTS ON ALL SLAVES
- E. START THE DDCMP PROTOCOL ON ALL SLAVES, TO ALL MASTERS, AND SCAN EACH LOGICAL LINK UNTIL IT ENTERS THE RUNNING STATE.
- F. EXIT TO MONITOR WITH INTERRUPTS ENABLED
- G. INPUT INTERRUPT SERVICE :
 - 1) IF LOGICAL LINK WAS JUST STARTED, INPUT A RCV BUFFER, AND DISABLE INPUTS ON THIS DEVICE.
 - 2) IF RCV BUFFER WAS PREVIOUSLY INPUT, INPUT A TRANSMIT BUFFER (SEND SAME DATA BACK TO MASTER).
- H. OUTPUT INTERRUPT SERVICE :
 - 1) IF TRANSMIT BUFFER WAS JUST RETURNED, CHECK FOR CORRECT BA, EA, TRANSMIT CHAR COUNT BITS.
 - 2) IF RCV BUFFER WAS JUST RETURNED, CHECK FOR CORRECT BA, EA, RCV CHAR COUNT BITS, AND RE-ENABLE INPUTS ON THIS DEVICE.
 - 3) IF ATTEMPT TO RESTART PROTOCOL IS RECEIVED FROM MASTER(S) BY ALL SLAVES, REPORT AN END OF PASS. THEN RESTART THE PROTOCOL ON ALL SLAVES TO THE MASTER(S).
 - 4) REPORT ANY ERRORS PROVIDED BY ANY SLAVE DEVICE.
 NOTE THAT AT THE END OF EACH PASS THE PROGRAM WILL REPORT ANY SOFT (DATA OR HEADER CRC) ERRORS THAT HAVE OCCURRED DURING THE PASS.
- I. REPEAT G AND H

8. OPERATION OPTIONS

- A. LOCATION DVID1 (DME 14) MAY BE CHANGED TO SELECT ANY COMBINATION OF DEVICES BIT0=DEVO, BIT1=DEV1BIT15=DEV15.

NOTE: IF DVID1 IS INITIALLY = 0, DME WILL BE DROPPED FROM TEST.

9. ERROR REPORTS

ALL ERROR REPORTS HAVE STANDARD FORMATS AS DESCRIBED IN THE DEC/X11 USER'S GUIDE. IN EACH ERROR PRINTOUT, THE SAME INFORMATION IS ALWAYS REPORTED, ALONG WITH THE ERROR TYPE AND PC, WHICH UNIQUELY IDENTIFY THE ERROR. THE FOLLOWING IS AN EXAMPLE PRINTOUT :

```
DMEAO PA 00046706 APC 004462 PASS #00001 SOFT ERROR #1
CSRA: 160210 CSRC: 100000 ASTAT: 000602 ERRTP: 000001
100000 000602 001005 000112 NNNNNN
```

ERRTP = 1 INDICATES A DATA ERROR (AS DESCRIBED IN THE DEC/X11 USER'S GUIDE), WHICH IS A SOFT ERROR. CSRA IS THE DEVICE CSR ADDRESS, CSRC AND ASTAT ARE THE CONTENTS OF THE FIRST TWO CSR REGISTERS (SEL0 AND SEL2). THE FOUR (5 IF DMV11) FIELDS OF NUMBERS ON THE THIRD LINE ARE THE CONTENTS OF ALL FOUR CSR REGISTERS (SEL0,2,4,6 (AND 10 IF DMV)). WHENEVER ERRTP = 0 IS REPORTED (UNDEFINED ERROR) THE USER MUST REFER TO THE DIAGNOSTIC LISTING FOR THE ACTUAL EXPLANATION OF THE ERROR. THIS IS RECOMMENDED FOR ALL ERRORS, BECAUSE THE DEC/X11 ERROR TYPE DEFINITIONS ARE QUITE GENERAL.

-

```

236
237
238 .TITLE DMEB DEC/X11 SYSTEM EXERCISER MODULE
239 ; DDXCOM VERSION 6 23-MAY-78
240 .LIST BIN
241 ;*****
241 000000' BEGIN:
242 000000' 046504 041105 040 MUDNAM: .ASCII /DMEB / ;MODULE NAME.
243 000005' 000 XFLAG: .BYTE OPEN ;USED TO KEEP TRACK OF WBUFF USAGE
244 000006' 160170 ADDR: 160170+0 ;1ST DEVICE ADDR.
245 000010' 000300 VECTOR: 300+0 ;1ST DEVICE VECTOR.
246 000012' 240 BR1: .BYTE PRTY5+0 ;1ST BR LEVEL.
247 000013' 240 BR2: .BYTE PRTY5+0 ;2ND BR LEVEL.
248 000014' 000001 DVID1: 0+1 ;DEVICE INDICATOR 1.
249 000016' 000000 SR1: OPEN ;SWITCH REGISTER 1
250 000020' 000000 SR2: OPEN ;SWITCH REGISTER 2
251 000022' 000000 SR3: OPEN ;SWITCH REGISTER 3
252 000024' 000000 SR4: OPEN ;SWITCH REGISTER 4
253 ;*****
254 000026' 150000 STAT: 150000 ;STATUS WORD.
255 000030' 000466' INIT: START ;MODULE START ADDR.
256 000032' 000252' SPOINI: MODSP ;MODULE STACK POINTER.
257 000034' 000000 PASCNT: 0 ;PASS COUNTER.
258 000036' 000001 ICONT: 1 ;# OF ITERATIONS PER PASS=1
259 000040' 000000 ICOUNT: 0 ;LOC TO COUNT ITERATIONS
260 000042' 000000 SOFCNT: 0 ;LOC TO SAVE TOTAL SOFT ERRORS
261 000044' 000000 HRDCNT: 0 ;LOC TO SAVE TOTAL HARD ERRORS
262 000046' 000000 SUPPAS: 0 ;LOC TO SAVE SOFT ERRORS PER PASS
263 000050' 000000 HRDPAS: 0 ;LOC TO SAVE HARD ERRORS PER PASS
264 000052' 000000 SYSCNT: 0 ;# OF SYS ERRORS ACCUMULATED
265 000054' 000000 RANNUM: 0 ;HULDS RANDOM # WHEN RAND MACRO IS CALLED
266 000056' CONFIG: ;RESERVED FOR MONITOR USE
267 000056' 000000 RES1: 0 ;RESERVED FOR MONITOR USE
268 000060' 000000 RES2: 0 ;RESERVED FOR MONITOR USE
269 000062' 000000 SVR0: OPEN ;LOC TO SAVE R0.
270 000064' 000000 SVR1: OPEN ;LOC TO SAVE R1.
271 000066' 000000 SVR2: OPEN ;LOC TO SAVE R2.
272 000070' 000000 SVR3: OPEN ;LOC TO SAVE R3.
273 000072' 000000 SVR4: OPEN ;LOC TO SAVE R4.
274 000074' 000000 SVR5: OPEN ;LOC TO SAVE R5.
275 000076' 000000 SVR6: OPEN ;LOC TO SAVE R6.
276 000100' 000000 CSRA: OPEN ;ADDR OF CURRENT CSR.
277 000102' SBADR: ;ADDR OF GOOD DATA, OR
278 000102' 000000 ACSR: OPEN ;CONTENTS OF CSR.
279 000104' WASADR: ;ADDR OF BAD DATA, OR
280 000104' 000000 ASTAT: OPEN ;STATUS REG CONTENTS.
281 000106' ERRTP: ;TYPE OF ERROR
282 000106' 000000 ASB: OPEN ;EXPECTED DATA.
283 000110' 000000 AWAS: OPEN ;ACTUAL DATA.
284 000112' 002224' RSTR: RSTR ;RESTART ADDRESS AFTER END OF PASS
285 000114' 000000 WDT0: OPEN ;WORDS TO MEMORY PER ITERATION
286 000116' 000000 WDFR: OPEN ;WORDS FROM MEMORY PER ITERATION
287 000120' 000000 INTR: OPEN ;# OF INTERRUPTS PER ITERATION
288 000122' 000000 IDNUM: 0 ;MODULE IDENTIFICATION NUMBER=0
289 000124' 006244' RBUFVA: BUFIN ;READ BUFFER VIRTUAL ADDRESS
290 000126' 000000 RBUFPA: UPEN ;READ BUFFER PHYSICAL ADDRESS
291 000130' 000000 RBUFEA: UPEN ;READ BUFFER EA BITS

```

DEC/X11-1 SYSTEM EXERCISER MACRO DEFINITION MODULE

292 000132' 001000
293 000134' 000000
294 000136' 000000
295 000140' 001000
296 000142' 000000
297 000144' 000000
298 000146' 000000
299 000150' 000000
300 000252'
301

RBUFSZ: 512. ;SIZE OF THE READ BUFFER
WBUFPA: OPEN ;WRITE BUFFER PHYSICAL ADDRESS
WBUFEA: OPEN ;WRITE BUFFER EA BITS
WBUFQR: 512. ;WRITE BUFFER SIZE REQUESTED
WBUFSZ: OPEN ;WRITE BUFFER SIZE AVAILABLE
CDERCT: OPEN ;CDATA/DATCK ERROR COUNT
CDWDCT: OPEN ;CDATA/DATCK WORD COUNT
FREE: OPEN ;RESERVED FOR FUTURE USE
MODSP:
;*****

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DEC/X11-1 SYSTEM EXERCISER MACRO DEFINITION MODULE

```

302
303
304 100000
305 040000
306 020000
307 010000
308 004000
309 002000
310 001000
311 000400
312 000200
313 000100
314 000040
315 000020
316 000010
317 000004
318 000002
319 000001
320
321
322
323 000305
324
325 021344
326
327 121000
328 000200
329 000001
330 000006
331
332
333
334
335 000001
336
337
338
339
340 000001
341 000002
342
343
344
345
346
347 000000
348 000001
349 000007
350 000011
351 000023
352 000034
353 000036
354 000037
355
356
357

;BIT DEFINITIONS
BIT15=100000
BIT14=40000
BIT13=20000
BIT12=10000
BIT11=4000
BIT10=2000
BIT9=1000
BIT8=400
BIT7=200
BIT6=100
BIT5=40
BIT4=20
BIT3=10
BIT2=4
BIT1=2
BIT0=1

GOODIN = 305 ;CODE LEFT IN BSEL6 AFTER INIT, IF NO ERRORS
RDLU16 = 021344 ;M8207 INSTRUCTION TO MOVE LU REG 16 INTO BSEL4
SWPBOT = 121000 ;DMV-11 ADDRESS OF "SWPBOT" SWITCHES
MRDY = 200 ;DMV-11 BIT INDICATING M-LOOP IS READY FOR NEXT COMMAND
REDLOC = 1 ;DMV-11 M-LOOP COMMAND TO READ INTERNAL MEMORY
TTLOOP = 6 ;DMV-11 M-LOOP COMMAND TO SETUP 56K BPS/INT LOOPBACK

;*****
;* SWITCH REGISTER 1 (SR1) BIT DEFINITIONS
;*****
NONLUP = BIT0 ;LOOPBACK MODE IF BIT = 0

;*****
;* SWITCH REGISTER 4 (SR4) BIT DEFINITIONS
;*****
DMVBIT = BIT0 ;UNIT IS DMV IF BIT = 1
MODE22 = BIT1 ;UNIT IS IN Q22 MODE IF BIT = 1

;*****
;* DEC/X11 ERROR CUDES
;*****
NUTDFW = 0 ;ERROR NOT DEFINED
DATERR = 1 ;DATA ERROR
SELERR = 7 ;SELECTION ENRROR
ILGINT = 11 ;ILLEGAL INTERRUPT OCCURRED, OR DONE NOT SET
NOINTR = 23 ;DEVICE FAILED TO INTERRUPT
NOINIT = 34 ;DEVICE WILL NOT INITIALIZE
BADRED = 36 ;UNABLE TO EXECUTE READ FUNCTION
BADWRT = 37 ;UNABLE TO EXECUTE WRITE FUNCTION

```

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DEC/X11-1 SYSTEM EXERCISER MACRO DEFINITION MODULE

```

358
359
360
361 000000
362 000000
363 000001
364 000002
365 000002
366 000003
367 000004
368 000004
369 000005
370 000006
371 000006
372 000007
373 000010
374 000010
375 000011
376
377
378
379
380 000002
381 000001
382 000004
383 000000
384
385
386
387
388
389
390 000001
391 000004
392 000000
393 000003
394 000006
395 000007
396 000002
397
398
399
400
401
402
403 000370
404 000370
405 000340
406 000340
407 140000
408 000077
409 000361
410
411
412
413

;*****
;* OFFSETS FOR DMP/DMV11 CSR ADDRESSES
;*****
SEL0 = 0
BSEL0 = 0
BSEL1 = 1
SEL2 = 2
BSEL2 = 2
BSEL3 = 3
SEL4 = 4
BSEL4 = 4
BSEL5 = 5
SEL6 = 6
BSEL6 = 6
BSEL7 = 7
SEL10 = 10
BSEL10 = 10
BSEL11 = 11

;*****
;* INPUT COMMAND TYPE CUDES - BSEL2
;*****
MUDEF = 2
CILIN = 1
BACCIT = 4
BACCIR = 0

;*****
;* OUTPUT COMMAND TYPE CUDES - BSEL2
;*****
CTLOUT = 1
BACCOT = 4
BACCOR = 0
BACORU = 3
BACOTS = 6
BACOTN = 7
INFOUT = 2

;*****
;* BIT MASKS FOR FIELDS IN COMMANDS
;*****
CMDMSK = 370 ;MASK FOR COMMAND TYPE IN BSEL2
MODMSK = 370 ;MASK FOR MODE FIELD IN BSEL6 FOR MODE DEF'N CMND
REQMSK = 340 ;MASK FOR REQUEST KEY IN BSEL6 FOR CTL IN CMND
RTNMSK = 340 ;MASK FOR RETURN KEY IN BSEL6 FOR INFO OUT CMND
CCMSK = 140000 ;MASK FOR CHARACTER COUNT IN SEL6
EAMSK = 077 ;MASK FOR BUS ADRS EA BITS IN BSEL7
MODESW = 361 ;MASK FOR DMP11 MODE SWITCHES IN 1BUS REG 16

```

```

414      ;* COMMAND CONTROL, INTERRUPT ENABLE BITS - BSEL0
415      ;*****
416      000200      RQI      = BIT7
417      000020      IEO      = BIT4
418      000001      IEI      = BIT0
419
420
421      ;*****
422      ;* COMMAND CONTROL BITS - BSEL2
423      ;*****
424      000200      RDY0     = BIT7
425      000020      RDY1     = BIT4
426      000010      Q22BIT  = BIT3
427
428
429      ;*****
430      ;* MICROPROCESSOR CONTROL COMMAND - BSEL1
431      ;*****
432      000200      RUN      = BIT7
433      000100      MCLR    = BIT6
434      000020      STEPLW  = BIT4
435      000010      LULDOP  = BIT3
436      000004      RUMO    = BIT2
437      000002      RQMI    = BIT1
438      000001      STEPMP  = BIT0
439
440
441
442      ;*****
443      ;* MODE DEFINITION COMMAND - BSEL6
444      ;*****
445      000000      HDPPDC  = 0
446      000001      FDPPDC  = 1
447      000002      HDPP   = 2
448      000003      FDPY   = 3
449      000004      HDCS   = 4
450      000005      FDCS   = 5
451      000006      HDTS   = 6
452      000007      FDTS   = 7
453
454
455      ;*****
456      ;* CONTROL IN COMMAND - BSEL6
457      ;*****
458
459      000200      WRITSS  = BIT7
460      000100      RDCTSS  = BIT6
461      000040      REDTSS  = BIT5
462      000001      ESTRIB  = 01
463      000002      KILLTM  = 02
464      000003      ISTART  = 03
465      000004      MAINT   = 04
466      000005      HALTST  = 05
467      000020      REDNOM  = 20
468      000021      WRINDM  = 21
469      000022      INTFUG  = 22

```

```

470      000112      DEROTB  = 112
471      000113      DERINB  = 113
472
473
474
475      ;*****
476      ;* CONTROL IN COMMAND - BSEL7
477      ;*****
478      000200      DISPIG  = BIT7
479      000100      ENBPIG  = BIT6
480      000040      LATPS   = BIT5
481      000020      ULATPS  = BIT4
482      000002      DISCMP  = BIT1
483      000001      ENACMP  = BIT0
484
485
486
487      ;*****
488      ;* BUFFER ADDRESS / CHARACTER COUNT IN/OUT COMMAND - SEL4
489      ;*****
490      100000      BA15    = BIT15
491      040000      BA14    = BIT14
492      020000      BA13    = BIT13
493      010000      BA12    = BIT12
494      004000      BA11    = BIT11
495      002000      BA10    = BIT10
496      001000      BA9     = BIT9
497      000400      BA8     = BIT8
498      000200      BA7     = BIT7
499      000100      BA6     = BIT6
500      000040      BA5     = BIT5
501      000020      BA4     = BIT4
502      000010      BA3     = BIT3
503      000004      BA2     = BIT2
504      000002      BA1     = BIT1
505      000001      BA0     = BIT0
506
507
508
509      ;*****
510      ;* BUFFER ADDRESS / CHARACTER COUNT IN/OUT - SEL6
511      ;*****
512      100000      BA17    = BIT15
513      040000      BA16    = BIT14
514      020000      CC13    = BIT13
515      010000      CC12    = BIT12
516      004000      CC11    = BIT11
517      002000      CC10    = BIT10
518      001000      CC9     = BIT9
519      000400      CC8     = BIT8
520      000200      CC7     = BIT7
521      000100      CC6     = BIT6
522      000040      CC5     = BIT5
523      000020      CC4     = BIT4
524      000010      CC3     = BIT3
525      000004      CC2     = BIT2

```



```

526          000002          CC1  = BIT1
527          000001          CC0  = BIT0
528
529
530
531
532          ;*****
533          ;* CONTROL OUT COMMAND - BSEL6
534          ;*****
534          000002          RTHREX = 2
535          000004          ITHREX = 4
536          000006          STHREX = 6
537          000010          STARUN = 10
538          000012          MNTRUN = 12
539          000014          MNHLI  = 14
540          000020          RINGDT = 20
541          000022          DEADTR = 22
542          000024          RUNST  = 24
543          000026          SASTIR = 26
544          000030          STRIR  = 30
545          000300          BUFTSM = 300
546          000302          NUNEXM = 302
547          000304          DISCON = 304
548          000306          QUOVHN = 306
549
550
551
552          ;*****
553          ;* INFORMATION OUT COMMAND - BSEL6
554          ;*****
555          000100          RDCRSS = BIT6
556          000040          REDTSS = BIT5
557          000000          NURET = 00
558          000010          RETMSI = 10
559          000020          BUFRIC = 20
560          000012          ENOTB  = 12
561          000013          ERINB  = 13
562
563
564
565          ;*****
566          ;* DMP OBUS REG 10 - TRANSMITTER BUFFER
567          ;*****
568          000200          TX7    = BIT7
569          000100          TX6    = BIT6
570          000040          TX5    = BIT5
571          000020          TX4    = BIT4
572          000010          TX3    = BIT3
573          000004          TX2    = BIT2
574          000002          TX1    = BIT1
575          000001          TX0    = BIT0
576
577          ;*****
578          ;* DMP OBUS REG 11
579          ;*****
580          000200          UC      = BIT7
581          000010          GOAH   = BIT3

```

```

582          000004          ABURT  = BIT2
583          000002          EDM    = BIT1
584          000001          SUM    = BIT0
585
586          ;*****
587          ;* DMP OBUS REG 12
588          ;*****
589          000200          IC      = BIT7
590          000100          BPOLL  = BIT6
591          000040          LULP   = BIT5
592
593          ;*****
594          ;* DMP OBUS REG 13
595          ;*****
596          000200          POLL   = BIT7
597          000100          DIR    = BIT6
598          000040          SELFR  = BIT5
599          000020          HDX    = BIT4
600          000010          MAINT1 = BIT3
601          000004          MAINT2 = BIT2
602          000002          SELSBY = BIT1
603
604          ;*****
605          ;* DMP OBUS REG 14
606          ;*****
607          000100          TXEN   = BIT6
608          000040          DISS1  = BIT5
609          000020          RDAX   = BIT4
610          000010          *AX    = BIT3
611          000004          ENAX   = BIT2
612          000002          AX2    = BIT1
613          000001          AX1    = BIT0
614
615          ;*****
616          ;* DMP OBUS REG 17
617          ;*****
618          000200          CRC2   = BIT7
619          000100          CRC1   = BIT6
620          000040          IDLE   = BIT5
621          000020          SECA   = BIT4
622          000010          STRIP  = BIT3
623          000004          RDALL  = BIT2
624          000002          IERR   = BIT1
625          000001          DDCMP  = BIT0
626
627          ;*****
628          ;* DMP IBUS REG 10 - RECEIVER BUFFER
629          ;*****
630          000200          RX7    = BIT7
631          000100          RX6    = BIT6
632          000040          RX5    = BIT5
633          000020          RX4    = BIT4
634          000010          RX3    = BIT3
635          000004          RX2    = BIT2
636          000002          RX1    = BIT1
637          000001          RX0    = BIT0

```

```

638
639
640
641
642      000200
643      000100
644      000040
645      000020
646      000010
647      000004
648      000002
649      000001
650
651
652
653
654      000200
655      000100
656      000040
657      000020
658      000010
659      000004
660      000002
661      000001
662
663
664
665
666      000200
667      000100
668      000040
669      000020
670      000010
671      000004
672      000002
673      000001
674
675
676
677
678      000020
679      000040
680      000100
681      000200
682
683
684
685
686      000200
687      000100
688      000040
689      000020
690      000010
691      000004
692      000002
693      000001

;*****
;* DMP IBUS REG 12
;*****
IC      = BIT7
IACT    = BIT6
LULP    = BIT5
IRDY    = BIT4
OVRT    = BIT3
RAB     = BIT2
EBLK    = BIT1
BCC     = BIT0

;*****
;* DMP IBUS REG 13
;*****
RING    = BIT7
UTR     = BIT6
RTS     = BIT5
MDX     = BIT4
MODR    = BIT3
CS      = BIT2
STBY    = BIT1
CARR    = BIT0

;*****
;* DMP IBUS REG 14
;*****
HEADY   = BIT7
TXEN    = BIT6
DISS1   = BIT5
HDAX    = BIT4
WAX     = BIT3
ENAX    = BIT2
AX2     = BIT1
AX1     = BIT0

;*****
;* DMP IBUS REG 16 -- DMV "SWPBT" (W/BITS RE-ARRANGED)
;*****
MDDSW0  = BIT4
MDDSW1  = BIT5
MDDSW2  = BIT6
ENABS#  = BIT7

;*****
;* DMP IBUS REG 17
;*****
SIGH    = BIT7
SIG0    = BIT6
TXDATA  = BIT5
OCUR    = BIT4
ICIR    = BIT3
TESTMD  = BIT2
MCLK    = BIT1
DOCMP   = BIT0
    
```

```

694
695
696
697
698
699
700      000001
701      000002
702      000004
703      000010
704
705
706
707
708
709
710      000001
711      000004
712      000010
713      000020
714      000040
715      000100
716
717
718
719
720
721
722      000001
723      000002
724      000004
725
726
727
728      000252' 000000
729
730      000254' 000000
731      000256' 000000
732      000260' 000000
733      000262' 000000
734      000264' 000000
735      000266' 000000
736      000270' 000000
737      000272' 000000
738      000274' 000000
739      000276' 000000
740      000300' 000000
741      000302' 000000
742      000304' 000000
743      000306' 000000
744      000310' 000000
745      000312' 000000
746      000314' 000000
747      000316' 000000
748      000320' 000000
749      000322' 000000

;*****
;* INTERRUPT FLAG BIT DEFINITIONS IN "FLAGS"
;*****
ININT   = BIT0      ; = 1 IF INPUT INTRPT OCCURRED
OUTINT  = BIT1      ; = 1 IF OUTPUT INTRPT OCCURRED
NONQI   = BIT2      ; = 1 FOR NON-QUEUED (REQUEST-AND-WAIT) INPUT OPERATION
NONQO   = BIT3      ; = 1 FOR NON-QUEUED (REQUEST-AND-WAIT) OUTPUT OPERATION

;*****
;* INTERRUPT STATUS WORD (INTABL) BIT DEFINITIONS
;*****
MDFDON  = BIT0      ;MODE DEFINITION DONE
ISTDON  = BIT2      ;ISTART DONE
BINDON  = BIT3      ;BACCIR DONE
BITDON  = BIT4      ;BACCIT DONE
BORDON  = BIT5      ;BACCON DONE
BOTDON  = BIT6      ;BACCOT DONE

;*****
;* ERROR FLAG BIT DEFINITIONS IN "ERRORS"
;*****
INTIMU  = BIT0      ;INPUT INTERRUPT TIMED-OUT
OUTIMO  = BIT1      ;OUTPUT INTERRUPT TIMED-OUT
BADINI  = BIT2      ;MASTER CLEAR FAILED ON DEVICE

BUFFEX: .WORD 0      ;ADJUSTED EXTENDED R/W BUFFER ADDRESS BITS

N.DEVS: .WORD 0      ;NUMBER OF DEVICES PRESENT
TOTAL:  .WORD 0      ;NUMBER OF DMP11'S CURRENTLY ACTIVE
COUNT: .WORD 0      ;ITERATION COUNT FOR MODULE
SAVB#F: .WORD 0      ;RCV ISR TEMPORARY STORAGE
SELECT: .WORD 0      ;SOFTWARE BIT MAP OF ACTIVE DEVICES
DEVPTR: .WORD 0      ;DEVICE SELECTION POINTER
LUNG16: .WORD 0      ;STORAGE FOR LU IBUS REG 16
CSEL0:  .WORD 0      ;STORAGE FOR INPUT COMMAND TO BE PERFORMED
CSEL2:  .WORD 0
CSEL4:  .WORD 0
CSEL6:  .WORD 0
CSEL10: .WORD 0
MSEL0:  .WORD 0      ;STORAGE FOR CSN'S RETURNED ON OUTPUT CHND
RSEL2:  .WORD 0
RSEL4:  .WORD 0
RSEL6:  .WORD 0
RSEL10: .WORD 0
FLAGS:  .WORD 0      ;INPUT AND OUTPUT INTERRUPT FLAG BITS
ERRORS: .WORD 0      ;ERROR FLAG BITS
INTIMR: .WORD 0      ;INPUT INTERRUPT TIMER
    
```

```

750 000324' 000000      OUTIMR: .WORD 0      ;OUTPUT INTERRUPT TIMER
751 000326' 000000      LLKCNT: .WORD 0      ;COUNT OF LOGICAL LINKS
752                                     ; IF P-TO-P, THIS IS SAME AS NO. OF DEVICES.
753                                     ; IF MULTIPOINT, THIS IS SAME AS NO. OF TRIBUTARIES.
754 000330' 000000      ISTCNT: .WORD 0      ;COUNT OF ISTART ATTEMPTS ON LOGICAL LINK
755 000332' 000000      SCHACH: .WORD 0      ;MISCELLANEOUS STORAGE LOCATION
756 000334' 000000      TRBADR: .WORD 0      ;CURRENT TRIB ADDRESS
757 000336' 000000      TRBMAX: .WORD 0      ;MAXIMUM TRIB ADDRESS
758 000340' 000000      LNKDUN: .WORD 0      ;NO. OF LOG. LNKS DONE WITH MSG
759 000342' 000000      INDDUN: .WORD 0      ;INPUT COUNT OF LOGICAL LINKS DONE WITH MSG
760                                     ; PER ITERATION
761 000344' 000000      OUTDUN: .WORD 0      ;OUTPUT COUNT OF LOGICAL LINKS DONE WITH MSG
762                                     ; PER ITERATION
763 000346' 000000      WBFPA: .WORD 0      ;WRITE BUFFER PHYSICAL ADDRESS
764 000350' 000000      WBFEA: .WORD 0      ;WRITE BUFFER EA BITS
765
766 ;*****
767 ;* INTABLE - INTERRUPT STATUS TABLE - 16 (MAX.) 2-WORD ENTRIES, 1 ENTRY
768 ;* PER LOGICAL LINK.
769 ;*
770 ;* IN EITHER POINT-TO-POINT OR MULTIPOINT TRIB MODE, UP TO 16 ENTRIES CAN BE
771 ;* USED, WHERE AN ENTRY CONSISTS OF 16 INTERRUPT STATUS FLAG BITS,
772 ;* FOLLOWED BY A WORD CONTAINING THE TRIB ADDRESS IN THE LOWER BYTE.
773 ;* IN ANY CASE, THE NO. OF OCCUPIED ENTRIES = THE NO. OF LOGICAL LINKS,
774 ;* UN THIS PDP-11 SYSTEM, KEPT IN LLKCNT.
775 ;*****
776 000352' 000040      INTABL: .BLKW 32.
777
778 ; REGISTER ADDRESS TABLE FOR ERROR REPORTS
779
780 REGTHL: .WORD 160170
781 000452' 160170      .WORD 160172
782 000454' 160172      .WORD 160174
783 000456' 160174      .WORD 160176
784 000460' 160176      .WORD 160200
785 000462' 160200      .WORD 177777
786 000464' 177777      .WORD
787
788

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789
790
791 -----
792 ; THIS IS START OF MODULE
793 -----
794 000466' 016767 177322 177570 START: MOV DVID1,SELECT ;GET ACTIVE DEVICES
795 000474' 001004      BNE SETUP ;BR IF ANY ARE SELECTED
796 000476' 004767 004424      DRPMOD: JSR PC,DISABL ;DISABLE INTERRUPTS ON ALL SELECTED DEVICES
797 000502' 104410 000000'      ENDS,BEGIN ;DROP THE DEVICE MODULE
798
799 -----
800 ;SETUP VECTORS FOR ACTIVE DEVICES, AND INITIALIZE THEM
801 -----
802 000506'
803 000506' 005067 177542      CLR N,DEVS ;CLEAR NO. OF DEVICES
804 000512' 005067 177602      CLR ERRORS ;CLEAR ERROR FLAGS
805 000516' 012767 000010 177572      MOV #NONQU,FLAGS ;SET FLAG FOR NON-QUEUED OUTPUT INTRPTS
806 000524' 004767 004354      JSR PC,CLR CMD ;CLEAR COMMAND STORAGE AREA
807 000530' 012704 000352'      MOV #INTABL,R4 ;INIT POINTER TO INTRPI STATUS TABLE
808 000534' 005024      CLR (R4)+ ;CLEAR A TABLE WORD
809 000536' 020427 000452'      CMP R4,#INTABL+64. ;SEE IF ALL ENTRIES CLEARED YET
810 000542' 103774      BLU 15 ;BR IF NOT YET
811 000544' 016702 177240      MOV VECTOR,R2 ;GET INITIAL VECTOR
812 000550' 016700 177232      MOV ADDR,R0 ;GET INITIAL ADDRESS
813 000554' 012703 005304'      MOV #11580,R3 ;INIT ISR POINTER
814 000560' 012767 000001 177500      MOV #BIT0,DEVPTR ;INIT SELECTION POINTER
815 000566' 005067 177534      CLR LLKCNT ;INIT LOGICAL LINK CNT
816
817 ;FIND A SELECTED DEVICE
818 28: BIT DEVPTR,SELECT ;SEE IF THIS DEVICE IS SELECTED
819 000600' 001021      BNE 56 ;BR IF SELECTED
820 000602' 006367 177460      ASL DEVPTR ;SHIFT SELECTION POINTER
821 000606' 001002      BNE 45 ;BR IF NOT ALL DEVICES SCANNED YET
822 000610' 000167 000410      JMP STRFUP ;ALL DEVICES SCANNED - START LINKS
823 000614' 062703 000036      ADD #36,R3 ;POP ISR POINTER
824 000620' 062702 000010      ADD #10,R2 ;POP VECTOR
825 000624' 005767 177174      TST SR4 ;IS THIS A DMV?
826 000630' 001402      BEQ 25$
827 000632' 062700 000010      ADD #10,R0 ;IF YES: POP DMV11 ADDRESS (20)
828 000636' 062700 000010      ADD #10,R0 ;IF NO: POP DMP11 ADDRESS (10)
829 000642' 000753      BR 25 ;CONTINUE
830
831 ; MASTER CLEAR THIS DEVICE
832 56: MOV# #MCLR,#SEL1(R0) ;SET MASTER CLEAR
833 000644' 112760 000100 000001      TST SR4 ;IS THIS A DMV?
834 000652' 005767 177146      BNE 23$ ;IF YES: SKIP LULOOP/RUN STUFF
835 000656' 001011      MOV# #RUN,#SEL1(R0) ;SET RUN BIT
836 000660' 142760 000200 000001      M1CB #RUN,#SEL1(R0) ;CLEAR RUN
837 000664' 152760 000010 000001      B1SB #LULOOP,#SEL1(R0) ;SET LULOOP
838 000702' 004767 003714      JSR PC,READ16 ;READ LU REG 16 FOR SWITCHES
839 000706' 004767 003530      JSR PC,INIDMP ;MASTER CLEAR DEVICE
840 000712' 132767 000004 177400      BIT# #BADINI,ERRORS ;SEE IF INIT WAS BAD
841 000720' 001070      BNE 45 ;IF INIT BAD, GO DROP MODULE
842
843 ; LOAD INPUT AND OUTPUT VECTORS FOR THIS DEVICE
844 000722' 010312      MOV R3,(R2) ;LOAD INPUT ISR VECTOR
845 000724' 116762 177062 000002      MOV# BHI,2(R2) ;LOAD PRIORITY
846 000732' 105062 000003      CLRB J(R2) ;CLEAR HI BYTE OF PROGRAM STATUS WORD
847 000736' 010063 000034      MOV R0,34(R3) ;LOAD DMP11 ADDRESS

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845 000742' 010362 000004      MUV      R3,4(R2)      ;LOAD OUTPUT ISR VECTOR
846 000746' 062762 000016 000004  ADD      #16,4(R2)     ;POINT TO OUTPUT ISR
847 000754' 116762 177032 000006  MOVWB   BH,6(R2)      ;LOAD PRIORITY
848 000762' 105062 000007      CLMB     7(R2)         ;CLEAR HI BYTE OF STATUS WORD
849
;DEFINE THE MODE OF THIS DEVICE (FDX HDX P-P M-P TRIB)
850 000766' 112767 000002 177300  MOVWB   #MODEP,CSEL2  ;SET MODE DEFINITION COMMAND
851 000774' 152760 000010 000001  B1SB   #LULUOP,#SEL1(R0) ;SET LULUOP TO FORCE MODE
852 001002' 126727 177010 000000  CMPB    SR1,#0        ;SEE IF GRP OF POINT-TO-POINT DEVICES
853 001010' 001041      BNE     #9           ;BR IF NOT
854 001012' 132767 000200 177250  BITB    #ENABSW,LURG16 ;SEE IF LU SWITCHES ENABLED
855 001020' 001410      BEQ     #6           ;BR IF NOT
856 001022' 132767 000020 177240  BITB    #MODSW0,LURG16 ;SEE IF HDX IN SWITCHES
857 001030' 001404      BEQ     #6           ;BR IF HDX
858 001032' 112767 000003 177240  MOVWB   #FDPP,CSEL6   ;SET FDX P-TO-P MODE
859 001040' 000403      BR      #7           ;GO SET MODE
860 001042' 112767 000002 177230 66: MOVWB   #HDP,CSEL6    ;SET HDX P-TO-P MODE
861 001050' 004767 003666 78: JSR     PC,INCMND    ;PERFORM MODE DEF'N CMND
862 001054' 104407 000000'      BREAKS,BEGIN          ;TEMPORARY RETURN TO MONITOR....
863 001060' 104407 000000'      BREAKS,BEGIN          ;THEN CONTINUE AT NEXT INSTRUCTION.
864 001064' 142760 000010 000001  B1CB   #LULUOP,#SEL1(R0) ;CLEAR LULUOP
865 001072' 032767 000001 177220  BIT     #INTIMO,ERRORS ;SEE IF INPUT INTRPT TIMED-OUT
866 001100' 001443      BEQ     #12          ;BR IF NO TIME-OUT
867 001102' 142760 000200 000001 86: B1CB   #RUN,#SEL1(R0) ;CLEAR RUN ON THIS DEVICE
868 001140' 000167 17362      JMP     DMPMOD        ;GO DROP MODULE
869 001114' 126727 176676 000001 98: CMPB    SR1,#1        ;SEE IF GRP OF M-P TRIBUTARIES
870 001122' 001020      BNE     #11          ;BR IF NOT
871 001124' 132767 000200 177136  BITB    #ENABSW,LURG16 ;SEE IF LU SWITCHES ENABLED
872 001132' 001410      BEQ     #10          ;BR IF NOT
873 001134' 132767 000020 177126  BITB    #MODSW0,LURG16 ;SET HDX IN SWITCHES
874 001142' 001404      BEQ     #10          ;BR IF HDX
875 001144' 112767 000007 177126  MOVWB   #FDT,CSEL6    ;SET FDX TRIB MODE
876 001152' 000736      BR      #7           ;GO SET MODE
877 001154' 112767 000006 177116 108: MOVWB   #HDT,CSEL6    ;SET HDX TRIB MODE
878 001162' 000732      BR      #7           ;GO SET MODE
879 001164' 004767 004000 118: JSR     PC,GETERR     ;LOAD ERROR INFORMATION FOR PRINTOUT
880 001170' 012767 000007 176710  MOVWB   #SELERR,ERRTYP ;SET CODE FOR SELECTION ERROR
881
;*****
882 001176' 104405 000000' 000000  HDRERS,BEGIN,NULL    ;OPERATOR SPEC'D INVALID SRI VALUE
883
;*****
884 001204' 000167 177266      JMP     DMPMOD        ;GO DROP THE MODULE
885 001210' 005267 177040 128: INC     N.DEVS        ;UPDATE THE NO. OF DEVICES TO RUN
886 001214' 105060 000000 128: CLMB    #SEL0(R0)     ;DISABLE INTRPTS FROM THIS DEVICE
887 001220' 000167 177356      JMP     #3           ;CONTINUE SETUP
888
-----
889
; ATTEMPT TO START THE PROTOCOL ON EACH LOGICAL LINK, AND IF A LOGICAL LINK
; CAN'T BE STARTED, DROP THE DEVICE WHICH CONTROLS THAT LINK, FROM TESTING
;-----
893 001224'
894 001224' 016767 177024 177074  STARTUP: MUV     N.DEVS,LLKCNT ;SET TOTAL NO. OF LOGICAL LINKS
895 001232' 012767 000001 177074  MUV     #1,TRIBADR    ;SET P-TO-P TRIB ADRS = 1
896 001240' 012705 000354'      MUV     #INTABL+2,R5  ;INIT STATUS TABLE TRIB ADRS POINTER
897 001244' 126727 176546 000001  CMPB    SR1,#1        ;SEE IF MULTIPOINT
898 001252' 001003      BNE     #18          ;BR IF NOT MULTIPOINT
899 001254' 016767 176542 177052  MUV     SR3,TRIBADR   ;INIT MULTIPOINT TRIB ADRS
900 001262' 016700 176520 15: MUV     ADDR,R0       ;INIT DEVICE ADRS

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901 001266' 012767 000001 176772      MOV     #BIT0,DEVPTX  ;INIT SELECTION POINTER
902
;FIND A SELECTED DEVICE
903 001274' 036767 176766 176762 28: BIT     DEVPTX,SELECT ;SEE IF THIS DEVICE IS SELECTED
904 001302' 001017      BNE     #22          ;BR IF SELECTED
905 001304' 006367 176756 208: ASL     DEVPTX        ;SHIFT SELECTION POINTER
906 001310' 001002      BNE     #21          ;BR TO KEEP SCANNING
907 001312' 000167 000704      JMP     #19          ;ALL DEVICES SCANNED - PROCEED
908
909 001316' 005767 176502 218: TST     SR4          ; IS THIS A DMV ?
910 001322' 001402      BEQ     #25          ;
911 001324' 062700 000010 258: ADD     #10,R0       ;IF YES: INCREMENT DMV11 ADDRESS (20)
912 001330' 062700 000010 258: ADD     #10,R0       ;IF NO: INCREMENT DMP11 ADDRESS (10)
913 001334' 062705 000004 258: ADD     #4,R5        ;INCR STATUS TABLE TRIB ADRS POINTER
914 001340' 000755      BR      #26          ;CONTINUE
915
;SET UP FOR CONTROL IN COMMAND
916 001342' 112767 000001 176724 228: MOVWB   #CTLIN,CSEL2  ;SET CONTROL IN CMND
917 001350' 016767 176760 176717  MOVWB   #TRIB,CSEL2+1 ;SET TRIB ADRS
918 001356' 016715 176752      MOV     #TRIB,CSEL2+1 ;SET TRIB ADRS
919 001362' 005067 176710      CLR     CSEL4         ;STORE THE TRIB ADRS IN STATUS TABLE
920 001366' 005067 176706      CLR     CSEL6
921
;ESTABLISH A LOGICAL LINK
922 001372' 112767 000001 176700  MOVWB   #ESTRIB,CSEL6 ;SET CODE FOR ESTABLISH TRIB CMND
923 001400' 042767 000002 176710  B1C     #OUTINT,FLAGS ;CLEAR OUTPUT INTRPT FLAG
924 001406' 004767 003330 176710  JSR     PC,INCMND    ;ESTABLISH THIS TRIBUTARY
925
;CHANGE THE SELECTION INTERVAL TO 5 SECONDS
926 001412' 005767 176406 218: TST     SR4          ; IS THIS A DMV ?
927 001416' 001404      BEQ     #23          ;
928 001420' 012767 000764 176650  MUV     #500,,CSEL4  ;IF YES: CONSTANT = 500.
929 001426' 000403      BR      #24          ;
930 001430' 012767 011610 176640 238: MUV     #5000,,CSEL4 ;IF NO: CONSTANT = 5000.
931 001436' 012767 000236 176634 248: MOV     #WRITSS+36,CSEL6 ;WRITE TSS ADDRESS 36 (SEL INTERVAL).
932 001444' 042767 000002 176644  B1C     #OUTINT,FLAGS ;CLEAR OUTPUT INTRPT FLAG.
933 001452' 004767 003264 248: JSR     PC,INCMND    ;CHANGE THE INTERVAL.
934
;ISTART THE LOGICAL LINK
935 001456' 005067 176614      CLR     CSEL4         ;CLEAR INTERVAL.
936 001462' 012767 000010 176640  MOV     #,,ISTCNT    ;INIT ISTART COUNTER FOR APPROX. 5 MINUTES
937
; STARTUP (EACH COUNT = 35. SEC.)
938 001470' 112767 000003 176602 38: MOVWB   #ISTART,CSEL6 ;SET CODE FOR ISTART REQUEST
939 001476' 042767 000002 176612  B1C     #OUTINT,FLAGS ;CLEAR OUTPUT INTRPT FLAG
940 001504' 004767 003232 176606  JSR     PC,INCMND    ;PERFORM ISTART CMND
941 001510' 012767 177777 176606  MOV     #-1,OUTIMR   ;INIT OUTPUT INTERRUPT TIMER
942 001516'
46: BREAKS,BEGIN          ;TEMPORARY RETURN TO MONITOR....
944 001522' 104407 000000'      BREAKS,BEGIN          ;THEN CONTINUE AT NEXT INSTRUCTION.
945 001526' 132767 000002 176562  BITB    #OUTINT,FLAGS ;SEE IF OUTPUT INTRPT SERVICED YET
946 001534' 001015      BNE     #58          ;BR IF YES
947 001536' 005367 176562  DEC     OUTIMR        ;DECREMENT TIMER
948 001542' 001365      BNE     #48          ;BR IF OUTPUT INTRPT DIDN'T TIME-OUT YET
949 001544' 004767 003420 176562  JSR     PC,GETERR     ;LOAD ERROR INFORMATION FOR PRINTOUT
950 001550' 012767 000023 176330  MOV     #NOINTR,ERRTYP ;CODE FOR DEVICE FAILED TO INTRPT
951
;*****
952 001556' 104405 000000' 000452'  HDRERS,BEGIN,REGTBL ;OUTPUT INTERRUPT TIMED-OUT
953
;*****
954 001564' 000167 000360      JMP     #146         ;GO DROP MODULE
955 001570' 142767 000370 176510 58: B1CB   #CMDMSK,#SEL2 ;MASK FOR OUTPUT COMMAND FIELD
956 001576' 126727 176504 000001  CMPB    #SEL2,#CTLOUT ;CHK FOR CTL OUT CMND RETURNED

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957 001004' 001412          BEQ    05          ;BR IF YES
958 001006' 004767 003356   JSR    PC,GETERR  ;LOAD ERROR INFORMATION FOR PRINTOUT
959 001012' 012767 000000 176266 MUV    #NOTDFN,ERRIYP ;CODE FOR ERROR NOT DEFINED (MUST INTERPRET
; CSR CONTENTS)
;*****
960
961
962 001020' 104405 000000' 000452' HDRS,BEGIN,REGTBL ;UNEXPECTED OUTPUT CMND
;*****
963
964 001026' 000167 000316   JMP    14$        ;GO DROP MODULE
;CHECK FOR LOGICAL LINK RUNNING YET
965
966 001032' 126727 176454 000024 05:  CMPB  HSEL6,#RUNST ;SEE IF PROTOCOL RUNNING ON THIS LOG LINK YET
967 001040' 001002          BNE    7$        ;BR IF NOT
968 001042' 000167 000312   JMP    15$        ;GO SELECT NEXT LOGICAL LINK
;CHECK FOR TRANSMIT THRESHOLD ERROR
969
970 001046' 126727 176440 000004 7$:  CMPB  HSEL6,#THREK ;SEE IF TRANSMIT THRESHOLD ERROR (TRIB NOT RESPONDING)
971 001054' 001416          BEQ    05        ;BR IF YES
972 001056' 126727 176430 000006   CMPB  HSEL6,#STHREK ;SEE IF SELECT THRESHOLD ERROR
973 001064' 001412          BEQ    05        ;BR IF YES
974 001066' 004767 003276   JSR    PC,GETERR  ;LOAD ERROR INFORMATION FOR PRINTOUT
975 001072' 012767 000000 176206 MUV    #NOTDFN,ERRIYP ;CODE FOR ERROR NOT DEFINED (MUST INTERPRET
; CSR CONTENTS)
;*****
976
977
978 001700' 104405 000000' 000452' HDRS,BEGIN,REGTBL ;UNEXPECTED CTL OUT CMND
;*****
979
980 001706' 000167 000236   JMP    14$        ;GO DROP MODULE
981 001712' 005367 176412   DEC    1STCNT    ;DECREMENT ISTART COUNTER
982 001716' 001012          BNE    9$        ;BR IF COUNT NOT OVERFLOWED YET
983 001720' 004767 003244   JSR    PC,GETERR  ;LOAD ERROR INFORMATION FOR PRINTOUT
984 001724' 012767 000000 176154 MUV    #NOTDFN,ERRIYP ;CODE FOR ERROR NOT DEFINED (MUST INTERPRET
; CSR CONTENTS)
;*****
985
986
987 001732' 104405 000000' 000452' HDRS,BEGIN,REGTBL ;TIMED-OUT ATTEMPTING TO START LINK
;*****
988
989 001740' 000167 000204   JMP    14$        ;GO DROP MODULE
;HALT PROTOCOL PRIOR TO ISTART RETRY
990
991 001744' 112767 000005 176326 9$:  MOVB  #HALST,CSEL6 ;SET CODE FOR HALT REQUEST
992 001752' 042767 000002 176336   BIC   #OUTINT,FLAGS ;CLEAR OUTPUT INTRPT FLAG
993 001760' 004767 002756   JSR   PC,INCMND  ;PERFORM HALT CMND TO RE-INIT LINK STATUS
994 001764' 012767 020000 176332   MOV   #20000,OUTIMR ;INIT OUTPUT INTERRUPT TIMER
100:
995 001772'
996 001772' 104407 000000'          BREAKS,BEGIN ;TEMPORARY RETURN TO MONITOR...
997 001776' 104407 000000'          BREAKS,BEGIN ;THEN CONTINUE AT NEXT INSTRUCTION.
998 002002' 032767 000002 176306   BIC   #OUTINT,FLAGS ;SEE IF OUTPUT INTRPT SERVICED YET
999 002010' 001015          BNE    11$       ;BR IF YES
1000 002012' 005367 176306   DEC   OUTIMR    ;DECREMENT TIMER
1001 002016' 001365          BNE    10$       ;BR IF OUTPUT INTRPT DIDN'T TIME-OUT YET
1002 002020' 004767 003144   JSR   PC,GETERR  ;LOAD ERROR INFORMATION FOR PRINTOUT
1003 002024' 012767 000023 176054 MUV   #NOINTM,ERRIYP ;CODE FOR DEVICE FAILED TO INTRPT
;*****
1004
1005 002032' 104405 000000' 000452' HDRS,BEGIN,REGTBL ;OUTPUT INTERRUPT TIMED-OUT
;*****
1006
1007 002040' 000167 000104   JMP    14$        ;GO DROP MODULE
1008 002044' 142767 000370 176234 11$: BICB  #CMDMSK,HSEL2 ;MASK FOR OUT CMND RETURNED
;CHECK FOR INFO OUT WITH "BUFFERS RETURNED" CODE
1009
1010 002052' 126727 176230 000002   CMPB  HSEL2,#INFOOUT ;CHK FOR INFO OUT CMND RETURNED
1011 002060' 001022          BNE    13$       ;BR IF NOT
1012 002062' 142767 000340 176222   BICB  #RIMMSK,HSEL6 ;MASK FOR RETURN KEY

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1013 002070' 126727 176216 000020   CMPB  HSEL6,#BUFWIC ;CHK FOR BUFFER RETURN COMPLETE
1014 002076' 001002          BNE    12$       ;BR IF NOT BUFFER RETURN COMPLETE
1015 002100' 000167 177364   JMP    3$        ;GO TRY TO ISTART AGAIN
1016 002104' 004767 003060 12$: JSR    PC,GETERR  ;LOAD ERROR INFORMATION FOR PRINTOUT
1017 002110' 012767 000000 175770 MUV    #NOTDFN,ERRIYP ;CODE FOR ERROR NOT DEFINED (MUST INTERPRET
; CSR CONTENTS)
;*****
1018
1019
1020 002116' 104405 000000' 000452' HDRS,BEGIN,REGTBL ;UNEXPECTED INFO OUT CMND
;*****
1021
1022 002124' 000411          BR     14$        ;GO DROP MODULE
1023 002126' 042767 000002 176162 13$: BIC   #OUTINT,FLAGS ;CLEAR OUTPUT INTRPT FLAG
1024 002134' 012767 020000 176162   MOV   #20000,OUTIMR ;INIT OUTPUT INTRPT TIMER
1025 002142' 105060 000002          CLMB  HSEL2(R0)  ;RELEASE THE PORT AGAIN
1026 002146' 000711          BR     10$       ;KEEP LOOKING FOR INFO OUT CMND
1027 002150' 005060 000000          CLR   SEL0(R0)  ;CLEAR RUN, DISABLE INTRPTS ON THIS DEVICE
1028 002154' 000167 176316   JMP    DRPMOD    ;GO DROP MODULE
;SELECT NEXT LOGICAL LINK TO START
1029
1030 002160' 105060 000002          15$: CLRB  HSEL2(R0)  ;CLEAR BSEL2 (INCLUDING ROY0)
1031 002164' 126727 175626 000001   CMPB  SM1,#1     ;SEE IF MULTIPOINT
1032 002172' 001007          BNE    16$       ;BR IF NOT MULTIPOINT
1033 002174' 005267 176134   INC   TRBADR    ;INCREMENT TRIB ADNS
1034 002200' 105767 176130   TSTB  TRBADH    ;SEE IF TRIB ADNS OVERFLOW
1035 002204' 001002          BNE    16$       ;BR IF NOT
1036 002206' 005267 176122   INC   TRBADH    ;IF TRIB ADNS = 0, MAKE IT 1
; TRIB ADNS = 0 IS ILLEGAL
1037
1038 002212' 105060 000000          16$: CLRB  HSEL0(R0) ;DISABLE INTRPTS FROM THIS DEVICE
1039 002216' 000167 177062   JMP    20$       ;GO START NEXT LOGICAL LINK
1040 002222' 000413          19$: BR   REENTR   ;GO BEGIN TESTING ON ALL LOGICAL LINKS
;-----
; RESTART ADDRESS FOR A PASS ON ALL DEVICES
;-----
1041
1042
1043
1044 002224' 012705 000020   RESTR: MOV  #16,,R5 ;INIT INTRPT STATUS TABLE ENTRY COUNTER
1045 002230' 012704 000352'   MOV  #INTABL,R4 ;INIT TABLE POINTER
1046 002234' 012714 000001   MOV  #MDFDUN,(R4) ;SET MODE DEF'N DONE FLAG IN STATUS BYTE
1047 002240' 062704 000004 1$:  ADD  #4,R4      ;INCR STATUS TABLE INDEX
1048 002244' 005305          DEC  R5         ;DECR TABLE ENTRY COUNTER
1049 002246' 001372          BNE  1$        ;BR IF MORE TO DO
1050 002250' 000412          BR   ENABLE
;-----
; PROGRAM ONLY COMES HERE AT START OF FIRST PASS
;-----
1051
1052
1053
1054
1055 002252' 012705 000020   REENTR: MOV  #16,,R5 ;INIT INTRPT STATUS TABLE ENTRY COUNTER
1056 002256' 012704 000352'   MOV  #INTABL,R4 ;INIT TABLE POINTER
1057 002262' 012714 000005 1$:  MOV  #MDFDUN11STDUN,(R4) ;INIT DONE FLAGS FOR ITERATION
1058 002266' 062704 000004   ADD  #4,R4      ;INCR STATUS TABLE INDEX
1059 002272' 005305          DEC  R5         ;DECREMENT COUNTER
1060 002274' 001372          BNE  1$        ;BR IF MORE TO DO
1061
1062
1063
1064
1065 002276' 005067 176040   ENABLE: CLR  INDRAM ;INIT INPUT DONE COUNT TO 0
1066 002302' 005067 176036   CLR  OUTDRAM  ;INIT OUTPUT DONE COUNT TO 0
1067 002306' 104415 000000' 000124' GETPAS,BEGIN, RBUFVA ;GET PHYSICAL ADDRESS FROM 16-BIT RBUFVA
1068 002314' 016767 175610 175730 MUV  RBUFEA,BUFFEX

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1069 002322' 006367 175724      ASL  BUFFER          ;GET RCV BUF EA BITS INTO BITS 6,7
1070 002326' 006367 175720      ASL  BUFFER
1071 002332' 032767 000002 175464  BIT  #MODE22,SR4     ;* DMV IN Q22 MODE ??
1072 002340' 001406              BEQ  58              ;* NO: DONE ADJUSTING...
1073 002342' 006367 175704      ASL  BUFFER          ;* YES: ADJUST FOR Q22 MODE
1074 002346' 006367 175700      ASL  BUFFER
1075 002352' 000367 175674      SWAB BUFFER          ;*
1076 002356' 016767 175544 175762 58:  MOV  #BUPFA,#BFPA   ;SET UP SAME BUFFER FOR READ AND WRITE
1077 002364' 016767 175662 175756  MOV  BUFFER,#BFEA
1078 002372' 016700 175410      MOV  ADDR,RO        ;INIT DEVICE ADDRESS
1079 002376' 012767 000001 175662  MOV  #BIT0,DEVPTR   ;INIT SELECTION POINTER
1080 002404' 042767 000010 175704  BIC  #NONQU,FLAGS   ;CLEAR FLAG TO GET QUEUED OUTPUT INTRPTS AGAIN
1081 002412' 036767 175650 175644 18:  BIT  DEVPTR,SELECT  ;SEE IF THIS DEVICE IS SELECTED
1082 002420' 001403              BEQ  JS              ;BR IF NOT SELECTED
1083 002422' 112760 000221 000000  MOVB #NQ11E11E0,BSEL0(RO) ;ENABLE INTRPTS, SET RQ1 ON THIS DEVICE
1084 002430' 005767 175370      TST  SR4            ; IS THIS A DMV ?
1085 002434' 001402              BEQ  JS
1086 002436' 062700 000010      ADD  #10,RO         ;IF YES: INCREMENT DEVICE ADDRESS (20)
1087 002442' 062700 000010      ADD  #10,RO         ;IF NO: INCREMENT DMP11 ADDRESS (10)
1088 002446' 006367 175614      ASL  DEVPTR        ;SHIFT SELECTION POINTER
1089 002452' 001357              BNE  15             ;BR IF NOT ALL ENABLED YET
1090 002454'                  RTNMON:
1091 002454' 104400 000000'      EXITS,BEGIN        ;EXIT TO MONITOR. MODULE WAIT FOR INTERRUPT.
1092
1093
1094

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1095
1096
1097
1098
1099
1100
1101
1102
1103
1104
1105
1106 002460'
1107 002460' 142760 000021 000000  ;*****
1108 002466' 032767 000004 175622  ;* INISR - THIS IS THE INPUT INTERRUPT SERVICE ROUTINE.
1109 002474' 001421              ;* WHEN THE INPUT INTERRUPT OCCURS, THE PROGRAM LOADS THE APPROPRIATE
1110 002476' 016760 175573 000003  ;* INPUT COMMAND INTO THE DEVICE CSR'S, AND MONITORS THE PROGRESS OF THE
1111 002504' 016760 175566 000004  ;* MODULE ON THE CURRENT ITERATION.
1112 002512' 016760 175562 000006  ;*****
1113 002520' 152767 000001 175570  INISR:
1114 002526' 142760 000200 000000  BICB #I11E11E0,BSEL0(RO) ;DISABLE INTERRUPTS
1115 002534' 000167 000416      BIT  #NONQ1,FLAGS    ;SEE IF NON-QUEUED INPUT OPERATION REQUESTED
1116 002540' 010004              BEQ  15              ;BR FOR QUEUED OPERATION
1117 002542' 166704 175240      MOVB CSEL2+1,BSEL3(RO) ;LOAD BSEL3
1118 002546' 005767 175252      MOV  CSEL4,SEL4(RO)  ;LOAD SEL4
1119 002552' 001401              MOV  CSEL6,SEL6(RO)  ;LOAD SEL6
1120 002554' 006204              BICB #ININT,FLAGS   ;SET INPUT INTRPT FLAG
1121 002556' 006204              BICB #RQ1,BSEL0(RO) ;CLEAR RQ1
1122 002560' 016467 000354' 175546  JMP  115
1123 002566' 032764 000020 000352' 18:  MOV  RO,R4          ;GET DEVICE ADDRESS
1124 002574' 001031              SUB  ADDR,R4        ;COMPUTE INTRPT STATUS TABLE INDEX
1125 002576' 032764 000010 000352'  TST  SR4            ; IS THIS A DMV?
1126 002604' 001117              BEQ  88
1127 002606' 032764 000004 000352'  ASH  R4             ;IF YES: DIVIDE BY 4 FOR INDEX (DMV)
1128 002614' 001053              ASH  R4             ;IF NO: DIVIDE BY 2 FOR INDEX (DMP)
1129 002616' 032764 000001 000352'  MOV  INTABL+2(R4),TRBADR ;GET TRIB ADRS FROM TABLE
1130 002624' 001026              BIT  #BITDON,INTABL(R4) ;SEE IF BACCIT JUST COMPLETED
1131 002626' 004767 002336      BNE  46             ;GO HANDLE UNSOLICITED INTERRUPT
1132 002632' 012767 000011 175246  BIT  #BINDON,INTABL(R4) ;SEE IF BACCIR JUST COMPLETED
1133 002640' 104405 000000' 000452'  BNE  78             ;BR IF YES, TO ISSUE BACCIT
1134 002646' 142760 000201 000000 38:  BIT  #ISTDON,INTABL(R4) ;SEE IF ISTART JUST COMPLETED
1135 002654' 000167 177574      BNE  65             ;BR IF YES, TO ISSUE BACCIR
1136 002660' 004767 002304      BIT  #MDFDON,INTABL(R4) ;SEE IF MODE DEFINITION JUST COMPLETED
1137 002664' 012767 000011 175214  BNE  56             ;BR IF YES, TO ISSUE ISTART
1138 002672' 104405 000000' 000452'  JSR  PC,GETERR      ;LOAD ERRNOH INFORMATION FOR PRINTOUT
1139 002674' 000000 000000      MOV  #ILGINT,ERRTYP ;CODE FOR ILLEGAL INTRPT OCCURRED
1140 002676' 000000 000000      ;*****
1141 002678' 104405 000000' 000452'  HNDERS,BEGIN,RECTBL ;UNSOLICITED INPUT INTERRUPT OCCURRED
1142 002680' 000762              ;*****
1143 002682' 000762              BR  JS
1144 002684' 112767 000001 175364  ;ISSUE ISTART CTL IN
1145 002686' 005060 000004 58:  MOVB #CTLIN,CSEL2   ;SET UP CONTROL IN COMMAND
1146 002688' 005060 000006      CLR  SEL4(RO)       ;CLEAR SEL4
1147 002690' 005060 000006      CLR  SEL6(RO)       ;CLEAR SEL6
1148 002692' 112760 000003 000006  MOVB #ISTART,BSEL6(RO) ;SET ISTART STATE
1149 002694' 052764 000004 000352'  BIT  #ISTDON,INTABL(R4) ;SET ISTART DONE IN STATUS WORD
1150 002696' 142760 000200 000000  BICB #RQ1,BSEL0(RO) ;DISABLE INPUTS TO WAIT OUTPUT

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1151 002742' 000502          BR      10$
1152                               ;ISSUE BACCIR
1153 002744' 112767 000000 175322 6$:  MOVB  #BACCIR,CSEL2 ;SET UP BACCIR COMMAND
1154 002752' 016760 175150 000004      MOV  #BFFPA,SEL4(R0) ;LOAD RCV BUF ADRS LO BITS
1155 002760' 012760 002000 000006      MOV  #1024,,SEL6(R0) ;SET RCV CHAR COUNT = 1024.
1156 002766' 156760 175260 000007      BISB  #BFFEX,BSEL7(R0) ;LOAD RCV BUF EA BITS
1157 002774' 032767 000002 175022      BIT   #MODE22,SR4    ; IS THIS A DMV IN Q22 MODE
1158 003002' 001411          BEQ   16$           ; IF YES:
1159 003004' 052767 000010 175262      BIS   #Q22BIT,CSEL2 ; SET Q22 MODE BIT
1160 003012' 012760 002000 000010      MOV  #1024,,SEL10(R0) ; SET RX CHAR CNT=1024.
1161 003020' 016760 175226 000006      MOV  #BFFEX,BSEL6(R0) ; LOAD RCV BUF EA BITS
1162 003026' 052764 000010 000352' 16$:  BIS   #BITDUN,INTABL(R4) ; SET BACCIR DONE IN STATUS WORD
1163 003034' 142760 000200 000000      BICB  #RUI,BSEL0(R0) ;DISABLE INPUT REQUESTS, UNTIL BUFFER IS RCV'D
1164 003042' 000442          BR      10$
1165                               ;ISSUE BACCIT
1166 003044' 112767 000004 175222 7$:  MOVB  #BACCIT,CSEL2 ;SET UP BACCIT COMMAND
1167 003052' 016760 175270 000004      MOV  #BFFPA,SEL4(R0) ;LOAD TX BUFFER ADDRESS LO BITS
1168 003060' 012760 002000 000006      MOV  #1024,,SEL6(R0) ;SET TX CHAR CNT = 1024.
1169 003066' 156760 175256 000007      BISB  #BFFEX,BSEL7(R0) ;LOAD TX BUF EA BITS
1170 003074' 032767 000002 174722      BIT   #MODE22,SR4    ; IS THIS A DMV IN Q22 MODE
1171 003102' 001411          BEQ   12$           ; IF YES:
1172 003104' 052767 000010 175162      BIS   #Q22BIT,CSEL2 ; SET Q22 MODE BIT
1173 003112' 012760 002000 000010      MOV  #1024,,SEL10(R0) ; SET RX CHAR CNT=1024.
1174 003120' 016760 175224 000006      MOV  #BFFEX,BSEL6(R0) ; LOAD RCV BUF EA BITS
1175 003126'          BEQ   12$           ; IF NO: LEAVE ALONE.
1176 003126' 052764 000020 000352' 12$:  BIS   #BITDUN,INTABL(R4) ;SET BACCIT DONE IN STATUS WORD
1177 003134' 142760 000200 000000      BICB  #RUI,BSEL0(R0) ;DISABLE INPUT REQUESTS, UNTIL BUFFER IS TX'D
1178 003142' 005267 175174          INC  INUM          ;INCR NO. OF LOGICAL LNKS DONE WITH MSG
1179 003146' 000400          BR      10$
1180                               ;LOAD TRIB ADRS AND COMMAND INTO CSR'S
1181 003150' 116760 175160 000003 10$:  MOVB  #RADMN,BSEL3(R0) ;LOAD TRIB ADRS
1182 003156' 116760 175112 000002 11$:  MOVB  CSEL2,BSEL2(R0) ;LOAD COMMAND, CLEAR MDI1
1183
1184 003164' 132760 000020 000002 18$:  CHKMUR: BITB  #RDY1,BSEL2(R0) ;SEE IF RDY1 SET AGAIN
1185 003172' 001402          BEQ   1$             ;BR IF RDY1 NOT SET AGAIN
1186 003174' 000167 177260          JMP  INISR          ;GO HANDLE NEXT INPUT
1187 003200' 132760 000200 000002 1$:   BITB  #RDY0,BSEL2(R0) ;SEE IF AN OUTPUT IS PENDING
1188 003206' 001402          BEQ   RTNINT        ;BR IF NOT
1189 003210' 000167 000012          JMP  OUTISM        ;GO HANDLE OUTPUT COMMAND
1190 003214' 152760 000021 000000 19$:  BISB  #IEI1IEU,BSEL0(R0) ;SET IEI AND IEO AGAIN
1191 003222' 000167 177226          JMP  RTNMMN        ;RETURN TO DEC/X11 TO AWAIT INTERRUPT
1192
1193
1194
1195
1196
1197
1198
1199
1200
1201
1202
1203
1204 003226'          ;*****
1205 003226' 142760 000021 000000 20$:  HICB  #IEI1IEU,BSEL0(R0) ;CLEAR IEI, IEO
1206 003234' 032767 000010 175054      BIT   #NUNQU,FLAGS  ;SEE IF NON-QUEUED OUTPUT INTRPT MODE

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1207 003242' 001427          BEQ   1$             ;BR IF NOT
1208 003244' 016067 000000 175032      MOV  SEL0(R0),RSEL0 ;READ AND STORE CSR'S
1209 003252' 016067 000002 175026      MOV  SEL2(R0),RSEL2
1210 003260' 016067 000004 175022      MOV  SEL4(R0),RSEL4
1211 003266' 016067 000006 175016      MOV  SEL6(R0),RSEL6
1212 003274' 005767 174524          TST   SR4           ; IS THIS A DMV ?
1213 003300' 001403          BEQ   20$          ; IF NO: SKIP SEL10 READ
1214 003302' 016067 000010 175004      MOV  SEL10(R0),RSEL10
1215 003310' 052767 000002 175000 20$:  BIS   #OUTINT,FLAGS ;SET OUTPUT INTERRUPT FLAG
1216 003316' 000167 177672          JMP  MININT        ;GO RETURN TO DEC/X11 MONITOR
1217 003322' 010004          MOV  R0,R4         ;GET DEVICE ADDRESS
1218 003324' 166704 174456          SUB  #ADDN,R4      ;COMPUTE INTRPT STATUS TABLE INDEX
1219 003330' 005767 174470          TST   SR4           ; IS THIS A DMV?
1220 003334' 001401          BEQ   22$          ; IF YES: DIVIDE BY 4 FOR INDEX (DMV)
1221 003336' 006204          ASR  R4            ;IF NO: DIVIDE BY 2 FOR INDEX (DMP)
1222 003340' 006204          ASK  R4            ;GET BSEL2 CONTENTS
1223 003342' 116001 000002          MOVB  BSEL2(R0),R1
1224 003346' 142701 000370          BICB  #CMDMSK,R1  ;MASK OFF ALL BUT CMND BITS
1225 003352' 120127 000004          CMPB  R1,#BACCOT  ;SEE IF BACCOT CMND JUST COMPLETED
1226 003356' 001437          BEQ   4$           ;BR IF YES
1227 003360' 120127 000000          CMPB  R1,#BACCOR  ;SEE IF BACCOR CMND JUST COMPLETED
1228 003364' 001534          BEQ   8$           ;BR IF YES
1229 003366' 120127 000003          CMPB  R1,#BACORU  ;SEE IF UNUSED RCV BUFFER RETURNED
1230 003372' 001002          BNE  20$          ;BR IF NOT
1231 003374' 000167 000632          JMP  21$          ;PROCEED
1232 003400' 120127 000001 20$:  CMPB  R1,#CTLOUT  ;SEE IF CONTROL OUT CMND JUST COMPLETED
1233 003404' 001002          BNE  2$           ;BR IF NOT
1234 003406' 000167 000444          JMP  12$          ;GO HANDLE CONTROL OUT COMMAND
1235 003412' 120127 000002 2$:   CMPB  R1,#INFOUT  ;SEE IF INFORMATION OUT CMND JUST COMPLETED
1236 003416' 001002          BNE  3$           ;BR IF NOT
1237 003420' 000167 000514          JMP  15$          ;GO HANDLE INFO OUT COMMAND
1238 003424' 004767 001540          JSR  PC,GETERR    ;LOAD ERROR INFORMATION FOR PRINTOUT
1239 003430' 012767 000011 174450      MOV  #ILGINT,ERRIYP ;SET CODE FOR ILLEGAL INTRPT OCCURRED
1240
1241 003436' 104405 000000' 000452' 3$:  HDRS, BEGIN, REGTBL ;UNSOLICITED OUTPUT INTRPT OCCURRED
1242
1243 003444' 142760 000020 000000      BICB  #IED,BSEL0(R0) ;UNSOLICITED OUTPUT INTRPT - CLEAR IEI
1244 003452' 000167 176776          JMP  RTNMMN        ;RETURN TO DEC/X11 MONITOR TO AWAIT INTRPT
1245
1246 003456' 026067 000004 174662 4$:  CMP  SEL4(R0),#BFFPA ;CHK FOR CORRECT TX BUFFER LO BITS RETURNED
1247 003464' 001411          BEQ   5$           ;BR IF CORRECT
1248 003466' 004767 001476          JSR  PC,GETERR    ;LOAD ERROR INFORMATION FOR PRINTOUT
1249 003472' 012767 000037 174406      MOV  #BADWRT,ERRIYP ;CODE FOR UNABLE TO EXEC WRITE FUNCTION
1250
1251 003500' 104405 000000' 000452' 5$:  HDRS, BEGIN, REGTBL ;INCORRECT TX BA BITS RETURNED
1252
1253 003506' 000453          BR      7$
1254 003510' 116001 000007 5$:  MOVB  BSEL7(R0),R1 ;GET BSEL7 CONTENTS
1255 003514' 142701 000077          BICB  #EAMSK,R1  ;MASK FOR BUS ADRS EA BITS
1256 003520' 032767 000002 174276      BIT   #MODE22,SR4    ; IF THIS IS A DMV IN Q22 MODE...
1257 003526' 001402          BEQ   30$          ; THEN GET EXTENDED ADDRESS (EA)
1258 003530' 116001 000006          MOVB  BSEL6(R0),R1 ; BITS FROM A DIFFERENT CSR.
1259 003534' 120167 174610 30$:  CMPB  R1,#BFFEA  ;CHK FOR CORRECT EA BITS RETURNED
1260 003540' 001411          BEQ   6$           ;BR IF CORRECT
1261 003542' 004767 001422          JSR  PC,GETERR    ;LOAD ERROR INFORMATION FOR PRINTOUT
1262 003546' 012767 000037 174332      MOV  #BADWRT,ERRIYP ;CODE FOR UNABLE TO EXEC WRITE FUNCTION

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1263 ;*****
1264 003554' 104405 000000' 000452' HDRS,BEGIN,REGTBL ;INCORRECT TX EA BITS RETURNED
1265 ;*****
1266 003562' 000425 BR 7S ;
1267 003564' 016001 MOV SEL6(R0),R1 ;GET SEL6 CONTENTS
1268 003570' 032767 BIT #MODE22,SR4 ;* IF THIS IS A DMV IN Q22 MODE...
1269 003576' 001402 BEQ 28S ;* THEN GET CHARACTER COUNT
1270 003600' 016001 MOV SEL10(R0),R1 ;* BITS FROM A DIFFERENT CSR.
1271 003604' 042701 BIC #CCMSK,R1 ;MASK FOR CHAR COUNT BITS
1272 003610' 020127 CMP R1,#1024. ;CHK FOR CORRECT CHAR CNT RETURNED
1273 003614' 001410 BEQ 7S ;BR IF CORRECT
1274 003616' 004767 JSR PC,GETERR ;LOAD ERROR INFORMATION FOR PRINTOUT
1275 003622' 012767 MOV #BADWRT,ERRTYP ;CODE FOR UNABLE TO EXEC WRITE FUNCTION
1276 ;*****
1277 003630' 104405 000000' 000452' HDRS,BEGIN,REGTBL ;INCORRECT TX CHAR CNT BITS RETURNED
1278 ;*****
1279 003636' 012764 MOV #MUDON,ISTDON,INTABL(R4) ;RESET STATUS FOR NEXT ITERATION
1280 003644' 152760 BISB #RQI,BSEL0(R0) ;ENABLE INPUTS AGAIN
1281 003652' 000167 JMP 21S
1282 ;HANDLE BACCOR
1283 003656' 026067 000004 174242 8S: CMP SEL4(R0),RBUFFA ;CHK FOR CORRECT RCY BUFFER LD BITS RETURNED
1284 003664' 001411 BEQ 9S ;BR IF CORRECT
1285 003666' 004767 JSR PC,GETERR ;LOAD ERROR INFORMATION FOR PRINTOUT
1286 003672' 012767 MOV #BADRED,ERRTYP ;CODE FOR UNABLE TO EXEC READ FUNCTION
1287 ;*****
1288 003700' 104405 000000' 000452' HDRS,BEGIN,REGTBL ;INCORRECT RCY BA BITS RETURNED
1289 ;*****
1290 003706' 000454 BR 11S ;
1291 003710' 116001 MOVB #SEL7(R0),R1 ;GET BSEL7 CONTENTS
1292 003714' 142701 BICB #LMSK,R1 ;MASK FOR BUS ADDR EA BITS
1293 003720' 032767 BIT #MODE22,SR4 ;* IF THIS IS A DMV IN Q22 MODE...
1294 003726' 001402 BEQ 31S ;* THEN GET EXTENDED ADDRESS (EA)
1295 003730' 116001 MOVB #SEL6(R0),R1 ;* BITS FROM A DIFFERENT CSR.
1296 003734' 120167 CMPB R1,BUFFEX ;CHK FOR CORRECT EA BITS RETURNED
1297 003740' 001411 BEQ 10S ;BR IF CORRECT
1298 003742' 004767 JSR PC,GETERR ;LOAD ERROR INFORMATION FOR PRINTOUT
1299 003746' 012767 MOV #BADRED,ERRTYP ;CODE FOR UNABLE TO EXEC READ FUNCTION
1300 ;*****
1301 003754' 104405 000000' 000452' HDRS,BEGIN,REGTBL ;INCORRECT RCY EA BITS RETURNED
1302 ;*****
1303 003762' 000426 BR 11S ;
1304 003764' 016001 MOV SEL6(R0),R1 ;GET SEL6 CONTENTS
1305 003770' 032767 BIT #MODE22,SR4 ;* IF THIS IS A DMV IN Q22 MODE...
1306 003776' 001402 BEQ 29S ;* THEN GET CHARACTER COUNT
1307 004000' 016001 MOV SEL10(R0),R1 ;* BITS FROM A DIFFERENT CSR.
1308 004004' 042701 BIC #CCMSK,R1 ;MASK FOR CHAR COUNT BITS
1309 004010' 020127 CMP R1,#1024. ;CHK FOR CORRECT CHAR CNT RETURNED
1310 004014' 001411 BEQ 11S ;BR IF CORRECT
1311 004016' 004767 JSR PC,GETERR ;LOAD ERROR INFORMATION FOR PRINTOUT
1312 004022' 012767 MOV #BADRED,ERRTYP ;CODE FOR UNABLE TO EXEC READ FUNCTION
1313 ;*****
1314 004030' 104405 000000' 000452' HDRS,BEGIN,REGTBL ;INCORRECT RCY CHAR CNT BITS RETURNED
1315 ;*****
1316 004036' 000400 BR 11S ;
1317 004040' 052764 BIS #BURDON,INTABL(R4) ;SET BACCOR DONE IN STATUS WORD
1318 004046' 152760 BISB #RQI,BSEL0(R0) ;ENABLE INPUTS AGAIN

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1319 004054' 000466 BR 21S ;PROCEED
1320 ;HANDLE CTL OUT
1321 004056' 126027 000006 000010 12S: CMPB #SEL6(R0),#STARUN ;SEE IF START RECEIVED FROM MASTER
1322 ; WHILE RUNNING
1323 004064' 001004 BNE 13S ;BR IF NOT
1324 004066' 142760 BICB #RQI,BSEL0(R0) ;DISABLE INPUTS TO AWAIT OUTPUT
1325 004074' 000456 BR 21S ;PROCEED
1326 004076' 126027 000006 000024 13S: CMPB #SEL6(R0),#KUNST ;SEE IF PROTOCOL JUST ENTERED THE RUNNING
1327 ; STATE
1328 004104' 001004 BNE 14S ;BR IF NOT
1329 004106' 152760 BISB #RQI,BSEL0(R0) ;ENABLE INPUTS AGAIN
1330 004114' 000446 BR 21S ;PROCEED
1331 004116' 004767 JSR PC,GETERR ;LOAD ERROR INFORMATION FOR PRINTOUT
1332 004122' 012767 MOV #NUTDFN,ERRTYP ;CODE FOR ERROR NOT DEFINED (MUST INTERPRET
1333 ; CSR CONTENTS)
1334 ;*****
1335 004130' 104405 000000' 000452' HDRS,BEGIN,REGTBL ;UNEXPECTED CTL OUT CMND
1336 ;*****
1337 004136' 000435 BR 21S ;
1338 ;HANDLE INFO OUT
1339 004140' 116001 000006 15S: MOVB #SEL6(R0),R1 ;GET BSEL6 CODE RETURNED
1340 004144' 142701 BICB #RTNMSK,R1 ;MASK FOR RETURN CODE
1341 004150' 120127 000020 CMPB R1,#BUFRFC ;CHECK FOR BUFFER RETURN COMPLETE
1342 004154' 001016 BNE 19S ;BR IF NOT
1343 004156' 005267 INC OUTDUN ;INCR CNT OF LOG LNKS RESTARTED BY MASTER
1344 004162' 026767 CMP OUTDUN,LLKCNT ;SEE IF ALL LOG LNKS DONE YET
1345 004170' 002420 BLT 21S ;BR IF ALL NOT DONE
1346 004172' 105060 CLR# BSEL2(R0) ;CLEAR BSEL2 (INCLUDING HDY0)
1347 004176' 004767 JSR PC,DISABL ;DISABLE INTRPTS ON ALL DEVICES
1348 004202' 104413 ENDITS,BEGIN ;SIGNAL END OF ITERATION.
1349 ;MONITOR SHALL TEST END OF PASS
1350 004206' 000167 JMP KESTKI ;GO START NEW PASS
1351 004212' 004767 JSR PC,GETERR ;LOAD ERROR INFORMATION FOR PRINTOUT
1352 004216' 012767 MOV #NUTDFN,ERRTYP ;CODE FOR ERROR NOT DEFINED (MUST INTERPRET
1353 ; CSR CONTENTS)
1354 ;*****
1355 004224' 104405 000000' 000452' HDRS,BEGIN,REGTBL ;UNEXPECTED INFO OUT CMND
1356 ;*****
1357 004232' 105060 21S: CLR# BSEL2(R0) ;CLEAR BSEL2 (INCLUDING HDY0)
1358 004236' 000167 JMP CHKMUR ;GO SEE IF HDY1 OR HDY0 STILL SET,
1359 ; AND RETURN TO DEC/X11 MONITOR IF NOT
1360
1361
1362 ;*****
1363 ;* DMVLP - THIS SUBROUTINE ENTERS M-LOOP, SETS TTL LOOPBACK AND TIMER 1 FOR
1364 ;* 56K BPS, AND EXITS MLOOP. (DMV ONLY)
1365 ;*****
1366 004242' 112760 000301 000001 DMVLP: MOV# #J01,BSEL1(R0) ;ENTER MAINTENANCE LOOP
1367 004250' 15:
1368 004250' 104407 BREAKS,BEGIN ;TEMPORARY RETURN TO MONITOR....
1369 004254' 104407 BREAKS,BEGIN ;THEN CONTINUE AT NEXT INSTRUCTION.
1370 004260' 132760 BIT# #RDY,BSEL2(R0) ;MAKE SURE MLOOP COMPLETE
1371 004266' 001770 BEQ 1S
1372 004270' 012760 MOV #TTLOOP,BSEL2(R0) ;ISSUE TTLOOP CONFIGURE COMMAND
1373 004276' 104407 BREAKS,BEGIN ;TEMPORARY RETURN TO MONITOR....
1374 004302' 104407 BREAKS,BEGIN ;THEN CONTINUE AT NEXT INSTRUCTION.

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1375 004306' 000240      NUP
1376 004310' 000240      NUP
1377 004312' 000207      RTS      PC      ;RETURN
1378
1379
1380 ;*****
1381 ;* DMVRD - THIS SUBROUTINE FORCES THE DMV WHOSE ADRS IS PASSED IN RO ON ENTRY
1382 ;* TO ENTER THE M-LOOP AND READ THE INTERNAL DMV LOCATION PASSED IN THE
1383 ;* WORD FOLLOWING THE CALL.
1384 ;*****
1385 004314' 112760 000301 000001 DMVRD: MOVW  #J01,BSEL1(RO) ;ENTER MAINTENANCE LOOP
1386 004322'
1387 004322' 104407 000000' 1$: BREAKS,BEGIN ;TEMPORARY RETURN TO MONITOR....
1388 004326' 104407 000000' BREAKS,BEGIN ;THEN CONTINUE AT NEXT INSTRUCTION.
1389 004332' 132760 000200 000002 BITB  #MRDY,BSEL2(RO) ;MAKE SURE MLOOP COMPLETE
1390 004340' 001770 BEQ  1$
1391 004342' 017660 000000 000004 MOV  #(SP),SEL4(RO) ;PASS DMV INTERNAL ADDRESS
1392 004350' 012760 000001 000002 MOV  #MEDLOC,BSEL2(RO) ;ISSUE READ COMMAND
1393 004356'
1394 004356' 104407 000000' 2$: BREAKS,BEGIN ;TEMPORARY RETURN TO MONITOR....
1395 004362' 104407 000000' BREAKS,BEGIN ;THEN CONTINUE AT NEXT INSTRUCTION.
1396 004366' 132760 000200 000002 BITB  #MRDY,BSEL2(RO) ;MAKE SURE MLOOP READ IS COMPLETE
1397 004374' 001770 BEQ  2$
1398 004376' 062716 000002 ADD  #2,(SP) ;FIX UP RETURN PC
1399 004402' 000207 RTS      PC      ;RETURN
1400
1401
1402 ;*****
1403 ;* EXECUT - THIS SUBROUTINE FORCES THE MICROPROCESSOR WHOSE ADRS IS PASSED
1404 ;* IN RO ON ENTRY TO EXECUTE AN INSTRUCTION WHICH IS PASSED IN THE WORD
1405 ;* FOLLOWING THE CALL.
1406 ;*****
1407 004404' 152760 000006 000001 EXECUT: BISB  #R0M0,R0M1,BSEL1(RO) ;SET R0M0, R0M1 BITS IN BSEL1
1408 004412' 017660 000000 000006 MOV  #(SP),SEL6(RO) ;PUT INSTRUCTION INTO SEL6
1409 004420' 152760 000007 000001 BISB  #R0M0,R0M1,STEPMP,BSEL1(RO) ;SET R0M0, R0M1, STEPMP IN BSEL1
1410 004426' 142760 000007 000001 BICB  #R0M0,R0M1,STEPMP,BSEL1(RO) ;CLEAR R0M0, R0M1, STEPMP IN BSEL1
1411 004434' 062716 000002 ADD  #2,(SP) ;FIX UP RETURN PC
1412 004440' 000207 RTS      PC      ;RETURN
1413
1414
1415 ;*****
1416 ;* INIDMP - THIS SUBROUTINE ISSUES A MASTER CLEAR AND CLEARS THE CSH'S,
1417 ;* FOR THE DEVICE WHOSE ADDRESS IS PASSED IN RO ON ENTRY.
1418 ;*****
1419 INIDMP: MOV  R1,-(SP) ;SAVE R1
1420 004442' 010146 JSR  PC,CLRCSH ;CLEAR BSEL0, SEL2,4,6
1421 004444' 004767 000236 MOVW  #MCLR,BSEL1(RO) ;SET MASTER CLEAR BIT
1422 004450' 112760 000100 000001 TST  SR4 ; IF DMV...
1423 004456' 005767 173342 BNE  6$ ; THEN DON'T SET RUN
1424 004462' 001003 ;
1425
1426 ;-----
1427 004464' 000240 ;:; MOVW  #RUN,BSEL1(RO) ;PATCH TO SET RUN BIT IF USING M206
1428 004466' 000240 NUP ; 112760
1429 004470' 000240 NUP ; 200
1430 NUP ; 1

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1431 004472' 042767 000004 173620 6$: BIC  #BADINI,ERRORS ;CLEAR BAD INIT ERROR FLAG
1432 004500' 012701 020000 MOV  #2000,R1 ;INITIALIZE INIT TIMER
1433 004504' 132760 000200 000001 1$: BITB  #RUN,BSEL1(RO) ;SEE IF RUN IS SET AGAIN
1434 004512' 001020 BNE  3$ ;RM IF NOT SET
1435 004514' 005301 DEC  R1 ;DECR TIMER
1436 004516' 001405 BEQ  2$ ;BR IF INIT TIMED-OUT
1437 004520' 104407 000000' BREAKS,BEGIN ;TEMPORARY RETURN TO MONITOR....
1438 004524' 104407 000000' BREAKS,BEGIN ;THEN CONTINUE AT NEXT INSTRUCTION.
1439 004530' 000765 BR  1$ ;GO CHECK AGAIN
1440 004532' 004767 000432 2$: JSR  PC,GETERR ;LOAD ERROR INFORMATION FOR PRINTOUT
1441 004536' 012767 000034 173342 MOV  #NDINIT,ERRTYP ;CODE FOR DEVICE WILL NOT INIT
1442 ;*****
1443 004544' 104405 000000' 000452' HDRS,BEGIN,REGTBL ;TIMED-OUT WAITING FOR RUN TO SET
1444 ;*****
1445 004552' 000414 BR  4$
1446 004554' 126027 000006 000305 3$: CMPB  BSEL6(RO),#GOODIN ;SEE IF INIT COMPLETE CODE SET BY MICRO-CPU
1447 004562' 001413 BEQ  5$ ;BR IF YES
1448 004564' 004767 000400 JSR  PC,GETERR ;LOAD ERROR INFORMATION FOR PRINTOUT
1449 004570' 012767 000034 173310 MOV  #NDINIT,ERRTYP ;CODE FOR DEVICE WILL NOT INIT
1450 ;*****
1451 004576' 104405 000000' 000452' HDRS,BEGIN,REGTBL ;INIT COMPLETE CODE NOT SET BY MICRO-CPU
1452 ;*****
1453 004604' 052767 000004 173506 4$: BIS  #BADINI,ERRORS ;SET BAD INIT ERROR FLAG
1454 004612' 004767 000070 5$: JSR  PC,CLRCSR ;CLEAR BSEL0, SEL2,4,6
1455 004616' 012601 MOV  (SP)+,R1 ;RESTURE R1
1456 004620' 000207 RTS      PC      ;RETURN
1457
1458
1459 ;*****
1460 ;* READ16 - THIS SUBROUTINE FORCES THE MICROPROCESSOR WHOSE ADDRESS IS
1461 ;* PASSED IN RO ON ENTRY, TO EXECUTE AN INSTRUCTION WHICH READS DMP LINE
1462 ;* UNIT #BUS REG 16 INTO BSEL4, AND THEN IT PLACES THE CONTENTS INTO LOC.
1463 ;* LURG16 (IF DMV: EXECUTE MLOOP, "SWPBOT" => LURG16, AND ADJUST).
1464 ;*****
1465 READ16: TST  SR4 ;IS THIS A DMV ?
1466 004622' 005767 173176 BNE  1$ ;BRANCH IF YES
1467 004626' 001007 JSR  PC,EXECUT ;DMP: EXECUTE MOVE INSTRUCTION
1468 ;WORD RDLU16
1469 004630' 004767 177550 MOVW  BSEL4(RO),LURG16 ;GET REG 16 CONTENTS INTO LURG16
1470 004634' 021344 BR  2$
1471 004636' 116067 000004 173424 JSR  PC,GETERR ;LOAD ERROR INFORMATION FOR PRINTOUT
1472 004644' 000415 MOV  #NDINIT,ERRTYP ;CODE FOR DEVICE WILL NOT INIT
1473 ;*****
1474 004646' 004767 177442 1$: JSR  PC,DMVRD ;DMV: READ "SWPBOT" SWITCHES
1475 004652' 121000 ;WORD SWPBOT
1476 004654' 116067 000006 173406 MOVW  BSEL6(RO),LURG16 ;PUT IT IN LURG16
1477 004662' 000241 CLC ;ADJUST "SWPBOT" BITS
1478 004664' 106267 173400 ASRB  LURG16 ; TO LOOK LIKE THE DMP
1479 004670' 103003 BCC  2$ ; REG 16 SWITCHES
1480 004672' 152767 000200 173370 BISB  #ENABSW,LURG16
1481
1482 004700' 105067 173365 2$: CLRB  LURG16+1 ;CLEAR HI BYTE
1483 004704' 000207 RTS      PC      ;RETURN
1484
1485
1486

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1487 ;*****
1488 ;* CLRCR - THIS SUBROUTINE CLEARS BSEL0, SEL2,4,6 (AND SEL10 IF DMV1), FOR
1489 ;* THE DEVICE WHOSE ADRS IS PASSED IN RO ON ENTRY
1490 ;*****
1491 004706' 105060 000000 CLRCR: CLR BSEL0(RO) ;CLEAR BSEL0
1492 004712' 005060 000002 CLR SEL2(RO) ;CLEAR SEL2
1493 004716' 005060 000004 CLR SEL4(RO) ;CLEAR SEL4
1494 004722' 005060 000006 CLR SEL6(RO) ;CLEAR SEL6
1495 004726' 005767 173072 TST SR4
1496 004732' 001402 BEQ 15 ;
1497 004734' 005060 000010 CLR SEL10(RO) ;IF DMV: CLEAR SEL10
1498 004740' 000207 15: RTS PC ;RETURN
1499
1500
1501
1502 ;*****
1503 ;* INCMND - THIS SUBROUTINE ISSUES AN INPUT COMMAND TO THE
1504 ;* DEVICE WHOSE ADDRESS IS PASSED IN RO, BY ENABLING INTERRUPTS
1505 ;* AND SETTING RWI. THEN, IT SETS NONQI IN FLAGS, FOR NON-QUEUED
1506 ;* (REQUEST-AND-WAIT) INPUT INTERRUPT OPERATION.
1507 ;* IF THE PROGRAM TIMES-OUT WAITING FOR COMMAND COMPLETION,
1508 ;* THE ERROR IS REPORTED, AND A RETURN IS MADE WITH THE INPUT INTERRUPT
1509 ;* TIME-OUT FLAG IN "ERRORS" SET TO 1.
1510 ;*****
1511 004742' 042767 000001 173350 INCMND: BIC #INTIM,ERRORS ;CLR INPUT INTRPT TIME-OUT FLAG
1512 004750' 052767 000004 173340 BIS #NONQI,FLAGS ;SET BIT FOR NON-QUEUED INPUT INTRPT OPERATION
1513 004756' 042767 000001 173332 BIC #ININT,FLAGS ;CLR INPUT INTRPT FLAG
1514 004764' 005067 173314 CLR RSEL0 ;CLEAR OUTPUT CMD RETURN AREA
1515 004770' 005067 173312 CLR RSEL2
1516 004774' 005067 173310 CLR RSEL4
1517 005000' 005067 173306 CLR RSEL6
1518 005004' 012767 030000 173310 MOV #30000,INTIM ;INIT INPUT INTRPT TIMER
1519 005012' 112760 000221 000000 MOVB #RWI:IEI:IEO,BSEL0(RO) ;REQUEST INPUT, ENABLE INTERRUPTS ON THIS DEVICE
1520 005020'
1521 005020' 104407 000000' BREAKS,BEGIN ;TEMPORARY RETURN TO MONITOR...
1522 005024' 104407 000000' BREAKS,BEGIN ;THEN CONTINUE AT NEXT INSTRUCTION.
1523 005030' 132767 000001 173260 BITH #ININT,FLAGS ;SEE IF INPUT INTRPT SERVICED YET
1524 005036' 001016 BNE 35 ;BR IF YES
1525 005040' 005367 173256 DEC INTIM ;DECREMENT TIMER
1526 005044' 001365 BNE 15 ;BR IF INPUT INTERRUPT DIDN'T TIME-OUT YET
1527 005046' 004767 000116 JSR PC,GETERR ;LOAD ERROR INFORMATION FOR PRINTOUT
1528 005052' 012767 000023 173026 MOV #NOINTR,ERRMTP ;CODE FOR DEV FAILED TO INTRPT
1529 ;*****
1530 005060' 104405 000000' 000452' HRDERS,BEGIN,REGTBL ;INPUT INTERRUPT TIMED-OUT
1531 ;*****
1532 005066' 052767 000001 173224 BIS #INTIM,ERRORS ;SET INPUT INTERRUPT TIME-OUT FLAG
1533 005074' 042767 000004 173214 BIC #NONQI,FLAGS ;CLEAR NON-QUEUED BIT
1534 005102' 000207 15: RTS PC ;RETURN
1535
1536
1537
1538
1539
1540 ;*****
1541 ;* CLRCMD - THIS SUBROUTINE CLEARS THE COMMAND AREA : CSEL2,CSEL4,CSEL6,CSEL10
1542 ;*****

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1543 005104' 005067 173164 CLRCMD: CLR CSEL2
1544 005110' 005067 173162 CLR CSEL4
1545 005114' 005067 173160 CLR CSEL6
1546 005120' 005067 173156 CLR CSEL10
1547 005124' 000207 15: RTS PC ;RETURN
1548
1549
1550
1551
1552 ;*****
1553 ;* DISABL - DISABLE INTERRUPTS ON ALL DEVICES
1554 ;*****
1555
1556 005126' 016700 172654 DISABL: MOV ADDR,RO ;INIT DEVICE ADDRESS
1557 005132' 012767 000001 173126 MOV #010,DEVPTB ;INIT SELECTION POINTER
1558 005140' 036767 173122 173116 BIT DEVPTB,SELECT ;SEE IF THIS DEVICE IS SELECTED
1559 005146' 001402 BNE 25 ;BR IF NOT SELECTED
1560 005150' 105060 000000 CLR BSEL0(RO) ;CLEAR RWI, IEI, IEO
1561 005154' 062700 000010 ADD #10,RU ;INCR DEVICE ADDRESS
1562 005160' 006367 173102 ASL DEVPTR ;SHIFT SELECTION POINTER
1563 005164' 001365 BNE 15 ;BR IF MORE TO SCAN
1564 005166' 000207 15: RTS PC ;RETURN
1565
1566
1567
1568
1569
1570 ;*****
1571 ;* GETERR - THIS SUBROUTINE LOADS THE CSR ADRS, SEL0 AND SEL2 CONTENTS INTO
1572 ;* CSRA, ACSR, AND ASTAT FOR ERRMTP PRINTOUT. THE DEVICE ADRS IS ASSUMED
1573 ;* TO BE IN RO ON ENTRY. THEN, IT LOADS THE CSR ADDRESSES INTO REGTBL.
1574 ;*****
1575 005170' 010067 172704 GETERR: MOV RU,CSRA ;LOAD CSR ADDRESS
1576 005174' 016067 000000 172700 MOV SEL0(RO),ACSR ;LOAD SEL0 CONTENTS
1577 005202' 016067 000002 172674 MOV SEL2(RO),ASTAT ;LOAD SEL2 CONTENTS
1578 005210' 010067 173236 MOV RU,REGTBL ;GET CSR ADDRESSES FOR DEVICE INTO REGTBL
1579 005214' 010067 173234 MOV RU,REGTBL+2
1580 005220' 062767 000002 173226 ADD #2,REGTBL+2
1581 005226' 010067 173224 MOV RU,REGTBL+4
1582 005232' 062767 000004 173216 ADD #4,REGTBL+4
1583 005240' 010067 173214 MOV RU,REGTBL+6
1584 005244' 062767 000006 173206 ADD #6,REGTBL+6
1585 005252' 005767 172546 TST SR4 ; IS THIS A DMV ?
1586 005256' 001004 BNE 15 ;
1587 005260' 012767 177777 173174 MOV #177777,REGTBL+10 ;NO: DISABLE SEL10
1588 005266' 000405 BR 25 ; AND GOTO RETURN
1589 005270' 010067 173166 15: MOV RU,REGTBL+10 ;YES: ENABLE SEL10
1590 005274' 062767 000010 173160 ADD #10,REGTBL+10
1591 005302' 000207 25: RTS PC ;RETURN
1592
1593
1594
1595
1596
1597
1598 ;*****

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1599
1600 ;* SERVICE CODE FOR LINKING A PARTICULAR DEVICE TO
1601 ;* A COMMON INPUT OR OUTPUT INTERRUPT SERVICE ROUTINE.
1602 ;*****
1603
1604 005304' IISR0:
1605 ;-----
1606 005304' 000004 000000' 005312' PIRQS,BEGIN,1$ ; QUEUE UP TO CONTINUE AT 1$ AND RTI
1607 ;-----
1608 005312' 016700 000022 1$: MOV ADDR0,R0 ;PUT DEVICE 0 CSR ADDRESS IN R0
1609 005316' 000167 175136 JMP INISR ;GO SERVICE INPUT INTERRUPT ON DEVICE 0
1610
1611 005322' OISR0:
1612 ;-----
1613 005322' 000004 000000' 005330' PIRQS,BEGIN,1$ ; QUEUE UP TO CONTINUE AT 1$ AND RTI
1614 ;-----
1615 005330' 016700 000004 1$: MOV ADDR0,R0 ;PUT DEVICE 0 CSR ADDRESS IN R0
1616 005334' 000167 175666 JMP OUTISR ;GO SERVICE OUTPUT INTERRUPT ON DEVICE 0
1617
1618 005340' 000000 ADDR0: .WORD 0 ;DEVICE CSR ADDRESS STORED HERE
1619
1620
1621
1622 005342' IISR1:
1623 ;-----
1624 005342' 000004 000000' 005350' PIRQS,BEGIN,1$ ; QUEUE UP TO CONTINUE AT 1$ AND RTI
1625 ;-----
1626 005350' 016700 000022 1$: MOV ADDR1,R0 ;PUT DEVICE 1 CSR ADDRESS IN R0
1627 005354' 000167 175100 JMP INISR ;GO SERVICE INPUT INTERRUPT ON DEVICE 1
1628
1629 005360' OISR1:
1630 ;-----
1631 005360' 000004 000000' 005366' PIRQS,BEGIN,1$ ; QUEUE UP TO CONTINUE AT 1$ AND RTI
1632 ;-----
1633 005366' 016700 000004 1$: MOV ADDR1,R0 ;PUT DEVICE 1 CSR ADDRESS IN R0
1634 005372' 000167 175630 JMP OUTISR ;GO SERVICE OUTPUT INTERRUPT ON DEVICE 1
1635
1636 005376' 000000 ADDR1: .WORD 0 ;DEVICE CSR ADDRESS STORED HERE
1637
1638
1639
1640 005400' IISR2:
1641 ;-----
1642 005400' 000004 000000' 005406' PIRQS,BEGIN,1$ ; QUEUE UP TO CONTINUE AT 1$ AND RTI
1643 ;-----
1644 005406' 016700 000022 1$: MOV ADDR2,R0 ;PUT DEVICE 2 CSR ADDRESS IN R0
1645 005412' 000167 175042 JMP INISR ;GO SERVICE INPUT INTERRUPT ON DEVICE 2
1646
1647 005416' OISR2:
1648 ;-----
1649 005416' 000004 000000' 005424' PIRQS,BEGIN,1$ ; QUEUE UP TO CONTINUE AT 1$ AND RTI
1650 ;-----
1651 005424' 016700 000004 1$: MOV ADDR2,R0 ;PUT DEVICE 2 CSR ADDRESS IN R0
1652 005430' 000167 175572 JMP OUTISR ;GO SERVICE OUTPUT INTERRUPT ON DEVICE 2
1653
1654 005434' 000000 ADDR2: .WORD 0 ;DEVICE CSR ADDRESS STORED HERE

```

```

1655
1656
1657
1658 005436' IISR3:
1659 ;-----
1660 005436' 000004 000000' 005444' PIRQS,BEGIN,1$ ; QUEUE UP TO CONTINUE AT 1$ AND RTI
1661 ;-----
1662 005444' 016700 000022 1$: MOV ADDR3,R0 ;PUT DEVICE 3 CSR ADDRESS IN R0
1663 005450' 000167 175004 JMP INISR ;GO SERVICE INPUT INTERRUPT ON DEVICE 3
1664
1665 005454' OISR3:
1666 ;-----
1667 005454' 000004 000000' 005462' PIRQS,BEGIN,1$ ; QUEUE UP TO CONTINUE AT 1$ AND RTI
1668 ;-----
1669 005462' 016700 000004 1$: MOV ADDR3,R0 ;PUT DEVICE 3 CSR ADDRESS IN R0
1670 005466' 000167 175534 JMP OUTISR ;GO SERVICE OUTPUT INTERRUPT ON DEVICE 3
1671
1672 005472' 000000 ADDR3: .WORD 0 ;DEVICE CSR ADDRESS STORED HERE
1673
1674
1675
1676 005474' IISR4:
1677 ;-----
1678 005474' 000004 000000' 005502' PIRQS,BEGIN,1$ ; QUEUE UP TO CONTINUE AT 1$ AND RTI
1679 ;-----
1680 005502' 016700 000022 1$: MOV ADDR4,R0 ;PUT DEVICE 4 CSR ADDRESS IN R0
1681 005506' 000167 174746 JMP INISR ;GO SERVICE INPUT INTERRUPT ON DEVICE 4
1682
1683 005512' OISR4:
1684 ;-----
1685 005512' 000004 000000' 005520' PIRQS,BEGIN,1$ ; QUEUE UP TO CONTINUE AT 1$ AND RTI
1686 ;-----
1687 005520' 016700 000004 1$: MOV ADDR4,R0 ;PUT DEVICE 4 CSR ADDRESS IN R0
1688 005524' 000167 175476 JMP OUTISR ;GO SERVICE OUTPUT INTERRUPT ON DEVICE 4
1689
1690 005530' 000000 ADDR4: .WORD 0 ;DEVICE CSR ADDRESS STORED HERE
1691
1692
1693
1694 005532' IISR5:
1695 ;-----
1696 005532' 000004 000000' 005540' PIRQS,BEGIN,1$ ; QUEUE UP TO CONTINUE AT 1$ AND RTI
1697 ;-----
1698 005540' 016700 000022 1$: MOV ADDR5,R0 ;PUT DEVICE 5 CSR ADDRESS IN R0
1699 005544' 000167 174710 JMP INISR ;GO SERVICE INPUT INTERRUPT ON DEVICE 5
1700
1701 005550' OISR5:
1702 ;-----
1703 005550' 000004 000000' 005556' PIRQS,BEGIN,1$ ; QUEUE UP TO CONTINUE AT 1$ AND RTI
1704 ;-----
1705 005556' 016700 000004 1$: MOV ADDR5,R0 ;PUT DEVICE 5 CSR ADDRESS IN R0
1706 005562' 000167 175440 JMP OUTISR ;GO SERVICE OUTPUT INTERRUPT ON DEVICE 5
1707
1708 005566' 000000 ADDR5: .WORD 0 ;DEVICE CSR ADDRESS STORED HERE
1709
1710

```

```

1711
1712 005570' IISR6:
1713 ;-----
1714 005570' 000004 000000' 005576' PIRQS,BEGIN,1$ ; QUEUE UP TO CONTINUE AT 1$ AND RTI
1715 ;-----
1716 005576' 016700 000022 1$: MOV ADDR6,R0 ;PUT DEVICE 6 CSR ADDRESS IN R0
1717 005602' 000167 174652 JMP INISR ;GO SERVICE INPUT INTERRUPT ON DEVICE 6
1718
1719 005606' OISR6:
1720 ;-----
1721 005606' 000004 000000' 005614' PIRQS,BEGIN,1$ ; QUEUE UP TO CONTINUE AT 1$ AND RTI
1722 ;-----
1723 005614' 016700 000004 1$: MOV ADDR6,R0 ;PUT DEVICE 6 CSR ADDRESS IN R0
1724 005620' 000167 175402 JMP OUTISR ;GO SERVICE OUTPUT INTERRUPT ON DEVICE 6
1725
1726 005624' 000000 ADDR6: .WORD 0 ;DEVICE CSR ADDRESS STORED HERE
1727
1728
1729
1730 005626' IISR7:
1731 ;-----
1732 005626' 000004 000000' 005634' PIRQS,BEGIN,1$ ; QUEUE UP TO CONTINUE AT 1$ AND RTI
1733 ;-----
1734 005634' 016700 000022 1$: MOV ADDR7,R0 ;PUT DEVICE 7 CSR ADDRESS IN R0
1735 005640' 000167 174614 JMP INISR ;GO SERVICE INPUT INTERRUPT ON DEVICE 7
1736
1737 005644' OISR7:
1738 ;-----
1739 005644' 000004 000000' 005652' PIRQS,BEGIN,1$ ; QUEUE UP TO CONTINUE AT 1$ AND RTI
1740 ;-----
1741 005652' 016700 000004 1$: MOV ADDR7,R0 ;PUT DEVICE 7 CSR ADDRESS IN R0
1742 005656' 000167 175344 JMP OUTISR ;GO SERVICE OUTPUT INTERRUPT ON DEVICE 7
1743
1744 005662' 000000 ADDR7: .WORD 0 ;DEVICE CSR ADDRESS STORED HERE
1745
1746
1747
1748 005664' IISR10:
1749 ;-----
1750 005664' 000004 000000' 005672' PIRQS,BEGIN,1$ ; QUEUE UP TO CONTINUE AT 1$ AND RTI
1751 ;-----
1752 005672' 016700 000022 1$: MOV ADDR10,R0 ;PUT DEVICE 10 CSR ADDRESS IN R0
1753 005676' 000167 174556 JMP INISR ;GO SERVICE INPUT INTERRUPT ON DEVICE 10
1754
1755 005702' OISR10:
1756 ;-----
1757 005702' 000004 000000' 005710' PIRQS,BEGIN,1$ ; QUEUE UP TO CONTINUE AT 1$ AND RTI
1758 ;-----
1759 005710' 016700 000004 1$: MOV ADDR10,R0 ;PUT DEVICE 10 CSR ADDRESS IN R0
1760 005714' 000167 175306 JMP OUTISR ;GO SERVICE OUTPUT INTERRUPT ON DEVICE 10
1761
1762 005720' 000000 ADDR10: .WORD 0 ;DEVICE CSR ADDRESS STORED HERE
1763
1764
1765
1766 005722' IISR11:

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```

1767
1768 005722' 000004 000000' 005730' PIRQS,BEGIN,1$ ; QUEUE UP TO CONTINUE AT 1$ AND RTI
1769 ;-----
1770 005730' 016700 000022 1$: MOV ADDR11,R0 ;PUT DEVICE 11 CSR ADDRESS IN R0
1771 005734' 000167 174520 JMP INISR ;GO SERVICE INPUT INTERRUPT ON DEVICE 11
1772
1773 005740' OISR11:
1774 ;-----
1775 005740' 000004 000000' 005746' PIRQS,BEGIN,1$ ; QUEUE UP TO CONTINUE AT 1$ AND RTI
1776 ;-----
1777 005746' 016700 000004 1$: MOV ADDR11,R0 ;PUT DEVICE 11 CSR ADDRESS IN R0
1778 005752' 000167 175250 JMP OUTISR ;GO SERVICE OUTPUT INTERRUPT ON DEVICE 11
1779
1780 005756' 000000 ADDR11: .WORD 0 ;DEVICE CSR ADDRESS STORED HERE
1781
1782
1783
1784 005760' IISR12:
1785 ;-----
1786 005760' 000004 000000' 005766' PIRQS,BEGIN,1$ ; QUEUE UP TO CONTINUE AT 1$ AND RTI
1787 ;-----
1788 005766' 016700 000022 1$: MOV ADDR12,R0 ;PUT DEVICE 12 CSR ADDRESS IN R0
1789 005772' 000167 174462 JMP INISR ;GO SERVICE INPUT INTERRUPT ON DEVICE 12
1790
1791 005776' OISR12:
1792 ;-----
1793 005776' 000004 000000' 006004' PIRQS,BEGIN,1$ ; QUEUE UP TO CONTINUE AT 1$ AND RTI
1794 ;-----
1795 006004' 016700 000004 1$: MOV ADDR12,R0 ;PUT DEVICE 12 CSR ADDRESS IN R0
1796 006010' 000167 175212 JMP OUTISR ;GO SERVICE OUTPUT INTERRUPT ON DEVICE 12
1797
1798 006014' 000000 ADDR12: .WORD 0 ;DEVICE CSR ADDRESS STORED HERE
1799
1800
1801
1802 006016' IISR13:
1803 ;-----
1804 006016' 000004 000000' 006024' PIRQS,BEGIN,1$ ; QUEUE UP TO CONTINUE AT 1$ AND RTI
1805 ;-----
1806 006024' 016700 000022 1$: MOV ADDR13,R0 ;PUT DEVICE 13 CSR ADDRESS IN R0
1807 006030' 000167 174424 JMP INISR ;GO SERVICE INPUT INTERRUPT ON DEVICE 13
1808
1809 006034' OISR13:
1810 ;-----
1811 006034' 000004 000000' 006042' PIRQS,BEGIN,1$ ; QUEUE UP TO CONTINUE AT 1$ AND RTI
1812 ;-----
1813 006042' 016700 000004 1$: MOV ADDR13,R0 ;PUT DEVICE 13 CSR ADDRESS IN R0
1814 006046' 000167 175154 JMP OUTISR ;GO SERVICE OUTPUT INTERRUPT ON DEVICE 13
1815
1816 006052' 000000 ADDR13: .WORD 0 ;DEVICE CSR ADDRESS STORED HERE
1817
1818
1819
1820 006054' IISR14:
1821 ;-----
1822 006054' 000004 000000' 006062' PIRQS,BEGIN,1$ ; QUEUE UP TO CONTINUE AT 1$ AND RTI

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```

1823 ;
1824 006062' 016700 000022 1S: MOV ADDR14,R0 ;PUT DEVICE 14 CSR ADDRESS IN R0
1825 006066' 000167 174366 JMP INISR ;GO SERVICE INPUT INTERRUPT ON DEVICE 14
1826
1827 006072' OISR14:
1828 ;
1829 006072' 000004 000000' 006100' PIRQS,BEGIN,1$ ; QUEUE UP TO CONTINUE AT 1$ AND RTI
1830 ;
1831 006100' 016700 000004 1S: MOV ADDR14,R0 ;PUT DEVICE 14 CSR ADDRESS IN R0
1832 006104' 000167 175116 JMP OUTISR ;GO SERVICE OUTPUT INTERRUPT ON DEVICE 14
1833
1834 006110' 000000 ADDR14: .WORD 0 ;DEVICE CSR ADDRESS STORED HERE
1835
1836
1837
1838 006112' IISR15:
1839 ;
1840 006112' 000004 000000' 006120' PIRQS,BEGIN,1$ ; QUEUE UP TO CONTINUE AT 1$ AND RTI
1841 ;
1842 006120' 016700 000022 1S: MOV ADDR15,R0 ;PUT DEVICE 15 CSR ADDRESS IN R0
1843 006124' 000167 174330 JMP INISR ;GO SERVICE INPUT INTERRUPT ON DEVICE 15
1844
1845 006130' OISR15:
1846 ;
1847 006130' 000004 000000' 006136' PIRQS,BEGIN,1$ ; QUEUE UP TO CONTINUE AT 1$ AND RTI
1848 ;
1849 006136' 016700 000004 1S: MOV ADDR15,R0 ;PUT DEVICE 15 CSR ADDRESS IN R0
1850 006142' 000167 175060 JMP OUTISR ;GO SERVICE OUTPUT INTERRUPT ON DEVICE 15
1851
1852 006146' 000000 ADDR15: .WORD 0 ;DEVICE CSR ADDRESS STORED HERE
1853
1854
1855
1856 006150' IISR16:
1857 ;
1858 006150' 000004 000000' 006156' PIRQS,BEGIN,1$ ; QUEUE UP TO CONTINUE AT 1$ AND RTI
1859 ;
1860 006156' 016700 000022 1S: MOV ADDR16,R0 ;PUT DEVICE 16 CSR ADDRESS IN R0
1861 006162' 000167 174272 JMP INISR ;GO SERVICE INPUT INTERRUPT ON DEVICE 16
1862
1863 006166' OISR16:
1864 ;
1865 006166' 000004 000000' 006174' PIRQS,BEGIN,1$ ; QUEUE UP TO CONTINUE AT 1$ AND RTI
1866 ;
1867 006174' 016700 000004 1S: MOV ADDR16,R0 ;PUT DEVICE 16 CSR ADDRESS IN R0
1868 006200' 000167 175022 JMP OUTISR ;GO SERVICE OUTPUT INTERRUPT ON DEVICE 16
1869
1870 006204' 000000 ADDR16: .WORD 0 ;DEVICE CSR ADDRESS STORED HERE
1871
1872
1873
1874 006206' IISR17:
1875 ;
1876 006206' 000004 000000' 006214' PIRQS,BEGIN,1$ ; QUEUE UP TO CONTINUE AT 1$ AND RTI
1877 ;
1878 006214' 016700 000022 1S: MOV ADDR17,R0 ;PUT DEVICE 17 CSR ADDRESS IN R0

```

```

1879 006220' 000167 174234 JMP INISR ;GO SERVICE INPUT INTERRUPT ON DEVICE 17
1880
1881 006224' OISR17:
1882 ;
1883 006224' 000004 000000' 006232' PIRQS,BEGIN,1$ ; QUEUE UP TO CONTINUE AT 1$ AND RTI
1884 ;
1885 006232' 016700 000004 1S: MOV ADDR17,R0 ;PUT DEVICE 17 CSR ADDRESS IN R0
1886 006236' 000167 174764 JMP OUTISR ;GO SERVICE OUTPUT INTERRUPT ON DEVICE 17
1887
1888 006242' 000000 ADDR17: .WORD 0 ;DEVICE CSR ADDRESS STORED HERE
1889
1890
1891

```

```

1892
1893
1894
1895
1896
1897 006244' 002000          BUFIN: .BLKB 1024. ;INPUT BUFFER (1024 BYTES)
1898
1899
1900
1901
1902
1903
1904 010244' 000036          ;***** PATCH AREA FOR DEBUG *****
1905                          PATCH: .BLKN JO.
1906                          ;*****
1907
1908
1909
1910
1911 000001                  .END
    
```

```

ABORT = 000004          582#
ACSR  000102R          278# 1576#
ADDR  000006R          244#   812   900  1078  1117  1218  1556
ADDR0 005340R          1608  1615  1618#
ADDR1 005376R          1626  1633  1636#
ADDR10 005720R          1752  1759  1762#
ADDR11 005756R          1770  1777  1780#
ADDR12 006014R          1788  1795  1798#
ADDR13 006052R          1806  1813  1816#
ADDR14 006110R          1824  1831  1834#
ADDR15 006146R          1842  1849  1852#
ADDR16 006204R          1860  1867  1870#
ADDR17 006242R          1878  1885  1888#
ADDR2  005434R          1644  1651  1654#
ADDR22= 001000          302#
ADDR3  005472R          1662  1669  1672#
ADDR4  005530R          1680  1687  1690#
ADDR5  005566R          1698  1705  1708#
ADDR6  005624R          1716  1723  1726#
ADDR7  005662R          1734  1741  1744#
ASB   000106R          282#
ASTAT 000104R          280# 1577#
AWAS  000110R          283#
AX1   = 000001          613#   673#
AX2   = 000002          612#   672#
BABTRB= 000026          543#
BACCIR= 000000          383#  1153
BACCIT= 000004          382#  1166
BACCOR= 000000          392#  1227
BACCOT= 000004          391#  1225
BACORU= 000003          393#  1229
BACOTN= 000007          395#
BACOTS= 000006          394#
BADINI= 000004          724#   838  1431  1453
BADMED= 000036          353#  1286  1299  1312
BADWRT= 000037          354#  1249  1262  1275
BA0   = 000001          505#
BA1   = 000002          504#
BA10  = 002000          495#
BA11  = 004000          494#
BA12  = 010000          493#
BA13  = 020000          492#
BA14  = 040000          491#
BA15  = 100000          490#
BA16  = 040000          513#
BA17  = 100000          512#
BA2   = 000004          503#
BA3   = 000010          502#
BA4   = 000020          501#
BA5   = 000040          499#
BA6   = 000100          498#
BA7   = 000200          497#
BA8   = 000400          496#
BA9   = 001000          495#
BCC   = 000001          649#
BEGIN = 000000R          241#   797   862   863   882   943   944   952   962   978   987   996   997
    
```


CXDMEB.P11 25-MAR-81 08:25

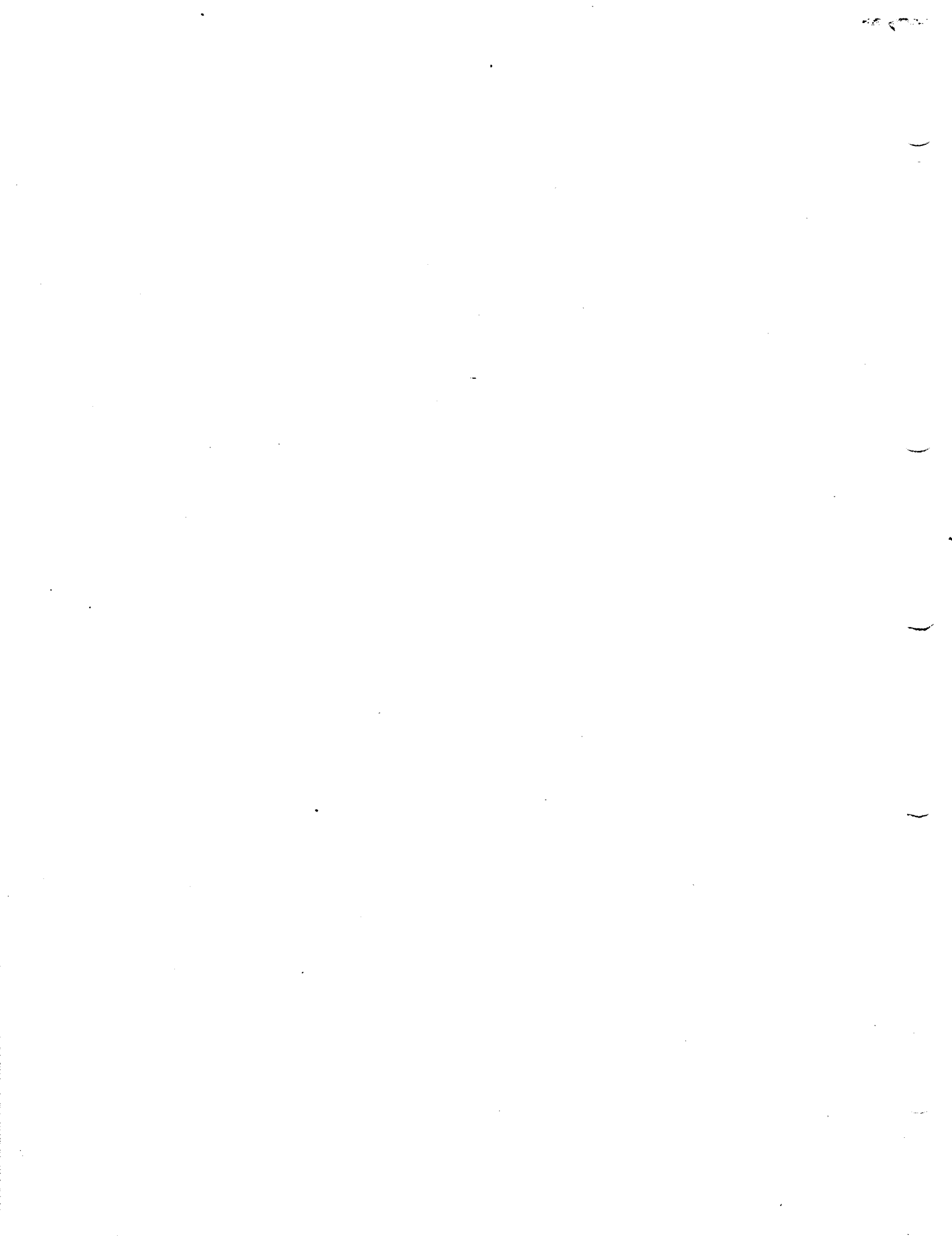
CROSS REFERENCE TABLE -- MACRO NAMES

BKMOD	1#																	
BREAK	1#	862	942	995	1367	1373	1386	1393	1437	1520								
BTD	1#																	
CKDATA	1#																	
DATACK	1#																	
DATERR	1#																	
DFSEVN	1#	302																
DSEVMT	1#	302																
END	1#																	
ENDIT	1#	1348																
ENDMOD	1#	797																
EQUATS	1#	302																
EXIT	1#	1090																
GETPA	1#	1067																
G#BUFF	1#																	
HRDER	2#	881	951	961	977	986	1004	1019	1133	1140	1240	1250	1263	1276	1287			
	1300	1313	1334	1354	1442	1450	1529											
IUMUD	1#																	
IOUDDP	1#																	
IOUDDK	1#																	
IOUDDX	1#	237																
ISRS	1603#	1604	1622	1640	1658	1676	1694	1712	1730	1748	1766	1784	1802	1820	1838			
	1856	1874																
MAP22	1#																	
MODULE	1#	237																
MSC	1#																	
MSCN	1#																	
MSCS	1#																	
NBKMOD	1#																	
OTDA	1#																	
PIRQ	1#	1604	1611	1622	1629	1640	1647	1658	1665	1676	1683	1694	1701	1712	1719			
	1730	1737	1748	1755	1766	1773	1784	1791	1802	1809	1820	1827	1838	1845	1856			
	1863	1874	1881															
RAND	1#																	
SBKMOD	1#																	
SOPER	1#																	

. ABS. 000000 000
010340 001

ERRORS DETECTED: 0
DEFAULT GLOBALS GENERATED: 0

XDMEB0,CXDMEB,SEQ/CHF/SOL/NL:TUC=DDXCOM,P11,CXDMEB,P11
RUN-TIME: 2 4 .8 SECONDS
RUN-TIME RATIO: 57/8=6.9
CORE USED: 19K (19 PAGES)



.NLIST
.NLIST SEQ,LD,BIN
.LIST
.REM *

SEQ 0001

IDENTIFICATION

PRODUCT CODE: AC-F087D-MC
PRODUCT NAME: CXRPDD0 RH70/RP04,5,6 MODULE
PRODUCT DATE: JANUARY 1980
MAINTAINER: DEC/X11 SUPPORT GROUP

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1. ABSTRACT

RPD IS AN IOMODX WHICH WILL EXERSIZE UP TO 8 RP04/5/6 DISK DRIVES ON AN RH11/RH70 CONTROLLER, OR DUAL-PORTED BETWEEN TWO SUCH CONTROLLERS. IN SINGLE-PORT MODE, THE ENTIRE SURFACE OF EACH DISK WILL BE TESTABLE (RANDOMLY OR SYSTEMATICALLY).

IN DUAL-PORT MODE, THE "A" CONTROLLER MODULE WILL ACCESS ONLY THE LOW HALF OF THE DISK, AND THE "B" CONTROLLER MODULE WILL ACCESS ONLY THE HIGH HALF OF THE DISK; NO COMMUNICATION WILL BE ATTEMPTED BETWEEN THE TWO PORTS. ONE COPY OF THIS MODULE MUST BE CONFIGURED FOR EACH PORT.

SINGLE/DUAL PORT MODE WILL BE AUTOMATICALLY SELECTED ON THE BASIS OF THE RPDT REGISTER OF EACH SELECTED DRIVE.

DURING TESTING, ANY BAD SECTOR FOUND WILL BE STORED AUTOMATICALLY AND WITHOUT OPERATOR INTERVENTION INTO THE MODULE'S BADSPOT TABLE. A MESSAGE CAN BE PRINTED TO NOTIFY THE OPERATOR OF THE BADSPOT ADDRESS (SEE OPERATOR OPTIONS). SECTORS LISTED IN THE TABLE WILL NOT BE TESTED ON ANY DRIVE.

THE WBUFREQ WORD IN THE HEADER MAY BE CHANGED AT WILL TO ALTER THE MODULE'S TRANSFER SIZE (AND OF COURSE A AND B-PORT SIZES NEED NOT MATCH): THIS WILL NOT ALTER THE EFFECTIVENESS OF THE BADSPOT TABLE OR CAUSE THE MODULE TO ATTEMPT ACCESS OF OUT-OF-RANGE DISK ADDRESSES. WITHIN THIS FRAMEWORK, THE MODULES WILL ACCESS AS MUCH OF THE DISK AS POSSIBLE. RANDOM SEEKS WILL BE USED UNLESS THIS OPTION IS DESELECTED IN SR1.

2. REQUIREMENTS

HARDWARE: 1 TO 8 RP04/5/6 DRIVES ON 1 OR 2 RH11/RH70 CONTROLLERS

STORAGE:: RPD REQUIRES:

DECIMAL WORDS:	1806
OCTAL WORDS:	3416
OCTAL BYTES:	7034

3. PASS DEFINITION

- ONE PASS CONSISTS OF 300 ITERATIONS.
- AN ITERATION CONSISTS OF THE FOLLOWING STEPS EXECUTED ON EACH SELECTED DRIVE:
 - A. WRITE DATA TO DISK. IN DUAL-PORT MODE, PORT "A" MODULE WILL TEST THE LOW HALF OF THE DISK (CYLINDERS 0 TO 407 FOR RP06, CYLINDERS 0 TO 205 FOR RP04/5), PORT "B" WILL EXERSIZE THE HIGH HALF (CYLINDERS 206 TO 410 FOR RP04/5, CYLINDERS 408 TO 814 FOR RP06). IN SINGLE-PORT MODE, THE MODULE WILL ACCESS THE ENTIRE DISK SURFACE (CYLINDERS 0 TO 410 FOR RP04/5, CYLINDERS 0 TO 814 FOR RP06).
 - B. WRITE-CHECK DATA JUST WRITTEN.
 - C. READ 256 WORDS (1 SECTOR) INTO MODULE READ BUFFER.
 - D. DO IN-CURE COMPARE OF READ BUFFER WITH FIRST 256 WORDS OF WRITE BUFFER.
 - E. RELEASE THE DRIVE FOR OTHER PORT TO TEST.

4. EXECUTION TIME

ONE PASS OF RPD RUNNING ALONE ON A PDP-11/70 TAKES ABOUT A MINUTE.

5. CONFIGURATION REQUIREMENTS

DEFAULT PARAMETERS:

DVA-176700 VCT-254 BR1-5 DVC-1

REQUIRED PARAMETERS:

SR1 MUST BE SET UP TO INDICATE WHICH PORT (A/B) THIS COPY OF THE MODULE IS TO TEST, IF ANY DRIVES ARE DUAL-PORTED. ALSO, BIT 7 MUST BE SET IF THE CONTROLLER IS AN RH70, AND CLEARED IF IT IS NOT AN RH70.

6. DEVICE/OPTION SETUP

MAKE SURE ALL DRIVES ARE POWERED UP, WRITE ENABLED, AND READY.
FOR DUAL-PORTED DRIVES: MAKE SURE CONTROLLER SELECT SWITCH IS IN A/B POSITION. IF DRIVE IS NOT CYCLED UP WITH SWITCH IN CORRECT POSITION, PLACE SWITCH CORRECTLY, AND DISABLE THEN RE-ENABLE DRIVE USING DRIVE DISABLE SWITCH.

7. OPERATOR OPTIONS

SR1

BIT 2

CLEAR(0):

TYPE OUT DATA LATE ERRORS (HARD ERROR) AND COUNT THEM INTERNALLY.

SET(1):

COUNT DATA LATE ERRORS INTERNALLY, BUT DO NOT TYPE OUT.

BIT 4

CLEAR(0):

THIS COPY OF RPD TESTS A-PORT ON ANY DUAL-PORTED DRIVES

SET(1):

THIS COPY OF RPD TESTS B-PORT ON ANY DUAL-PORTED DRIVES

BIT 5

CLEAR(0):

TEST DISKS WITH RANDOM SEEKS

SET(1):

DISABLE RANDOM SEEKS. INCREMENT SECTOR BY ONE EACH CYCLE.

BIT 6

CLEAR(0):

DO NOT TYPE OUT BADSPOTS.

SET(1):

ALWAYS TYPE OUT ANY BADSPOT NOT ALREADY RECORDED ON BADSPOT TABLE.

BIT 7

CLEAR(0):

CONTROLLER IS RH11

SET(1):

CONTROLLER IS RH70 (HAS RHBAE AND RHCS3)

8. NON-STANDARD PRINTOUTS

A. MOST PRINTOUTS HAVE THE STANDARD FORMATS.

B. ERROR MESSAGE DUMP RH11 REGISTERS IN THE FOLLOWING ORDER:

RHCS1 RHWC RHBA RPDA RHCS2 RPDS HPER1 RPAS
 RPLA RHDB RPMR RPDT RPSN RPOF RPDC RPCC
 RPER2 RPER3 RPEC1 RPEC2

ADDITIONALLY FOR THE RH70 RPBAE RHCS3

C. THE BAD SECTOR MESSAGE (WHEN ENABLED) LOOKS LIKE:

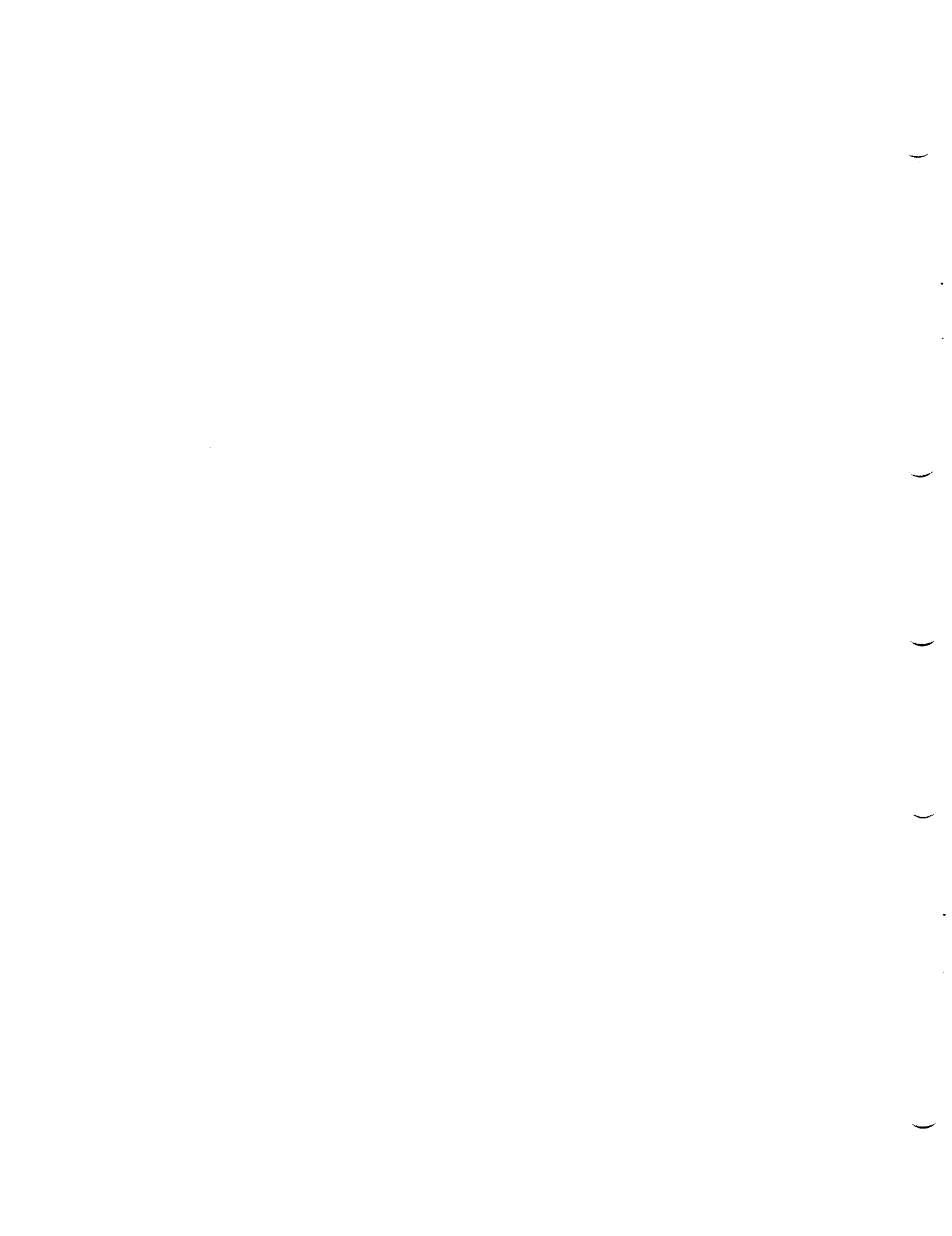
'DRIVE X: BADSPOT AT (OCTAL) CYL: XX, TRK: XX, SEC: XX'
 WHERE 'X' IS AN OCTAL DIGIT. NO ERROR IS ASSOCIATED WITH THIS MESSAGE.

10. CHANGE HISTORY

NOTE: HISTORY STARTS WITH REV. D0

D0: ENHANCED ERROR REPORTING ROUTINES.

8



369
370 000000'
(1) 000000'
(2)
(2)
(2)
(2)
(2) 000000'
(2) 000000' 050122 042104 040
(2) 000005' 000
(2) 000006' 176700
(2) 000010' 000254
(2) 000012' 240
(2) 000013' 000
(2) 000014' 000001
(2) 000016' 000000
(2) 000020' 000000
(2) 000022' 000000
(2) 000024' 000000
(2)
(2) 000026' 150000
(2) 000030' 002054'
(2) 000032' 000252'
(2) 000034' 000000
(2) 000036' 001500
(2) 000040' 000000
(2) 000042' 000000
(2) 000044' 000000
(2) 000046' 000000
(2) 000050' 000000
(2) 000052' 000000
(2) 000054' 000000
(2) 000056'
(2) 000056' 000000
(2) 000060' 000000
(2) 000062' 000000
(2) 000064' 000000
(2) 000066' 000000
(2) 000070' 000000
(2) 000072' 000000
(2) 000074' 000000
(2) 000076' 000000
(2) 000100' 000000
(2) 000102'
(2) 000102' 000000
(2) 000104'
(2) 000104' 000000
(2) 000106'
(2) 000106' 000000
(2) 000110' 000000
(2) 000112' 002236'
(2) 000114' 000000
(2) 000116' 000000
(2) 000120' 000000
(2) 000122' 000104
(2) 000124' 000722'

```
.SBTTL MODULE HEADER BLOCK ;DRB001
IMODX <RPDD >,176700,254,5,0,0,1500,104,RBUF,256,,256.
MODULE 150000,RPDD ,176700,254,5,0,0,1500,104,RBUF,256,,256.
.TITLE RPDD DEC/X11 SYSTEM EXERCISER MODULE
; DXDCOM VERSION 6 23-MAY-78
.LIST BIN
;*****
BEGIN:
MODNAM: .ASCII /RPDD / ;MODULE NAME.
XFLAG: .BYTE OPEN ;USED TO KEEP TRACK OF WBUF USAGE
ADDR: 176700+0 ;1ST DEVICE ADDR.
VECTOR: 254+0 ;1ST DEVICE VECTOR.
BR1: .BYTE PRTY5+0 ;1ST BR LEVEL.
BR2: .BYTE PRTY0+0 ;2ND BR LEVEL.
DVID1: 0+1 ;DEVICE INDICATOR 1.
SR1: OPEN ;SWITCH REGISTER 1
SR2: OPEN ;SWITCH REGISTER 2
SR3: OPEN ;SWITCH REGISTER 3
SR4: OPEN ;SWITCH REGISTER 4
;*****
STAT: 150000 ;STATUS WORD.
INIT: START ;MODULE START ADDR.
SPOINT: MODSP ;MODULE STACK POINTER.
PASCNT: 0 ;PASS COUNTER.
ICONT: 1500 ;# OF ITERATIONS PER PASS=1500
ICOUNT: 0 ;LOC TO COUNT ITERATIONS
SOPCNT: 0 ;LOC TO SAVE TOTAL SOFT ERRORS
HRDCNT: 0 ;LOC TO SAVE TOTAL HARD ERRORS
SOPPAS: 0 ;LOC TO SAVE SOFT ERRORS PER PASS
HRDPAS: 0 ;LOC TO SAVE HARD ERRORS PER PASS
SYSCNT: 0 ;# OF SYS ERRORS ACCUMULATED
RANNUM: 0 ;HOLDS RANDOM # WHEN RAND MACRO IS CALLED
CONFIG:
RES1: 0 ;RESERVED FOR MONITOR USE
RES2: 0 ;RESERVED FOR MONITOR USE
SVR0: OPEN ;LOC TO SAVE R0.
SVR1: OPEN ;LOC TO SAVE R1.
SVR2: OPEN ;LOC TO SAVE R2.
SVR3: OPEN ;LOC TO SAVE R3.
SVR4: OPEN ;LOC TO SAVE R4.
SVR5: OPEN ;LOC TO SAVE R5.
SVR6: OPEN ;LOC TO SAVE R6.
CSRA: OPEN ;ADDR OF CURRENT CSR.
SBADR: OPEN ;ADDR OF GOOD DATA, OR
ACSR: OPEN ;CONTENTS OF CSR.
WASADR: OPEN ;ADDR OF BAD DATA, OR
ASTAT: OPEN ;STATUS REG CONTENTS.
ERRTYP: OPEN ;TYPE OF ERRUR
ASB: OPEN ;EXPECTED DATA.
A*AS: OPEN ;ACTUAL DATA.
RSTRT: RSTRT ;RESTART ADDRESS AFTER END OF PASS
WDTO: OPEN ;WORDS TO MEMORY PER ITERATION
WDFR: OPEN ;WORDS FROM MEMORY PER ITERATION
INTR: OPEN ;# OF INTERRUPTS PER ITERATION
IDNUM: 104 ;MODULE IDENTIFICATION NUMBER=104
RBUFVA: RBUF ;READ BUFFER VIRTUAL ADDRESS
```

(2) 000126' 000000
(2) 000130' 000000
(2) 000132' 000400
(2) 000134' 000000
(2) 000136' 000000
(2) 000140' 000400
(2) 000142' 000000
(2) 000144' 000000
(2) 000146' 000000
(2) 000150' 000000
(2) 000040
(2)
(2)
(2)
(3)
(2) 000252'
(2)

```
RBUFPA: OPEN ;READ BUFFER PHYSICAL ADDRESS
RBUFEA: OPEN ;READ BUFFER EA BITS
RBUFZ: 256 ;SIZE OF THE READ BUFFER
WBUFPA: OPEN ;WRITE BUFFER PHYSICAL ADDRESS
WBUFEA: OPEN ;WRITE BUFFER EA BITS
WBUFZ: 256 ;WRITE BUFFER SIZE REQUESTED
WBUFFS: OPEN ;WRITE BUFFER SIZE AVAILABLE
CDRECT: OPEN ;CDATA/DAICK ERROR COUNT
CDWCT: OPEN ;CDATA/DAICK WORD COUNT
FREE: OPEN ;RESERVED FOR FUTURE USE
;*****
.REPT SPSIZ ;MODULE STACK STARTS HERE.
.NLIST
.WORD 0
.LIST
.ENDR
MODSP:
;*****
```

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372 .SBTTL MODULE PRIVATE DATA ;DRB001
373 000252' BADSPT: ;BAD SPOT TABLE. THERE IS ROOM FOR 48 ;DRB001
374 ;BAD SPOTS, IN TWO-WORD FORMAT, CYLINDER ;DRB001
375 ;FIRST, THEN SECTOR IN LOW BYTE OF SECOND ;DRB001
376 ;WORD, AND TRACK IN HIGH BYTE. ;DRB001
380 000252' 177777 ;CYLINDER ADDRESS ;DRB001
(1) 000254' 177777 ;TRACK/SECTOR ADDRESS ;DRB001
(1) 000256' 177777 ;CYLINDER ADDRESS ;DRB001
(1) 000260' 177777 ;TRACK/SECTOR ADDRESS ;DRB001
(1) 000262' 177777 ;CYLINDER ADDRESS ;DRB001
(1) 000264' 177777 ;TRACK/SECTOR ADDRESS ;DRB001
(1) 000266' 177777 ;CYLINDER ADDRESS ;DRB001
(1) 000270' 177777 ;TRACK/SECTOR ADDRESS ;DRB001
(1) 000272' 177777 ;CYLINDER ADDRESS ;DRB001
(1) 000274' 177777 ;TRACK/SECTOR ADDRESS ;DRB001
(1) 000276' 177777 ;CYLINDER ADDRESS ;DRB001
(1) 000300' 177777 ;TRACK/SECTOR ADDRESS ;DRB001
(1) 000302' 177777 ;CYLINDER ADDRESS ;DRB001
(1) 000304' 177777 ;TRACK/SECTOR ADDRESS ;DRB001
(1) 000306' 177777 ;CYLINDER ADDRESS ;DRB001
(1) 000310' 177777 ;TRACK/SECTOR ADDRESS ;DRB001
(1) 000312' 177777 ;CYLINDER ADDRESS ;DRB001
(1) 000314' 177777 ;TRACK/SECTOR ADDRESS ;DRB001
(1) 000316' 177777 ;CYLINDER ADDRESS ;DRB001
(1) 000320' 177777 ;TRACK/SECTOR ADDRESS ;DRB001
(1) 000322' 177777 ;CYLINDER ADDRESS ;DRB001
(1) 000324' 177777 ;TRACK/SECTOR ADDRESS ;DRB001
(1) 000326' 177777 ;CYLINDER ADDRESS ;DRB001
(1) 000330' 177777 ;TRACK/SECTOR ADDRESS ;DRB001
(1) 000332' 177777 ;CYLINDER ADDRESS ;DRB001
(1) 000334' 177777 ;TRACK/SECTOR ADDRESS ;DRB001
(1) 000336' 177777 ;CYLINDER ADDRESS ;DRB001
(1) 000340' 177777 ;TRACK/SECTOR ADDRESS ;DRB001
(1) 000342' 177777 ;CYLINDER ADDRESS ;DRB001
(1) 000344' 177777 ;TRACK/SECTOR ADDRESS ;DRB001
(1) 000346' 177777 ;CYLINDER ADDRESS ;DRB001
(1) 000350' 177777 ;TRACK/SECTOR ADDRESS ;DRB001
(1) 000352' 177777 ;CYLINDER ADDRESS ;DRB001
(1) 000354' 177777 ;TRACK/SECTOR ADDRESS ;DRB001
(1) 000356' 177777 ;CYLINDER ADDRESS ;DRB001
(1) 000360' 177777 ;TRACK/SECTOR ADDRESS ;DRB001
(1) 000362' 177777 ;CYLINDER ADDRESS ;DRB001
(1) 000364' 177777 ;TRACK/SECTOR ADDRESS ;DRB001
(1) 000366' 177777 ;CYLINDER ADDRESS ;DRB001
(1) 000370' 177777 ;TRACK/SECTOR ADDRESS ;DRB001
(1) 000372' 177777 ;CYLINDER ADDRESS ;DRB001
(1) 000374' 177777 ;TRACK/SECTOR ADDRESS ;DRB001
(1) 000376' 177777 ;CYLINDER ADDRESS ;DRB001
(1) 000400' 177777 ;TRACK/SECTOR ADDRESS ;DRB001
(1) 000402' 177777 ;CYLINDER ADDRESS ;DRB001
(1) 000404' 177777 ;TRACK/SECTOR ADDRESS ;DRB001
(1) 000406' 177777 ;CYLINDER ADDRESS ;DRB001
(1) 000410' 177777 ;TRACK/SECTOR ADDRESS ;DRB001
(1) 000412' 177777 ;CYLINDER ADDRESS ;DRB001
(1) 000414' 177777 ;TRACK/SECTOR ADDRESS ;DRB001
(1) 000416' 177777 ;CYLINDER ADDRESS ;DRB001

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(1) 000420' 177777 ;TRACK/SECTOR ADDRESS ;DRB001
(1) 000422' 177777 ;CYLINDER ADDRESS ;DRB001
(1) 000424' 177777 ;TRACK/SECTOR ADDRESS ;DRB001
(1) 000426' 177777 ;CYLINDER ADDRESS ;DRB001
(1) 000430' 177777 ;TRACK/SECTOR ADDRESS ;DRB001
(1) 000432' 177777 ;CYLINDER ADDRESS ;DRB001
(1) 000434' 177777 ;TRACK/SECTOR ADDRESS ;DRB001
(1) 000436' 177777 ;CYLINDER ADDRESS ;DRB001
(1) 000440' 177777 ;TRACK/SECTOR ADDRESS ;DRB001
(1) 000442' 177777 ;CYLINDER ADDRESS ;DRB001
(1) 000444' 177777 ;TRACK/SECTOR ADDRESS ;DRB001
(1) 000446' 177777 ;CYLINDER ADDRESS ;DRB001
(1) 000450' 177777 ;TRACK/SECTOR ADDRESS ;DRB001
(1) 000452' 177777 ;CYLINDER ADDRESS ;DRB001
(1) 000454' 177777 ;TRACK/SECTOR ADDRESS ;DRB001
(1) 000456' 177777 ;CYLINDER ADDRESS ;DRB001
(1) 000460' 177777 ;TRACK/SECTOR ADDRESS ;DRB001
(1) 000462' 177777 ;CYLINDER ADDRESS ;DRB001
(1) 000464' 177777 ;TRACK/SECTOR ADDRESS ;DRB001
(1) 000466' 177777 ;CYLINDER ADDRESS ;DRB001
(1) 000470' 177777 ;TRACK/SECTOR ADDRESS ;DRB001
(1) 000472' 177777 ;CYLINDER ADDRESS ;DRB001
(1) 000474' 177777 ;TRACK/SECTOR ADDRESS ;DRB001
(1) 000476' 177777 ;CYLINDER ADDRESS ;DRB001
(1) 000500' 177777 ;TRACK/SECTOR ADDRESS ;DRB001
(1) 000502' 177777 ;CYLINDER ADDRESS ;DRB001
(1) 000504' 177777 ;TRACK/SECTOR ADDRESS ;DRB001
(1) 000506' 177777 ;CYLINDER ADDRESS ;DRB001
(1) 000510' 177777 ;TRACK/SECTOR ADDRESS ;DRB001
(1) 000512' 177777 ;CYLINDER ADDRESS ;DRB001
(1) 000514' 177777 ;TRACK/SECTOR ADDRESS ;DRB001
(1) 000516' 177777 ;CYLINDER ADDRESS ;DRB001
(1) 000520' 177777 ;TRACK/SECTOR ADDRESS ;DRB001
(1) 000522' 177777 ;CYLINDER ADDRESS ;DRB001
(1) 000524' 177777 ;TRACK/SECTOR ADDRESS ;DRB001
(1) 000526' 177777 ;CYLINDER ADDRESS ;DRB001
(1) 000530' 177777 ;TRACK/SECTOR ADDRESS ;DRB001
(1) 000532' 177777 ;CYLINDER ADDRESS ;DRB001
(1) 000534' 177777 ;TRACK/SECTOR ADDRESS ;DRB001
(1) 000536' 177777 ;CYLINDER ADDRESS ;DRB001
(1) 000540' 177777 ;TRACK/SECTOR ADDRESS ;DRB001
(1) 000542' 177777 ;CYLINDER ADDRESS ;DRB001
(1) 000544' 177777 ;TRACK/SECTOR ADDRESS ;DRB001
(1) 000546' 177777 ;CYLINDER ADDRESS ;DRB001
(1) 000550' 177777 ;TRACK/SECTOR ADDRESS ;DRB001
381 000552' ENDBBK: ;DRB001
382 ;DRB001
383 000004 DLATE = BIT2 ;DRB001
384 000020 BPORT = BIT4 ;DRB001
385 000040 NORAND = BIT5 ;DRB001
386 000100 BDSPT = BIT6 ;DRB001
387 000200 RH70 = BIT7 ;DRB001
388 137400 HROCS2 =137400 ;DRB001
389 177777 HRDER1 =177777 ;DRB001
390 167777 HRDER2 =167777 ;DRB001
391 140153 HRDER3 =140153 ;DRB001

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392                                     ;**--16           ;DRB001
393 000552' 000000 000000 000000 S:      0,0,0,0,0,0,0,0,0,0,0,0,0,0,0,0,0,0,0,0
    000560' 000000 000000 000000
    000566' 000000 000000 000000
    000574' 000000 000000 000000
    000602' 000000 000000 000000
    000610' 000000 000000 000000
    000616' 000000 000000 000000
    000624' 000000

394                                     ;DO NOT CHANGE THE ORDER OF THE NEXT 4 LOCATIONS
395                                     ;NEEDED FOR MAP22 ROUTINE
396 000626' 000000 PA18:  0
397 000630' 000000 XMEM:  0
398 000632' 000000 PA22:  0
399 000634' 000000 EA22:  0
400
401 000636' 000252' NITB8K: BADSP1 ; NEXT OPEN BADSPUT IN TABLE ;**--5           ;DRB001
402
403 000640' 000000 CYLLIM: 0 ; UPPER DISK LIMIT (DEPENDS ON DISK TYPE) ;DRB001
404 000642' 000000 CYLNRD: 0 ; ABSOLUTE CYLINDER ADDRESS OF CURRENT OP.;DRB001
405 000644' 000000 CYL:  0 ; RELATIVE DISK ADDRESS ;DRB001
406 000646' DSKADR: ; FOR REFERENCING IRACK/SECTOR AS WORD ;DRB001
407 000646' 000 SECTOR: .BYTE 0 ; SECTOR ADDRESS OF CURRENT OPERATION ;DRB001
408 000647' 000 TRACK: .BYTE 0 ; TRACK ADDRESS OF CURRENT OPERATION ;DRB001
409
410 000650' 000000 BADCYL: 0 ; FROM DRIVE LOCATION REGISTERS, ON FINDING BADSPOT;DRB001
411 000652' 000000 BADSEC: 0 ; FROM RFDA ON BADSPOT OCCURANCE ;DRB001
412 000654' 000000 BADTRK: 0 ; FROM KPDA+1 ON BADSPOT ;DRB001
413
414 000656' 000000 LASCYL: 0 ; LAST CYLINDER WHICH CAN BE WRITTEN ;DRB001
415 000660' 000000 LASTRK: 0 ; LAST TRACK WHICH CAN BE WRITTEN ;DRB001
416 000662' 000000 LASSEC: 0 ; LAST SECTOR WHICH CAN BE WRITTEN ;DRB001
417
418 000664' 000000 SIZTRK: 0 ; NUMBER OF TRACKS IN WBUF SZ ;DRB001
419 000666' 000000 SIZSEC: 0 ; REMAINDER OF WBUF SZ, IN SECTORS ;DRB001
420 000670' 000000 FLAG:  0
421 000672' 000000 DVICE:  0
422 000674' 000000 DRIVE:  0
423 000676' 000000 UNITNO: 0
424
425 000700' 000000 DLTCNT: 0 ;**--1           ;DRB001
426 000702' 000000 FUNC:  0
427 000704' 000000 TIMER:  0
428 000706' 000000 ZERO:  0
429 000710' 000000 FERADR: 0
430 000712' 000000 CLK:  0
431 000714' 000000 TRY:  0
432 000716' 000000 STORE: 0
433 000720' 000000 HMDERR: 0
434 000722' 000400 RBUF:  .BLKW  256.
435
436 001722' TABLE:
437 001722' 000552' S
438 001724' 000554' S+2
439 001726' 000556' S+4
440 001730' 000560' S+6

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441 001732' 000562' S+10
442 001734' 000564' S+12
443 001736' 000566' S+14
444 001740' 000570' S+16
445 001742' 000572' S+20
446 001744' 000574' S+22
447 001746' 000576' S+24
448 001750' 000600' S+26
449 001752' 000602' S+30
450 001754' 000604' S+32
451 001756' 000606' S+34
452 001760' 000610' S+36
453 001762' 000612' S+40
454 001764' 000614' S+42
455 001766' 000616' S+44
456 001770' 000620' S+46
457 001772' 000622' S+50
458 001774' 000624' S+52
459
460 001776' 177777 177777 ;**--1           ;DRB001
461 002000' 000000 RHCS1: 0
462 002002' 000000 RHW:  0
463 002004' 000000 RHBA:  0
464 002006' 000000 RPDA:  0
465 002010' 000000 RHCS2: 0
466 002012' 000000 RPDS:  0
467 002014' 000000 RPER1: 0
468 002016' 000000 RPAS:  0
469 002020' 000000 RPLA:  0
470 002022' 000000 RHDB:  0
471 002024' 000000 RPMR:  0
472 002026' 000000 RPDT:  0
473 002030' 000000 RPSN:  0
474 002032' 000000 RPUF:  0
475 002034' 000000 RPDC:  0
476 002036' 000000 RPCC:  0
477 002040' 000000 RPER2: 0
478 002042' 000000 RPER3: 0
479 002044' 000000 RPEC1: 0
480 002046' 000000 RPEC2: 0
481 002050' 000000 RHBAE: 0
482 002052' 000000 RHCS3: 0
483

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485 .SBTTL MODULE CODE ;DRB001
486 002054' 012767 000400 176032 START: MOV #256.,WDIU ;256 WORDS TO MEM/ITERATION
487 002062' 012767 000400 176026 MOV #256.,WDFR ;256 WORDS FROM MEM/ITERATION
488 002070' 012767 000004 176022 MOV #4,INTR ;4 INTERRUPTS/ITERATION
489 002076' 012701 000252' MOV #BADSP1,R1 ;BAD SPOT TABLE REV D
490 002102' 022721 177777 4S: CMP #=-1,(R1)+ ;END YET ? REV D
491 002106' 001375 BNE 4S ;REV D
492 002110' 005741 TST -(R1) ;REV D
493 002112' 010167 176520 MOV R1,NXTBBK ;RESET POINTER ;REV D
494 002116' 016767 175672 176546 MOV DV101,DVICE ;GET DRIVES TO TEST
495 002124' 123727 000041 000011 CMPH @#41,#11 ;IF RP IS LOAD MEDIUM THEN
496 002132' 001021 BNE JS ;BEGIN
497 002134' 113700 000040 MOVB @#40,R0 ; GET LOAD-DEVICE NUMBER
498 002140' 012701 000001 MOV #1,R1 ; INITIALIZE DEVICE MASK
499 002144' 105700 1S: TSTH R0 ; WHILE NOT POINTING AT LOAD-DEVICE DO
500 002146' 001403 BEQ 2S ; BEGIN
501 002150' 006301 ASL R1 ; SHIFT MASK TO NEXT DEVICE
502 002152' 105300 DECB R0 ; KEEP TRACK OF SHIFTING
503 002154' 000773 BR 1S ; END
504 002156' 130167 176510 2S: BITH R1,DVICE ; IF LOAD DEVICE IS SELECTED THEN
505 002162' 001405 BEQ 3S ; BEGIN
506 002164' 113767 000040 176504 MOVB @#40,UNITNO ; MOVE LOAD-DEVICE NUMBER TO DRYE
507 002172' 004767 002266 JSR PC,DROP ; DROP THE LOAD-DEVICE
508 ; END
509 002176' 3S: ;END
510 002176' 005067 176442 CLR CYL ;START AT CYLINDER 0 ;DRB001
511 002202' 012767 000377 176436 MOV #377,DSKADR ;START AT TRACK 0, SECTOR 0 ;DRB001
512 ;(* INCREMENT BEFORE TEST *) ;DRB001
513 002210' 004767 004500 JSR PC,SETUP
514 002214' 004767 004356 JSR PC,REZET ;CLEAR THE RH
515 002220' 032767 000200 175570 BIT #RH70,SH1 ;RH70 ? ;DJM01
516 002226' 001003 BNE RESTRT ;NO DONT PRI BAE & CS3 ;DJM01
517 002230' 012767 177777 176364 MOV #=-1,S+50 ;SHORTEN IEMP STORAGE ;DJM01
518 002236' RESTRT:
519
520 002236' 104415 000000' 000124' GETPAS,BEGIN, RBUFVA ;GET PHYSICAL ADDRESS FROM 16-BIT RBUFVA
521
522 002244' 012767 177777 176424 LOOP1: MOV #=-1,UNITNO ;PRE-SET UNIT NUMBER
523 002252' 104414 000000' GwBUFS, BEGIN ;GET WRITE BUFFER INFORMATION
524 ;**=1 ;DRB001
525 002256' LOOP2:
526 002256' 004767 000744 3S: JSR PC,PICKDR ;GO PICK A DRIVE
527 002262' 103407 BCS 1S ;RETURNS HERE IF ALL DRIVES DONE ;DRB001
528 002264' 004767 001572 JSR PC,WRILIM ;GET DISK LIMITS FOR THIS TYPE OF DISK ;DRB001
529 002270' 005067 176420 CLR TRY ;ELSE CLR RE TRY COUNT
530 002274' 004767 000022 JSR PC,CYCLE ;GO DO A CYCLE ON THIS DRIVE
531 002300' 000766 BR LOOP2 ;DO IT TO NEXT DRIVE
532
533 002302' 005767 176364 1S: TST DVICE ;ANYBODY LEFT TO CHECK?
534 002306' 001002 BNE 2S ;BR IF YES
535 002310' 104410 000000' ENDS,BEGIN ;
536 002314' 2S:
(1) 002314' 104413 000000' ENDTs,BEGIN ;SIGNAL END OF ITERATION.
(1) ;MONITOR SHALL TEST END OF PASS
537 002320' 000751 BR LOOP1 ;BR BACK IF NO
538

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539 002322' CYCLE: ;**=-4 ;DRB001
540 ;DRB001
541 002322' 004767 000772 JSR PC,PICKBK ;SELECT A SECTOR TO TEST
542 002326' 004567 000026 JSR RS,WRITE ;GO WRITE A BLOCK
543 002332' 004567 000116 JSR RS,WRITCK ;GO DO WRITE CHECK
544 002336' 004567 000206 JSR RS,READ ;GO READ A BLOCK
545 002342' 104412 000000' 000126' CQATAS,BEGIN,RBUFPA ; REQUEST FOR MONITOR TO CHECK DATA
(1) 002350' 002352' .+2 ; IF ERROR, CONTINUE
546 002352' 004767 000266 JSR PC,RELESE ;RELEASE THE DRIVE ;DRB001
547 002356' 000207 RIS PC ;END CYCLE ;DRB001
548

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550                                     ;**=66                                     ;DRB001
551                                     ;
552                                     ;
553                                     ;
554                                     ;
555                                     ;
556                                     ;
557                                     ;
558                                     ;
559                                     ;
560                                     ;
561                                     ;
562                                     ;
563                                     ;
564 002360' 012767 000161 176314 WRITE: MOV #161,FUNC ; LOAD WRITE FUNCTION
565 002366' 012767 002360' 176314 MOV #WRITE,FERADR ;SAVE WHERE WE WERE
566 002374' 016746 175542 MOV WBUF SZ, -(SP) ;GET WRITE SIZE
567 002400' 005416 NEG (SP) ;NEGATE IT
568 002402' 012677 177374 MOV (SP)+, @RHWC ; LOAD WORD COUNT
569 002406' 016777 175522 177370 MOV WBUF PA, @RHBA ; LOAD BUFFER ADDRESS
570 002414' 016777 176226 177364 MOV DSKADR, @RPDA ; LOAD DISK ADDRESS
571 002422' 016777 176214 177404 MOV CYLNDR, @RPDC ; LOAD CYLINDER ADDRESS ;DRB001
572 ; LINEUP WBUFEA ; LINE UP EA BITS FOR RHCS1
573 002450' 000167 000302 JMP GU ; CONTINUE
574 002454' 012767 000151 176220 WRITCK: MOV #151,FUNC ; LOAD WRITE-CHECK FUNCTION
575 002462' 012767 002454' 176220 MOV #WRITCK,FERADR ;SAVE WHERE WE WERE
576 002470' 016746 175446 MOV WBUF SZ, -(SP) ;GET WRITE SIZE
577 002474' 005416 NEG (SP) ;NEGATE IT
578 002476' 012677 177300 MOV (SP)+, @RHWC ; LOAD WORD COUNT
579 002502' 016777 175426 177274 MOV WBUF PA, @RHBA ; LOAD BUFFER ADDRESS
580 002510' 016777 176132 177270 MOV DSKADR, @RPDA ; LOAD DISK ADDRESS
581 002516' 016777 176120 177310 MOV CYLNDR, @RPDC ; LOAD CYLINDER ADDRESS ;DRB001
582 ; LINEUP WBUFEA ; LINE UP EA BITS FOR RHCS1
583 002544' 000167 000206 JMP GU ; CONTINUE
584 002550' 012767 000171 176124 READ: MOV #171,FUNC ; LOAD READ FUNCTION
585 002556' 012767 002550' 176124 MOV #READ,FERADR ;SAVE WHERE WE WERE
586 002564' 016746 175342 MOV RBUF SZ, -(SP) ;GET READ SIZE
587 002570' 005416 NEG (SP) ;NEGATE IT
588 002572' 012677 177204 MOV (SP)+, @RHWC ; LOAD WORD COUNT
589 002576' 016777 175324 177200 MOV RBUF PA, @RHBA ; LOAD BUFFER ADDRESS
590 002604' 016777 176036 177174 MOV DSKADR, @RPDA ; LOAD DISK ADDRESS
591 002612' 016777 176024 177214 MOV CYLNDR, @RPDC ; LOAD CYLINDER ADDRESS ;DRB001
592 ; LINEUP WBUFEA ; LINE UP EA BITS FOR RHCS1
593 002640' 000167 000112 JMP GU ; CONTINUE
594 002644' 016777 176026 177136 RELEASE: MOV UNITNO, @RHCS2 ;SET UP UNIT TO RELEASE ;DRB001
595 002652' 012777 000013 177120 MOV #13, @RHCS1 ;EXECUTE RELEASE COMMAND ;DRB001
596 002660' 104407 000000' BREAKS, BEGIN ;TEMPORARY RETURN TO MONITOR....
(1) 002664' 104407 000000' BREAKS, BEGIN ;THEN CONTINUE AT NEXT INSTRUCTION.
597 002670' 000207 RTS PC ;IT'S RELEASED ;DRB001
598 ;**=33 ;DRB001
599
600 002672' 016777 176000 177110 CLEAR: MOV UNITNO, @RHCS2 ; LOAD UNIT ADDRESS
601 002700' 012777 000011 177072 MOV #11, @RHCS1 ; ISSUE A DRIVE CLEAR
602 002706' 000240 NOP ;WAIT
603 002710' 000240 NOP ;FOR DRIVE CLEAR TO FINISH
604 002712' 012777 000023 177060 MOV #23, @RHCS1 ;ISSUE A PACK ACK

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605 002720' 105777 177054 1s: TSTB @RHCS1
606 002724' 100401 BMI 2s
607 002726' 000774 BK 1s ; NO, WAIT TILL DONE
608 002730' 017746 177062 2s: MOV @RPAS, -(SP) ;CLEAR AS BIT
609 002734' 012677 177056 MOV (SP)+, @RPAS
610 002740' 012777 040000 177032 MOV #BIT14, @RHCS1 ; CLEAR ANY CONTROLLER ERRORS
611 002746' 012777 010000 177056 MOV #BIT12, @RPOF ; SET BIT FOR 11 FORMAT
612 002754' 000205 RTS R5 ; RETURN
613
614 002756' 016777 175714 177024 GU: MOV UNITNO, @RHCS2 ; LOAD UNIT SELECT
615 002764' 032767 001000 175064 BIT #ADDR22, RES1 ;22 BIT SUPPORT? ;DRB001
616 002772' 001434 BEQ 1s ;NO ;DRB001
617 002774' 017767 177004 175624 MOV @RHBA, PA18 ;GET 18 BIT ADDR
618 003002' 006267 175622 ASR XMEM ;SHIFT EA BITS TO POSITION 4,5
619 003006' 006267 175616 ASR XMEM
620 003012' 006267 175612 ASR XMEM
621 003016' 006267 175606 ASR XMEM
622 003022' 104416 000000' 000626' MAP22s, BEGIN, PA18 ; GET 22-BIT ADDR FROM 18-BIT ADDR
623 003030' 016777 175576 176746 MOV PA22, @RHBA ;LOAD BA REG
624 003036' 016777 175572 177004 MOV EA22, @RHBAE ;LOAD BAE REG
625 003044' 042767 000034 175562 BIC #34, EA22 ;CLEAR UNWANTED BITS
626 003052' 000367 175556 SWAB EA22 ;LOAD INTO BITS 8,9
627 003056' 016767 175552 175544 MOV EA22, XMEM ;LOAD XMEM TO SET INTO FUNCTION CODE
628 003064' 056767 175540 175610 1s: BIS XMEM, FUNC ; LOAD EXTENDED MEMORY BITS
629 003072' 016777 175604 176700 MOV FUNC, @RHCS1 ; EXECUTE THE FUNCTION
630 003100' 104400 000000' EXIIS, BEGIN ;EXIT TO MONITOR. MODULE WAIT FOR INTERRUPT.
631 003104' 010046 NTRUPT: MOV RO, -(SP) ;SAVE RO
632 003106' 016700 175564 MOV UNINQ, RO
633 003112' 116000 003110' MOVB BITTAR(RO), RO
634 003116' 017746 176674 MOV @RPAS, -(SP)
635 003122' 040016 BIC RO, (SP)
636 003124' 012677 176666 MOV (R6)+, @RPAS
637 003130' 012600 MOV (SP)+, RO
638
(1) 003132' 000004 000000' 003140' VIROS, BEGIN, 1s ; QUEUE UP TO CONTINUE AT 1s AND RTI
(1) ;
639
640 003140' 004767 002656 1s: JSR PC, ERRORS ;GO CHECK FOR ERRORS
641 003144' 103401 BCS 2s ;ERRORS DETECTED ;DRB001
642 003146' 000205 KTS R5 ;OTHERWISE, RETURN OK
643
644
645 003150' 105767 175544 TSTB HRDERR ;DON'T RETRY ON HARD ERROR REV D
646 003154' 100415 BMI 4s ;DRB001
647 003156' 005267 175532 INC TRY ;COUNT AN ERROR
648 003162' 026727 175526 000003 CMP TRY, #3 ;TOO MANY FOR THIS CYCLE? ;DRB001
649 003170' 002002 BGE 5s ;BR IF SO
650 003172' 000177 175512 JMP #FERADR ;RE-EXECUTE THE DRIVER ROUTINE ;DRB001
651 003176' 104403 000000' 011106' 5s: MSGNS, BEGIN, EXCED
652 003204' 004767 002664 JSR PC, BAD ;UPDATE BAD BLOCK INFO.
653 003210' 012605 4s: MOV (SP)+, R5 ;RESTORE R5
654
655 003212' 004767 000102 3s: JSR PC, PICKBK ;TRY A DIFFERENT BLOCK ;DRB001
656 003216' 005367 175454 DEC UNITNO ;WANT TO RE-DO SAME DRIVE WERE ON
657 003222' 000167 177030 JMP LOOP2 ;GO DO IT
658 ;**=2 ;DRB001

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659
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664 003226'
665 003226' 005267 175444
666 003232' 026727 175440 000010
667 003240' 001002
668 003242' 000261
669 003244' 000420
670 003246' 016700 175424
671 003252' 136067 003310' 175412
672 003260' 001001
673 003262' 000761
674 003264'
675
676 003264' 004567 003132
677 003270' 103402
678 003272' 004767 002730
679 003276' 004767 000700
680 003302' 103751
681 003304' 000241
682 003306' 000207
683 003310' 001001
684 003312' 004004
685 003314' 020020
686 003316' 100100
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689 003320'
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698 003320' 032767 000040 174470
699 003326' 001403
700 003330' 105267 175312
701 003334' 000432
702 003336'
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704
705 003336' 104417 000000'
706 003342' 016700 174506
707 003346' 042700 176000
708 003352' 010067 175266
709 003356' 016700 174472
710 003362' 000300
711 003364' 042700 177740
712 003370' 110067 175252
713 003374' 016700 174454
714 003400' 000241

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715 003402' 006100
716 003404' 006100
717 003406' 006100
718 003410' 006100
719 003412' 042700 177740
720
721
722 003416' 110067 175225
723 003422'
724 003422' 126727 175220 000025
725 003430' 003412
726 003432' 105267 175211
727 003436' 016700 175204
728 003442' 012701 000026
729 003446' 004767 000760
730 003452' 110067 175170
731 003456'
732
733 003456' 126727 175165 000022
734 003464' 003412
735 003466' 005267 175152
736 003472' 116700 175151
737 003476' 012701 000023
738 003502' 004767 000724
739 003506' 110067 175135
740 003512'
741 003512' 026767 175126 175120
742 003520' 002420
743 003522' 032767 000040 174266
744 003530' 001404
745 003532' 005067 175106
746 003536' 005067 175104
747 003542'
748
749 003542' 016700 175076
750 003546' 016701 175066
751 003552' 004767 000654
752 003556' 010067 175062
753 003562'
754 003562'
755 003562' 016767 175056 175052
756 003570' 032767 000020 174220
757 003576' 001407
758 003600' 032767 000001 175062
759 003606' 001403
760 003610' 066767 175024 175024
761 003616'
762 003616' 026767 175020 175032
763 003624' 003431
764 003626' 016767 175024 175006
765 003634' 032767 000040 174154
766 003642' 001404
767 003644' 005067 174774
768 003650' 005067 174772
769 003654'
770 003654' 126767 174767 174776

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RPDD DEC/X11 SYSTEM EXERCISER MODULE MACY11 30A(1052) 12-MAR-80 07:45 PAGE 2-12 ;DRB001 SEQ 0017
 XRPDD0.P11 12-MAR-80 07:43 MODULE CODE

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771 003662' 003412 BLE 11S ; BEGIN ;DRB001
772 003664' 116767 174770 174755 MOVH LASTRK,TRACK ; TRACK := LASTRK ;DRB001
773 003672' 126767 174750 174762 CMPB SECTOR,LASSEC ; IF SECTOR GT LASSEC THEN ;DRB001
774 003700' 003403 BLE 12S ; BEGIN ;DRB001
775 003702' 116767 174754 174736 MOVH LASSEC,SECTOR ; SECTOR := LASSEC ;DRB001
776 003710' 12S: ; END ;DRB001
777 003710' 11S: ; END ;DRB001
778
779 003710' 20S:
780
781 ;(* NOW, CHECK BADSPOT TABLE *) ;DRB001
782 003710' 012700 000252' MOV #BADSPOT,R0 ;R0 := ADDRESS OF TABLE ;DRB001
783 003714' 016701 174716 MOV NXTRBK,R1 ;R1 := END OF TABLE ;DRB001
784 003720' 116702 174722 MOVH SECTOR,R2 ;R2 := SECTOR ;DRB001
785 003724' 116703 174717 MOVH TRACK,R3 ;R3 := TRACK ;DRB001
786 003730' 016704 174706 MOV CYLNDR,R4 ;R4 := CYLNDR ;DRB001
787 ;(* CALCULATE UPPER LIMIT OF THIS WRITE *) ;DRB001
788 003734' 066702 174726 ADD SIZSEC,R2 ;R2 := R2 + # OF SECTORS IN WRITE ;DRB001
789 003740' 020227 000025 CMP R2,#21. ;IF R2 GT 21. THEN ;DRB001
790 003744' 003403 BLE 13S ;BEGIN ;DRB001
791 003746' 005203 INC R3 ; R3 := R3 + 1 (INCR. UPPER TRACK) ;DRB001
792 003750' 162702 000026 SUB #22.,R2 ; TRUNCATE R2 ;DRB001
793 003754' 13S: ;END ;DRB001
794 003754' 066703 174704 ADD SIZTRK,R3 ;R3 := R3 + TRKSIZ ;DRB001
795 003760' 020327 000022 CMP R3,#18. ;IF R3 GT 18. THEN ;DRB001
796 003764' 003403 BLE 14S ;BEGIN ;DRB001
797 003766' 005204 INC R4 ; R4 := R4 + 1 (INCR. UPPER CYLINDER) ;DRB001
798 003770' 162703 000023 SUB #19.,R3 ; TRUNCATE R3 ;DRB001
799 003774' 14S: ;END ;DRB001
800 003774' 020001 15S: CMP R0,R1 ;WHILE R0 LT R1 DO ;DRB001
801 003776' 020230 BGE 16S ;BEGIN ;DRB001
802 004000' 026710 174636 CMP CYLNDR,(R0) ; IF CYLNDR LE (R0) AND R4 GE (R0) ;DRB001
803 004004' 003022 BGT 17S ; AND TRACK LE 3(R0) AND R3 GE 3(R0) ;DRB001
804 004006' 020410 CMP R4,(R0) ; AND SECTOR LE 2(R0) AND R2 GE 2(R0) THEN ;DRB001
805 004010' 002420 BLT 17S ; ;DRB001
806 004012' 126760 174631 000003 CMPB TRACK,3(R0) ; ;DRB001
807 004020' 003014 BGT 17S ; ;DRB001
808 004022' 120360 000003 CMPB #3,3(R0) ; ;DRB001
809 004026' 002411 BLT 17S ; ;DRB001
810 004030' 126760 174612 000002 CMPB SECTOR,2(R0) ; ;DRB001
811 004036' 003005 BGT 17S ; ;DRB001
812 004040' 120260 000002 CMPB #2,2(R0) ; ;DRB001
813 004044' 002402 BLT 17S ; BEGIN ;DRB001
814 004046' 000167 177246 JMP PICKBK ; TRY ANOTHER SECTOR ;DRB001
815 004052' 17S: ; END ;DRB001
816 004052' 062700 000004 ADD #4,R0 ; INCREMENT TO NEXT BAD SPOT ;DRB001
817 004056' 000746 BR 15S ;END ;DRB001
818 004060' 16S: ; ;DRB001
819 004060' 000207 RTS PC ;CPU GO HOME!!!! ;DRB001
820 ;DRB001
821 ;DRB001
822 004062' WRTLIM: ;DRB001
823 ;(* WE ALLOW USER TO CHANGE #BUF SZ "AT WILL"--THEREFORE *) ;DRB001
824 ;(* WE DO NOT AUTOMATICALLY KNOW THE TRANSFER SIZE. *) ;DRB001
825 ;(* COMPUTATION IS MADE EASIER, FURTHERMORE, IF WE HAVE *) ;DRB001
826 ;(* THE SIZE BROKEN INTO TRACKS AND SECTORS (SINCE *) ;DRB001

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RPDD DEC/X11 SYSTEM EXERCISER MODULE MACY11 30A(1052) 12-MAR-80 07:45 PAGE 2-13 ;DRB001 SEQ 0018
 XRPDD0.P11 12-MAR-80 07:43 MODULE CODE

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827 ;(* #BUF SZ IS ONE WORD, THE TRANSFER CAN'T BE GREATER *) ;DRB001
828 ;(* THAN 12 TRACKS). THIS ROUTINE ALSO CALCULATES THE *) ;DRB001
829 ;(* UPPER LIMITS ON DISK WRITES SO AS NOT TO OVERFLOW *) ;DRB001
830 ;(* HIGH CYLINDER. *) ;DRB001
831
832 004062' 016700 174054 MOV #BUF SZ,R0 ;R0 := ALLOCATED WRITE BUFFER ;DRB001
833 004066' 000300 SWAB R0 ;R0 := R0 / 256. ;DRB001
834 004070' 042700 177400 BIC #177400,R0 ; (* R0 IS #BUF SZ IN SECTORS *) ;DRB001
835 004074' 005067 174564 CLR SIZTRK ;SIZTRK := 0 ;DRB001
836 004100' 020027 000026 CMP R0,#22. ;WHILE R0 GE 22. DO ;DRB001
837 004104' 002405 BLT 2S ;BEGIN ;DRB001
838 004106' 105267 174552 INCB SIZTRK ; SIZTRK := SIZTRK + 1 ;DRB001
839 004112' 162700 000026 SUB #22.,R0 ; DECREMENT SECTORS BY 1 TRACK ;DRB001
840 004116' 000770 BR 1S ;END ;DRB001
841 004120' 010067 174542 2S: MOV R0,SIZSEC ;SIZSEC := REMAINDER ;DRB001
842 004124' 012701 000022 MOV #18.,R1 ;R1 := HIGH TRACK ;DRB001
843 004130' 012702 000025 MOV #21.,R2 ;R2 := HIGH SECTOR ;DRB001
844 004134' 160002 SUB R0,R2 ;R2 := R2 - SIZSEC ;DRB001
845 004136' 005702 IST R2 ;IF R2 LT 0 THEN ;DRB001
846 004140' 002003 BGE 3S ;BEGIN ;DRB001
847 004142' 062702 000026 ADD #22.,R2 ; INCREMENT BACK UP ;DRB001
848 004146' 005301 DEC R1 ; CAN'T FIT THAT LAST TRACK... ;DRB001
849 004150' 3S: ;END ;DRB001
850 004150' 166701 174510 SUB SIZTRK,R1 ;R1 := R1 - SIZTRK ;DRB001
851 004154' 005701 IST R1 ;IF R1 LT 0 THEN ;DRB001
852 004156' 002004 BGE 4S ;BEGIN ;DRB001
853 004160' 062701 000023 ADD #19.,R1 ; INCREMENT R1 BACK UP ;DRB001
854 004164' 005367 174466 DEC LASCYL ; LASCYL := LASCYL - 1 ;DRB001
855 004170' 4S: ;END ;DRB001
856 004170' 010167 174464 MOV R1,LASTRK ;LASTRK := HIGHEST TRACK WE CAN WRITE ;DRB001
857 004174' 010267 174462 MOV R2,LASSEC ;LASSEC := HIGHEST SECTOR WE CAN WRITE ;DRB001
858 004200' 000207 RTS PC ;END OF ROUTINE ;DRB001
859 004202' GETDVT: ;DRB001
860 ;(* FIND WHICH TYPE OF DRIVE THIS IS. IF RP04/5, SET CYLLIM *) ;DRB001
861 ;(* TO 206. IF RP06, SET CYLLIM TO 408. DROP THE DRIVE IF *) ;DRB001
862 ;(* IT IS NOT A RP04/5/6 *) ;DRB001
863
864 004202' 016777 174470 175600 MOV UNIND0,#RHCS2 ;SELECT THE DRIVE ;DRB001
865 004210' 027727 175612 024022 CMP #RPD1,#24022 ;IF IT IS AN RP06 DUAL PORT THEN ;DRB001
866 004216' 001012 BNE 1S ;BEGIN ;DRB001
867 004220' 052767 000001 174442 BIS #BIT0,FLAG ; DUAL PORT ;DRB001
868 004226' 012767 000627 174404 MOV #407.,CYLLIM ; CYLLIM := 408. ;DRB001
869 004234' 012767 001455 174414 MOV #813.,LASCYL ; LASCYL := DISK HIGH LIM ;DRB001
870 004242' 000471 BR 2S ;END ;DRB001
871 004244' 1S: ;ELSE ;DRB001
872 004244' 027727 175556 024021 CMP #RPD1,#24021 ;IF IT IS RP04/5 DUAL PORT THEN ;DRB001
873 004252' 001404 BEO 3S ; ;DRB001
874 004254' 027727 175546 024020 CMP #RPD1,#24020 ; ;DRB001
875 004262' 001012 BNE 4S ;BEGIN ;DRB001
876 004264' 3S: ;DRB001
877 004264' 052767 000001 174376 BIS #BIT0,FLAG ; DUAL PORT ;DRB001
878 004272' 012767 000315 174340 MOV #205.,CYLLIM ; CYLLIM := 206. ;DRB001
879 004300' 012767 000631 174350 MOV #409.,LASCYL ; LASCYL := DISK HIGH LIM ;DRB001
880 004306' 000447 BR 5S ;END ;DRB001
881 004310' 4S: ;ELSE ;DRB001
882 004310' 027727 175512 020022 CMP #RPD1,#20022 ;IF RP06 SINGLE PORT THEN ;DRB001

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883 004316' 001012 BNE 6S ;BEGIN ;DRB001
884 004320' 042767 000001 174342 BIC #BIT0,FLAG ; SINGLE PORT ;DRB001
885 004326' 012767 001456 174304 MOV #14.,CYLLIM ; CAN USE ENTIRE DISK ;DRB001
886 004334' 012767 001455 174314 MOV #13.,LASCYL ; LASCYL := DISK HIGH LIMIT ;DRB001
887 004342' 000431 BK 2S ;END ;DRB001
888 004344' 6S: ;ELSE ;DRB001
889 004344' 027727 175456 020021 CMP @RPDT,#20021 ;IF RP04/5 SINGLE PORT THEN ;DRB001
890 004352' 001404 BEQ 5S ; ;DRB001
891 004354' 027727 175446 020020 CMP @RPDT,#20020 ; ;DRB001
892 004362' 001012 BNE 9S ; ;DRB001
893 004364' 8S: ;BEGIN ;DRB001
894 004364' 042767 000001 174276 BIC #BIT0,FLAG ; SINGLE PORT ;DRB001
895 004372' 012767 000632 174240 MOV #410.,CYLLIM ; USE ENTIRE DISK ;DRB001
896 004400' 012767 000631 174250 MOV #409.,LASCYL ; LASCYL := DISK HIGH LIMIT ;DRB001
897 004406' 000407 BR 2S ;END ;DRB001
898 004410' 9S: ;ELSE ;DRB001
899 004410' ;BEGIN ;DRB001
900 004410' 004767 000050 JSR PC,DROP ; DROP THE DRIVE ;DRB001
901 004414' 104403 000000' 011176' MSGNS,BEGIN,BADTYP ;ASCII MESSAGE CALL WITH COMMON HEADER
902 004422' 000261 SEC ; ERROR RETURN ;DRB001
903 004424' 000401 BK 7S ; ;DRB001
904 004426' 5S: ;END ;DRB001
905 004426' 2S: ; ;DRB001
906 004426' 000241 CLC ;NO ERROR ;DRB001
907 004430' 7S: ; ;DRB001
908 004430' 000207 RTS PC ;RETURN ;DRB001
909 004432' MODLUS: ;DRB001
910 ;(* THIS LITTLE ROUTINE COMPUTES R0 MODULUS R1 *) ;DRB001
911 ;(* I.E., THE REMAINDER OF R0/R1, AND RETURNS THE *) ;DRB001
912 ;(* ANSWER IN R0. *) ;DRB001
913 004432' 020001 1S: CMP R0,R1 ;WHILE RO GE R1 DO ;DRB001
914 004434' 002402 BLT 2S ;BEGIN ;DRB001
915 004436' 160100 SUB #1,R0 ; R0 := R0-R1 ;DRB001
916 004440' 000774 BR 1S ;END ;DRB001
917 004442' 2S: ; ;DRB001
918 004442' 000207 RTS PC ;THAT'S ALL ;DRB001
919
920 004444' 012700 000722' CLRRB: MOV #RHUF,R0 ;CLEAR RBUF BUFFER
921 004450' 016701 173456 MOV RBUF$2,R1 ;GET ITS ADDR AND SIZE
922 004454' 005020 CLRCOM: CLM (R0)+ ;CLEAR ANOTHER
923 004456' 005301 DEC R1 ;COUNT ANOTHER
924 004460' 001375 BNE CLRCOM ;BR BACK TILL DONE
925 004462' 000207 RIS PC
926
927 004464' 012701 000001 DROP: MOV #1,R1 ; INITIALIZE DROP PICKER
928 004470' 016700 174202 MOV UNITNO,R0 ; GET THE DRIVE NUMBER
929 004474' 001403 BEQ 2S ; IF DRIVE 0 GO DROP IT
930 004476' 006301 1S: ASL R1 ; POINT TO NEXT DRIVE
931 004500' 005300 DEC R0 ; IS THIS THE ONE ?
932 004502' 001375 BNE 1S ; NO, LOOK AGAIN
933 004504' 040167 174162 2S: BIC R1,DVICE ; DROP THE DRIVE
934 ;*****
(1) ;CONVERT UNITNO TO ASCII AND
(1) 004510' 104420 000000' 000676' OTOAS,BEGIN,UNITNO,ADRI ;STORE AT ADRI
(1) 004516' 011374'
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(1) ;*****
935 004520' 000207 RTS PC ; RETURN
936 ;
937 ;
938 ;
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940 ;
941 ;
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943 ;
944 ;
945 ;
946 ;
947 ;
948 ;
949 ;
950 ;
951 ;
952 ;
953 004522' 005067 000426 ERROR: CLR DLTFLG ;INITIALIZE DLT FLAG
954 004526' 104403 000000' 011360' MSGNS,BEGIN,ASTER ;ASCII MESSAGE CALL WITH COMMON HEADER
955 004534' 005067 174160 CLR HRDERR ;RESET ERROR INDICATOR
956 004540' 032767 100000 174014 BIT #BIT15,S+10 ;
957 004546' 001417 BEQ 14S ;
958 004550' 005267 174124 INC DLTCNT ;
959 004554' 052767 000001 000372 BIS #1,DLTFLG ;SET FLAG FOR DATA LATE
960 004562' 032767 000004 173226 BIT #DLATE,SR1 ;
961 004570' 001006 BNE 14S ;
962 004572' 052767 177777 174120 BIS #-1,HRDERR ;
963 004600' 104403 000000' 011112' MSGNS,BEGIN,DLTERR ;ASCII MESSAGE CALL WITH COMMON HEADER
964 004606' 032777 020000 175164 14S: BIT #BIT13,@RHCS1 ;MSSBUS CONTROL PARITY ERR?
965 004614' 001406 BEQ 5S ;
966 004616' 052767 177777 174074 BIS #-1,HRDERR ;SET HARD ERR
967 004624' 104403 000000' 011064' MSGNS,BEGIN,MCPERR ;ASCII MESSAGE CALL WITH COMMON HEADER
968 004632' 032767 137400 173722 5S: BIT #HRDCS2,S+10 ;ANY ERRORS ?
969 004640' 001402 BEQ 1S ;
970 004642' 004767 000310 JSR PC,CHKCS2 ;GO SEE THE BITS
971 004646' 032767 040000 173710 1S: BIT #BIT14,S+12 ;ANY SET IN THE ERROR REG'S ?
972 004654' 001432 BEQ 4S ;
973 004656' 032767 177777 173702 BIT #HRDLR1,S+14 ;
974 004664' 001402 BEQ 2S ;
975 004666' 004767 000426 JSR PC,CHKERR1 ;
976 004672' 032767 167777 173712 2S: BIT #HRDLR2,S+40 ;ANY ERRORS IN ER2
977 004700' 001406 BEQ 3S ;
978 004702' 052767 177777 174010 BIS #-1,HRDERR ;
979 004710' 104403 000000' 011210' MSGNS,BEGIN,ER2ERR ;ASCII MESSAGE CALL WITH COMMON HEADER
980 004716' 032767 140153 173670 3S: BIT #HRDLR3,S+42 ;TEST ER3
981 004724' 001406 BEQ 4S ;
982 004726' 052767 177777 173764 BIS #-1,HRDERR ;SET HARD ERROR INDICATOR
983 004734' 104403 000000' 011214' MSGNS,BEGIN,ER3ERR ;ASCII MESSAGE CALL WITH COMMON HEADER
984 004742' 032767 040000 173612 4S: BIT #BIT14,S+10 ; WCE ?
985 004750' 001406 BEQ 6S ;
986 004752' 052767 000002 000174 BIS #2,DLTFLG ;SET FLAG FOR SUFT TO BE USED IF DLT ALSO OCCURE
987 004760' 104403 000000' 011220' MSGNS,BEGIN,WCEERR ;ASCII MESSAGE CALL WITH COMMON HEADER
988 004766' 032767 040000 173556 6S: BIT #BIT14,S ;TRE ?
989 004774' 001406 BEQ 7S ;
990 004776' 052767 000002 000150 BIS #2,DLTFLG ;SET SOFT FLAG
991 005004' 104403 000000' 011056' MSGNS,BEGIN,TRERR ;ASCII MESSAGE CALL WITH COMMON HEADER
992 005012' 032767 100000 173532 7S: BIT #BIT15,S ;SC ?
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993 005020' 001406 BEQ 8S
994 005022' 052767 000002 000124 HIS #2,DLTFLG ;SET FLAG
995 005030' 104403 000000' 011224' MSGNS,BEGIN,SCERR ;ASCII MESSAGE CALL WITH COMMON HEADER
996 005036' 005767 173520 8s: TST S+10
997 005042' 100405 BMI 10S
998 005044' 016767 174752 173644 MOV RHDB,STORE
999 005052' 005067 174744 CLR RHDB
1000 005056' 005767 173636 10s: TST HRDERR ;ANNY HARD ERROR'S ?
1001 005062' 100414 BMI 12S ;IF TRUE THEN BR
1002 005064' 032767 000001 000062 BIT #1,DLTFLG ;DLT ? #DLATE IN SR1 MUST BE SET IF TRUE
1003 005072' 001404 BEQ 22S
1004 005074' 022767 000003 000052 CMP #3,DLTFLG ;I; (SOFT & DLT NOT COUNTED) ?
1005 005102' 001007 BNE 11S
1006 005104' 22s:
1007 ;*****
(1) 005104' 104406 000000' 001722' SUPERS,BEGIN,TABLE ;
(1) ;*****
1008 005112' 000403 BR 11S ;
1009 005114' 12s:
(1) ;*****
(1) 005114' 104405 000000' 001722' HDRS,BEGIN,TABLE ;
(1) ;*****
1010 005122' 005767 174674 11s: IST RHDB
1011 005126' 001003 BNE 9S
1012 005130' 016767 173562 174664 MOV STORE,RHDB
1013 005136' 004767 001064 9s: JSR PC,NOTRDY
1014 005142' 000261 SEC
1015
1016 005144' 104403 000000' 011360' MSGNS,BEGIN,ASTER ;ASCII MESSAGE CALL WITH COMMON HEADER
1017 005152' 000207 RTS PC
1018
1019 005154' 000000 DLTFLG: _WORD 0 ;DLT FLAG TO BE USED FOR SOFT ERROR
1020 ; & INHIBIT DLT COMPARISONS
1021
1022 005156' 052767 177777 173534 CHKCS2: BIS #-1,HRDERR ;SET HARD ERROR
1023 005164' 104403 000000' 011230' MSGNS,BEGIN,CS2HD ;ASCII MESSAGE CALL WITH COMMON HEADER
1024 005172' 032767 020000 173362 1s: BIT #BIT13,S+10 ;DUPE ?
1025 005200' 001403 BEQ 2S
1026 005202' 104403 000000' 011234' MSGNS,BEGIN,UPER ;ASCII MESSAGE CALL WITH COMMON HEADER
1027 005210' 032767 010000 173344 2s: BIT #BIT12,S+10 ;NED ?
1028 005216' 001403 BEQ 3S
1029 005220' 104403 000000' 011240' MSGNS,BEGIN,NEDER ;ASCII MESSAGE CALL WITH COMMON HEADER
1030 005226' 032767 004600 173326 3s: BIT #BIT11,S+10 ;NXM ?
1031 005234' 001403 BEQ 4S
1032 005236' 104403 000000' 011244' MSGNS,BEGIN,NXM ;ASCII MESSAGE CALL WITH COMMON HEADER
1033 005244' 032767 002000 173310 4s: BIT #BIT10,S+10
1034 005252' 001403 BEQ 5S
1035 005254' 104403 000000' 011250' MSGNS,BEGIN,PGERR ;ASCII MESSAGE CALL WITH COMMON HEADER
1036 005262' 032767 001000 173272 5s: BIT #BIT9,S+10 ;MXF ?
1037 005270' 001403 BEQ 6S
1038 005272' 104403 000000' 011254' MSGNS,BEGIN,MXPER ;ASCII MESSAGE CALL WITH COMMON HEADER
1039
1040 005300' 032767 000400 173254 6s: BIT #BIT8,S+10
1041 005306' 001403 BEQ 7S
1042 005310' 104403 000000' 011260' MSGNS,BEGIN,MDPER ;ASCII MESSAGE CALL WITH COMMON HEADER
1043 005316' 000207 RIS PC

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1044
1045 005320' 011264' CHKER1: MSGNS,BEGIN,ERRHD ;ASCII MESSAGE CALL WITH COMMON HEADER
(1) 005320' 104403 000000' 011264' BIT #BIT15,S+14 ;DATA CHECK ERROR ?
1046 005326' 032767 100000 173232 BEQ 1S
1047 005334' 001406 BMI 1S ;SET SOFT ERR
1048 005336' 052767 000002 177610 HIS #2,DLTFLG ;SET SOFT ERR
1049 005344' 104403 000000' 011270' MSGNS,BEGIN,DCRER ;ASCII MESSAGE CALL WITH COMMON HEADER
1050 005352' 032767 000020 173206 1s: BIT #BIT4,S+14 ;FORMAT ERROR ?
1051 005360' 001423 BEQ 2S
1052 005362' 104403 000000' 011274' MSGNS,BEGIN,FERER ;ASCII MESSAGE CALL WITH COMMON HEADER
1053 005370' 016767 173172 173320 MOV S+14,STORE ;SAVE ER1
1054 005376' 042767 077757 173312 BIC #77757,STORE
1055 005404' 022767 100020 173304 CMP #BIT15 ! BIT4,STORE
1056 005412' 001003 BNE 21S
1057 005414' 052767 177777 173276 BIS #-1,HRDERR ;SET HARD ERROR INDICATOR
1058 005422' 052767 000002 177524 21s: BIS #2,DLTFLG ;SET SOFT ERR
1059
1060 005430' 032767 000400 173130 2s: BIT #BIT8,S+14 ;HCRC ?
1061 005436' 001406 BEQ 3S
1062 005440' 052767 000002 177506 BIS #2,DLTFLG ;SET SOFT ERR
1063 005446' 104403 000000' 011300' MSGNS,BEGIN,HMC ;ASCII MESSAGE CALL WITH COMMON HEADER
1064 005454' 032767 000200 173104 3s: BIT #BIT7,S+14 ;HEADER COMP ERR ?
1065 005462' 001420 BEQ 5S
1066 005464' 104403 000000' 011304' MSGNS,BEGIN,HCE ;ASCII MESSAGE CALL WITH COMMON HEADER
1067 005472' 016767 173070 173216 MOV S+14,STORE
1068 005500' 042767 177177 173210 BIC #177177,STORE ;SAVE ONLY HCRC & HCE
1069 005506' 022767 000600 173202 CMP #600,STORE
1070 005514' 001403 BEQ 5S ;SOFT ERROR IF BOTH SET
1071 005516' 052767 177777 173174 BIS #-1,HRDERR ;SET HRD ERR
1072 005524' 032767 077157 173034 5s: BIT #77157,S+14 ;ONLY HARD'S LEFT
1073 005532' 001001 BNE 6S
1074 005534' 000207 RTS PC
1075
1076 005536' 052767 177777 173154 6s: BIS #-1,HRDERR ;SET HRD ERR
1077 005544' 032767 040000 173014 BIT #BIT14,S+14 ;UNS ?
1078 005552' 001403 BEQ 7S
1079 005554' 104403 000000' 011310' MSGNS,BEGIN,UNSER ;ASCII MESSAGE CALL WITH COMMON HEADER
1080 005562' 032767 020000 172776 7s: BIT #BIT13,S+14 ;DPI ?
1081 005570' 001403 BEQ 8S
1082 005572' 104403 000000' 011314' MSGNS,BEGIN,OP1ER ;ASCII MESSAGE CALL WITH COMMON HEADER
1083 005600' 032767 010000 172760 8s: BIT #BIT12,S+14 ;DTE ?
1084 005606' 001403 BEQ 9S
1085 005610' 104403 000000' 011320' MSGNS,BEGIN,DTER ;ASCII MESSAGE CALL WITH COMMON HEADER
1086 005616' 032767 004000 172742 9s: BIT #BIT11,S+14 ;MLE ?
1087 005624' 001403 BEQ 10S
1088 005626' 104403 000000' 011324' MSGNS,BEGIN,WLER ;ASCII MESSAGE CALL WITH COMMON HEADER
1089 005634' 032767 002000 172724 10s: BIT #BIT10,S+14 ;IAE ?
1090 005642' 001403 BEQ 20S
1091 005644' 104403 000000' 011330' MSGNS,BEGIN,IAER ;ASCII MESSAGE CALL WITH COMMON HEADER
1092 005652' 032767 001000 172706 20s: BIT #BIT9,S+14 ;
1093 005660' 001403 BEQ 17S ;
1094 005662' 104403 000000' 011364' MSGNS,BEGIN,AOE ;ASCII MESSAGE CALL WITH COMMON HEADER
1095 005670' 032767 000040 172670 17s: BIT #BIT9,S+14 ;
1096 005676' 001403 BEQ 11S ;
1097 005700' 104403 000000' 011370' MSGNS,BEGIN,WCF ;ASCII MESSAGE CALL WITH COMMON HEADER
1098 005706' 032767 000100 172652 11s: BIT #BIT6,S+14 ;ECH ?

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1099 005714' 001405          BEQ      12$
1100 005716' 104403          MSGNS,BEGIN,ECHR      ;ASCII MESSAGE CALL WITH COMMON HEADER
1101 005724' 004767          JSR      PC,BAD
1102 005730' 032767          BIT      #BIT3,S+14      ;PAR ?
1103 005736' 001403          BEQ      13$
1104 005740' 104403          MSGNS,BEGIN,PARK      ;ASCII MESSAGE CALL WITH COMMON HEADER
1105 005746' 032767          BIT      #BIT2,S+14      ;RMR ?
1106 005754' 001403          BEQ      14$
1107 005756' 104403          MSGNS,BEGIN,RMR      ;ASCII MESSAGE CALL WITH COMMON HEADER
1108 005764' 032767          BIT      #BIT1,S+14      ;LIR ?
1109 005772' 001403          BEQ      15$
1110 005774' 104403          MSGNS,BEGIN,ILHR      ;ASCII MESSAGE CALL WITH COMMON HEADER
1111 006002' 032767          BIT      #BIT0,S+14      ;ILF ?
1112 006010' 001403          BEQ      16$
1113 006012' 104403          MSGNS,BEGIN,ILFR      ;ASCII MESSAGE CALL WITH COMMON HEADER
1114 006020' 000207          RTS      PC
1115
1116 ; R E V D END OF UPDATE
1117
1118 ;////////////////////////////////////
1119 ;////////////////////////////////////
1120 ;////////////////////////////////////
1121 ;////////////////////////////////////
1122 ;////////////////////////////////////
1123
1127 006022'          ERRORS:
1128 006022' 005777 173752      TST      @RHCS1          ;IF NO ERROR THEN          ;DRB001
1129 006026' 100402          BMI      1$            ;BEGIN                      ;DRB001
1130 006030' 000241          CLC
1131 006032' 000207          RTS      PC            ; SET NO ERROR              ;DRB001
1132
1133 006034'          1$:
1134 006034' 000261          SEC
1135 006036' 005000          CLR      R0            ;SET C BIT TO INDICATE ERROR ;DRB001
1136 006040' 016701 173734      MOV      RHCS1,R1       ;INIT INDEX
1137 006044'          3$:
1138
1139 006044' 012160 000552'      MOV      (R1)+,(R0)     ; BEGIN JF REGISTER POINTERS ;DRB001
1140 006050' 005720          TST      (R0)+         ; REPEAT                    ;DRB001
1141 006052' 022760 177777 000552' CMP      #-1,(R0)       ; BEGIN                     ;DRB001
1142 006060' 001401          BEQ      33$           ; MOV REGISTER TO TEMPORARY ;DRB001
1143 006062' 000770          BR       33$           ; ADVANCE POINTER          ;DRB001
1144 006064' 004767 000470      JSR      PC,ERSUH1     ; END ? REV D              ;DRB001
1145
1146 ;YES END JF TABLE          ;OJMO1
1147 ; NO GO DO IT AGAIN        ;OJMO1
1148 ; GET SOME TRIVIAL INFO    ;DRB001
1149
1148 006070' 000167 176426      JMP      ERUK          ; REV. DO ;MCP
1149
1150 006074' 032767 000100 171714 BAD: BIT      #HDSPT,SR1      ; IF OPERATOR WANTS TYPEOUT THEN ;DRB001
1151 006102' 001434          BEQ      12$
1152 006104'          11$:
1153 006104' 116767 172450 172540 MOV      S+6,BADSEC     ; SET CURRENT SECTOR        ;DRB001
1154 006112' 116767 172443 172534 MOV      S+7,BADTRK     ; SET CURRENT TRACK         ;DRB001
1155 006120' 016767 172464 172522 MOV      S+8,BADCYL     ; SET CURRENT CYLINDER      ;DRB001
1156
1157 ;*****
1158 ;CONVERT UNITNO TO ASCII AND
1159
1160 006126' 104420 000000' 000676' OTOAS,BEGIN,UNITNO,ADRI ;STORE AT ADRI
1161 006134' 011374'
1162 ;*****
1163 ;CONVERT BADCYL TO ASCII AND
1164 ;STORE AT CYLNO
1165 006136' 104420 000000' 000650' OTOAS,BEGIN,BADCYL,CYLNO
1166 006144' 007472'
1167 ;*****
1168 ;CONVERT BADTRK TO ASCII AND
1169 ;STORE AT TRKNO
1170 006146' 104420 000000' 000654' OTOAS,BEGIN,BADTRK,TRKNO
1171 006154' 007510'
1172 ;*****
1173 ;CONVERT BADSEC TO ASCII AND
1174 ;STORE AT SECNO
1175 006156' 104420 000000' 000652' OTOAS,BEGIN,BADSEC,SECNO
1176 006164' 007526'
1177 ;*****
1178 MSGNS,BEGIN,BADMES      ;ASCII MESSAGE CALL WITH COMMON HEADER
1179 006174' 026727 172436 000552' 12$:
1180 006202' 002010          CMP      NXTBBK,#ENDBBK ; IF MORE SPACE IN TABLE THEN ;DRB001
1181 006204' 016700 172426      BGE      13$           ; BEGIN                     ;DRB001
1182 006210' 016720 172434      MOV      NXTBBK,R0      ; NO POINTS TO NEXT FREE SLOT IN TABLE;DRB001
1183 006214' 016720 172340      MOV      BADCYL,(R0)+   ; STUFF CYLINDER NUMBER     ;DRB001
1184 006220' 010067 172412      MOV      S+6,(R0)+     ; STUFF REST OF DISK ADDRESS ;DRB001
1185 006224' 000207          MOV      R0,NXTBBK     ; UPDATE POINTER           ;DRB001
1186 006224' 000207          RTS      PC            ; END                       ;DRB001
1187
1188
1189
1190 006226' 012767 077777 172456 NJFRDY: MOV      #77777,CLK      ; SET THE TIMER
1191 006234'          4$:
1192 006234' 032777 004000 173536      BIT      #BIT11,@RHCS1 ;DO I HAVE THE DRIVE DVA?
1193 006242' 001406          BEQ      6$            ;NO
1194 006244' 010046          MOV      R0,-(SP)
1195 006246' 012600          MOV      (SP)+,R0      ;JUST WASTE A LITTLE TIME
1196 006250' 032777 004000 173522      BIT      #BIT11,@RHCS1 ;STILL GOT IT?
1197 006256' 001017          BNE      2$
1198 006260'          6$:
1199 006260' 104407 000000'          BREAKS,BEGIN          ;TEMPORARY RETURN TO MONITOR....
1200 006264' 104407 000000'          BREAKS,BEGIN          ;THEN CONTINUE AT NEXT INSTRUCTION.
1201 006270' 104407 000000'          BREAKS,BEGIN          ;TEMPORARY RETURN TO MONITOR....
1202 006274' 104407 000000'          BREAKS,BEGIN          ;THEN CONTINUE AT NEXT INSTRUCTION.
1203 006300' 005367 172406      DEC      CLK            ;COUNT # OF TRIES
1204 006304' 001353          BNE      4$           ;NOT DONE YET
1205 006306' 104403 000000' 011146' MSGNS,BEGIN,NOT ;ASCII MESSAGE CALL WITH COMMON HEADER
1206 006314' 000426          BR       7$
1207 006316' 004567 174350      JSR      RS,CLEAR      ;COULD NOT GET DRIVE
1208 ;SET THE CONTROLLER AND DRIVE

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(1) ;STORE AT ADRI
(1) 006126' 104420 000000' 000676' OTOAS,BEGIN,UNITNO,ADRI
(1) 006134' 011374'
;*****
;CONVERT BADCYL TO ASCII AND
;STORE AT CYLNO
(1) 006136' 104420 000000' 000650' OTOAS,BEGIN,BADCYL,CYLNO
(1) 006144' 007472'
;*****
;CONVERT BADTRK TO ASCII AND
;STORE AT TRKNO
(1) 006146' 104420 000000' 000654' OTOAS,BEGIN,BADTRK,TRKNO
(1) 006154' 007510'
;*****
;CONVERT BADSEC TO ASCII AND
;STORE AT SECNO
(1) 006156' 104420 000000' 000652' OTOAS,BEGIN,BADSEC,SECNO
(1) 006164' 007526'
;*****
MSGNS,BEGIN,BADMES ;ASCII MESSAGE CALL WITH COMMON HEADER
1160 006174' 026727 172436 000552' 12$:
1161 006202' 002010          CMP      NXTBBK,#ENDBBK ; IF MORE SPACE IN TABLE THEN ;DRB001
1162 006204' 016700 172426      BGE      13$           ; BEGIN                     ;DRB001
1163 006210' 016720 172434      MOV      NXTBBK,R0      ; NO POINTS TO NEXT FREE SLOT IN TABLE;DRB001
1164 006214' 016720 172340      MOV      BADCYL,(R0)+   ; STUFF CYLINDER NUMBER     ;DRB001
1165 006220' 010067 172412      MOV      S+6,(R0)+     ; STUFF REST OF DISK ADDRESS ;DRB001
1166 006224' 000207          MOV      R0,NXTBBK     ; UPDATE POINTER           ;DRB001
1167 006224' 000207          RTS      PC            ; END                       ;DRB001
1168
1169
1170
1171
1172
1173
1174
1175
1176 006226' 012767 077777 172456 NJFRDY: MOV      #77777,CLK      ; SET THE TIMER
1177 006234'          4$:
1178 006234' 032777 004000 173536      BIT      #BIT11,@RHCS1 ;DO I HAVE THE DRIVE DVA?
1179 006242' 001406          BEQ      6$            ;NO
1180 006244' 010046          MOV      R0,-(SP)
1181 006246' 012600          MOV      (SP)+,R0      ;JUST WASTE A LITTLE TIME
1182 006250' 032777 004000 173522      BIT      #BIT11,@RHCS1 ;STILL GOT IT?
1183 006256' 001017          BNE      2$
1184 006260'          6$:
1185 006260' 104407 000000'          BREAKS,BEGIN          ;TEMPORARY RETURN TO MONITOR....
1186 006264' 104407 000000'          BREAKS,BEGIN          ;THEN CONTINUE AT NEXT INSTRUCTION.
1187 006270' 104407 000000'          BREAKS,BEGIN          ;TEMPORARY RETURN TO MONITOR....
1188 006274' 104407 000000'          BREAKS,BEGIN          ;THEN CONTINUE AT NEXT INSTRUCTION.
1189 006300' 005367 172406      DEC      CLK            ;COUNT # OF TRIES
1190 006304' 001353          BNE      4$           ;NOT DONE YET
1191 006306' 104403 000000' 011146' MSGNS,BEGIN,NOT ;ASCII MESSAGE CALL WITH COMMON HEADER
1192 006314' 000426          BR       7$
1193 006316' 004567 174350      JSR      RS,CLEAR      ;COULD NOT GET DRIVE
1194 ;SET THE CONTROLLER AND DRIVE

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1191 006322' 004567 000074 JSR R5,READY ; IS DRIVE READY ?
1192 006326' 000434 BR 15 ; YES, CONTINUE
1193 006330' 004767 000224 5S: JSR PC,EKRSUB1 ; LOAD ERROR INFORMATION
1194 006334' 005000 CLR R0 ;MOVE DRIVE REG INTO TABLE
1195 006336' 016701 173436 MOV RHCS1,R1
1196 006342' 012160 000552' 22S: MOV (R1)+,S(R0)
1197 006346' 005720 TST (R0)+
1198 006350' 022700 000046 CMP #46,R0
1199 006354' 001372 BNE 22S
1200 006356' 012767 000006 171522 MOV #6,ERRTYP ;DRIVE NOT READY
1201 ;*****
(1) 006364' 104405 000000' 001722' HRDERS,BEGIN,TAHLE ; DRIVE NOT READY
(1) ;*****
1202 ;***-1 ;DRB001
1203 006372' 004767 174246 7S: JSR PC,RELEASE ;RELEASE THE DRIVE ;DRB001
1204 006376' 004767 176062 JSR PC,DROP ; NU, DROP THE DRIVE
1205 006402' 104403 000000' 011164' MSGNS,BEGIN,DRP ;ASCII MESSAGE CALL WITH COMMON HEADER
1206 006410' 016706 171416 MOV SPOINT,R6
1207 006414' 000167 173636 JMP LOOP2
1208 006420' 000207 1S: RTS PC ;RETURN
1209 ;
1210 -----
1211
1212 006422' 016777 172250 173360 READY: MOV UNITNO,@RHCS2 ; LOAD UNIT ADDRESS
1213 006430' 017700 173344 MOV @RHCS1,R0
1214 006434' 032700 004000 BIT #BIT11,R0
1215 006440' 001433 BEQ 1S
1216 006442' 017700 173344 MOV @MPDS,R0 ; SAVE STATUS IN R0
1217 006446' 105700 TSIB R0 ; DRIVE READY ?
1218 006450' 100027 BPL 1S ; NO
1219 006452' 032700 000100 BIT #BIT6,R0 ; VOLUME VALID ?
1220 006456' 001424 BEQ 1S ; NO
1221 006460' 032700 000400 BIT #BIT8,R0 ; DRIVE PRESENT ?
1222 006464' 001421 BEQ 1S ; NO
1223 006466' 032700 004000 BIT #BIT11,R0 ; WRITE LOCKED ?
1224 006472' 001016 BNE 1S ; YES
1225 006474' 032700 010000 BIT #BIT12,R0 ; MEDIUM ON LINE ?
1226 006500' 001413 BEQ 1S ; NO
1227 006502' 032700 040000 BIT #BIT14,R0 ; ANY ERRORS ?
1228 006506' 001010 BNE 1S ; YES
1229 006510' 005700 TST R0 ; ATTENTION SET ?
1230 006512' 100406 BMI 1S ; YES
1231 006514' 032777 004000 173256 BIT #BIT11,@RHCS1 ;DVA SET?
1232 006522' 001402 BEQ 1S ;BR IF NOT
1233 ;
1234 006524' 000261 SEC ; READY ;***-3 ;DRB001
1235 006526' 000401 BR 2S ; RETURN ;DRB001
1236 006530' 000241 1S: CLC ; NOT READY ;DRB001
1237 006532' 000205 2S: RTS R5 ;RETURN ;DRB001
1238 ;
1239 -----
1240
1241 006534' 014167 171346 ERSUB2: MOV -(R1),ASB ; LOAD THE DATA
1242 006540' 010167 171346 MOV R1,SBA08 ; LOAD ADDRESS OF DATA WRITTEN
1243 006544' 014267 171340 MOV -(R2),AWAS ; LOAD THE DATA
1244 006550' 010267 171330 MOV R2,WASADR ; LOAD ADDRESS OF DATA READ

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1245 006554' 005721 TST (R1)+ ; RESET REG. 1
1246 006556' 005722 TST (R2)+ ; RESET REG. 2
1247
1248 006560' 016767 173214 171312 ERSUB1: MOV RHCS1,CSRA ; LOAD ADR OF CURRENT CSR
1249 006566' 017767 173206 171306 MOV @RHCS1,ACSR ; LOAD CONTENTS OF CURRENT CSR
1250 006574' 000207 RTS PC ; RETURN
1251
1252
1253 006576' 017700 173176 REZET: MOV @RHCS1,R0
1254 006602' 032700 004000 BIT #BIT11,R0
1255 006606' 001005 BNE 3S
1256 006610' 104407 000000' BREAKS,BEGIN ;TEMPORARY RETURN TO MONITOR....
(1) 006614' 104407 000000' BREAKS,BEGIN ;THEN CONTINUE AT NEXT INSTRUCTION.
1257 006620' 000766 BR REZET
1258 006622' 012777 000040 173160 3S: MOV #BIT5,@RHCS2 ; ISSUE AN RH11 INIT
1259 006630' 012767 077777 172054 MOV #77777,CLK ; SET THE TIMER
1260 006636' 105777 173136 1S: TSIB @RHCS1 ; CONTROLLER READY ?
1261 006642' 100417 BMI 2S ; YES, CONTINUE
1262 006644' 104407 000000' BREAKS,BEGIN ;TEMPORARY RETURN TO MONITOR....
(1) 006650' 104407 000000' BREAKS,BEGIN ;THEN CONTINUE AT NEXT INSTRUCTION.
1263 006654' 005367 172032 DEC CLK ; WAIT SOME MORE ?
1264 006660' 001366 BNE 1S ; YES
1265 006662' 012767 000003 171216 MOV #3,ERRTYP ;CONTROLLER NOT READY
1266 ;*****
(1) 006670' 104405 000000' 000000 HRDERS,BEGIN,NULL ; CONTROLLER NOT READY
(1) ;*****
1267 006676' 104410 000000' ENDS,BEGIN ;
1268 006702' 017746 173110 2S: MOV @RPAS,-(SP)
1269 006706' 012677 173104 MOV (SP)+,@RPAS
1270 006712' 000207 RTS PC ; RETURN
1271 ;
1272 -----
1273
1274 006714' 016700 171066 SETUP: MOV ADDH,R0 ; GET DEVICE ADDRESS
1275 006720' 010067 173054 MOV R0,RHCS1 ; GENERATE REGISTER ADDRESSES
1276 006724' 062700 000002 ADD #2,R0
1277 006730' 010067 173046 MOV R0,RHWC
1278 006734' 062700 000002 ADD #2,R0
1279 006740' 010067 173040 MOV R0,RHBA
1280 006744' 062700 000002 ADD #2,R0
1281 006750' 010067 173032 MOV R0,RPDA
1282 006754' 062700 000002 ADD #2,R0
1283 006760' 010067 173024 MOV R0,RHCS2
1284 006764' 062700 000002 ADD #2,R0
1285 006770' 010067 173016 MOV R0,RPDS
1286 006774' 062700 000002 ADD #2,R0
1287 007000' 010067 173010 MOV R0,RPFR1
1288 007004' 062700 000002 ADD #2,R0
1289 007010' 010067 173002 MOV R0,RPAS
1290 007014' 062700 000002 ADD #2,R0
1291 007020' 010067 172774 MOV R0,RFLA
1292 007024' 062700 000002 ADD #2,R0
1293 007030' 010067 172766 MOV R0,RHDB
1294 007034' 062700 000002 ADD #2,R0
1295 007040' 010067 172760 MOV R0,RPFR
1296 007044' 062700 000002 ADD #2,R0

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1297 007050' 010067 172752      MOV      R0,RPDT
1298 007054' 062700 000002      ADD      #2,R0
1299 007060' 010067 172744      MOV      R0,RPSN
1300 007064' 062700 000002      ADD      #2,R0
1301 007070' 010067 172736      MOV      R0,RPOF
1302 007074' 062700 000002      ADD      #2,R0
1303 007100' 010067 172730      MOV      R0,RPUC
1304 007104' 062700 000002      ADD      #2,R0
1305 007110' 010067 172722      MOV      R0,RPCC
1306 007114' 062700 000002      ADD      #2,R0
1307 007120' 010067 172714      MOV      R0,RPER2
1308 007124' 062700 000002      ADD      #2,R0
1309 007130' 010067 172706      MOV      R0,RPER3
1310 007134' 062700 000002      ADD      #2,R0
1311 007140' 010067 172700      MOV      R0,RPEC1
1312 007144' 062700 000002      ADD      #2,R0
1313 007150' 010067 172672      MOV      R0,RPEC2
1314
1315 007154' 032767 001000 170674  BIT      #ADDR22,RES1 ;DO WE HAVE 22-BIT ADDR SUPPORT? ;DRB001
1316 007162' 001415 000000 170674  SEQ      15 ;NO ;DRB001
1317 007164' 032767 000200 170624  BIT      #RH70,SR1 ;IS THIS AN RH70 ;DRB001
1318 007172' 001411 000000 170624  SEQ      15 ;NO ;DRB001
1319 007174' 062700 000002      ADD      #2,R0
1320 007200' 010067 172644      MOV      R0,RHBAE
1321 007204' 062700 000002      ADD      #2,R0
1322 007210' 010067 172636      MOV      R0,RHCS3
1323
1324 007214' 000403 000000 170632  BR       25 ;DRB001
1325 007216' 042767 001000 170632 18: BIC      #ADDR22,RES1 ; CAN'T USE 22 BIT ADDRESSING ;DRB001
1326 007224' 016700 170580 28: MOV      VECTOR,R0 ; GET VECTOR ADDRESS
1327 007230' 012720 003104' MOV      #NTRUPT,(R0)+ ; SET POINTER JUST IN CASE
1328 007234' 116710 170552 MOVVB   BRL,(R0) ; SET PRIORITY
1329 007240' 000207 RIS      PC ; RETURN
1330

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1332      .SBTTL  MODULE MESSAGES
1333
1334 007242' 042440 051122 051117 MES1:  .ASCIZ  ' ERRORS' ;DRB001
007250' 000045 000000 000000 ;DRB001
1335 007252' 050040 051101 052111 MES2:  .ASCIZ  ' PARITY' ;DRB001
007260' 000131 000000 000000
1336 007262' 042040 044522 042526 MES3:  .ASCIZ  ' DRIVE ' ;DRB001
007270' 000040 000000 000000
1337 007272' 046440 051501 041123 MES4:  .ASCIZ  ' MASSBUS' ;DRB001
007300' 051525 000000 000000
1338 007303' 040 040504 040524 MES5:  .ASCIZ  ' DATA' ;DRB001
007310' 000 000000 000000
1339 007311' 040 042522 051124 MES6:  .ASCIZ  ' RETRY EXCEEDED%' ;DRB001
007316' 020131 054105 042503
007324' 042105 000045 000045
1340 007332' 046040 052101 000105 MES7:  .ASCIZ  ' LATE' ;DRB001
1341 007340' 047516 020124 042522 MES8:  .ASCIZ  ' NOT READY%' ;DRB001
007346' 042101 022531 000000
1342 007353' 040 047503 046125 MES9:  .ASCIZ  ' COULD NOT GET' ;DRB001
007360' 020104 047516 020124
007366' 042507 000124
1343 007372' 042040 047522 050120 MES10: .ASCIZ  ' DROPPED' ;DRB001
007400' 042105 000000 000000
1344 007403' 072 047040 052117 MES11: .ASCIZ  ': NOT RP04/5/6%' ;DRB001
007410' 051040 030120 027464
007416' 027465 022466 000000
1345 007423' 040 051124 047101 MES12: .ASCIZ  ' TRANSFER' ;DRB001
007430' 043123 051105 000000
1346 007435' 045 000000 MES13: .ASCIZ  '%'; ;DRB001
1347 007437' 072 041040 042101 MES15: .ASCIZ  ': BADSPOT AT (OCTAL) CYL: ' ;DRB001
007444' 050123 052117 040440
007452' 020124 047450 052103
007460' 046101 020051 054503
007466' 035114 000040
1348 007472' 000002 CYLNO:  .BLKB  2 ;DRB001
1349 007474' 020040 020040 020054 MES16: .ASCIZ  ', TRK: ' ;DRB001
007502' 051124 035113 000040
1350 007510' 000004 TRKNO:  .BLKB  4 ;DRB001
1351 007514' 020040 020054 042523 MES17: .ASCIZ  ', SEC: ' ;DRB001
007522' 035103 000040
1352 007526' 000004 SECNO:  .BLKB  4 ;DRB001
1353 007532' 020040 000045 M&S18: .ASCIZ  '%'; ;DRB001
1354 007536' 052440 045516 047516 MES19: .ASCIZ  ' UNKNOWN' ;DRB001
007544' 047127 000000
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1367 007547' 110 051101 020104 MES20: .ASCIZ  'HARD ERROR(S) SET IN RPER2 %'
007554' 051105 047522 024122
007562' 024523 051440 052105

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007570'	044440	020116	050122				
007576'	051105	020062	022440				
007604'	000						
1368	007605'	110	051101	020104	MES21:	.ASCIZ	'HARD ERROR(S) SET IN RPER3 '
	007612'	051105	047522	024122			
	007620'	024523	051440	052105			
	007626'	044440	020116	050122			
	007634'	051105	020063	022440			
	007642'	000040					
1369	007644'	051127	052111	020105	MES22:	.ASCIZ	'WRITE CHECK ERROR '
	007652'	044103	041505	020113			
	007660'	051105	047522	020122			
	007666'	022440	000040				
1370	007672'	041523	051440	052105	MES23:	.ASCIZ	'SC SET '
	007700'	020040	000045				
1371	007704'	044124	020105	047506	MES24:	.ASCIZ	'THE FOLLOWING ERROR BITS ARE SET IN RPCS2 '
	007712'	046114	053517	047111			
	007720'	020107	051105	047522			
	007726'	020122	044502	051524			
	007734'	040440	042522	051440			
	007742'	052105	044440	020116			
	007750'	050122	051503	020062			
	007756'	020045	020045	000			
1372	007763'	125	044516	052502	MES25:	.ASCIZ	'UNIHUS PARITY ERROR '
	007770'	020123	040520	044522			
	007776'	054524	042440	051122			
	010004'	051117	022440	000040			
1373	010012'	047516	020116	054105	MES26:	.ASCIZ	'NON EXISTANT DRIVE '
	010020'	051511	040524	052116			
	010026'	042040	044522	042526			
	010034'	022440	000040				
1374	010040'	054116	020115	022440	MES27:	.ASCIZ	'NXM '
	010046'	000					
1375	010047'	120	047522	051107	MES28:	.ASCIZ	'PROGRAM ERROR PGE '
	010054'	046501	042440	051122			
	010062'	051117	020040	043520			
	010070'	020105	022440	000040			
1376	010076'	044515	051523	042105	MES29:	.ASCIZ	'MISSED TRANSFER '
	010104'	052040	040522	051516			
	010112'	042506	020122	022440			
	010120'	020040	000				
1377	010123'	115	051501	041123	MES30:	.ASCIZ	'MASSBUS DATA LINE PARITY ERROR '
	010130'	051525	042040	052101			
	010136'	020101	044514	042516			
	010144'	050040	051101	052111			
	010152'	020131	051105	047522			
	010160'	020122	022440	000040			
1378	010166'	044124	020105	047506	MES31:	.ASCIZ	'THE FOLLOWING BITS ARE SET IN RPER1 '
	010174'	046114	053517	047111			
	010202'	020107	044502	051524			
	010210'	040440	042522	051440			
	010216'	052105	044440	020116			
	010224'	050122	051105	020061			
	010232'	022440	022440	000040			
1379	010240'	040504	040524	041440	MES32:	.ASCIZ	'DATA CHECK ERROR '
	010246'	042510	045503	042440			

010254'	051122	051117	020040				
1380	010262'	020045	000		MES33:	.ASCIZ	'FORMAT ERROR '
	010265'	106	051117	040515			
	010272'	020124	051105	047522			
	010300'	020122	022440	020040			
	010306'	000					
1381	010307'	040	042510	042101	MES34:	.ASCIZ	'HEADER CRC ERROR '
	010314'	051105	041440	041522			
	010322'	042440	051122	051117			
	010330'	022440	000040				
1382	010334'	042510	042101	051105	MES35:	.ASCIZ	'HEADER COMPARE ERROR '
	010342'	041440	046517	040520			
	010350'	042522	042440	051122			
	010356'	051117	020040	020045			
	010364'	000					
1383	010365'	104	044522	042526	MES36:	.ASCIZ	'DRIVE UNSAFE '
	010372'	052440	051516	043101			
	010400'	020105	022440	000040			
1384	010406'	050117	051105	052101	MES37:	.ASCIZ	'OPERATION INCOMPLETE '
	010414'	047511	020116	047111			
	010422'	047503	050115	042514			
	010430'	042524	022440	000040			
1385	010436'	051104	053111	020105	MES38:	.ASCIZ	'DRIVE TIMING ERROR '
	010444'	044524	044515	043516			
	010452'	042440	051122	051117			
	010460'	022440	000040				
1386	010464'	051127	052111	020105	MES39:	.ASCIZ	'WRITE LOCK ERROR '
	010472'	047514	045503	042440			
	010500'	051122	051117	022440			
	010506'	000040					
1387	010510'	047111	040526	044514	MES40:	.ASCIZ	'INVALID ADDRESS ERROR '
	010516'	020104	042101	051104			
	010524'	051505	020123	051105			
	010532'	047522	020122	020045			
	010540'	000					
1388	010541'	105	041503	044040	MES41:	.ASCIZ	'ECC HARD ERROR '
	010546'	051101	020104	051105			
	010554'	047522	020122	020045			
	010562'	000					
1389	010563'	120	051101	052111	MES42:	.ASCIZ	'PARITY ERROR '
	010570'	020131	051105	047522			
	010576'	020122	020045	000			
1390	010603'	122	043505	051511	MES43:	.ASCIZ	'REGISTER MODIFICATION REFUSED '
	010610'	042524	020122	047515			
	010616'	044504	044506	040503			
	010624'	044524	047117	051040			
	010632'	043105	051525	042105			
	010640'	020040	020045	000			
1391	010645'	111	046114	043505	MES44:	.ASCIZ	'ILLEGAL REGISTER '
	010652'	046101	051040	043505			
	010660'	051511	042524	020122			
	010666'	020045	000				
1392	010671'	111	046114	043505	MES45:	.ASCIZ	'ILLEGAL FUNCTION '
	010676'	046101	043040	047125			
	010704'	052103	047511	020116			
	010712'	020045	000				

1393	010715'	040	022445	020045	MES46:	.ASCIZ	'*****'
	010722'	020040	025052	025052			
	010730'	025052	025052	025052			
	010736'	025052	025052	025052			
	010744'	025052	025052	025052			
	010752'	025052	025052	025052			
	010760'	025052	025052	025052			
	010766'	025052	022440	020045			
	010774'	000					
1394	010775'	101	042104	042522	MES47:	.ASCIZ	'ADDRESS OVERFLOW ERROR '
	011002'	051523	047440	042526			
	011010'	043122	047514	020127			
	011016'	051105	047522	020122			
	011024'	020045	000				
1395	011027'	127	044522	042524	MES48:	.ASCIZ	'WRITE LOCK FAILURE '
	011034'	046040	041517	020113			
	011042'	040506	046111	051125			
	011050'	020105	020045	000			

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J R E V D END OF UPDATE

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1407	011056'		.EVEN				JDRB001
1408							JDRB001
1409	011056'	007423'	TRERR:	MES12			JDRB001
1410	011060'	007242'		MES1			JDRB001
1411	011062'	177777		177777			JDRB001
1412							JDRB001
1413	011064'	007272'	MCPERR:	MES4			JDRB001
1414	011066'	007252'		MES2			JDRB001
1415	011070'	007242'		MES1			JDRB001
1416	011072'	177777		177777			JDRB001
1417							JDRB001
1418	011074'	007272'	MDPERR:	MES4			JDRB001
1419	011076'	007303'		MES5			JDRB001
1420	011100'	007252'		MES2			JDRB001
1421	011102'	007242'		MES1			JDRB001
1422	011104'	177777		177777			JDRB001
1423							JDRB001
1424	011106'	007311'	EXCED:	MES6			JDRB001
1425	011110'	177777		177777			JDRB001
1426							JDRB001
1427	011112'	007303'	DLTERR:	MES5			JDRB001
1428	011114'	007332'		MES7			JDRB001
1429	011116'	007242'		MES1			JDRB001
1430	011120'	177777		177777			JDRB001
1431							JDRB001
1432	011122'	007262'	BADMES:	MES3			JDRB001
1433	011124'	011401'		NUM6			JDRB001
1434	011126'	007437'		MES15			JDRB001
1435	011130'	007474'		MES16			JDRB001
1436	011132'	007514'		MES17			JDRB001

1437	011134'	007532'		MES18			JDRB001
1438	011136'	177777		177777			JDRB001
1439							JDRB001
1440	011140'	007536'	UNKNWN:	MES19			JDRB001
1441	011142'	007242'		MES1			JDRB001
1442	011144'	177777		177777			JDRB001
1443							JDRB001
1444	011146'	007262'	NOT:	MES3			JDRB001
1445	011150'	007340'		MES6			JDRB001
1446	011152'	177777		177777			JDRB001
1447							JDRB001
1448	011154'	007353'	TOUT:	MES9			JDRB001
1449	011156'	007262'		MES3			JDRB001
1450	011160'	007435'		MES13			JDRB001
1451	011162'	177777		177777			JDRB001
1452							JDRB001
1453	011164'	007262'	DRP:	MES3			JDRB001
1454	011166'	011401'		NUM6			JDRB001
1455	011170'	007372'		MES10			JDRB001
1456	011172'	007435'		MES13			JDRB001
1457	011174'	177777		177777			JDRB001
1458							JDRB001
1459	011176'	007262'	BADTYP:	MES3			JDRB001
1460	011200'	011401'		NUM6			JDRB001
1461	011202'	007372'		MES10			JDRB001
1462	011204'	007403'		MES11			JDRB001
1463	011206'	177777		177777			JDRB001
1464							JDRB001
1465							JDRB001
1469							JDRB001
1470							JDRB001
1471							JDRB001
1472							JDRB001
1473							JDRB001
1474	011210'	007547'	ER2ERR:	MES20			JDRB001
1475	011212'	177777		177777			JDRB001
1476							JDRB001
1477	011214'	007605'	ER3ERR:	MES21			JDRB001
1478	011216'	177777		177777			JDRB001
1479							JDRB001
1480	011220'	007644'	WCEERR:	MES22			JDRB001
1481	011222'	177777		177777			JDRB001
1482							JDRB001
1483	011224'	007672'	SCEERR:	MES23			JDRB001
1484	011226'	177777		177777			JDRB001
1485							JDRB001
1486	011230'	007704'	CS2HD:	MES24			JDRB001
1487	011232'	177777		177777			JDRB001
1488							JDRB001
1489	011234'	007763'	UPER:	MES25			JDRB001
1490	011236'	177777		177777			JDRB001
1491							JDRB001
1492	011240'	010012'	NEUER:	MES26			JDRB001
1493	011242'	177777		177777			JDRB001
1494							JDRB001
1495	011244'	010040'	NXM:	MES27			JDRB001

J R E V D UPDATES

1496	011246'	177777	
1497			
1498	011250'	010047'	PGERR: MES28
1499	011252'	177777	177777
1500			
1501	011254'	010076'	MXFER: MES29
1502	011256'	177777	177777
1503			
1504	011260'	010123'	MDPER: MES30
1505	011262'	177777	177777
1506			
1507	011264'	010166'	ER1HD: MES31
1508	011266'	177777	177777
1509			
1510	011270'	010240'	DCKER: MES32
1511	011272'	177777	177777
1512			
1513	011274'	010265'	FERER: MES33
1514	011276'	177777	177777
1515			
1516	011300'	010307'	HCRC: MES34
1517	011302'	177777	177777
1518			
1519	011304'	010344'	HCE: MES35
1520	011306'	177777	177777
1521			
1522	011310'	010365'	UNSER: MES36
1523	011312'	177777	177777
1524			
1525	011314'	010406'	OPIER: MES37
1526	011316'	177777	177777
1527			
1528	011320'	010436'	DTER: MES38
1529	011322'	177777	177777
1530	011324'	010464'	WLER: MES39
1531	011326'	177777	177777
1532	011330'	010510'	IAER: MES40
1533	011332'	177777	177777
1534			
1535	011334'	010541'	ECHR: MES41
1536	011336'	177777	177777
1537			
1538	011340'	010563'	PARR: MES42
1539	011342'	177777	177777
1540			
1541	011344'	010603'	RMRR: MES43
1542	011346'	177777	177777
1543			
1544	011350'	010645'	ILRR: MES44
1545	011352'	177777	177777
1546			
1547	011354'	010671'	ILFR: MES45
1548	011356'	177777	177777
1549	011360'	010715'	ASTER: MES46
1550	011362'	177777	177777
1551			

1552	011364'	010775'	ADE: MES47		REV. DO
1553	011366'	177777	177777		
1554					
1555	011370'	011027'	WCF: MES48		REV. DO
1556	011372'	177777	177777		
1557					
1558					
1559					
1560					
1561					
1562					
1563					
1564					
1565					
1566	011374'	000005	ADRI: .BLKB 5		
1567	011401'	040	NUMB: .ASCIZ / /		
1568		011404'	.EVEN		
1569					
1570					
1571					
1572		000001			

; R E V D END UPDATE

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.END

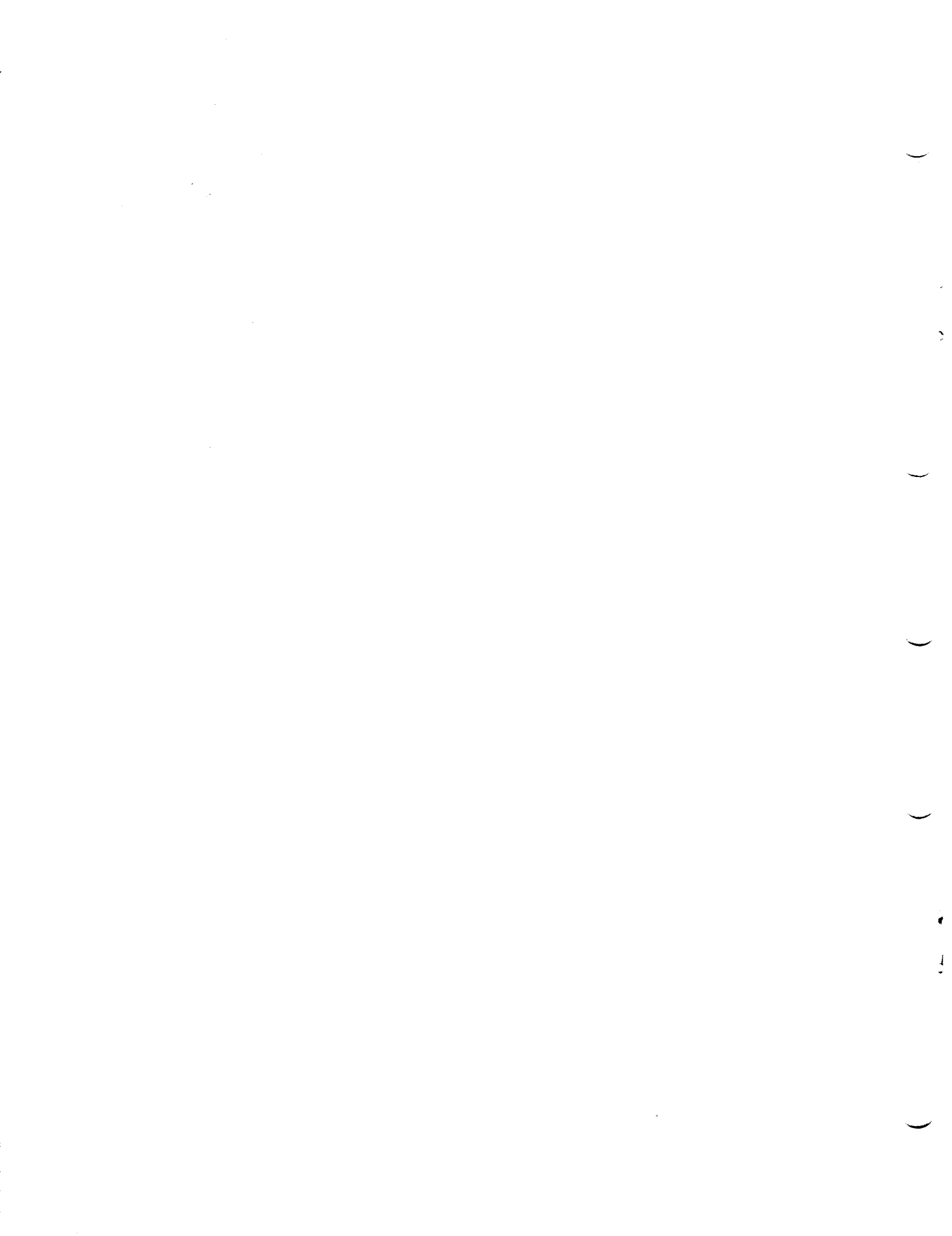
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BKMOD	95#																			
BREAK	185#	596	1184	1185	1256	1262														
BTOD	204#																			
CKDATA	240#	545																		
DATAK	249#																			
DATEKR	138#																			
DFSEVN	272#	370																		
DSEVNT	282#	370																		
END	175#	535	1267																	
ENDIT	166#	536																		
ENDMOD	171#																			
EQUATS	288#	370																		
EXIT	120#	630																		
GETPA	231#	520																		
GWHUFF	219#	523																		
HRDER	128#	1009	1201	1266																
IOMOD	91#																			
IOMODP	115#																			
IOMODR	111#																			
IOMODX	107#	370																		
LINEUP	551#	572	582	592																
MAP22	235#	622																		
MODULE	8#	370																		
MSG	154#																			
MSGN	158#	901	954	963	967	979	983	987	991	995	1016	1023	1026	1029	1032					
	1035	1038	1042	1045	1049	1052	1063	1066	1079	1082	1085	1088	1091	1094	1097					
	1100	1104	1107	1110	1113	1160	1188	1205												
MSGS	162#																			
NBKMOD	103#																			
OTOA	190#	934	1156	1157	1158	1159														
PIRQ	179#	638																		
RAND	124#	705																		
SBKMOD	99#																			
SOFER	144#	1007																		

. ABS. 000000 000
 011404 001

ERRORS DETECTED: 0
 DEFAULT GLOBALS GENERATED: 0

XRPDD0.UBJ,XRPDD0.LST/CRF=DDXCOM.P11,XRPDD0.P11
 RUN-TIME: 6 12 1 SECONDS
 RUN-TIME RATIO: 45/22=2.0
 CORE USED: 8K (15 PAGES)



1

.NLIST SEQ,LD,BIN
.REPT 0

.NLIST TTM

IDENTIFICATION

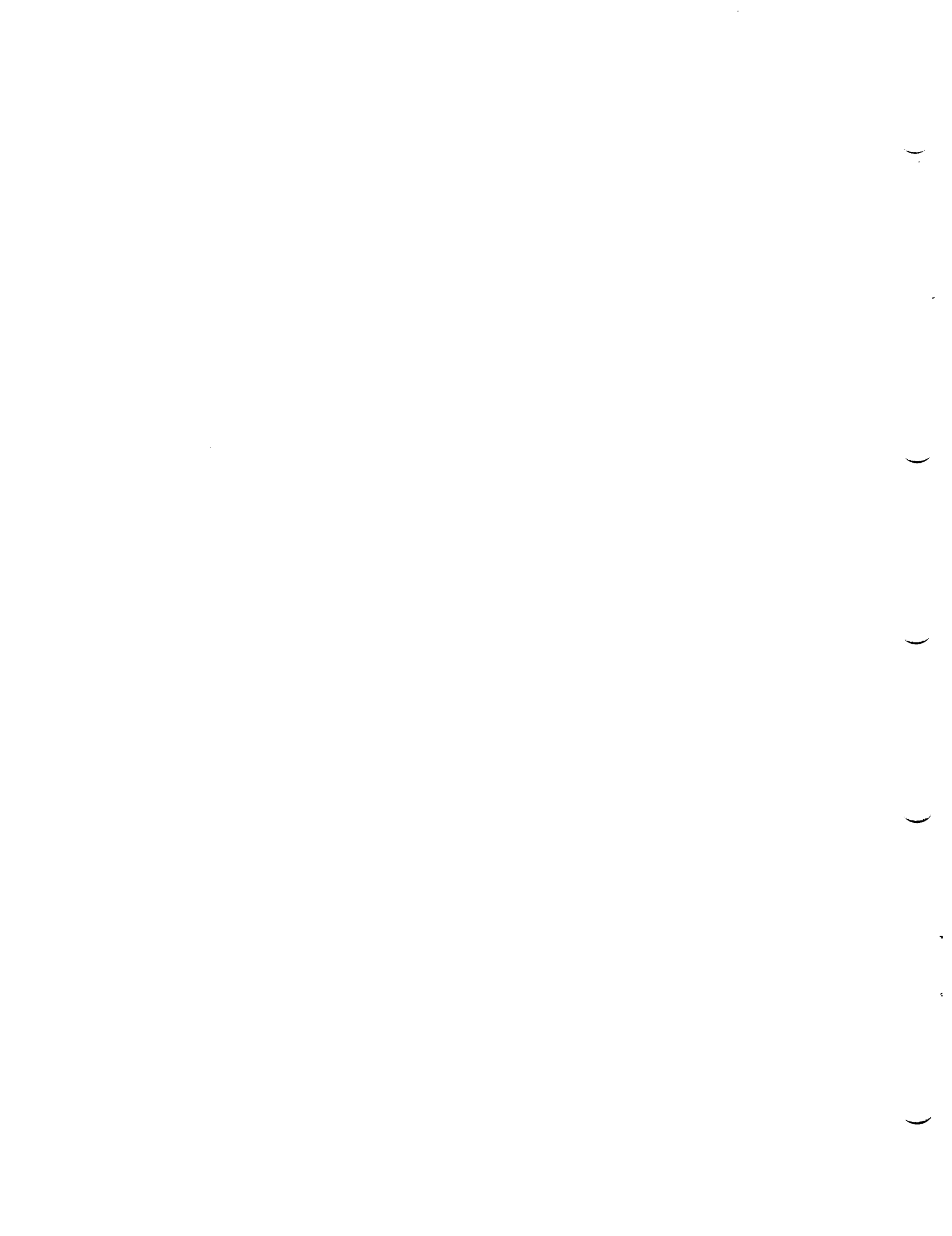
PRODUCT CODE: AC-F098C-MC
PRODUCT NAME: CXRXBC0 RX02 MODULE
PRODUCT DATE: APRIL 1979
MAINTAINER: TAPE DIAGNOSTICS

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1. ABSTRACT

RXB IS AN IOMODX THAT EXERCISES TWO RX02 FLOPPY DISKS ON THE UNIBUS. IT EXERCISES BOTH DRIVES BY WRITING AND READING ALL AVAILABLE DRIVES.

ERRORS ARE CHECKED FOR BUFFER FILL, WRITE, READ, AND DATA COMPARE. TWO RETRIES ARE DONE FOR EACH WRITE OR READ STATUS ERROR. ALL ERRORS ARE REPORTED ON THE CONSOLE TTY.

2. REQUIREMENTS

HARDWARE: 1 OR 2 DISKETTES WITH AN RX02 CONTROLLER

STORAGE:: RXB REQUIRES:
1. DECIMAL WORDS: 1661
2. OCTAL WORDS: 03175
3. OCTAL BYTES: 6372

3. PASS IDENTIFICATION

ONE PASS OF THE RXB MODULE CONSISTS OF 130. WRITE AND READ PASSES ON AVAILABLE DRIVES. THE TEST SEQUENCE WRITES THEN READS EVERY THIRD SECTOR OF EVERY TENTH TRACK STARTING AT TRACK 1 SECTOR 1.

THE ENTIRE DISKETTE IS DONE, EACH PASS OF THE DISKETTE STARTS AS SHOWN:

1. STARTS AT SECTOR #1/TRACK #1
END OF PASS
2. STARTS AT SECTOR #2/TRACK #1
END OF PASS
3. STARTS AT SECTOR #3/TRACK #1
END OF PASS
4. STARTS AT SECTOR #1/TRACK #2
...ETC.

4. EXECUTION TIME

ONE PASS OF RXB RUNNING ALONE ON THE PDP-11/05 AVERAGES .75 MINUTES FOR 2 DRIVES.

5. CONFIGURATION REQUIREMENTS

DEFAULT PARAMETERS: DEVADR:177170
VECTOR:264
BR:5
DEVcnt:2

REQUIRED PARAMETERS: NONE

6. DEVICE/OPTION SETUP

ASSURE ALL DRIVES ARE POWERED UP, DISKETTES INSTALLED,
AND READY.

7. MODULE OPERATION

- A. SETUP DRIVE REGISTER ADDRESSES AND MODULE VARIABLES
- B. SELECT DRIVES FOR TEST - IF NONE AVAILABLE, DROP MODULE
- C. INITIALIZE DRIVES
- D. SET AVAILABLE DRIVES TO SINGLE DENSITY
- E. WRITE AND READ ALL AVAILABLE DRIVES - IF ERROR, REPORT AND
RETRY FAILING FUNCTION UP TO RETRY LIMIT.
- F. DO DATA COMPARE FOR ALL READS - IF ERROR, REPORT
- G. UPDATE TRACK + SECTOR - DO E. + F. UNTIL ALL SECTORS
OF AVAILABLE DRIVES ARE DONE.
- H. CHANGE DENSITY OF AVAILABLE DRIVES
- I. DO E., F. & G. FOR NEW DENSITY
- J. UPDATE STARTING ADDRESS, GO TO D

8. OPERATION OPTIONS

SRI BIT 0 SET(1):
IF RETRY LIMIT IS EXCEEDED ON ANY FUNCTION, REPORT
HARD ERROR AND DROP THE MODULE.

SRI BIT 0 CLEAR(0):
IF RETRY LIMIT IS EXCEEDED, CONTINUE WITH NEXT TEST.

9. NON-STANDARD PRINTOUTS

- A. ALL PRINTOUTS HAVE THE STANDARD FORMAT DESCRIBED IN THE DEC/X11 DOCUMENT.
- B. ERROR MESSAGES DUMP THE CONTENTS OF THE RX02 REGISTERS IN THE FOLLOWING ORDER.

PXCS (COMMAND REGISTER)
RXES (ERROR REGISTER)
FXTA (TRACK ADDRESS)
FXSA (SECTOR ADDRESS)
PXSB (WORD COUNT REG.) / (DEFINITIVE ERROR CODE)->----
RXSB1 (DRV1 TRACK ADR) / (DRV0 TRACK ADR) |
RXSP2 (TARGET SECTOR) / (TARGET TRACK) |
RXSB3 (TRACK ADR SELECTED DRV*) / (MICROCODE STATUS)->-- |

(UPPER BYTE-ODD) / (LOWER BYTE-EVEN) |

* = ONLY MEANINGFUL ON A CODE 150 ERROR, SEEK ERROR. |

MICROCODE STATUS <-----

BIT #7 = UNIT SELECT
BIT #6 = DENSITY DRIVE #1
BIT #5 = HEAD LOAD
BIT #4 = DENSITY DRIVE #0

TABLE OF DEFINITIVE ERROR CODES <-----

KXNDV0= 10	/DRIVE 0 FAILED TO SEE HOME ON INITIALIZE. NO ERROR BIT
KXNDV1= 20	/DRIVE 1 FAILED TO SEE HOME ON INITIALIZE. NO ERROR BIT
KERTRK= 40	/TRIED TO ACCESS A TRACK GREATER THAN 76.
KHOMERR= 50	/HOME WAS FOUND BEFORE DESIRED TRACK WAS REACHED.
KSELFERR= 60	/SELF DIAGNOSTIC ERROR.
KXHDR= 70	/DESIRED SECTOR COULD NOT BE FOUND AFTER LOOKING AT 52 HEADERS.
KWPROT= 100	/WRITE FUNCTION ATTEMPTED ON A WRITE PROTECTED DISK.
KTIMERR=110	/MORE THAN 40 MICROSECONDS AND NO SEPCLOCK SEEN.
KXPRAM=120	/A PREAMBLE COULD NOT BE FOUND.
KXIDAM=130	/PREAMBLE FOUND BUT NO ID MARK FOUND WITHIN ALLOWABLE TIME.
KCHCER=140	/CRC ERROR ON WHAT APPEARED TO BE A HEADER. ERROR IS NOT ASSERTED.
KXSKER=150	/TRACK ADDRESS OF GOOD HEADER DOES NOT COMPARE WITH DESIRED TRACK.
KXSTRYS=160	/TOO MANY TRIES FOR AN IDAM.
KXODAM= 170	/DATA AM NOT FOUND IN ALLOTTED TIME.
KDCRCER=200	/CRC ERROR ON READING THE SECTOR FROM THE DISK.
KMANER= 220	/R/W ELECTRONICS FAILED MAINTENANCE MODE TEST.
KWCNOV= 230	/WORD COUNT OVERFLOW.
KSTDER= 240	/WRONG KEY WORD FOR SET MEDIA DENSITY COMMAND.

C. RETRIES: EACH WRITE OR READ STATUS ERROR IS ACCOMPANIED BY
A RETRY NUMBER:

RETRY 0: IS THE ORIGINAL ERROR
RETRY 1: IS THE FIRST RETRY OF THAT ERROR (SAME ADDRESS)
RETRY 2: IS THE SECOND RETRY OF THAT ERROR (SAME ADDRESS)
NOW DROP THE MODULE IF SR1=1 OR
CONTINUE TO NEXT ADDRESS IF SR1=0

10. DEVICE REGISTERS

CODE	FUNCTION
0	= FILL BUFFER
1	= EMPTY BUFFER
2	= WRITE SECTOR
3	= READ SECTOR
4	= SET DENSITY ** TAKES 15 SECONDS **
5	= READ MAINTENANCE STATUS
6	= WRITE SECTOR WITH DELETED DATA
7	= READ ERROR CODE

!.....
!15 !14 !13 !12 !11 !10 !09 !08 !07 !06 !05 !04 !03 !02 !01 !00 !	
RXCS: !EPR !INT !XM !XM !RX2 !SID !DEN !TR !IE !DON !DRV !FUN !FUN !FUN !GO !	
WC: ! X X X X X X X X !	WORD COUNT
BA: ! BASE ADDRESS	
PXES: ! X ! X ! X ! X ! XM ! WC ! SID ! DRV ! DRV ! DEL ! OSK ! DEN ! AC ! INT ! SID ! CRC !	
! ! ! ! ! ! OVFI !#1 !#1 !RDY !DAT !DEN !ERR !LOW !DON !RDY !	
RXTA: ! X ! X ! X ! X ! X ! X ! X ! X ! X ! 0 !	TRACK ADDRESS [0 -> 76, LEGAL]
RXSA: ! X ! X ! X ! X ! X ! X ! X ! X ! X ! 0 ! 0 ! 0 !	SECTOR ADDRESS [1 -> 26, LEGAL]

.ENDP

```

000000* IOMODX <RXRC >,177170,264,5,0,2,202,137,RBUF,64,,66.
000000* MODULE 150000,RXBC ,177170,264,5,0,2,202,137,RBUF,64,,66.
      ,TITLE RXBC DEC/X11 SYSTEM EXERCISER MODULE
      ,DDXCOM VERSION 6
      ,LIST RIN
;*****
000000* REGINI
000000* 054122 P41502 040 MODNAM1 ,ASCII /RXRC /MODULE NAME.
000000* 000 XFLAG1 ,RYTF OPEN ;USED TO KEEP IPACK OF WBUFF USAGE
000000* 177170 ADDR1 177170+0 ;1ST DEVICE ADDR.
000000* 000264 VECTOP1 264+0 ;1ST DEVICE VECTOR.
000000* 243 BR11 ,RYTE PRTY5+0 ;1ST PR LEVEL.
000000* 000 RR21 ,RYTE PRTY0+0 ;2ND PR LEVEL.
000000* 000003 DVID11 2+1 ;DEVICE INDICATOR 1.
000000* 000000 SR11 OPEN ;SWITCH REGISTER 1
000000* 000000 SR21 OPEN ;SWITCH REGISTER 2
000000* 000000 SR31 OPEN ;SWITCH REGISTER 3
000000* 000000 SR41 OPEN ;SWITCH REGISTER 4
;*****
000000* 150000 STAT1 150000 ;STATUS WORD.
000000* 000376 INIT1 START ;MODULE START ADDR.
000000* 000252 SPOINT1 MODDSP ;MODULE STACK POINTER.
000000* 000000 PASCNT1 0 ;PASS COUNTER.
000000* 000202 ICONT1 202 ;# OF ITERATIONS PER PASS=202
000000* 000000 ICOUNT1 0 ;LOC TO COUNT ITERATIONS
000000* 000000 SOFCNT1 0 ;LOC TO SAVE TOTAL SOFT ERRORS
000000* 000000 HRDCNT1 0 ;LOC TO SAVE TOTAL HARD ERRORS
000000* 000000 SOFPAS1 0 ;LOC TO SAVE SOFT ERRORS PER PASS
000000* 000000 HRDPAS1 0 ;LOC TO SAVE HARD ERRORS PER PASS
000000* 000000 SYSCNT1 0 ;# OF SYS ERRORS ACCUMULATED
000000* 000000 RANNDM1 0 ;HOLDS RANDOM # WHEN RAND MACRO IS CALLED
000000* 000000 CONFIG1 ;RESERVED FOR MONITOR USE
000000* 000000 RES11 0 ;RESERVED FOR MONITOR USE
000000* 000000 RES21 0 ;RESERVED FOR MONITOR USE
000000* 000000 SVR01 OPEN ;LOC TO SAVE R0.
000000* 000000 SVR11 OPEN ;LOC TO SAVE R1.
000000* 000000 SVR21 OPEN ;LOC TO SAVE R2.
000000* 000000 SVR31 OPEN ;LOC TO SAVE R3.
000000* 000000 SVR41 OPEN ;LOC TO SAVE R4.
000000* 000000 SVR51 OPEN ;LOC TO SAVE R5.
000000* 000000 SVR61 OPEN ;LOC TO SAVE R6.
000000* 000000 CSRA1 OPEN ;ADDR OF CURRENT CSR.
000000* 000102 SRADR1 OPEN ;ADDR OF GOOD DATA, OR
000000* 000000 ACSR1 OPEN ;CONTENTS OF CSR.
000000* 000104 WABADR1 OPEN ;ADDR OF BAD DATA, OR
000000* 000000 ASTAT1 OPEN ;STATUS REG CONTENTS.
000000* 000106 ERPTYP1 OPEN ;TYPE OF ERROR
000000* 000000 ASB1 OPEN ;EXPECTED DATA.
000000* 000110 AWAS1 OPEN ;ACTUAL DATA.
000000* 000112 RSTR1 RESTRT ;RESTART ADDRESS AFTER END OF PASS
000000* 000114 WDT01 OPEN ;WORDS TO MEMORY PER ITERATION
000000* 000116 WDFR1 OPEN ;WORDS FROM MEMORY PER ITERATION
000000* 000120 INTR1 OPEN ;# OF INTERRUPTS PER ITERATION
000000* 000137 IDNUM1 137 ;MODULE IDENTIFICATION NUMBER=137
000000* 005770 RBUFVA1 RBUF ;READ BUFFER VIRTUAL ADDRESS
000000* 000000 RBUFPA1 OPEN ;READ BUFFER PHYSICAL ADDRESS

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000130* 000000 RBUFEA1 OPEN ;READ BUFFER FA BITS
000132* 000100 RBUFSZ1 64 ;SIZE OF THE READ BUFFER
000134* 000000 WRUFPA1 OPEN ;WRITE BUFFER PHYSICAL ADDRESS
000136* 000000 WRUFEA1 OPEN ;WRITE BUFFER FA BITS
000140* 000102 WRUFRQ1 66 ;WRITE BUFFER SIZE REQUESTED
000142* 000000 WRUFSZ1 OPEN ;WRITE BUFFER SIZE AVAILABLE
000144* 000000 CDRECT1 OPEN ;CDATA/DATCK ERROR COUNT
000146* 000000 CDWDCT1 OPEN ;CDATA/DATCK WORD COUNT
000150* 000000 FREE1 OPEN ;RESERVED FOR FUTURE USE
000000* 000000 .REPT 8PSIZ ;MODULE STACK STARTS HERE.
000000* .PLIST
000000* .WORD 0
000000* .LIST
000000* .ENDR
000252* MODDSP1
;*****

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324 000252* 000000
325 000254* 000000
326 000256* 000000
327 000260* 000000
328 000262* 000000
329 000264* 000000
330 000266* 000000
331 000270* 000000
332 000272* 000000
333 000274* 000000
334 000276* 000000
335 000300* 000000
336 000302* 000100
337 000304* 000000
338 000306* 000000
339 000310* 000000
340 000312* 000000
341 000314* 000000
342 000316* 000000
343 000320* 000000
344 000322* 000000
345 000324* 000000
346 000326* 000000
347 000330* 000000
348 000332* 000000
349 000334* 000000
350 000336* 000000
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356 000340* 000000
357 000342* 000000
358 000344* 000000
359 000346* 000000
360 000350* 000000
361 000352* 000000
362 000354* 000000
363 000356* 000000
364 000360* 000350*
365 000362* 000364*
366 000364* 000366*
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370 000366* 177170
371 000370* 177172
372 000372* 000264
373 000374* 000111
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;----FLAGS AND COUNTERS -----
DVIDIX: 0 ;HOLDS WHICH DRIVES TO TEST
UNIT0: 0 ;UNIT 0 FLAG
UNIT1: 0 ;UNIT 1 FLAG
DRVN: 0 ;DRIVE NUMBER
CMD: 0 ;COMMAND SAVE
UTTI: 0 ;UNIT UNDER TEST
UTI: 0 ;UNIT UNDER COMMAND
WTF: 0 ;WRITE FLAG
TAI: 0 ;CURRENT TRACK ADDRESS
SAI: 0 ;CURRENT SECTOR ADDRESS
TASAV: 0 ;STARTING TRACK ADDRESS SAVE
SASAV: 0 ;STARTING SECTOR ADDRESS SAVE
WDCNT: 100 ;WORD COUNTER
DENSITY: 0 ;DENSITY
DENF: 0 ;DENSITY FLAG
TOMLT: 0 ;TIME OUT MULTIPLIER
TOCNT: 0 ;TIME OUT COUNTER
INTI: 0 ;INITIALIZE FLAG
ITDNE: 0 ;INIT DONE FLAG
TSSFLG: 0 ;TSS DONE WITH UNIT FLAG
INLOOP: 0 ;IN LOOP FLAG
ITBYP: 0 ;RESTART INIT BYPASS FLAG
BKBYPS: 0 ;BREAK BYPASS FLAG
RTRYFL: 0 ;PTRY FLAG
EXRCT: 0 ;NON EXISTENT MEMORY ERROR COUNTER
SERFL: 0 ;STATUS ERROR FLAG
FINI: 0 ;FINI FLAG

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;----TEMPORARY STORAGE REGISTERS -----
SPXCS: 0
SRXES: 0
SRXTA: 0
SRXSA: 0
SRXSH: 0
SRXSB1: 0
SRXSB2: 0
SRXSB3: 0
DECVA: SPXSP ;DEFINITIVE ERROR CODE VIRTUAL ADR
      +2 ;DEFINITIVE ERROR CODE PHYSICAL ADR
      +2 ;DEFINITIVE ERROR CODE EXTENDED ADW BITS

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;----CONSTANTS -----
HXCS: 177170 ;HX02 COMMAND REGISTER - ADDRESS
PXDB: 177172 ;PX02 DATA BUFFER - ADDRESS
VECI: 264 ;VECTOR
VARIFY: 111 ;VARIFY WD FOR SET DENSITY=ASCII "I"

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;REGINROUTINE (MODULE 0,0 -- CONTROL)
; IF START
; ; INITIALIZE
; ; CALL MOD 1,0 - INITIALIZE DEVICE
; ; CALL MOD 6,0 - CHECK STATUS
; RESTART
; BEGIN0
; ; IF INLOOP NOT SET
; ; ; THEN
; ; ; IF INITIALIZE_BYPASS NOT SET
; ; ; ; THEN-INITIALIZE-TRACK & SECTOR
; ; ; ; ENDF
; ; ; IF FINI_FLAG NOT SET
; ; ; ; THEN
; ; ; ; CALL MOD 2,0 - OUTPUT (*WRITE)
; ; ; ; IF FINI_FLAG NOT SET
; ; ; ; ; THEN
; ; ; ; ; CALL MOD 3,0 - INPUT (READ)
; ; ; ; ; IF FINI_FLAG NOT SET
; ; ; ; ; ; THEN
; ; ; ; ; ; CALL MOD 4,0 - PROCESS SECTOR
; ; ; ; ; ; SET INLOOP_FLAG & CALL ITERATION
; ; ; ; ; ENDF
; ; ; ; ENDF
; ; ; ENDF
; ; ENDF
; ; CLEAR INLOOP
; ; SET INITIALIZE_BYPASS FLAG
; ; IF SECTOR_DONE_FLAG SET
; ; ; THEN
; ; ; ; CLEAR SECTOR_DONE_FLAG
; ; ; ; CALL MOD 8,0-UNITS DONE CHECK
; ; ; ; IF UNIT_DONE SET
; ; ; ; ; THEN
; ; ; ; ; SET TRACK_ADR=TRACK_ADR+10.
; ; ; ; ; IF TRACK_ADR > 76
; ; ; ; ; THEN-SET TRACK_ADR=TRACK_ADR SAVE
; ; ; ; ; INCREMENT SECTOR_ADDRESS_SAVE & CLEAR INITIALIZE_BYPASS_FLAG
; ; ; ; ; IF SECTOR_ADDRESS_SAVE=4
; ; ; ; ; ; THEN
; ; ; ; ; ; SET SECTOR_ADDRESS_SAVE=1
; ; ; ; ; ; INCREMENT TRACK_ADDRESS_SAVE
; ; ; ; ; ; IF TRACK_ADDRESS_SAVE=11.
; ; ; ; ; ; ; THEN
; ; ; ; ; ; ; SET TRACK_ADDRESS_SAVE=1
; ; ; ; ; ; ; COMMENT DENSITY_FLAG
; ; ; ; ; ; ; CALL MOD 7,0 - SET DENSITY
; ; ; ; ; ; ; ENDF
; ; ; ; ; ENDF
; ; ; ; ENDF
; ; ; ENDF
; ; ; ENDF
; ; CALL MOD 5,0-SELECT UNIT FOR TEST
; ; DOWNTIL FINI=1
; ; FINI-->END
;ENDROUTINE

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```
MOD 0,0 CONTROL -- PROCESS SECTORS -----  
START: NOP  
431 000376* 000240 ;MOD 0,0 CONTROL -- PROCESS SECTORS -----  
432 000400* 016706 177426 START: NOP  
433 000400* 016706 177426 MOV SPOINT,SP ;INITIALIZE STACK  
434 000400* 016706 177664 MOV #1,TASAV ;PRESET TRACK ADDRESS SAVE  
435 000412* 012767 000001 177660 MOV #1,SASAV ;PRESET SECTOR ADDRESS  
436 000420* 012767 000100 177654 MOV #100,WDCNT ;SET WORD COUNT FOR SINGL DENSITY  
437 000426* 005067 177704 CLR FIN ;CLEAR FINI FLAG  
438 000432* 005067 177650 CLR DEN ;CLEAR DENSITY FLAG  
439 000436* 005067 177642 CLR DENSTY ;CLEAR DENSITY  
440 000442* 005067 177606 CLR UNIT0 ;CLEAR UNIT#0 AVAIL FLAG  
441 000446* 005067 177604 CLR UNIT1 ;CLEAR UNIT#1 AVAIL FLAG  
442 000452* 005067 177606 CLR UT ;CLEAR UNIT UNDER TEST  
443 000456* 005067 177576 CLR DRVN ;CLEAR DRIVE #  
444 000462* 005067 177646 CLR SFRFL ;CLEAR STATUS ERR FLAG  
445 000466* 005067 177640 CLR NXERCT ;CLEAR NON-EXISTENT MEMORY ERFOF COUNT  
446 000472* 016767 177316 177552 MOV DVID1,DVID1X ;GET DRIVE COUNT  
447 000500* 122737 000015 000041 CMPR #15,#41 ;IF RX02 FLOPPY WAS LOAD MEDIUM  
448 000506* 001033 BNE FIRST ;THEN  
449 000510* 105737 000040 TST #40 ;IF DRIVE 0  
450 000514* 001033 BNE D1 ;THEN  
451 000516* 042767 000001 177526 BIC #1,DVID1X ;KILL TEST FOR DRIVE 0  
452 000524* 032767 000001 177262 BIF #1,DVID1 ;WAS IT TO BE TESTED?  
453 000532* 001421 BEQ FIRST ;IF NO  
454 000534* 012767 000260 004062 MOV #260,DNUM ;SET DRIVE # FOR PRINT  
455 000544* 004412 RR NUAV ;IF IO NOT AVAIL MSG  
456 000544* 042767 000002 177500 D1: BIC #2,DVID1X ;KILL TEST FOR DRIVE 1  
457 000552* 032767 000002 177234 BIT #2,DVID1 ;IF WAS TO BE TESTED  
458 000564* 001406 BEQ FIRST ;THEN  
459 000562* 012767 000261 004634 MOV #261,DNUM ;SET DRIVE #1 FOR PRINT  
460 000570* 104403 MSGN$,BEGIN,MUNT NUAV: ;ASCII MESSAGE CALL WITH COMMON HEADER  
461 000576* 012767 000001 177510 FIFST: MOV #1,INT ;SET INIT FLAG  
462 000604* 004767 000240 JSR PC,VSET ;GO SETUP ADDRESS & VECTOR  
463 000610* 032777 004000 177550 BIT #4000,RRXCS ;IF DEVICE IS  
464 000616* 001005 BNE GO ;RX01 THEN  
465 000620* 104403 MSGN$,BEGIN,DRP4 ;ASCII MESSAGE CALL WITH COMMON HEADER  
466 000626* 000167 000324 GO: JMP FINI ;GO DROP MODULE  
467 000632* 004767 000324 MOV PC,INITIAL ;INITIALIZE DEVICE---DO MOD 1,0  
468 000636* 005767 177474 TST FIN ;IF FINI FLAG  
469 000642* 001145 BNE FINI ;NOT SET THEN  
470 000644* 004767 001364 JSR PC,STATUS ;CHECK STATUS-----DO MOD 6,0  
471 000650* 005767 177462 TST FIN ;IF FINI FLAG  
472 000654* 001140 BNE FINI ;NOT SET, THEN  
473 000656* 005067 177432 CLR INT ;RESET INIT FLAG  
474 000662* 004767 001256 CALL SFLO ;GO SELECT UNIT-----DO MOD 5,0  
475 000666* 000240 RESTRT: NOP  
476 000670* 005767 177426 AGND0: TST INLOOP ;IF INLOOP  
477 000674* 001042 BNE BKLOOP ;NOT SET, THEN  
478 000676* 005767 177422 TST ITRYPS ;IF INITIALIZE BYPASS FLAG  
479 000702* 001006 BNE EDRYPS ;NOT SET, THEN  
480 000704* 016767 177370 177362 MOV SASAV,SA ;PRESET SECTOR STARTING ADDRESS  
481 000712* 016767 177360 177352 MOV TASAV,TA ;PRESET TRACK STARTING ADDRESS  
482 000720* 005767 177412 EDRYFS: TST FIN ;IF FINI FLAG  
483 000724* 001026 BNE BKLOOP ;NOT SET, THEN  
484 000726* 104410 GFTPA$,BEGIN,RRUFVA ;GET PHYSICAL ADDRESS FROM 16-BIT RRUFVA  
485 000734* 104410 000000* 000124* ;GET WRITE BUFFER INFORMATION  
486 000734* 104410 000000*
```

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487 000740* 004767 000266 WRITE: JSR PC,OUTPUT ;GO WRITE-----DO MOD 2,0  
488 000744* 005767 177366 TST FIN ;TEST FINI FLAG  
489 000750* 001014 BNE BKLOOP ;IF SET THEN BR  
490 000752* 004767 000602 READ: JSR PC,INPUT ;GO READ-----DO MOD 3,0  
491 000756* 005767 177354 TST FIN ;TEST FINI FLAG  
492 000762* 001007 BNE BKLOOP ;IF SET THEN BR  
493 000764* 004767 001116 CALL TSS ;GO UPDATE SECTOR = DO MOD 4,0  
494 000770* 012767 000001 177324 MOV #1,INLOOP ;SET INLOOP FLAG  
495 000776* 104413 000000* EMDITS,BEGIN ;SIGNAL END OF ITERATION,  
496 ;MONITOR SHALL TEST END OF PASS  
497 001002* 005067 177314 BKLOOP: CLR INLOOP ;CLEAR INLOOP FLAG  
498 001006* 012767 000001 177310 MOV #1,ITRYPS ;SET INITIALIZE BYPASS FLAG  
499 001014* 005767 177300 TST TSSFLG ;IF UNIT TSS  
500 001020* 001453 BEQ DOUNTL ;IS DONE, THEN  
501 001022* 005067 177272 CLR TSSFLG ;CLEAR FLAG FOR TSS DONE  
502 001026* 004767 002214 CALL UDONCK ;CALL UNIT DONE CHECK = DO MOD 8,0  
503 001032* 005767 177260 TST UTDONE ;IF UNIT DONE  
504 001036* 001442 BEQ 1$ ;NOT SET, THEN  
505 001040* 002767 000012 177224 ADD #12,TA ;INCREMENT TRACK ADDRESS  
506 001046* 022767 000114 177216 CMP #114,TA ;SEE IF DONE TRACKS  
507 001054* 103033 BHIS 1$ ;IF NOT BR  
508 001056* 016767 177214 177206 MOV TASAV,TA ;PRESET TRACK ADDRESS  
509 001064* 005067 177234 CLR ITRYPS ;CLEAR INITIALIZE BYPASS FLAG  
510 001070* 005267 177204 INC SASAV ;BUMP STARTING SECTOR ADDRESS  
511 001074* 022767 000004 177176 CMP #4,SASAV ;IF SFCTORS = DONE (THREE)  
512 001102* 001020 BNE 1$ ;THEN  
513 001104* 012767 000001 177166 MOV #1,SASAV ;RESET STARTING SECTOR  
514 001112* 005267 177160 INC TASAV ;BUMP STARTING TRACK ADDRESS  
515 001116* 022767 000013 177152 CMP #11,TASAV ;IF TRACKS=DONE (TEN)  
516 001124* 001007 BNE 1$ ;THEN  
517 001126* 012767 000001 177142 MOV #1,TASAV ;RESET TRACK STARTING ADDRESS  
518 001134* 005167 177146 COM DEN ;CHANGE DENSITY  
519 001142* 004767 001570 JSR PC,DENCH ;SET DEVICE DENSITY  
520 001144* 004767 000774 1$: CALL SFLO ;SELECT UNIT-----DO MOD 5,0  
521 001150* 005767 177162 ROUNTL: TST FIN ;DO UNTIL FINI FLAG  
522 001154* 001644 BEQ RESTRT ;SET  
523 001156* FINI: ;  
524 001156* 104410 000000* END$,BEGIN ;DROP THE MODULE  
525 ;-----
```

```
526 ;MOD 1,0 INITIALIZE DEVICE -----
527
528 001162* 012767 040000 177072 INITIAL: MOV #40000,CMD ;SET INT COMMAND
529 001170* 016777 177066 177170 MOV CMD,0RXCS ;INIT UNIT #
530 001176* 024767 003142 JSR PC,AWDN ;GO AWAIT DONE
531 001202* 005767 177130 TST FIN ;IF FINI FLAG
532 001206* 001010 BNE ENDINT ;EQUALS ZERO THEN
533 001210* 005777 177152 TST 0RXCS ;SET IF ERROR ON INIT
534 001214* 100005 BPL ENDINT ;IF NOT BR
535 001216* 104403 000000* 005450* MSGN8,BEGIN,DRP1 ;ASCII MESSAGE CALL WITH COMMON HEADER
536 001224* 004767 002564 JSR PC,TDRP ;DO TEST DROP CLEAN-UP
537 001230* 000207 ENDINT: RTS PC ;RETURN
538 -----
539 ;MOD 2,0 WRITE SUBROUTINE -----
540
541
542 001232* 012767 000001 177030 OUTPUT: MOV #1,WTF ;SET WRITE FLAG
543 001240* 004767 000134 JSR PC,OURUF2 ;THEN O/P RX02 BUFFER = DO MOD 2.1
544 001244* 005767 177066 TST FIN ;IF FINI FLAG
545 001250* 001050 BNE ENDOUT ;EQUALS ZERO THEN
546 001252* 012767 000260 004150 MOV #260,RTYN ;RESET RETRY COUNTER-ASCII "0"
547 001260* 016767 177000 176774 181 MOV UTT,CMD ;SELECT DRIVE
548 001266* 052767 000105 176766 BIS #105,CMD ;SET TO WRITE SECTOR + INT ENABLE
549 001274* 056767 177004 176760 BIS DENSITY,CMD ;SET DENSITY
550 001302* 016777 176754 177056 MOV CMD,0RXCS ;LOAD COMMAND
551 001310* 004767 003140 JSR PC,AWTR ;GO AWAIT TRANSFER READY
552 001314* 005767 177016 TST FIN ;IF FINI FLAG
553 001320* 001024 BNE ENDOUT ;EQUALS ZERO THEN
554 001322* 016777 176746 177040 MOV SA,0RXDB ;LOAD SECTOR ADDRESS
555 001330* 004767 003120 JSR PC,AWTR ;GO AWAIT TRANSFER READY
556 001334* 005767 176776 TST FIN ;IF FINI FLAG
557 001340* 001014 BNE ENDOUT ;EQUALS ZERO THEN
558 001342* 016777 176724 177020 MOV TA,0RXDR ;LOAD TRACK ADDRESS
559 001350* 004767 001764 JSR PC,INTEP ;WAIT FOR INTERRUPT
560 001354* 005767 176756 TST FIN ;IF FINI FLAG
561 001360* 001004 BNE ENDOUT ;NOT SET, THEN
562 001362* 022767 000003 176740 CMP #3,RTYFL ;IF RETRY FLAG
563 001370* 001333 BNE 18 ;EQUALS 3 THEN
564 001372* 005067 176672 ENDOUT: CLW WTF ;CLEAR WRITE FLAG
565 001376* 000207 RTS PC ;RETURN
566 -----
```

```
567 ;MOD 2,1 OUTPUT RX02 BUFFER -----
568
569 001400* 012767 000001 176654 OUBUF2: MOV #1,CMD ;SET FILL BUFFER COMMAND
570 001406* 056767 176672 176646 BIS DENSITY,CMD ;SET DENSITY
571 001414* 016701 176516 MOV WBUFEA,R1 ;GET EXT. ADR. BITS
572 001420* 000301 SWAB R1 ;
573 001422* 050167 176634 BIS R1,CMD ;SET EXT. ADR. BITS
574 001426* 016777 176630 176732 MOV CMD,0RXCS ;LOAD COMMAND
575 001434* 004767 003014 JSP PC,AWTR ;WAIT FOR "TR"
576 001440* 005767 176672 TST FIN ;IF FINI FLAG
577 001444* 001044 BNE ENDOUT ;EQUALS ZERO THEN
578 001446* 016777 176630 176714 MOV WDCNT,0RXDB ;LOAD WORD COUNT FOR OUTPUT BUFFER
579 001454* 005267 176646 INC BRKYP5 ;SET BREAK BYPASS FLAG
580 001460* 004767 002770 JSR PC,AWTR ;WAIT FOR "TR"
581 001464* 005067 176636 CLR BRKYP5 ;CLEAR BREAK BYPASS FLAG
582 001470* 005767 176642 TST FIN ;IF FINI FLAG
583 001474* 001030 BNE ENDOUT ;EQUALS ZERO THEN
584 001476* 016777 176432 176664 MOV WUPPA,0RXDR ;LOAD BASE ADDRESS FOR OUTPUT BUFFER
585 001504* 004767 002634 JSP PC,AWDN ;WAIT FOR "DONE"
586 001510* 005767 176622 TST FIN ;IF FINI FLAG
587 001514* 001020 BNE ENDOUT ;EQUALS ZERO THEN
588 001516* 004767 002442 CALL CRNXP ;CALL CHECK NON-EXISTENT MEMORY ERRORS
589 001522* 005777 176640 TST 0RXCS ;IF DEVICE ERROR BIT
590 001526* 100013 BPL ENDOUT ;IF SET THEN
591 001530* 004767 002526 CALL SUPTRG ;CALL SETUP PRINT REGISTERS
592 001534* 012767 000035 176344 MOV #35,FRTP ;SETUP FILL BUFFER ERROR
593 ;*****
594 001542* 104405 000000* 000000 HRDRS,BEGIN,NULL ;BUFFER FILL ERROP
595 ;*****
596 001550* 104402 000000* 005442* MSGS,BEGIN,BFER2 ; ASCII MESSAGE CALL WITH NO HEADER
597 001556* 000207 ENDOUT: RTS PC ;RETURN
598 -----
```



```

599 ;MOD 3.0 READ SUBROUTINE -----
600
601 001560 012767 000260 003642 INPUT: MOV #260,RTYN ;RESET RETRY COUNTER=ASCII "0"
602 001566 016767 176472 176466 181 MOV UTT,CMD ;SELECT DRIVE
603 001574 052767 000107 176460 HIS #107,CMD ;SET READ COMMAND + INT ENB
604 001602 056767 176476 176452 BIS DENSITY,CMD ;SET DENSITY
605 001610 016777 176446 176550 MOV CMD,RRXCS ;LOAD COMMAND
606 001616 004767 002632 JSP PC,AWTR ;GO AWAIT TRANSFER READY
607 001622 005767 176510 TST FIN ;IF FINI FLAG
608 001626 001023 RRF ENDIN ;NOT SET, THEN
609 001630 016777 176446 176532 MOV SA,RRXDF ;LOAD SECTOR ADDRESS
610 001636 004767 002612 JSR PC,AWTR ;GO AWAIT TRANSFER READY
611 001642 005767 176470 TST FIN ;IF FINI FLAG
612 001646 001013 RRF ENDIN ;NOT SET, THEN
613 001650 016777 176416 176512 MOV TA,RRXDB ;LOAD TRACK ADDRESS
614 001656 004767 001456 JSR PC,INTER ;WAIT FOR INTERRUPT
615 001662 022767 000003 176440 CMP #3,RTHYFL ;IF RETRY FLAG
616 001670 001336 RNE 18 ;EQUALS 3, THEN
617 001672 004767 000002 JSR PC,INRUF2 ;THEN GET RX02 BUFFER - DO MOD 3.1
618 001676 000207 ENDIN: RTS PC ;RETURN
619 ;-----

```

```

620 ;MOD 3.1 INPUT RX02 BUFFER -----
621
622 001700 005767 176430 INRUF2: TST SEFPL ;IF STATUS ERROR NOT SET
623 001704 001077 RNE ENDIN2 ;THEN
624 001706 012767 000003 176346 MOV #3,CMD ;SET EMPTY BUFFER COMMAND
625 001714 056767 176364 176340 BIS DENSITY,CMD ;SET DENSITY
626 001722 016701 176202 MOV RPUFEA,R1 ;GET EXT. ADR. BITS
627 001726 000301 SWAB R1 ;
628 001730 050167 176326 BIS R1,CMD ;SET EXT. ADR. BITS
629 001734 016777 176322 176424 MOV CMD,RRXCS ;ELSE LOAD COMMAND
630 001742 004767 002506 JSR PC,AWTR ;WAIT FOR "TR" DO MOD U,TR
631 001746 005767 176364 TST FIN ;IF FINI FLAG
632 001752 001054 RNE ENDIN2 ;IF FINI FLAG
633 001754 016777 176322 176406 MOV WDCNT,RRXDB ;IF FINI FLAG
634 001762 005267 176340 INC RKBYP5 ;IF FINI FLAG
635 001766 004767 002462 JSR PC,AWTP ;THEN LOAD WORD COUNT FOR INPUT BUFFER
636 001772 005067 176330 CLR RBBYP5 ;SET BREAK BYPASS FLAG
637 001776 005767 176334 TST FIN ;IF FINI FLAG
638 002002 001040 RNE ENDIN2 ;IF FINI FLAG
639 002004 016777 176116 176356 MOV RBUFEA,RRXDB ;IF FINI FLAG
640 002012 004767 002326 JSR PC,AWDN ;THEN LOAD BASE ADDRESS FOR INPUT BUFFER
641 002016 005767 176314 TST FIN ;WAIT FOR "DONE"
642 002022 001030 RNE ENDIN2 ;IF FINI FLAG
643 002024 005777 176336 TST RRXCS ;IF DEVICE ERROR BIT
644 002030 100021 BPL 18 ;SET, THEN
645 002032 004767 002126 CALL CKNXER ;CALL CHECK NON-EXISTENT MEMORY ERRORS
646 002036 005777 176324 TST RRXCS ;IF DEVICE ERROR BIT
647 002042 100020 BPL ENDIN2 ;IF DEVICE ERROR BIT
648 002044 004767 002212 CALL SUPTRG ;IS STILL SET, THEN
649 002050 012767 000030 176030 MOV #30,ERRTPY ;CALL SETUP PRINT REGISTERS
;***** ;SETUP DATA TRANSFER ERROR
;***** ;
;***** ;
;***** ;
650 ;***** ;
651 002056 104405 000000 000000 HPDEFB,BEGIN,NULL ;EMPTY BUFFER ERROR
652 ;***** ;
653 002064 104402 000000 005434 MSGSB,BEGIN,BFEF1 ;ASCII MESSAGE CALL WITH NO HEADER
654 002072 000004 HP ENDIN2 ;ERR TO EXIT
655 ;***** ;
656 002074 104412 000000 000126 181 CPDATA6,BEGIN,RBUFEA ;REQUEST FOR MONITOR TO CHECK DATA
657 002102 002104 +2 ;IF ERROR, CONTINUE
658 002104 000207 ENDIN2: RTS PC ;RETURN
659 ;-----

```

```

660 ;MOD 4,0 SECTOR UPDATE -----
661
662 002106* 002767 000003 176160 TSS: ADD #3,SA ;BUMP SECTOR ADDRESS
663 002114* 022767 000033 176152 CMP #33,SA ;SEF IF DONE SECTORS
664 002122* 101401 RLOS 10 ;IF SO: BR
665 002124* 000406 BR ENDTSS ;RETURN
666 002126* 016767 176146 176140 10: MOV SASAV,SA ;RESET SA
667 002134* 012767 000001 176156 MOV #1,TSSFLG ;SET TSS FLAG- (UNIT DONE WITH TSS)
668 002142* 000207 ENDTSS: RTS PC
669 ;-----
670 ;MOD 5,0 SELECT UNIT FOR TEST -----
671
672
673 002144* 005767 176110 SELD: TST DRVN ;SEE IF DRIVE 0
674 002150* 001415 REQ 20 ;IF SO: BR
675 002152* 005067 176102 CLR DRVN ;ELAE SET TO 0
676 002156* 005767 176074 UNIT: TST UNIT1 ;SEE IF UNIT 1 AVAILARLF
677 002162* 001401 BFO 10 ;IF SO: BR
678 002164* 000767 BP SFLD ;GO SEE IF UNIT IS AVAILARLF
679 002166* 012767 000020 176070 10: MOV #20,UTT ;SET UNIT UNDER TEST
680 002174* 012767 000261 003222 MOV #261,DNUM ;SET DRIVE NUMBER FOR PRINTS
681 002202* 000413 BR ENDTSEL ;RETURN
682 002204* 005167 176050 20: COM DRVN ;SWTCH UNITS
683 002210* 005767 176040 TST UNIT0 ;SEE IF UNIT 0 AVAILARLF
684 002214* 001401 BFO 30 ;IF SO: BR
685 002216* 000752 BR SFLD ;SELECT NEXT
686 002220* 005067 176040 30: CLR UTT ;SELECT DRIVE 0
687 002224* 012767 000260 003172 MOV #260,DNUM ;SET DRIVE NUMBER FOR PRINTS
688 002232* 000207 ENDTSEL: RTS PC ;RETURN
689 ;-----

```

```

690 ;MOD 6,0 DEVICE STATUS -----
691
692 002234* 000240 STATUS: NOP
693 002236* 004767 000120 00: JSP PC,UNIT0 ;CHECK STATUS UNIT 0 = DO MOD 6.1
694 002242* 005767 176006 TST UNIT0 ;IF UNIT 0 IS
695 002246* 001012 BNE U1 ;AVAIL, THEN
696 002250* 005767 176040 TST INT ;IF INIT FLAG
697 002254* 001407 REQ U1 ;IS SET, THEN
698 002256* 005067 176002 CLR UTT ;SET UNIT=0
699 002262* 012767 000260 003134 MOV #260,DNUM ;SET DRIVE # FOR PRINT
700 002270* 004767 000322 JSR PC,DNSET ;SETUP DENSITY UNIT 0 = DO MOD 6.3
701 002274* 004767 000200 U1: JSP PC,UNIT1 ;CHECK STATUS UNIT 1 = DO MOD 6.2
702 002300* 005767 175752 TST UNIT1 ;IF UNIT 1 IS
703 002304* 001014 BNE U1 ;AVAIL, THEN
704 002306* 005767 176002 TST INT ;IF INIT FLAG
705 002312* 001422 REQ ENDTST ;IS SET, THEN
706 002314* 012767 000020 175742 MOV #20,UTT ;SET UNIT=1
707 002322* 012767 000261 003074 MOV #261,DNUM ;SET DRIVE # FOR PRINT
708 002330* 004767 000262 JSR PC,DNSET ;SETUP DENSITY UNIT 1 = DO MOD 6.3
709 002334* 000411 BR ENDTST ;RR TO END STATUS
710 002336* 005767 175712 10: TST UNIT0 ;IF UNIT 0
711 002342* 001406 REQ ENDTST ;NOT AVAIL, THEN
712 002344* 104403 000000* 005460* MSGN0,BEGIN,DRP2 ;ASCII MESSAGE CALL WITH COMMON HEADER
713 002352* 012767 000001 175756 MOV #1,FIN ;GO DROP MODULE
714 002360* 000207 ENDTST: RTS PC ;RETURN
715 ;-----
716
717 ;MOD 6.1 UNIT 0 STATUS -----
718
719
720 002362* 032767 000001 175667 UNIT: RIT #1,DVIDIX ;IF UNIT 0
721 002370* 001437 RFO 10 ;SELECTED, THEN
722 002372* 012767 000013 175662 MOV #13,CMD ;READ STATUS UNIT 0
723 002400* 005767 175700 175654 RJS DENSITY,CMD ;SET DENSITY
724 002406* 016777 175650 175752 MOV CMD,ORXCS ;SEND COMMAND
725 002414* 004767 001724 JSR PC,AWDN ;GO AWAIT DONE
726 002420* 005767 175712 TST FIN ;IF FINI FLAG
727 002424* 001024 BNE ENDT0 ;EQUALS ZERO THEN
728 002426* 017000 175736 MOV ORXDR,R0 ;GET RXES
729 002432* 032700 000200 BIT #200,R0 ;IF DRIVE RDY
730 002436* 001017 BNE ENDT0 ;NOT SET, THEN
731 002440* 012767 000260 002756 MOV #260,DNUM ;SET DRIVE # FOR PRINT
732 002446* 012767 000006 175432 MOV #6,FRPTYP
733 ;*****
734 002454* 104405 000000* 000000 HDRS$,BEGIN,NULL ;UNIT 0 NOT AVAILARLF
735 ;*****
736 002462* 104402 000000* 005510* MSGS$,BEGIN,NUNT ; ASCII MESSAGE CALL WITH NO HEADER
737 002470* 012767 177777 175556 10: MOV #-1,UNIT0 ;DESELECT UNIT 0
738 002476* 000207 ENDT0: RTS PC ;RETURN
739 ;-----

```

```

;MOD 6.2 UNIT 1 STATUS -----
740
741
742 002500* 012767 000002 175544 UNIT1: BIT #2,DVIDIX ;IF UNIT 1
743 002506* 001437 REQ 16 ;SELECTED, THEN
744 002510* 012767 000033 175544 MOV #33,CMD ;READ STATUS UNIT 1
745 002516* 056767 175562 175536 RIS DENSITY,CMD ;SET DENSITY
746 002524* 016777 175532 175634 MOV CMD,ARXCS ;SEND COMMAND
747 002532* 004767 001600 JSP PC,AWDN ;GO AWAIT DONE
748 002536* 005767 175574 TST FIN ;IF FINI FLAG
749 002542* 001024 BNE ENDUT1 ;EQUALS ZERO THEN
750 002544* 017700 175620 MOV ARXDB,RA ;READ RXES
751 002550* 032700 000200 BIT #200,P0 ;IF DRIVE RDY
752 002554* 001017 BNE ENDUT1 ;NOT SET, THEN
753 002556* 012767 000261 002640 MOV #261,DNUM ;SET DRIVE # FOR PRINT
754 002564* 012767 000006 175314 MOV #6,ERRTYP
755
756 002572* 104405 000000* 000000 *****
757 *****
758 002600* 104402 000000* 005510* MSGS$,REGIN,NUINT ;ASCII MESSAGE CALL WITH NO HEADER
759 002606* 012767 177777 175442 181 MOV #=-1,UNIT1 ;DEFLECT UNIT 1
760 002614* 000207 ENDUT1: RTS PC ;RETURN
-----
;MOD 6.3 DEVICE DENSITY SETUP -----
761
762
763
764
765
766 002616* 012767 000013 175436 DNSET: MOV #13,CMD ;SET READ STATUS
767 002624* 056767 175434 175430 BIS UTT,CMD ;SET UNIT
768 002632* 016777 175424 175526 MOV CMD,ARXCS ;SEND COMMAND
769 002640* 004767 001500 JSR PC,AWDN ;GO AWAIT DONE
770 002644* 005767 175466 TST FIN ;IF FINI FLAG
771 002650* 001030 BNE XDNSET ;IS ZERO, THEN
772 002652* 012767 000011 175402 MOV #11,CMD ;SET SINGLE DENSITY
773 002660* 056767 175400 175374 BIS UTT,CMD ;SET UNIT
774 002666* 016777 175372 175472 MOV CMD,ARXCS ;SEND COMMAND
775 002674* 004767 001554 JSR PC,AWTR ;GO AWAIT "TR"
776 002700* 005767 175432 TST FIN ;IF FINI FLAG IS
777 002704* 001012 BNE XDNSET ;ZERO
778 002706* 016777 175462 175454 MOV VERIFY,ARXDE ;SEND VERIFY WORD
779 002714* 104403 000000* 005716* MSGS$,REGIN,SETSDN ;ASCII MESSAGE CALL WITH COMMON HEADER
780 002722* 004767 001416 JSR PC,AWDN ;WAIT FOR "DONE"
781 002726* 004767 000440 JSR PC,SECK ;GO CHECK FOR EPROR
782 002732* 000207 XDNSET: RTS PC ;RETURN
-----

```

```

;MOD 7.0 DENSITY CHANGE -----
784
785
786 002734* 000240 DENCH: NOP
787 002736* 005767 175314 TST UNIT1 ;IF UNIT 1
788 002742* 001007 RFE 16 ;IS AVAIL
789 002744* 012767 000020 175314 MOV #20,UT ;SET TO DO UNIT 1
790 002752* 012767 000261 002444 MOV #261,DNUM ;SET DRIVE # FOR PRINT
791 002760* 000405 BR 28 ;CONTINUE
792 002762* 005067 175300 181 CLR UT ;ELSE SET TO DO UNIT 0
793 002766* 012767 000260 002430 MOV #260,DNUM ;SET DRIVE # FOR PRINT
794 002774* 005767 175306 281 TST DFN ;IF DENSITY FLAG
795 002780* 001051 BNE 55 ;EQUALS ZERO (SINGLE DENSITY) THEN
796 002782* 012767 000111 175252 MOV #111,CMD ;SETUP SET DENSITY = SINGLE COMMAND
797 002790* 056767 175252 175244 RIS UT,CMD ;SETUP UNIT UNDER TEST
798 002800* 016777 175240 175342 MOV CMD,ARXCS ;SEND COMMAND
799 002804* 004767 001424 JSR PC,AWTR ;WAIT FOR "TR"
800 002808* 005767 175302 TST FIN ;IF FINI FLAG
801 002810* 001103 BNE FADDNC ;EQUALS ZERO THEN
802 002816* 016777 175332 175324 MOV VERIFY,ARXDB ;SEND VERIFY COMMAND
803 002820* 104403 000000* 005734* MSGS$,REGIN,SSGLDN ;ASCII MESSAGE CALL WITH COMMON HEADER
804 002824* 004767 000262 JSR PC,INTER ;WAIT FOR "DONE" & INTERRUPT
805 002828* 005767 175254 TST FIN ;IF FINI FLAG
806 002830* 001070 BNE ENDDNC ;EQUALS ZERO THEN SET
807 002834* 005767 175176 TST UT ;IF
808 002838* 001411 BFO 45 ;UNIT 1 WAS DONE
809 002842* 005067 175170 CLR UT ;SET TO DO UNIT 0
810 002846* 012767 000260 002320 MOV #260,DNUM ;SET DRIVE # FOR PRINT
811 002850* 005767 175144 TST UNIT0 ;IF UNIT 0
812 002854* 001001 RFE 45 ;IS AVAIL
813 002858* 000733 BR 30 ;SET UNIT 0 DENSITY
814 002862* 012767 000000 175162 481 MOV #0,DENSITY ;SET DENSITY=SINGLE
815 002866* 000450 BR FADDNC ;BRANCH TO END
816 002870* 012767 000511 175130 581 MOV #511,CMD ;ELSE SET DENSITY=DOUBLE COMMAND
817 002874* 056767 175130 175122 RIS UT,CMD ;SETUP UNIT UNDER TEST
818 002878* 016777 175116 175220 MOV CMD,ARXCS ;SEND COMMAND
819 002882* 004767 001302 JSR PC,AWTR ;WAIT FOR "TR"
820 002886* 005767 175160 TST FIN ;IF FINI FLAG
821 002890* 001032 BNE ENDDNC ;EQUALS ZERO THEN
822 002894* 016777 175210 175202 MOV VERIFY,ARXDB ;SEND VERIFY COMMAND
823 002898* 104403 000000* 005750* MSGS$,REGIN,SDBLDDN ;ASCII MESSAGE CALL WITH COMMON HEADER
824 002902* 004767 000140 JSR PC,INTER ;WAIT FOR "DONE" & INTERRUPT
825 002906* 005767 175132 TST FIN ;IF FINI FLAG
826 002910* 001017 BNE FADDNC ;EQUALS ZERO THEN
827 002914* 005767 175054 TST UT ;IF
828 002918* 001411 RFO 68 ;UNIT 1 WAS DONE
829 002922* 005067 175046 CLR UT ;SET TO DO UNIT 0
830 002926* 012767 000260 002176 MOV #260,DNUM ;SET DRIVE # FOR PRINT
831 002930* 005767 175022 TST UNIT0 ;IF UNIT 0
832 002934* 001001 RFE 68 ;IS AVAIL
833 002938* 000733 BR 58 ;CONTINUE
834 002942* 012767 000400 175040 681 MOV #400,DENSITY ;SET DENSITY = DOUBLE
835 002946* 000207 ENDENCH: RTS PC ;RETURN
-----

```

```

R37 ;MOD 8,U = UNIT DONF CHECK -----
R39
R39 003246* 005767 175012 UDNCX: TST UTT ;IF UNIT 0
R40 003252* 001006 BNE 18 ;UNDER TEST, THEN
R41 003254* 005767 174776 TST UNIT1 ;IF UNIT 1
R42 003260* 001003 BNE 18 ;AVAILABLE, THEN
R43 003262* 005067 175033 CLR UTDONE ;CLEAR UNITS DONE FLAG
R44 003266* 000403 BR XUDNCK ;RR TO MOD EXIT
R45 003270* 012767 000001 175020 18: MOV #1,UTDONE ;SET UNITS DONF FLAG
R46 003276* 000207 XUDNCK: RETURN ;RETURN
R47
R48
R49 ;MOD U,V6 VECTOR SET UP -----
R50
R51 003300* 016700 174504 VSET: MOV VVECTOR,R0
R52 003304* 012720 003344* MOV #NTRUPT,(R0)+ ;SET UP INTERRUPT HANDLER ADDRESS
R53 003310* 116710 174476 MOV# RR1,(R0) ;SET RR LEVEL
R54 003314* 016767 174466 175044 MOV ADDR,RXCS ;SET ADDRESS OF RXCS
R55 003322* 016767 174466* 175040 MOV ADDR,RXDR ;GET ADDRESS OF RXCS
R56 003330* 062767 000002 175032 ADD #2,RXDR ;SET ADDRESS OF DATA BUFFER
R57 003336* 000207 RTS PC
R58
R59 ;MOD U,INTR INTERRUPT HANDLER -----
R60
R61
R62 003340* 104400 000000* INTER: EXIT0,REGIN ;EXIT TO MONITOR, MODULE WAIT FOR INTERRUPT.
R63 003340* 104400 000000*
R64 003344* NTRUPT:
R65 ;-----
R66 003344* 000004 000000* 003352* ;FIPO0,REGIN,18 ;QUEUE UP TO CONTINUE AT 18 AND RTI
R67 ;-----
R68 003352* 004767 000014 18: JSP PC,SECK ;GO CHECK POP ERROR
R69 003356* 016700 174426 MOV VVECTOR,R0 ;GET VECTOR ADDRESS
R70 003362* 005720 TST (R0)+ ;RUMP POINTER
R71 003364* 016710 174422 MOV BR1,(R0) ;ASSURE RESET TO BR LEVEL 5
R72 003370* 000207 RTS PC ;RETURN
R73 ;-----

```

```

R74 ;MOD U,EC STATUS ERROR CHECK SUBROUTINE -----
R75
R76 003372* 005067 174736 SECK: CLR SEFPL ;CLEAR STATUS ERROR FLAG
R77 003376* 005767 174764 174656 BIS BRXCS,CMD ;GET RXCS
R78 003404* 005767 174652 TST CMD ;SEE IF ERROR
R79 003410* 100404 BMI 18 ;IF SO: BR
R80 003412* 012767 000003 174710 MOV #3,RTYRFL ;SET RETRY FLAG, SO NO PETRY
R81 003420* 000423 BR ENDEC ;ELSE RETURN
R82 003422* 004767 000634 18: CALL SUPTRG ;CALL SETUP PRINT REGISTERS
R83 003426* 016767 174632 174626 MOV UTT,CMD ;SELECT DRIVE
R84 003434* 004767 000422 CALL DVEC ;GET DEVICE ERROR CODE
R85 003440* 004767 000026 CALL CKDNER ;CHECK FOR DENSITY ERROR
R86 003444* 005767 174620 TST WTF ;SEE IF WRITE ERROR
R87 003450* 001003 BNE 20 ;IF SO: BR
R88 003452* 004767 000066 CALL CKRDER ;CHECK READ ERROR
R89 003456* 000402 BR CL ;GO CLEAR ERROR
R90 003460* 004767 000122 20: CALL CWYTER ;CALL CHECK WRITE ERROR
R91 003464* 004767 000160 CL: JSR PC,CLEAR ;CLEAR ERRORS
R92 003470* 000207 ENDEC: RTS PC ;RETURN
R93 ;-----
R94 ;MOD U,CKDNER = CHECK DENSITY ERRORS -----
R95
R96
R97 003472* 016705 174642 CKDNER: MOV SPXCS,R5 ;SETUP RXCS FOR TEST
R98 003476* 042705 177761 BIC #177761,R5 ;CLEAR ALL BUT COMMAND
R99 003502* 022705 000010 CMP #0,R5 ;IF COMMAND WAS
R00 003506* 001015 BNE XDNFR ;SET DENSITY, THEN
R01 003510* 012767 000001 174620 MOV #1,FIN ;SET FINI FLAG
R02 003516* 005767 174564 TST DEN ;IF
R03 003522* 001004 BNE 18 ;SINGLE DENSITY, THEN
R04 003524* 104403 000000* 005520* MSGN$,RFIN$,SFTSDE ;ASCII MESSAGE CALL WITH COMMON HEADER
R05 003532* 000403 BR XDNFR ;CONTINUE
R06 003534*
R07 003534* 104403 000000* 005536* 18: MSGN$,RFIN$,SETDUE ;ASCII MESSAGE CALL WITH COMMON HEADER
R08 003542* 000207 XDNFR: RETURN ;RETURN
R09 ;-----
R10 ;MOD U,CKRDER = CHECK READ ERRORS -----
R11
R12
R13 003544* 012767 000000 174334 CKRDER: MOV #0,EPRTP
R14 ;*****
R15 003552* 104406 000000* 004626* SOFERS,RFIN$,TABLE ;READ ERROR
R16 ;*****
R17 003560* 005767 174522 TST DEN ;IF DENSITY FLAG IS
R18 003564* 001004 BNE 18 ;EQUAL TO ZERO (SINGLE) THEN
R19 003566* 104402 000000* 005674* MSGS$,BEGIN$,RTERSDD ;ASCII MESSAGE CALL WITH NO HEADER
R20 003574* 000403 BR 20 ;GO PRINT ERRORS
R21 003576*
R22 003576* 104402 000000* 005652* 18: MSGS$,RFIN$,RTERDD ;ASCII MESSAGE CALL WITH NO HEADER
R23 003604* 000207 20: RETURN ;RETURN
R24 ;-----

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925 ;MOD U,CKWTER - CHECK WRITE ERRORS -----
926
927 003606* 012767 000000 174272 CKWTER: MOV #0,ERRTYP
928 ;*****
929 003614* 104406 000000* 004626* SCFFRS,REGIN,TABLE ;WRITE ERROR
930 ;*****
931 003622* 005767 174460 TST DFN ;IF DENSITY FLAG IS
932 003626* 001004 BNE 18 ;EQUAL TO ZFPO (SINGLE) THEN
933 003630* 104402 000000* 005630* MSGS6,REGIN,WTERED ;ASCII MESSAGE CALL WITH NO HEADER
934 003636* 000403 BR 28 ;GO PRINT ERRORS
935
936 003640* 104402 000000* 005606* 18: MSGS6,REGIN,WTERDD ;ASCII MESSAGE CALL WITH NO HEADER
937 003646* 000207 28: RETURN ;RETURN
938 ;-----
939
940 ;MOD U,CLR CLFAP ERRORS -----
941
942 003650* 005267 174460 CLEAR: INC SERFL ;SET STATUS ERROR FLAG
943 003654* 105767 174470 TSTB SFXSB ;SET IF R.E.C. ERROR
944 003660* 001421 REQ 26 ;IF NOT: BR
945 003662* 012767 000000 174372 MOV #0000,CMD ;SET COMMAND= INIT
946 003670* 016777 174366 174470 MOV CMD,PRXCS ;REINITIALIZE
947 003676* 004767 000442 18: JSR PC,AWDN ;GO AWAIT DONE
948 003702* 005777 174460 TST PRXCS ;SET IF ERROR ON INIT
949 003706* 100006 HPL 28 ;IF NOT: BR
950 003710* 104403 000000* 005466* MSGN6,REGIN,DRP3 ;ASCII MESSAGE CALL WITH COMMON HEADER
951 003716* 004767 000072 JSR PC,DRP ;GO DROP MODULE
952 003722* 000431 BR ENDCLR
953 003724* 005267 001500 28: INC RTYN ;RUMP RETRY COUNTER
954 003730* 016703 001474 MOV RTYN,R3 ;GET COUNTER
955 003734* 042703 177770 FIC #17770,R3 ;MARK ASCII
956 003740* 010367 174364 MOV R3,RTYRFL ;SET RETRY FLAG
957 003744* 022703 000003 CMP #3,R3 ;SEE IF DONE RETRIES
958 003750* 001016 BNE ENDCLR ;IF NOT: BR
959 ;*****
960 003752* 104405 000000* 004626* HRDPRS,REGIN,TABLE ;RECLASSIFICATION OF SOFT ERK
961 ;*****
962 003760* 032767 000001 174030 BIT #1,SRI ;SEE IF SHOULD DROP MODULE
963 003766* 001404 REQ 35 ;IF NOT: BR
964 003770* 012767 000001 174340 MOV #1,FIN ;GO DROP MODULE
965 003776* 000403 BR ENDCLR
966 004000* 012767 000260 001422 38: MOV #260,PTYN ;RESET RETRY COUNTER
967 004006* 005067 174336 FNDCLR: CLR SFXSB ;CLFAP ERROR
968 004012* 000207 RTS PC ;RETURN
969 ;-----

```

```

970 ;MOD U,DRP TEST DRCP CLEAN-UP -----
971
972 004014* 004767 000242 DRP: CALL SHPTRG ;CALL SETUP PRINT REGISTERS
973 004020* 005067 174236 CIR CMD ;CLEAR COMMAND WORD
974 004024* 004767 000032 CALL DVEC ;GET DEVICE ERROR CODE
975 004030* 005767 174302 TST FIN ;IF FINI FLAG
976 004034* 001011 BNE ENDDRP ;EQUALS ZERO THEN
977 004036* 012767 000034 174042 26: MOV #34,ERRTYP
978 ;*****
979 004044* 104405 000000* 004626* HRDPRS,REGIN,TABLE ;INITIALIZE ERROR
980 ;*****
981 004052* 012767 000001 174256 MOV #1,FIN ;GO DROP MODULE
982 004060* 000207 ENDDR: RTS PC
983 ;-----
984
985 ;MOD U,DVEC ---- GET DEVICE ERROR CODE -----
986
987 004062* 000240 DVEC: NOP ;
988 004064* 052767 000017 174170 BIS #17,CMD ;FOR ERROR CODE COMMAND
989 004072* 104415 000000* 000360* GETPAS,REGIN,DECVA ;GET PHYSICAL ADDRESS FROM 16-BIT DECVA
990 004100* 016701 174256 MOV DFCVA+2,R1 ;GET DEF E.C. PHYS ADP
991 004104* 016702 174254 MOV DFCVA+4,R2 ;GET DEF E.C. XTEND ADP BITS
992 004110* 000302 SWAP R2 ;
993 004112* 050267 174144 BIS R2,CMD ;SET EXTEND ADP BITS
994 004116* 016777 174140 174242 MOV CMD,PRXCS ;SEND COMMAND
995 004124* 005267 174176 INC BRKYPB ;SET BRKAP BYPASS FLAG
996 004130* 004767 000320 JSR PC,AWTR ;THEN GO AWAIT TR
997 004134* 005067 174166 CLR BRKYPB ;CLEAR BREAK BYPASS FLAG
998 004140* 005767 174172 TST FIN ;IF FINI FLAG
999 004144* 001006 BNE ENDVEC ;EQUALS ZERO THEN
1000 004146* 010177 174216 MOV R1,PRXDR ;SET BASE ADDR FOR LOAD EXT ERR REG.
1001 004152* 004767 000166 JSR PC,AWDN ;GO AWAIT DONE
1002 004156* 004767 000002 CALL CNXER ;CALL CHECK NON-EXIST MEMOPY ERROR
1003 004162* 000207 EDDVEC: RTS PC ;RETURN
1004 ;-----

```

```

1005 ;MOD U,CKNXER - CHECK NON-EXISTENT MEMORY ERRORS -----
1006
1007
1008 004164* 032777 004000 174176 CKNXER: BIT #4000,0RXDB ;IF NONEXISTENT MEMORY
1009 004172* 001432 BEQ XNFR ;ERROR, THEN
1010 004174* 004767 000062 CALLI SUPTRG ;CALL SETUP PRINT REGISTERS
1011 004200* 012777 040000 174160 MOV #40000,0RXCS ;INITIALIZE DEVICE (TO CLEAR ERROR)
1012 004206* 012767 000010 173672 MOV #10,ERRTYP ;SET NON-EXISTENT MEMORY ERROR
1013 ;*****
1014 004214* 104405 000000* 000000 HDRERS,BEGIN,0ULL ;NON-EXISTENT MEMORY ERROR
1015 ;*****
1016 004222* 104402 005764* 000000* MSGS0,NXERMS,BEGIN
1017 004230* 005267 174076 INC NXERCT ;INCREMENT NONEXISTENT ERROR COUNT MEMORY
1018 004234* 022767 000004 174070 CMP #4,NXERCT ;IF NON-EXISTENT MEMORY
1019 004242* 101006 BHI XNXER ;ERROR COUNT=3, THEN
1020 004244* 012767 000000 174064 10: MOV #1,FIN ;SET FINI FLAG
1021 004252* 104402 000000* 005504* MSGS0,BEGIN,DPP5 ; ASCII MESSAGE CALL WITH NO HEADER
1022 004260* 000207 XNXFR: RETURN ;RETURN
1023 ;-----
1024
1025 ;MOD U,SUPG - SETUP PRINT REGISTERS -----
1026
1027 004262* 017767 174100 174050 SUPTRG: MOV 0RXCS,0RXCS ;GET RXCS
1028 004270* 056767 173766 174042 BIS CMD,0RXCS ;SET 0RXCS TO SHOW COMMAND
1029 004276* 017767 174066 174036 MOV 0RXDB,0RXES ;GET RXES
1030 004304* 016767 173764 174034 MOV SA,0RXSA ;GET SECTION ADDRESS
1031 004312* 016767 173754 174024 MOV TA,0RXTA ;SET TRACK ADDRESS
1032 004320* 016767 174014 173554 MOV 0RXCS,0ACSR ;SET CONTENTS OF RXCS
1033 004326* 016767 174034 173544 MOV 0RXCS,0CSR ;SET ADDRESS OF RXCS
1034 004334* 016767 174002 173542 MOV 0SPXS,0ASTAT ;GET 0PXS
1035 004342* 000207 RETURN ;RETURN
1036 ;-----

```

```

1037 ;MOD U,WDN AWAIT DONE BIT SUBROUTINE -----
1038
1039 004344* 005067 173700 AWDN: CLR T0MLT ;PRESET TIME OUT MULTIPLIER
1040 004350* 005067 173736 10: CLR T0CNT ;PRESET TIME OUT COUNTER
1041 004354* 032777 000040 174004 20: BIT #40,0RXCS ;SEE IF DONE SET
1042 004362* 001033 RNF 30 ;IF 001 BR
1043 004364* 104407 000000* BREA0,BEGIN ;TEMPORARY RETURN TO MONITOR,...
1044 004370* 104407 000000* BREA0,BEGIN ;THEN CONTINUE AT NEXT INSTRUCTION.
1045 004374* 005267 173712 INC T0CNT ;BUMP TIME OUT COUNTER
1046 004400* 001365 BNE 20 ;IF NOT TIMED OUT: BR
1047 004402* 005267 173707 INC T0MLT ;INCREMENT TIMEOUT MULTIPLIER
1048 004406* 022767 000002 173674 CMP #2,T0MLT ;IF ON 2ND
1049 004414* 101355 BHI 10 ;TIMEOUT PASS, THEN
1050 004416* 004767 177640 CALL SUPTRG ;CALL SETUP PRINT REGISTERS
1051 004422* 012767 000011 173456 MOV #11,ERRTYP
1052 ;*****
1053 004430* 104405 000000* 000000 HDRERS,BEGIN,0ULL ;DONE BIT TIME OUT
1054 ;*****
1055 004436* 104402 000000* 005554* MSGS0,BEGIN,DPP5 ; ASCII MESSAGE CALL WITH NO HEADER
1056 004444* 012767 000001 173664 MOV #1,FIN ;DROD MODULE
1057 004452* 000207 RTS PC ;EXIT
1058 ;-----
1059
1060 ;MOD U,ATR AWAIT TRANSFER READY SUBROUTINE -----
1061
1062 004454* 005067 173630 AATR: CLR T0MLT ;PRESET TIMEOUT MULTIPLIER
1063 004460* 005067 173626 10: CLR T0CNT ;PRESET TIME OUT COUNTER
1064 004464* 032777 000040 173674 20: BIT #40,0RXCS ;IF DONE BIT NOT
1065 004472* 001026 BNE 40 ;SET, THEN
1066 004474* 032777 000200 173664 BIT #200,0RXCS ;SEE IF TRANSFER READY SET
1067 004502* 001046 BNE 60 ;IF 001 BR
1068 004504* 005767 173616 TST 0KBYP0 ;IF BREAK BYPASS FLAG
1069 004510* 001004 BNE 30 ;NOT SET, THEN
1070 004512* 104407 000000* BREA0,BEGIN ;TEMPORARY RETURN TO MONITOR,...
1071 004516* 104407 000000* BREA0,BEGIN ;THEN CONTINUE AT NEXT INSTRUCTION.
1072 004522* 005267 173564 INC T0CNT ;BUMP TIME OUT COUNTER
1073 004526* 001356 BNE 20 ;IF NOT TIMED OUT: BR
1074 004530* 005267 173554 INC T0MLT ;INCREMENT TIMEOUT MULTIPLIER
1075 004534* 022767 000002 173546 CMP #2,T0MLT ;IF ON 2ND
1076 004542* 101346 BHI 10 ;TIMEOUT PASS, THEN
1077 004544* 004767 177512 CALL SUPTRG ;CALL SETUP PRINT REGISTERS
1078 004550* 012767 000040 173330 40: MOV #40,ERRTYP
1079 ;*****
1080 004556* 104405 000000* 000000 HDRERS,BEGIN,0ULL ;TRANSFER READY TIME OUT
1081 ;*****
1082 004564* 012767 000001 173544 MOV #1,FIN ;GO DROD MODULE
1083 004572* 032777 000040 173566 BIT #40,0RXCS ;IF DONE BIT
1084 004600* 001044 BEQ 50 ;SET, THEN
1085 004602* 104402 000000* 005574* MSGS0,BEGIN,TRDNER ; ASCII MESSAGE CALL WITH NO HEADER
1086 004610* 000003 RR 60 ;EXIT
1087 004612*
1088 004612* 104402 000000* 005564* 50: MSGS0,BEGIN,TRTO ; ASCII MESSAGE CALL WITH NO HEADER
1089 004620* 005067 173502 60: CLR 0KBYP0 ;CLEAR BREAK BYPASS FLAG
1090 004624* 000207 RTS PC ;RETURN
1091 ;-----

```

```
1092 ;--- MESSAGE TABLE -----
1093
1094 004626* 000340* TABLE:
1095 004626* 000342* ARXCS: SPXCS
1096 004630* 000344* ARXES: SPXFS
1097 004632* 000344* ARXTA: SRXTA
1098 004634* 000346* ARXSA: SPXSA
1099 004636* 000350* ARXSB: SRXSB
1100 004640* 000352* ARXSB1: SPXSB1
1101 004642* 000354* ARXSB2: SRXSB2
1102 004644* 000356* ARXSB3: SPXSB3
1103 004646* 177777 -1
1104 ;-----
1105 ;--- MESSAGES -----
1106
1107
1108 004650* 054122 030460 051440 MSG01: .ASCIZ 'RX01 SUBSYSTEM'
1109 004656* 041125 054523 052123
1110 004664* 046505 000
1111 004667* 040 044524 042515 MSG02: .ASCIZ ' TIME OUT'
1112 004674* 047440 052125 000
1113 004701* 040 051104 053111 MSG03: .ASCIZ ' DRIVE '
1114 004706* 020105 000
1115 004711* 040 037055 051104 MSG04: .ASCIZ ' ->DROP MODULE%'
1116 004716* 050117 046440 042117
1117 004724* 046125 022505 000
1118 004731* 040 047111 052111 MSG05: .ASCIZ ' INITIALIZE'
1119 004736* 040511 044514 042537
1120 004744* 000
1121 004745* 040 047516 052440 MSG06: .ASCIZ ' NO UNITS TO TEST'
1122 004752* 044516 051524 052040
1123 004760* 020117 042524 052123
1124 004766* 000
1125 004767* 045 051040 044505 MSG07: .ASCIZ '% REINITIALIZE'
1126 004774* 044516 044524 046101
1127 005002* 055111 000105
1128 005006* 051440 052105 026440 MSG08: .ASCIZ ' SET -> WAITING ON'
1129 005014* 020076 040527 052111
1130 005022* 047111 020107 047117
1131 005030* 000
1132 005031* 040 044506 046114 MSG09: .ASCIZ ' FILL BUFFER'
1133 005036* 041040 043125 047506
1134 005044* 000122
1135 005046* 051440 047111 046107 MSG10: .ASCIZ ' SINGLE'
1136 005054* 000105
1137 005056* 042040 052517 046102 MSG12: .ASCIZ ' DOUBLE'
1138 005064* 000105
1139 005066* 042040 047117 020105 MSG13: .ASCIZ ' DONE BIT'
1140 005074* 044502 000124
1141 005100* 052040 040522 051516 MSG14: .ASCIZ ' TRANSFER READY'
1142 005106* 042506 020122 042522
1143 005114* 042101 000131
1144 005120* 042040 047105 044523 MSG15: .ASCIZ ' DENSITY'
1145 005126* 054524 000
1146 005131* 040 051105 047522 MSG17: .ASCIZ ' ERROR- RETRY!'
1147 005136* 026522 051040 052105
```

```
1148 005144* 054522 000072
1149 005150* 020045 042523 000124 MSG18: .ASCIZ '% SFT'
1150 005156* 020045 051127 052111 MSG19: .ASCIZ '% WRITF'
1151 005164* 000105
1152 005166* 020045 042522 042101 MSG20: .ASCIZ '% READ'
1153 005174* 000
1154 005175* 040 046505 052120 MSG21: .ASCIZ ' EMPTY BUFFER'
1155 005202* 020131 052502 043106
1156 005210* 051105 000
1157 005213* 040 047516 020124 MSG22: .ASCIZ ' NOT AVIALABLE'
1158 005220* 053101 040511 040514
1159 005226* 046102 000105
1160 005232* 042440 051122 051117 MSG23: .ASCIZ ' ERROR'
1161 005240* 000
1162 005241* 045 026440 051476 MSG24: .ASCIZ '% ->SETTING DISKETTE TO'
1163 005246* 052105 044524 043516
1164 005254* 042040 051511 042513
1165 005262* 052124 020105 047524
1166 005270* 000
1167 005271* 045 020040 044440 MSG25: .ASCIZ '% IF INTERRUPTED-> MAY NEED REFORMAT%'
1168 005276* 020106 047111 042524
1169 005304* 051122 050125 042524
1170 005312* 026504 020076 040515
1171 005320* 020131 042516 042105
1172 005326* 051040 043105 051117
1173 005334* 040515 022524 000
1174 005341* 045 000
1175 005343* 055 020076 047516 MSG26: .ASCIZ '% '
1176 005350* 042516 044530 052123 MSG27: .ASCIZ '-> NONEXISTENT MEMORY ERROR - INITIALIZE DEVICE'
1177 005356* 047105 020124 042515
1178 005364* 047515 054522 042440
1179 005372* 051122 051117 026440
1180 005400* 044440 044516 044524
1181 005406* 046101 055111 020105
1182 005414* 042504 044526 042503
1183 005422* 000
1184 005424* 030040 000
1185 005424* 030040 000
1186 005430* 000
1187 005430* 030040 000
1188 005434* 000
1189
DNUN: .EVEN
      .ASCIZ ' 0'
      .EVEN
RTYN: .ASCIZ ' 0'
      .EVEN
;-----
```

```
1190 ;---- FORMATTED MESSAGES -----
1191 005434 005175 BFER1: MSG21 ]"EMPTY BUFFER"
1192 005436 005232 MSG23 ]"ERROR"
1193 005440 177777 -1
1194 005442 005031 BFER2: MSG9 ]"FILL BUFFER"
1195 005444 005232 MSG23 ]"ERROR"
1196 005446 177777 -1
1197 005450 004731 DRP1: MSG5 ]"INITIALIZE"
1198 005452 005232 MSG23 ]"ERROR"
1199 005454 004711 MSG4 ]"DROP MODULE"
1200 005456 177777 -1
1201 005460 004745 DRP2: MSG6 ]"NO UNITS TO TEST"
1202 005462 004711 MSG4 ]"DROP MODULE"
1203 005464 177777 -1
1204 005466 004767 DRP3: MSG7 ]"REINITIALIZE"
1205 005470 005232 MSG23 ]"ERROR"
1206 005472 004711 MSG4 ]"DROP MODULE"
1207 005474 177777 -1
1208 005476 004650 DRP4: MSG1 ]"RX01 SUBSYSTEM"
1209 005500 004711 MSG4 ]"DROP MODULE"
1210 005502 177777 -1
1211 005504 004711 DRF5: MSG4 ]"DROP MODULE"
1212 005506 177777 -1
1213 005510 004701 MINT: MSG3 ]"DRIVE"
1214 005512 005424 DNUM ]"#"
1215 005514 005213 MSG22 ]"NOT AVAILABLE"
1216 005516 177777 -1
1217 005520 004701 SETSDE: MSG3 ]"DRIVE"
1218 005522 005424 DNUM ]"#"
1219 005524 005150 MSG10 ]"SET"
1220 005526 005046 MSG11 ]"SINGLE"
1221 005530 005120 MSG15 ]"DENSITY"
1222 005532 005232 MSG23 ]"ERROR"
1223 005534 177777 -1
1224 005536 004701 SETDDE: MSG3 ]"DRIVE"
1225 005540 005424 DNUM ]"#"
1226 005542 005150 MSG10 ]"SET"
1227 005544 005056 MSG12 ]"DOUBLE"
1228 005546 005120 MSG15 ]"DENSITY"
1229 005550 005232 MSG23 ]"ERROR"
1230 005552 177777 -1
1231 005554 005066 DNT0: MSG13 ]"DONE BIT"
1232 005556 004667 MSG2 ]"TIME OUT"
1233 005560 004711 MSG4 ]"DROP MODULE"
1234 005562 177777 -1
1235 005564 005100 TRT0: MSG14 ]"TRANSFER READY"
1236 005566 004667 MSG2 ]"TIME OUT"
1237 005570 004711 MSG4 ]"DROP MODULE"
1238 005572 177777 -1
1239 005574 005066 TPDNEP: MSG13 ]"DONE BIT"
1240 005576 005006 MSG8 ]"SET => WAITING ON"
1241 005600 005100 MSG14 ]"TRANSFER READY"
1242 005602 004711 MSG4 ]"=>DROP MODULE"
1243 005604 177777 -1
1244 005606 004701 WTERDD: MSG3 ]"DRIVE"
1245 005610 005424 DNUM ]"#"

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```
1246 005612 005156 MSG19 ]"WRITE"
1247 005614 005056 MSG12 ]"DOUBLE"
1248 005616 005120 MSG15 ]"DENSITY"
1249 005620 005131 MSG17 ]"ERROR-RETRY!"
1250 005622 005430 RTYN ]"#"
1251 005624 005341 MSG26 ]"CRLF"
1252 005626 177777 -1
1253 005630 004701 WTERSD: MSG3 ]"DRIVE"
1254 005632 005424 DNUM ]"#"
1255 005634 005156 MSG19 ]"WRITE"
1256 005636 005046 MSG11 ]"SINGLE"
1257 005640 005120 MSG15 ]"DENSITY"
1258 005642 005131 MSG17 ]"ERROR-RETRY!"
1259 005644 005430 RTYN ]"#"
1260 005646 005341 MSG26 ]"CRLF"
1261 005650 177777 -1
1262 005652 004701 PTERDD: MSG3 ]"DRIVE"
1263 005654 005424 DNUM ]"#"
1264 005656 005166 MSG20 ]"READ"
1265 005660 005056 MSG12 ]"DOUBLE"
1266 005662 005120 MSG15 ]"DENSITY"
1267 005664 005131 MSG17 ]"ERROR-RETRY!"
1268 005666 005430 RTYN ]"#"
1269 005670 005341 MSG26 ]"CRLF"
1270 005672 177777 -1
1271 005674 004701 WTERSD: MSG3 ]"DRIVE"
1272 005676 005424 DNUM ]"#"
1273 005678 005166 MSG20 ]"READ"
1274 005680 005046 MSG11 ]"SINGLE"
1275 005682 005120 MSG15 ]"DENSITY"
1276 005684 005131 MSG17 ]"ERROR-RETRY!"
1277 005686 005430 RTYN ]"#"
1278 005688 005341 MSG26 ]"CRLF"
1279 005690 177777 -1
1280 005692 004701 SETSDN: MSG3 ]"DRIVE"
1281 005694 005424 DNUM ]"#"
1282 005696 005241 MSG24 ]"SETTING DISKETTE TO"
1283 005698 005046 MSG11 ]"SINGLE"
1284 005700 005120 MSG15 ]"DENSITY"
1285 005702 005271 MSG25 ]"IF INTERRUPTED=MAY NEED REFORMAT"
1286 005704 177777 -1
1287 005706 004701 SSGLDN: MSG3 ]"DRIVE"
1288 005708 005424 DNUM ]"#"
1289 005710 005241 MSG24 ]"SETTING DISKETTE TO"
1290 005712 005046 MSG11 ]"SINGLE"
1291 005714 005120 MSG15 ]"DENSITY"
1292 005716 177777 -1
1293 005718 004701 SDBLDN: MSG3 ]"DRIVE"
1294 005720 005424 DNUM ]"#"
1295 005722 005241 MSG24 ]"SETTING DISKETTE TO"
1296 005724 005056 MSG12 ]"DOUBLE"
1297 005726 005120 MSG15 ]"DENSITY"
1298 005728 177777 -1
1299 005730 005343 NXERMS: MSG27 ]"=>NON EXISTENT MEMORY ERROR"
1300 005732 177777 -1
1301 ;-----

```


OUBUF2	001400R	543	569*
OUTPUT	001232R	487	542*
PASCNT	000034R	272*	
PIRQ8	000000R	322*	866
POPSP	005726	322*	
POPSP2	022626	322*	
PPTY0	000000R	322*	
PPTY0	000000R	262	322*
PPTY1	000043	322*	
PPTY2	000101	322*	
PPTY3	000141	322*	
PPTY4	000200	322*	
PPTY5	000240	261	322*
PPTY6	000300	322*	
PPTY7	000340	322*	
PS	177776	322*	
PSN	177776	322*	
PUSH	005746	322*	
PUSH2	024646	322*	
RAND6	104417	322*	
RANNUM	000054R	290*	
RBUF	005770R	304	1305*
RBUFEA	000130R	306*	626
RBUFPA	000126R	305*	639 656
RBUFSZ	000132R	307*	
RBUFVA	000124R	304*	485
READ	000752R	490*	
RESTRT	000666R	299	476* 527
RES1	000056R	292*	
RES2	000060R	283*	
RSTRT	000112R	299*	
RTERDD	005652R	922	1262*
RTERSDD	005674R	919	1271*
RTRVFL	000330R	347*	615 880* 956*
RTYN	005430R	546*	601* 953* 954 966* 1187* 1250 1259 1268 1277
RXCS	000366R	370*	464 529* 533 550* 574* 589 605* 629* 643 646 724* 746*
		768*	774* 798* 810* 854* 877 946* 948 994* 1011* 1027 1033 1041
RXDB	000370R	1064	1066 1083 1084* 1085* 1086* 1087* 1088* 1089* 1090* 1091* 1092* 1093* 1094* 1095*
SA	000274R	371*	554 558* 578* 584* 609* 613* 633* 639* 726 750 778* 802*
SASAV	000300R	333*	622* 856* 1000* 1029
SBADP	000102R	292*	481* 554 609 662* 663 666* 1030
SDBLDN	005750R	823	1293*
SECK	003372R	781	860 876*
SELD	002144R	475*	520* 678 685
SERFL	000334R	349*	444* 622 876* 942*
SETDDE	005536R	907	1224*
SEISDE	005520R	904	1217*
SETSDB	005716R	779	1280*
SOF CNT	000042R	275*	
SOFER6	104406	322*	915 929
SOPPAS	000046R	277*	
SPOINT	000032R	271*	433
SPSIZ	000040R	1	315
SRXCS	000340R	356*	897 1027* 1028* 1032 1095

SRXES	000342R	357*	1029*	1034	1096
SRXSA	000346R	359*	1030*	1098	
SRXSR	000350R	360*	364	943	967* 1099
SRXSB1	000352R	361*	1100		
SRXSB2	000354R	362*	1101		
SRXSB3	000356R	363*	1102		
SRXTA	000344R	358*	1031*	1097	
SR1	000016R	264*	962		
SR2	000020R	265*			
SR3	000022R	266*			
SR4	000024R	267*			
SSGLDN	005734R	803	1287*		
START	000376R	270*	432*		
STAT	000026R	269*			
STATUS	002234R	471	692*		
SUPTRG	004262P	591*	648*	882*	972* 1010* 1027* 1050* 1077*
SVR0	000062R	284*			
SVR1	000064R	285*			
SVR2	000066R	286*			
SVR3	000070R	287*			
SVR4	000072R	288*			
SVR5	000074R	289*			
SVR6	000076R	290*			
SYSCNT	000052R	279*			
TA	000272P	332*	482*	505*	506* 508* 558 613 1031
TABLE	004626R	915	929	960 979 1094*	
TASAV	000276R	334*	434*	482 508 514*	515 517*
IDRP	000014P	536	951	972*	
TOCNT	000312R	340*	1040*	1045*	1063* 1072*
TOMLT	000310R	339*	1039*	1047*	1048 1062* 1074* 1075
TRDNEP	005574P	1085	1239*		
TRPDID	000022	322*			
TRTO	005564R	1088	1235*		
TSS	002106P	493*	662*		
TSSFLG	000320P	343*	499	501*	667*
UDNCK	003246R	502*	839*		
UNIT0	000254R	325*	440*	683 694 710 737*	811 831
UNIT1	000256R	326*	441*	676 702 759*	787 841
UNIT0	002362R	693	720*		
UNIT1	002500R	701	742*		
UT	000266R	330*	789*	792*	797 807 809* 817 827 829*
UTDNF	000316R	342*	503	843*	845*
UTT	000264F	329*	442*	547	602 679* 686* 698* 706* 767 773 839 843
U0	002236R	693*			
U1	002274R	695	697	701*	
VARIFY	000374R	373*	778	802	822
VEC	000372R	372*			
VECTOP	000010R	260*	851	869	
VSET	003300R	463	851*		
WASADF	000104R	294*			
WBUFEA	000136R	309*			
WBUFPA	000134R	308*			
WBUFPO	000140R	310*			
WBUFSZ	000142R	311*			
WDCNT	000302R	336*	436*	578	633
WDFR	000116R	301*			

WDIO	000114R	300#							
WRIF	000740R	487#							
WTRDD	005609R	936	1244#						
WTRSD	005630R	933	1253#						
WTF	000270R	331#	542#	564#	886				
XDNER	003542R	900	905	908#					
XDNSET	002732R	771	777	782#					
XFLAG	000005R	258#							
XNXER	004260R	1009	1019	1022#					
XUDNCK	003276R	844	846#						
.	= 006372R	365	366	657	1184#	1186#	1188#	1306#	

. ABS. 000000 000
006372 001

ERRORS DETECTED: 0
DEFAULT GLOBALS GENERATED: 0

XRXRC0,XRXBC0/SOL/CFP:SYM=DDXCOM,XRXRC0
RUN-TIME: 7 12 1 SECONDS
RUN-TIME RATIO: 90/21=4.2
CORE USED: 9K (17 PAGES)

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.REPT 0

IDENTIFICATION

PRODUCT CODE: AC-5234b-MC
 PRODUCT NAME: CXVSC80 VSV11 CSS DEC/X MOD
 DATE CREATED: OCT 1,1980
 MAINTAINER: CSS PRODUCTS GROUP
 AUTHOR(S): GUS PASQUANTONIO
 PAUL TAYLOR
 DICK GURDON

REVISION HISTORY:

PAUL TAYLOR	18-AUG-81	<VERSION A1>
DICK GURDON	20-DEC-81	<<VER B>>

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 MAYNARD MASS.

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ABSTRACT:

"XVSCB" IS A DEC/X11 SOFTWARE MODULE WHICH EXERCISES A VSV11/VSI1 GRAPHIC SYSTEM. THE "XVSCB" DEC/X11 MODULE CONSISTS OF

- *** VSV-11 INSTRUCTION SET.
- *** SPECIAL MACROS AND OPDEFS.
- *** DEVICE REGISTER ASSIGNMENTS
- *** JOY-STICK AND TIME-OUT INTERRUPT SERVICE ROUTINES.
- *** LOOK-UP-TABLE SERVICE ROUTINES.
- *** PROGRAM START RESTART
- *** PART 1 -- THE STANDARD VISUAL REFERENCE FRAME
- *** PART 2 -- THE OLD BOUNCING BALLS
- *** SR1 BIT0 = 1 = ENABLE PART 2.
- *** SR1 BIT1 = 1 = USE TWO MEMORY DYNAMIC MODE.
- *** SR1 BIT2 = 1 = DISABLE BELL ON BOUNCE.
- *** SR2 BIT0 = 1 = ENABLE COLOR LOOKUP TABLE <VERSION A1>
- *** NOTE:

THE EXERCISER WILL WORK WITH A SINGLE MEMORY CHANNEL, EXCEPT FOR THE COMBINATION OF SR1 BIT0=1 AND SR1 BIT1=1,THE VSV11/VSI1 CAN HAVE UP TO FOUR SEPARATE MEMORY CHANNELS (0 THRU 3) THOSE ARE JUMPER SELECTABLE ON THE MODULE,TO RUN THE TEST WITH BOTH SR1 BIT0=1 AND SR1 BIT1=1,TWO MEMORY CHANNELS ARE NEEDED, REFER THE VSV TECHNICAL MANUAL (FIGURE 2-4) FOR DYNAMIC CONFIGURATION.

- ALSO IF THE COLOR LOOKUP IS SPECIFIED ONLY ONE VSV IS EXERCISED. THIS IS DONE BY SETTING BIT 0 OF SR2.
- SR1 = 0 ;TEST PATTERN ONLY
 - SR1 = 1 ;DISPLAY+BOUNCING BALL+BELL
 - SR1 = 2 ;TEST PATTERN ONLY
 - SR1 = 3 ;TEST PATTERN+BOUNCING BALL+BELL(TWO CHANNELS)
 - SR1 = 4 ;TEST PATTERN ONLY
 - SR1 = 5 ;TEST PATTERN+BOUNCING BALL
 - SR1 = 6 ;TEST PATTERN ONLY
 - SR1 = 7 ;TEST PATTERN+BOUNCING BALL (TWO CHANNELS)

- *** PART 1 -- DISPLAY CODE
- *** PART 2 -- DISPLAY CODE
- *** ASCII CHARACTER ADDRESS TABLE AND SUBPIX
- *** 256 LEVEL LUMINANCE (GREY-SCALE) TABLE
- *** PATCH AREA AND END



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REQUIREMENTS:

PDP-11/LSI-11 PROCESSOR WITH 16K MEMORY OR MORE MEMORY
CONSOLE DEVICE (LA30,LA36,VT50,VT100,ETC.)
XXDP + LOAD DEVICE (RA,RP,RL,TR,DT,ETC.)
1 TO 8 VSV11 SYSTEMS, EACH CONSISTS OF:

M7004 IMAGE PROCESSOR
M7002 IMAGE MEMORY (1 TO 4 CHANNELS)
M7004 COLOR LOOK-UP RAM (OPTIONAL)
VCG04 SYNC GENERATOR WITH CURSOR CONTROL
DISPLAY MONITOR, COLOR OR MONOCHROME (OPTIONAL)

PASS DEFINITION:

ONE PASS OF THE "XVSCB" CONSISTS OF ONE ITERATION OF TEST SEQUENCE.

EXECUTION TIME:

ONE PASS OF "XVSCB" ALONE ON PDP11/34 TAKES ABOUT 10SEC.

CONFIGURATION REQUIREMENTS:

DEFAULT PARAMETERS:
DEVADR:172010,VECTOR:320,BR1:4,ICONST:1.

.ENDR

135
136 000000
000000

000000
000000 126 123 103
000003 102 040
000005 000
000006 172010
000010 000320
000012 200
000013 000
000014 000001
000016 000000
000020 000000
000022 000000
000024 000000

000026 140000
000030 001000
000032 000222
000034 000000
000036 000001
000040 000000
000042 000000
000044 000000
000046 000000
000050 000000
000052 000000
000054 000000
000056 000000
000060 000000
000062 000000
000064 000000
000066 000000
000070 000000
000072 000000
000074 000000
000076 000000
000100 000000
000102
000102 000000
000104
000104 000000
000106
000106 000000
000110 000000
000112 001026
000114 000000
000116 000000
000120 000000
000122 000040

```

IOMOD <VSCB >,172010,320,4,0,0,1
MODULE 140000,VSCB,172010,320,4,0,0,1,
.TITLE VSCB DEC/X11 SYSTEM EXERCISER MODULE
; DDACOM VERSION 6 23-MAY-78
.LIST BIN
;*****
BEGIN:
MODNAM: .ASC11 /VSCB / ;MODULE NAME.
XFLAG: .BYTE OPEN ;USED TO KEEP TRACK OF #BUFF USAGE
ADDR: 172010+0 ;1ST DEVICE ADDR.
VECTOR: 320+0 ;1ST DEVICE VECTOR.
BR1: .BYTE PRTY4+0 ;1ST BR LEVEL.
BR2: .BYTE PRTY0+0 ;2ND BR LEVEL.
DVID1: 0+1 ;DEVICE INDICATOR 1.
SR1: OPEN ;SWITCH REGISTER 1
SR2: OPEN ;SWITCH REGISTER 2
SR3: OPEN ;SWITCH REGISTER 3
SR4: OPEN ;SWITCH REGISTER 4
;*****
STAT: 140000 ;STATUS WORD.
INIT: START ;MODULE START ADDR.
SPOINI: MODSP ;MODULE STACK POINTER.
PASCNT: 0 ;PASS COUNTER.
ICONT: 1 ;# OF ITERATIONS PER PASS=1
ICOUNT: 0 ;LOC TO COUNT ITERATIONS
SOFCNT: 0 ;LOC TO SAVE TOTAL SOFT ERRORS
HRDCNT: 0 ;LOC TO SAVE TOTAL HARD ERRORS
SOFPAS: 0 ;LOC TO SAVE SOFT ERRORS PER PASS
HRDPAS: 0 ;LOC TO SAVE HARD ERRORS PER PASS
SYSCNT: 0 ;# OF SYS ERRORS ACCUMULATED
RANNUM: 0 ;HOLDS RANDOM # WHEN RAND MACRO IS CALLED
CONFIG:
RES1: 0 ;RESERVED FOR MONITOR USE
RES2: 0 ;RESERVED FOR MONITOR USE
SVR0: OPEN ;LOC TO SAVE R0.
SVR1: OPEN ;LOC TO SAVE R1.
SVR2: OPEN ;LOC TO SAVE R2.
SVR3: OPEN ;LOC TO SAVE R3.
SVR4: OPEN ;LOC TO SAVE R4.
SVR5: OPEN ;LOC TO SAVE R5.
SVR6: OPEN ;LOC TO SAVE R6.
CSRA: OPEN ;ADDR OF CURRENT CSR.
SBADR: OPEN ;ADDR OF GOOD DATA, OR
ACSR: OPEN ;CONTENTS OF CSR.
#ASADR: OPEN ;ADDR OF BAD DATA, UN
ASTAT: OPEN ;STATUS REG CONTENTS.
ERRTYP: OPEN ;TYPE OF ERROR
ASB: OPEN ;EXPECTED DATA.
#AS: OPEN ;ACTUAL DATA.
RSTRT: RSTRT ;RESTART ADDRESS AFTER END OF PASS
#MDS: OPEN ;#WORDS TO MEMORY PER ITERATION
#MFR: OPEN ;#WORDS FROM MEMORY PER ITERATION
INTR: OPEN ;# OF INTERRUPTS PER ITERATION
IDNUM: OPEN ;MODULE IDENTIFICATION NUMBER
;REPT SPS1Z ;MODULE STACK STARTS HERE.
    
```

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.NLIST
.WORD 0
.LIST
.ENDR

000272 MODSP:
;*****
;
; DECK MODULE FOR THE VSV-11 GRAPHICS IMAGE PROCESSER.
;
; NOTE THAT THIS PROGRAM REQUIRES THE MACRO PACKAGE DDxCDM.C6.
;
.SBTTL
.SRTTL
.SBTTL *** DECK MODULE FOR VSV-11
.SBTTL
.SBTTL
;
; BUILD A RUN TIME MODULE UNDER XXDP+ AS FOLLOWS:
;
; .R DXCL<CR>
;
;      :
;      : BLAH=BLAH=BLAH      ; GREETINGS.
;      :
;      : *CNF/NP<CR>        ; CONFIGURE, NO PROMPT.
;      : *MUNITOR: C<CR>    ; USE C MONITOR.
;      : *MDL VSCA<CR>      ; THIS MODULE NAME...
;      : *MDL NAMZ<CR>      ; ...AND ANY OTHERS.
;      :
;      :
;      : *EX<CR>           ; LEAVE CONFIGURE MODE.
;
;
; *LINK ODEV:NAME,BIN=IDEV:LIBNAM.LIB<CR>
; SYS SIZE: 16000<CR>
;
;      :
;      : BLAH=BLAH=BLAH    ; LINKER PROGRESS REPORTS.
;      :
;      :
;      : LINK DUNE
;      : *
;
;PRGSIZ=      ^H<LASTAD>+1 ; PROGRAM SIZE IN 1/8K UNITS.
.NLIST ME
.LIST MEB
    
```

```

.SBTTL *** VSV-11 INSTRUCTION SET.
;
; PLOTTING INSTRUCTIONS.
;
;
174          100000    CHAR= 100000      ; CHARACTER MODE.
175          104000    SVEC= 104000     ; SHORT VECTOR MODE.
176          110000    LVFC= 110000     ; LONG VECTOR MODE.
177          114000    APNT= 114000     ; ABSOLUTE POINT MODE.
178          120000    GHX= 120000      ; GRAPH/HISTOGRAM X MODE.
179          124000    GHY= 124000      ; GRAPH/HISTOGRAM Y MODE.
180          130000    HPNT= 130000     ; RELATIVE POINT MODE.
;
; PIXEL DATA FIELD <10:2> FOR THE ABOVE OPCODES.
;
181          002700    LO=      ^B100000000000      ; INTENSITY LEVEL 0 (BLACK)
182          002000    NONE=  LO
183          003774    ALL=    LO<255.*4>          ; MAX IS 256. GREY SCALE LEVELS.
184          003774    #HITE= ALL
185          002170    BLU=    LO<36.*4>           ; ON THE GREY-SCALE, BLUE = L36...
186          002500    RED=    LO<120.*4>          ; ...RED = L120...
187          003140    GRN=    LO<230.*4>          ; ...AND GREEN = L230.
;
; BIT MAP MODE DEFINITIONS.
;
188          136000    BM14= 136000     ; MODE 1 WITH 4 BIT PIXELS.
189          137000    BM18= 137000     ; MODE 1 WITH 8 BIT PIXELS.
190          137000    ; PIXEL COUNT IN <8:0>.
;
191          134000    BM04= 134000     ; MODE 0 WITH 4 BIT PIXELS.
192          135000    BM08= 135000     ; MODE 0 WITH 8 BIT PIXELS.
193          000000    M32= 0           ; 32 SQUARE PIXEL ARRAY.
194          000001    M64= 1           ; 64 SQUARE.
195          000002    M128= 2          ; 128 SQUARE.
196          000003    M256= 3          ; 256 SQUARE.
197          000010    EX2= 10          ; EXPAND BY 2, NO SMOOTHING.
198          000020    EX4= 20          ; EXPAND BY 4, NO SMOOTHING.
199          000050    EXSM2= 50        ; EXPAND AND SMOOTH BY 2.
200          000060    EXSM4= 60        ; EXPAND AND SMOOTH BY 4.
201          000100    R256= 100        ; 256 X 256 RESOLUTION.
    
```



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214 ;
215 ;CONTROL AND STATUS INSTRUCTIONS.
216 ;
217 146040 CUOFF= 146040 ;CURSOR (JOY-STICK) VIDEO OFF.
218 146060 CUON= 146060 ;CURSOR VIDEO ON.
219 146100 CUR0= 146100 ;READ JSX,JSY => DXR,OYR.
220 146016 CUIM= 146016 ;INTERRUPT ON MATCH (SWITCH DISABLED).
221 146013 CUIS= 146013 ;INTERRUPT ON SWITCH (MATCH DISABLED).
222 146012 CUIOFF= 146012 ;INTERRUPTS (BOTH) OFF.
223
224 150000 SETHB= 150000 ;SET GRAPH/HISTO BASE LINE.
225 152000 SETCB= 152000 ;SET CHARACTER BASE ADDRESS.
226
227 160000 DJMP= 160000 ;DISPLAY JUMP.
228 164000 DNOP= 164000 ;DISPLAY NO-OP.
229 165000 DPDP= 165000 ;DPDP = HTS FROM CHARACTER SUB-PIX.
230 164001 SYNC= DNOP+1 ;PROCEED IN SYNC = DNOP + N <8:0>.
231 ;WAITS FOR N OCCURRENCES OF VERTICAL...
232 ;...SYNC START, THEN PROCEEDS...
233 ;...(16.6MS @ 60HZ OR 20MS @ 50HZ).
234
235 000010 ADDRAS= 10 ; DIFFERENCE BETWEEN VSV11 BASE ADDRESSES <VERSION A1>
236 000020 VECBAS= 20 ; DIFFERENCE BETWEEN VECTORS <VERSION A1>
237 170200 SWTCH= 170200 ;SWITCH READ/WRITE STATUS.
238 170140 CLRME= 170140 ;CLEAR IMAGE MEMORY TO 0'S.
239 170100 SETME= 170100 ;SET IMAGE MEMORY TO CURRENT PIX DATA.
240
241 172000 STOP= 172000 ;DISPLAY STOP.
242 171000 SIOFF= 171000 ;STOP INTERRUPT DISABLE.
243 171400 SION= 171400 ;STOP INTERRUPT ENABLE.
244 173000 STUPN= STOP!SIOFF ;STOP, DO NOT INTERRUPT.
245 173400 STOP1= STOP!SION ;STOP AND INTERRUPT.
246
247 174100 GXI= 174100 ;SET X INCREMENT <5:0> FOR GRAPH/HST Y.
248 174100 GYI= GXI ;SET Y INCREMENT <5:0> FOR GRAPH/HST X.
249
250 176000 PROTEC= 176000 ;PROTECT MEMORY (OFF).
251 176050 READ= 176040!SWE ;READ MEM (DISPLAY), SWITCH ENABLED.
252 176034 WRT= 176024!SWE ;WRITE MEM (1'S AND 0'S), SWITCH ENABLED.
253 176036 WRT1= 176026!SWE ;WRITE MEM (1'S ONLY), SWITCH ENABLED.
254 000010 SWE= 10 ; ENABLE SWITCH READ/WRITE.
255 176074 RDWRT= READ!WRT ;READ, WRITE ALL DURING RETRACE.
256 176076 RDWRT1= READ!WRT1 ;HEAD, WRITE 1'S DURING RETRACE.
257
258 000000 CH0= 0 ;CHANNEL SELECT BITS...
259 000400 CH1= 400 ;...FOR MEMORY CONTROL...
260 001000 CH2= 1000 ;...INSTRUCTIONS (17bXXX).
261 001400 CH3= 1400
262
263 000000 JS0= CH0 ;JOY-STICK UNIT SELECT BITS...
264 000400 JS1= CH1 ;...FOR CURSOR CONTROL...
265 001000 JS2= CH2 ;...INSTRUCTIONS (14bXXX).
266 001400 JS3= CH3
  
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268 ;
269 ; COLUP LOOK-UP-RAM EQUATES
270 ;
271 100000 LDUM= 100000 ; CSR REGISTER -- FUNCTION DONE BIT.
272 010000 LREAD= *B1000000000000 ; READ DATA FROM ADDRESS <7:0>.
273 002000 LDI= *B10000000000 ; INTERRUPT ON READ/WRITE DONE.
274
275 010000 LSET= *B100000000000 ; DATA REGISTER -- SET ALL ADDRESSES...
276 ;...WITH DATA IN <11:0>...
277 ;...BLUE<11:8>, GREEN<7:4>, RED<3:0>.
278
279 000100 GCOFF= *B1000000 ; MAINTENANCE REGISTER...
280 ;...GAMMA CORRECTION OFF.
281
282 ;
283 ;OTHER USEFUL DEFINITIONS.
284 ;
284 040000 I= *B1000000000000000 ;INTENSIFY VECTOR OR POINT.
285 020000 MSX= *B10000000000000 ;MINUS SIGNS FOR...
286 000100 MSY= *B1000000 ;...SHORT FORM VECTOR DATA.
287 020000 MXY= *B10000000000000 ;MINUS SIGN FOR LONG FORM DATA.
288 040000 HST= *B10000000000000 ;USE GHX/GHY DATA AS HISTOGRAM.
289
290 001776 MAXX= 511.*2 ;MAXIMUM X ADDRESS (512 X 512 X 2).
291 001776 MAXY= MAXX ;SAME FOR Y.
292 000776 HAFX= <MAXX/2>-1 ;HALF MAX X (CENTER SCREEN).
293 000776 HAFY= HAFX ;SAME FOR Y.
294
295 001776 MAXY50= 511.*2 ;MAX VISIBLE Y ON 50HZ SYSTEM.
296 000776 HAFY50= <MAXY50/2>-1 ;HALF VISIBLE Y.
297
298 001676 MAXY60= <511.-32.>*2 ;MAX VISIBLE Y ON 60HZ SYSTEM.
299 000736 HAFY60= <MAXY60/2>-1 ;HALF VISIBLE Y.
300
301 000002 DX= 2 ;DELTA X (1 PIXEL UNIT).
302 000002 DYI= DX ;DELTA Y, INTERLACED MODE.
303 000004 DYNI= DX*2 ;DELTA Y, NON-INTERLACED MODE.
304
305 000014 SETRR= 0+14 ; SET RELOCATE REGISTER WITH <15:4>...
306 ;...DSR<15:4> BECOMES RR<17:6>...
307 ;...VSV RELOCATES ON 32. WORD BOUNDARY.
  
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309      .SBTTL *** SPECIAL MACROS AND UPDEFS.
310      ;
311      ; MACROS FOR ASSEMBLING VECTOR DATA.
312      ; CALLING ARGUMENTS ARE:
313      ;   A = INTENSIFY BIT, "I" OR "U".
314      ;   B = DELTA X, +/-0 THRU 1776 (76 IF SHORT TYPE).
315      ;   C = DELTA Y,          DITTO
316      ;
317      .MACRO SKY A,B,C      ;ASSEMBLE SHORT (1 WORD) DATA.
318      SI = "B1000000000000000"
319      SX = <B*200>
320      SY = C
321      .IIF IDN <U> <A>, SI = 0
322      .IIF LT B, SX = <-B*200!<"B1000000000000000"
323      .IIF LT C, SY = <-C>!<"B10000000"
324      .WORD SI+SX+SY
325      .ENDM SKY
326
327      .MACRO LXV A,B,C      ;ASSEMBLE LONG (2 WORDS) DATA.
328      SI = "B1000000000000000"
329      SX = B
330      SY = C
331      .IIF IDN <U> <A>, SI = 0
332      .IIF LT B, SX = <-B>!<"B1000000000000000"
333      .IIF LT C, SY = <-C>!<"B1000000000000000"
334      .WORD SI+SX, SY
335      .ENDM LXV
336
337      ;SOME HANDY -11 UPDEF'S.
338      ;
339      SKP1= BK+1      ;SKIP NEXT WORD.
340      SKP2= BK+2      ;SKIP NEXT 2 WORDS.
341      SKP3= BK+3      ;SKIP NEXT 3 WORDS.
  
```

```

343      .SBTTL *** DEVICE REGISTER ASSIGNMENTS
344      ;
345      ; DEVICE REGISTER ASSIGNMENTS.
346      ;
347      DPC: 172010      ;DISPLAY PC
348      DRK:
349      DSR: 172012      ;DISPLAY STATUS REGISTER (ALSO REL REG).
350      DXR: 172014      ;DISPLAY X POSITION REGISTER.
351      DYR: 172016      ;DISPLAY Y POSITION REGISTER.
352
353      LSR: 172020      ; LOOK-UP-RAM CSR.
354      LDR: 172022      ; LOOK-UP-RAM DATA REGISTER.
355      LMR: 172024      ; LOOK-UP-RAM MAINTENANCE REGISTER.
356
357      STPV: 320        ;STOP VECTOR
358      JSMV: 324        ;JOY-STICK MATCH VECTOR.
359      TOTV: 330        ;TIME-OUT VECTOR
360      JSSV: 334        ;JOY-STICK SWITCH (DEMAND) VECTOR.
361      LUTV: 340        ; LUT READ/WRITE DONE VECTOR.
362      ; MISCELLANEOUS STORAGE REGISTERS.
363      ;
364      SDPC: 0          ; SAVED DPC ON INTERRUPT.
365      SOSR: 0          ; DITTO OSR
366      SDXR: 0          ; DITTO DXR
367      SDYR: 0          ; DITTO DYR
368
369      LUTFLG: 0        ; +1 IF LUT (A025) INSTALLED.
370
371      SLSR: 0          ; SAVED LUT STATUS.
372      SLDR: 0          ; DITTO DATA.
373      SLMR: 0          ; DITTO MAINT.
374
375      FIDX: 0          ; FRAME INDEX COUNTER (FOR PART 1).
376      FCD: 8          ; FRAME CHANGE DELAY COUNT... <VERSION A1>
377      FCD1: 8          ; ...IS DECREMENTED HERE. <VERSION A1>
378      ADDR: 0          ; CURRENT BASE ADDRESS <VERSION A1>
379      VECIX: 0         ; CURRENT VECTOR ADDRESS <VERSION A1>
380      DEVON: 0         ; CURRENT DEVICE CHECK MASK <VERSION A1>
381      DIVCHK: 0        ; USER SPECIFIED DEVICE MASK <VERSION A1>
382
383      T0: 0            ; SOME TEMP REGISTERS.
384      T1: 0
385      T2: 0
386      T3: 0
387
388      VADDR: DBEGIN    ; DISPLAY CODE VIRTUAL ADDRESS.
389      PADDR: 0         ; CONVERTED TO PHYSICAL HERE...
390      EABITS: 0        ; ...WITH EA BITS HERE <5:4>.
391      DSTRT: 0         ; DISPLAY START (CLEAR) DISPATCHER.
392      DLOOP: 0         ; DISPLAY LOOPING DISPATCHER.
393      RELOC: SETRR     ; PLUS RELOCATION BITS IN <15:4>.
394      RELOK: 0         ; NON-ZERO (CAN'T RUN) IF RELOCATION...
395      ; ...BOUNDARY IS NOT A MULTIPLE...
396      ; ...OF 100B (32. WORDS).
397      BADPAD: 0        ; PHYSICAL ADDRESS WHICH CAUSED...
398      ; ..."RELOK" TO BE NON-ZERO.
399
  
```

```

400 000340 000000          TEMPS: 0          ;STORE TEMP PS
401 000342 000000          TEMPC: 0         ;STORE TEMP PC
402          000004          MEMERR#4        ;NON EXIST MEM TRAP
  
```

```

404          ,SBTTL *** JOY-STICK AND TIME-OUT INTERRUPT SERVICE ROUTINES.
405          ;
406          ; JOY-STICK INTERRUPT SERVICE.
407          ;
408 000344          ;MATCH:
409 000344          ;SWITCH:
410 000352 000004 000000' 000352' 18: PIR#S,BEGIN,18          ; QUEUE UP TO CONTINUE AT 18 AND RTI
411 000356 004767 000074          ; JSR PC,SAVDPU          ; SAVE REGISTERS...
412          000240 000240          ; 240,240          ; ...AND THATS ALL (FOR NOW).
413          ;
414          ;
415          ;
416 000362 000167 001424          ; JMP R#SM          ; RESUME AND EXIT.
417          ;
418          ; DISPLAY TIME-OUT MEANS TROUBLE == QUIL.
419          ;
420 000366          ;TIMOUT:
421 000374 000004 000000' 000374' 18: PIR#S,BEGIN,18          ; QUEUE UP TO CONTINUE AT 18 AND RTI
422 000400 004767 000052          ; JSR PC,SAVDPU          ; SAVE REGISTERS.
423 000406 004401 000000' 000420' 18: MSGS,BEGIN,25          ; ASCII MESSAGE CALL
424 000414 000167 000406          ; BIC DEVON,R3          ; CLR THIS ONE FROM MASK <<VER B>>
425          JMP RESINT          ; GET THE NEXT ONE
426 000420 045 040 040 28: ,ASCIZ '% VSCAO -- DPU TIME-OUT%'
427          000423 126 123 103
428          000426 101 060 040
429          000431 055 055 040
430          000434 104 120 125
431          000437 040 124 111
432          000442 115 105 055
433          000445 117 125 124
434          000450 045 000
435          ;
436          ; .EVEN
437          ;
438          ; COMMON ROUTINE TO SAVE DPU REGISTERS.
439          ;
440          ;
441 000452 017767 177544 177572 SAVDPU: MOV #DPC,SOPC          ; SAVE ALL DPU REGISTERS.
442 000460 017767 177540 177566 MOV #USK,SDSR
443 000466 017767 177534 177562 MOV #UXR,SDXR
444 000474 017767 177530 177556 MOV #DYR,SDYR
445 000502 000240 240
446 000504 000240 240          ; AND ANYTHING ELSE WE MIGHT NEED.
447 000506 000240 240
448 000510 000207 RTS PC
  
```

```

440          .SBTTL *** LOOK-UP-TABLE SERVICE ROUTINES.
441          ;
442          ; NORMALLY, WE DON'T EXPECT ANY LUT INTERRUPTS.
443          ; REPORT AND RESTART IF WE GET ONE.
444          ;
445          UNXLI:
446          000512 000004 000000' 000520' PIRQS,BEGIN,1$ ; QUEUE UP TO CONTINUE AT 1$ AND RTI
447          000520 017767 177506 177536 1$:  MOV  #LSR,$LSR ; SAVE LUT REGISTERS.
448          000526 017767 177502 177532  MOV  #LDR,$LDR
449          000534 017767 177476 177526  MOV  #LMP,$LMP
450          000542 000240 000240 240,240 ; IN CASE WE NEED SOMETHING ELSE
451          000546 104401 000000' 000560' MSGS,BEGIN,2$ ;ASCII MESSAGE CALL
452          000554 000167 000246 JMP  RESTART ;...AND RESTART.
453          000560 045 040 040 2$: .ASCIZ '% VSCAO -- UNEXPECTED LUT INTERRUPT%'
454          000563 126 123 103
455          000566 101 060 040
456          000571 055 055 040
457          000574 125 116 105
458          000577 130 120 105
459          000602 103 124 105
460          000605 104 040 114
461          000610 125 124 040
462          000613 111 116 124
463          000616 105 122 122
464          000621 125 120 124
465          000624 045 000
466          .EVEN
467          ;
468          ; BLAST A 256 LEVEL GREY-SCALE FROM THE DATA IN "LUMTBL".
469          ;
470          GRASCL: MOV  #2$,#LUTV ; SET DONE VECTOR.
471          000634 012777 000656' 177414 MOV  #L0110,$LSM ; INTERRUPT AND ADDR 0.
472          000634 012777 002000 177370 MOV  #LUMTBL,R0 ; TABLE ADDRESS.
473          000642 012700 015112' 1$:  MOV  (R0)+,$LDR ; FILL THE LUT RAM.
474          000646 012077 177362 EXITS,BEGIN ;EXIT TO MONITOR. MODULE WAIT FOR INTERRUPT.
475          000652 104400 000000' 2$:
476          000656 PIRQS,BEGIN,3$ ; QUEUE UP TO CONTINUE AT 3$ AND RTI
477          000656 000004 000000' 000664' 3$: TSTB #LSM ; LUT RAM FULL ??
478          000664 105777 177342 BNE  1$ ; NO, LOOP.
479          000670 001366 BR  BLAIT ; YES, RETURN.
480          000672 000431
481          ;
482          ; BLAST 16 SHADES OF THE BASIC COLOR IN (R0).
483          ;
484          SHADES: MOV  #3$,#LUTV ; SET WRITE DONE VECTOR.
485          000674 012777 000732' 177346 MOV  #L0110,$LSR ; ADDR 0, INTERRUPT WHEN DONE.
486          000702 012777 002000 177322 HIC  #'C421,R0 ; SET DIMMEST SHADE THIS COLOR.
487          000710 042700 177356 CLR  R1 ; 1ST SHADE IS ALWAYS BLACK.
488          000714 005001 1$:  MOV  #16,$R2 ; 16 WORDS / SHADE.
489          000716 012702 000020 2$:  MOV  R1,$LDR ; WRITE A WORD.
490          000722 010177 177306 EXITS,BEGIN ;EXIT TO MONITOR. MODULE WAIT FOR INTERRUPT.
491          000726 104400 000000' 3$:
492          000732 000004 000000' 000740' PIRQS,BEGIN,4$ ; QUEUE UP TO CONTINUE AT 4$ AND RTI
493          000740 105777 177266 4$: TSTB #LSM ; LUT RAM FULL ??
494          000744 001404 BEQ  5$ ; EXIT IF SO.
495          000746 005302 DEC  R2 ; THIS SHADE DONE ??
496          000750 001364 BNE  2$ ; NOT YET, LOOP.
  
```

```

482          000752 060001          ADD  R0,R1 ; YES, INCREMENT THE SHADE...
483          000754 000740          HR  1$ ; ...AND LOOP.
484          000756
485          5$:
486          ;
487          ; COMMON EXIT FROM EITHER PATTERN.
488          ;
489          BLXIT: CLR  #LSR ; CLEAR STATUS...
490          000756 005077 177250 MOV  #GCOFF,$LMP ;...GAMMA CORRECT OFF...
491          000762 012777 000100 177246 MOV  #UNXLI,$LUTV ;...RESET VECTOR...
492          000770 012777 000512' 177252 RTS  PC ;...AND RETURN.
493          000776 000207
  
```

```

493          ,SBTTL *** PROGRAM START=RESTART
494          ;
495          ; INITIALIZE DISPLAY ADDRESSES, AND VECTORS.
496          ; BLAST THE LUT (IF IT'S THERE).
497          ;
498          START:
499          001000 032767 000001 177012      BIT      #1,SR2      ; USING COLOUR LOOKUP      <VERSION A1>
500          001006 001404                   BEQ      15          ; NO                        <VERSION A1>
501          001010 012767 000001 177270      MOV      #1,DIVCHK   ; YES= FORCE ONE DEVICE    <VERSION A1>
502          001016 000411                   BR       ONE         ;                          <VERSION A1>
503          001020 016767 176770 177270 18:  MOV      DVID1,T3    ; GET MASK AND SAVE IT    <<VER B>>
504
505          001026 016767 177264 177252      RESTRI:  MOV      T3,DIVCHK   ; PUT MASK INTO WORK LOCATION <VERSION A1>
506          001034 016767 177256 177252      MOV      T3,T2      ; ALSO INTO T2 COUNTER    <<VER B>>
507
508          001042
509          001042 012767 000001 177234      MOV      #1,DEVON    ; START CHECK AT DEVICE 1  <VERSION A1>
510          001050 016767 176732 177222      MOV      ADDR,ADDRX  ; SET DEVICE START ADDRESS <VERSION A1>
511          001056 016767 176726 177216      MOV      VECIOR,VECTXX ; AND VECIOR START        <VERSION A1>
512
513          001064 005767 177216      RESTRA:  TST      DIVCHK   ; ANY DEVICE TO TEST      <VERSION A1>
514          001070 001002                   BNE     77S         ; YES                      <VERSION A1>
515          001072 104410 000000'          ENDS,HEGIN         ;
516          001076 005767 177202      77S:    TST      DEVON      ; ALL MODULES TESTED      <VERSION A1>
517          001102 001736                   BEQ     START      ; YES RESTART FROM THE TOP <VERSION A1>
518          001104 036767 177174 177174      BIT      DEVON,DIVCHK ; IS THIS MODULE ON       <VERSION A1>
519          001112 001012                   BNE     76S         ; YES USE IT              <VERSION A1>
520          001114 062767 000010 177156      ADD      #ADDRHAS,ADDRX ; ELSE STEP TO NEXT ONE   <VERSION A1>
521          001122 062767 000020 177152      ADD      #VECBAS,VECTXX ; AND VECTOR              <VERSION A1>
522          001130 006367 177150      ASL     DEVON        ;                          <VERSION A1>
523          001134 001721                   BEQ     START      ; IF ALL DONE RESET TO TOP <VERSION A1>
524          001136 000757                   BR      77S         ;
525          001140
526          001140 016700 177134      76S:    MOV      ADDRX,R0     ; GET BASE DEVICE ADDRESS. <VERSION A1>
527          001144 012701 000222'          MOV      #DPC,R1     ;                          <VERSION A1>
528          001150 010021                   MOV      R0,(R1)+    ; SET REGISTER ADDRESSES.
529          001152 062700 000002          ADD      #2,R0       ;
530          001156 020127 000240'          CMP     R1,#STPV    ; END OF ADDRESS BLOCK ??
531          001162 001372                   BNE     1S          ; BR IF NOT.
532
533          001164 016700 177112      26:    MOV      VECTXX,R0   ; YES, SET VECIOR ADDRESSES. <VERSION A1>
534          001170 010021                   MOV      R0,(R1)+    ;
535          001172 062700 000004          ADD      #4,R0       ;
536          001176 020127 000252'          CMP     R1,#LUTV+2  ;
537          001202 001372                   BNE     2S          ; NO
538
539          001204 016700 177030      MOV     STPV,R0      ; YES, SET DPU VECTORS...
540          001210 116701 176576      MOV     BR1,R1       ; ...AND PRIORITIES.
541          001214 042701 177437      BIC     #C340,R1    ;
542          001220 012720 002022'          MOV     #STOP1,(R0)+ ; SET STUP...
543          001224 010120                   MOV     K1,(R0)+    ;
544          001226 012720 000344'          MOV     #MATCH,(R0)+ ; ...MATCH...
545          001232 010120                   MOV     K1,(R0)+    ;
546          001234 012720 000366'          MOV     #TIMOUT,(R0)+ ; ...TIME-OUT...
547          001240 010120                   MOV     K1,(R0)+    ;
548          001242 012720 000344'          MOV     #S1TCH,(R0)+ ; ...AND SWITCH VECTORS.
549          001246 010120                   MOV     R1,(R0)+    ;
  
```

```

550          001250 012720 000512'          MOV     #UNALI,(R0)+ ; SET LUT DONE VECTOR.
551          001254 010120                   MOV     R1,(R0)+    ;
552
553          001256 005227                   LUTIN:  INC     (PC)+  ; AUTO-SIZE FOR LUT INSTALLED.
554          001260 177777                   ; UNCE=UNLY SWITCH.
555          001262 001040                   BNE     1S          ;
556          001264 013767 000004 177050      MOV     #MEMERR,TEMPC ;STORE ADDRESS AT NOW EXISTANCE
557          ;MEMORY TRAP (TEMPARARY)
558          001272 013767 000006 177040      MOV     #MEMERR+2,IFMPS ;STORE PS
559          001300 012737 003362' 000004      MOV     #TRP1,#MEMERR ;NON EXIST MEM TRAP HERE
560          001306 013737 177776 000006      MOV     #PS, #MEMERR+2 ;GET CURRENT PS
561          001314 042737 177437 000006      BIC     #177437,#MEMERR+2 ;CLEAR UNWANTED BITS
562          001322 005067 176734          CLR     LUTFLG      ; ASSUME NO LUT INSTALLED.
563          001326 032767 000001 176464      BIT     #1,SR2      ; USING COLOUR LOOKUP
564          001334 001405                   BEQ     JS          ;
565          001336 005777 176670          TST     #LSH        ; TRAP AND RESTART IF NO LUT.
566          001342 012767 000001 176712      MOV     #1,LUTFLG   ; OTHERWISE, SET LUT AVAILABLE.
567          001350
568          001350 016737 176766 000004      38:    MOV     TEMP, #MEMERR ;RECOVER ERRVEC PC
569          001356 016737 176756 000006      MOV     TEMPS, #MEMERR+2 ;
570
571          001363
572          18:
  
```

```

574 ;
575 ; NOW COMPUTE ANY RELOCATION OFFSET BASED ON:
576 ;   OFFSET = PHYSICAL - VIRTUAL
577 ;
578 ; VSV RELOCATES ON 32. WORD (100 BYTES OCTAL) BOUNDARIES,
579 ; IN THE TRADITIONAL KI STYLE.
580 ; DURING PROGRAM RELOCATION (IF ENABLED), PA<5:0> AND VA<5:0>
581 ; SHOULD BE IDENTICAL. IF NOT, THE RELOCATION PROCESS DIDN'T
582 ; WORK RIGHT. TELL THE MAN, BUT DON'T TRY TO RUN ANYTHING.
583 001364          ;
584 001364 012767 004154' 010730  SETUP:  MOV  #SUR.1,ACAT+2 ; SET SUBROUTINE CALLS...
585 001372 012767 005132' 010724  MOV  #SUB.2,ACAT+4 ;...IN VIRTUAL CHAR...
586 001400 012767 012270' 010720  MOV  #SUB.3,ACAT+6 ;...ADDRESS TABLE...
587 001406 012767 003726' 176704  MOV  #DBEGIN,VADDR ; CONVERT VIRTUAL TO PHYSICAL.
588 001414 104415 000000' 000320'  GETPAS,BEGIN, VADDR ;GET PHYSICAL ADDRESS FROM 16-BIT VADDR
589 001422 016701 176674  MOV  PADDR,R1
590 001426 042701 000077  BIC  #77,R1 ; PA<15:6> => R1.
591 001432 016700 176666  MOV  LABITS,R0 ; PA<17:16> => R0<5:4>.
592 001436 000360  SHAB  R0
593 001440 006100  ROL  R0
594 001442 006100  ROL  R0 ; PA<17:16> => R0<15:14>.
595 001444 006001  ROR  R1
596 001446 006001  ROR  R1
597 001450 050001  BIS  R0,R1 ; PA<17:6> => R1<15:4>.
598 001452 016700 176642  MOV  VADDR,R0
599 001456 042701 000077  BIC  #77,R0 ; VA<15:6> => R0.
600 001462 000241  CLC
601 001464 006000  ROK  R0
602 001466 006000  ROR  R0 ; VA<15:6> => R0<13:4>.
603 001470 160001  SUB  R0,R1 ; PHY = VIRT = RELOC.
604 001472 052701 000014  BIS  #SETRR,R1 ; MAKE SETRR INSTRUCTION...
605 001476 010167 176630  MOV  R1,RELOC ;...FOR DSR (RR REGISTER)...
606 001502 016767 176612 176616  MOV  VADDR,DSRTR ;...AND VIRTUAL START FOR OPC.
607
608 001510 005067 176620  CLR  RELOC ; NOW, ASSUME RELOC IS VALID.
609 001514 016700 176602  MOV  PADDR,R0
610 001520 166700 176574  SUB  VADDR,R0
611 001524 042700 177700  BIC  #77,R0 ; PA<5:0> = VA<5:0> ???
612 001530 001414  BEQ  15 ; WE'RE OK IF SU.
613 001532 005167 176576  COM  RELOC ; NO, INHIBIT START-UP.
614 001536 026767 176560 176572  CMP  PADDR,HADPAD ; SAME PA AS LAST TIME ??
615 001544 001406  BEQ  15 ; YES, DON'T REPORT AGAIN.
616 001546 016767 176550 176562  MOV  PADDR,BADPAD ; NO, SAVE THE BAD NUMBER...
617 001554 104401 000000' 001564'  MSGS,BEGIN,CANT ;ASCII MESSAGE CALL
618 001562 000423 18:  BR  PARTI ;...AND FALL THRU.
619 ; "RELOC" NON-ZERO WILL CAUSE...
620 ;...US TO "ENDIT". SOONER DR...
621 ;...LATER, WE'LL RESTART AND...
622 ;...GET A NEW RELOCATION VALUE.
623
624 001564 045 040 040  CANT: .ASCIZ '% VSCA0 -- BAD RELOCATION BOUNDARY%'
001567 126 123 103
001572 101 060 040
001575 055 055 040
001600 102 101 104
001603 040 122 105
001606 114 117 103
  
```

```

001611 101 124 111
001614 117 116 040
001617 102 117 125
001622 116 104 101
001625 122 131 045
001630 060
625 .EVEN
  
```

```

627 ;SBTTL *** PART 1 -- THE STANDARD VISUAL REFERENCE FRAME
628 ;
629 ; CLEAR SCREEN, BLAST LUT (OPTIONAL) AND START DISPLAY.
630 ;
631 001632 005767 176476 PART1: TST RFLUC
632 001636 001402 BEQ 1$ ; ** NOP TO DEBUG **
633 001640 000167 000620 JMP DUNE2 ; RFLUC INVALID, CAN'T RUN.
634
635 001644 013767 000004 176470 1$: MOV **MEMERR,TEMP ;STORE ADDRESS AT NON EXISTANCE
636 ;MEMORY TRAP (TEMPARARY)
637 001652 013767 000006 176460 MOV **MEMERR+2,TEMPS ;STORE PS
638 001660 012737 003404 000004 MOV #RNP2,**MEMERR ;NON EXIST MEM TRAP HERE
639 001666 016777 176440 176330 MOV RFLUC,RDUSH ; SET RR REGISTER.
640 001674 016777 176426 176320 MOV DSTART,DOPC ; CLEAR SCREEN RAMS.
641 001702 2$: BREAKS,BEGIN ;TEMPORARY RETURN TO MONITOR....
        001706 104407 000000' ;THEN CONTINUE AT NEXT INSTRUCTION.
642 001712 005777 176306 TS1 0DSR
643 001716 100371 BPL 2$
644 001720 005767 176336 TST LUTFLG
645 001724 001402 BEQ 3$
646 001726 004767 176674 JSH PC,GRASCL ; BLAST THE LUT.
647 001732 016737 176404 000004 3$: MOV TEMPC,**MEMERR ;RECOVER ERRVEC PC
648 001740 016737 176374 000006 MOV TEMPS,**MEMERR+2 ;RECOVER PS
649 001746 012777 002022' 176264 MOV #STOP1,#STIPV ; SET DPU STOP VECTOR.
650 001754 016767 176346 176346 MOV DSTART,DLOOP
651 001762 062767 000020 176340 ADD #LOGO-DHEGIN,DLOOP ; SET 1ST FRAME ADDRESS.
652 001770 005067 176276 CLR FIDX ; INIT FRAME INDEX.
653
654 001774 016767 176274 176274 NEXT: MOV FCD,FCD1 ; SET/RESET FRAME CHANGE DELAY.
655 002002 016777 176322 176212 MOV DLOOP,DOPC ; START/RESTART DPU...
656 002010 000402 SKP2 ;...OP...
657 002012 005277 176204 RESM: INC MDPC ;...RESUME.
658 002016 104400 000000' EXITS,BEGIN ;EXIT TO MONITOR. MODULE WAIT FOR INTERRUPT.
659 ;
660 ; RETURN HERE ON STOP INTERRUPT.
661 ; CHANGE FRAMES AFTER A SUITABLE DELAY.
662 ;
663 002022 STUP1:
        002022 000004 000000' 002030' PIRUS,BEGIN,1$ ; QUEUE UP TO CONTINUE AT 1$ AND RTI
664 002030 004767 176416 1$: JSR PC,SAVDPU ; SAVE DPU REGISTERS.
665 002034 005367 176236 DEC FCD1 ; CHANGE DELAY EXPIRED ??
666 002040 001360 RNE NEXT+6 ; NOT YET, LOOP ON THIS FRAME.
667 002042 062767 000002 176222 ADD #2,FIDX ; YES, BUMP INDEX...
668 002050 016700 176216 MOV FIDX,R0
669 002054 000170 002060' JMP #2$(R0) ;...AND CHANGE FRAMES.
670 002060 001632' 2$: PART1 ; FRAME 1 (CLEAR AND LOGO).
671 002062 002076' FR2 ; 2 (OCTAGONS).
672 002064 002106' FR3 ; 3 (GRAPH/HISTO Y).
673 002066 002116' FR4 ; 4 (GRAPH/HISTO X).
674 002070 002130' FR5 ; 5 (BIT MAPS).
675 002072 002224' FR6 ; 6 (COLOR BARS).
676 002074 002354' HUES ; CHANGE COLOURS (OPTIONAL).

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```

678 ;
679 ; SET UP SEQUENCE FOR EACH FRAME.
680 ;
681 002076 062767 000110 176224 FR2: ADD #UCIGNS-LOGO,DLOOP ; ADJUST TO FRAME 2.
682 002104 000403 SKP3
683 002106 062767 000140 176214 FR3: ADD #GRY-OCTGNS,DLOOP ; ADJUST TO FRAME 3.
684 002114 000403 SKP3
685 002116 062767 000310 176204 FR4: ADD #GRX-GRY,DLOOP ; ADJUST TO FRAME 4.
686 002124 000167 177644 JMP NEXT
687
688 002130 012700 005132' FR5: MOV #0BUFH,R0 ; FRAME 5 REQUIRES THE BUFFER.
689 002134 012701 000400 MOV #256,,R1 ; SET UP TO FILL IT.
690 002140 012702 177777 MOV #+1,R2
691 002144 000407 BR 2$
692 002146 032701 000077 1$: BIT #77,R1 ; BREAK EVERY 64 TIMES...
693 002152 001004 BNE 2$ ;...SO WE DON'T HOG THE CPU.
694 002154 104407 000000' BREAKS,BEGIN ;TEMPORARY RETURN TO MONITOR....
        002160 104407 000000' BREAKS,BEGIN ;THEN CONTINUE AT NEXT INSTRUCTION.
695 002164 010220 2$: MOV #2,(R0)+ ; FILL BIT MAP BUFFER #ITH...
696 002166 005020 CLR (R0)+ ;...ALTERNATE 1'S AND 0'S.
697 002170 005301 DEC R1
698 002172 001365 HNE 1$ ; LOOP 'TIL DONE.
699 002174 016767 176074 176074 MOV FCD,FCD1 ; ADJUST FRAME DELAY...
700 002202 006267 176070 ASH FCD1 ;...CAUSE BIT MAPS...
701 002206 006267 176064 ASH FCD1 ;...TAKE TOO LONG !!
702 002212 062767 000310 176110 ADD #R14-GRX,DLOOP ; ADJUST ADDRESS TO FRAME 5.
703 002220 000167 177556 JMP NEXT+6
704
705 002224 012700 005132' FR6: MOV #0BUFR,R0 ; FRAME 6 REQUIRES THE BUFFER.
706 002230 012701 112000 MOV #LVECILO,R1
707 002234 012702 000500 MOV #500,R2 ; +500 Y VALUE.
708 002240 012703 020500 MOV #MAXI500,R3 ; -500 Y VALUE.
709 002244 000407 BP 2$
710 002246 032701 000077 1$: BIT #77,R1 ; BREAK ONCE IN A WHILE...
711 002252 001004 BNE 2$ ;...LIKE BEFORE.
712 002254 104407 000000' BREAKS,BEGIN ;TEMPORARY RETURN TO MONITOR....
        002260 104407 000000' BREAKS,BEGIN ;THEN CONTINUE AT NEXT INSTRUCTION.
713 002264 010120 2$: MOV R1,(R0)+ ; SET VECTOR!...
714 002266 012720 040000 MOV #110,(R0)+ ;...X...
715 002272 010220 MOV R2,(R0)+ ;...+Y...
716 002274 012720 000002 MOV #2,(R0)+
717 002300 005020 CLR (R0)+ ; MOVE OVER 1...
718 002302 062701 000004 ADD #4,R1 ;...AND INCREMENT THE LEVEL.
719 002306 010120 MOV R1,(R0)+ ; SET VECTOR!+4
720 002310 012720 040000 MOV #110,(R0)+ ;...X...
721 002314 010320 MOV R3,(R0)+ ;...-Y...
722 002316 012720 000002 MOV #2,(R0)+
723 002322 005020 CLR (R0)+ ; MOVE OVER...
724 002324 062701 000004 ADD #4,R1 ;...AND INCREMENT LEVEL AGAIN.
725 002330 032701 001777 BIT #1777,R1
726 002334 001344 BNE 1$ ; LOOP 'TIL ALL LEVELS SET.
727 002336 012720 165000 MOV #DPOP,(R0)+ ; THEN, TERMINATE THE BUFFER.
728 002342 062767 000060 175760 ADD #BARS-B14,DLOOP ; ADJUST TO FRAME 6 ADDRESS.
729 002350 000167 177420 JMP NEXT ;...AND DO IT.

```

```

731 ;
732 ; NOW IF LUT IS THERE, REBLAST IT TO DISPLAY
733 ; 16 SHADES OF EACH OF THE NTSC BASIC COLORS.
734 ; DISPLAY IS STATIC (STOPPED) DURING THIS PHASE.
735 ;
736 002354 005767 175702 HUES: TST LUTFLG
737 002360 001435 BEQ DONE1 ; BYPASS IF NO LUT AVAILABLE.
738 002362 012705 002434 MOV #JS,R5 ; COLOR TABLE ADDRESS.
739 002366 012500 1S: MOV (R5)+,R0 ; 1ST/NEXT COLOR.
740 002370 001431 BEQ DONE1 ; BR IF END OF TABLE.
741 002372 004767 176276 JSR PC,SHADES ; BLAST IT.
742 002376 016767 175672 MOV FCD,FCD1
743 002404 006367 175666 ASL FCD1 ; EXTEND THE DELAY TIME.
744 002410 006367 175662 ASL FCD1
745 002414 2S:
002414 104407 000000' BREAKS,BEGIN ;TEMPORARY RETURN TO MONITOR....
002420 104407 000000' BREAKS,BEGIN ;THEN CONTINUE AT NEXT INSTRUCTION.
746 002424 005367 175646 DEC FCD1
747 002430 001371 BNE ZS ; DELAY...
748 002432 000755 BR 1S ;...AND LOOP (THRU ALL COLORS.
749
750 002434 007400 3S: 7400 ; BLUE
751 002436 000017 0017 ; RED
752 002440 007417 7417 ; MAGENTIA
753 002442 000360 0360 ; GREEN
754 002444 007760 7760 ; CYAN
755 002446 000377 0377 ; YELLOW
756 002450 007777 7777 ; WHITE
757 002452 000000 0 ; BLACK (END)
758 ;
759 ; THIS IS A COMMON "END-ITERATION" POINT FOR BOTH PARTS.
760 ;
761 002454 032767 000001 175334 DONE1: HIT #BIT0,SRI ; PART 2 ENABLED ??
762 002462 001031 BNE PART2 ; DO IT IF SO.
763 002464 046767 175614 175622 DONE2: BIC DEVON,T2 ; DONE ALL MODULES?
764 002472 001421 BEQ PASS ; YES SIGNAL EOP <<VER B>>
765 002474 006367 175604 ASL DEVON ; SELECT NEXT MODULE <<VER A1>
766 002500 032767 000001 175312 BIT #1,SRI ; USING COLOR LOOKUP? <<VER B>>
767 002506 001403 BEQ ZS ; NO! <<VER B>>
768 002510 062767 000010 175562 ADD #10,ADDRX ; FOUR MORE CSR'S THEN <<VER B>>
769 002516 062767 000010 175554 2S: ADD #ADDRS,ADDRX ; GET NEW ADDRESS <<VER A1>
770 002524 062767 000020 175550 ADD #VECHAS,VECTXX ; PLUS VECTOR <<VER A1>
771 002532 000167 176326 JMP RESTRA ; DO NEXT ONE
772 ;
773 002536 PASS:
002536 104413 000000' ENDDITS,BEGIN ; SIGNAL END OF ITERATION.
774 002542 000167 176260 JMP RESTRIT ; GO FOR MORE <<VER B>>
775 ;

```

```

777 .SBTTL *** PART 2 -- THE OLD BOUNCING BALLS
778 .SHTTL *** SRI BIT0 = 1 = ENABLE PART 2.
779 .SRTTL *** SRI BIT1 = 1 = USE #PITE-IN-RETRACE MODE.
780 .SBTTL *** SRI BIT2 = 1 = DISABLE BELL ON BOUNCE.
781 .SHTTL
782 ;
783 ; DISPLAY TWO OCTAGONS FLOATING AROUND AT RANDOM.
784 ; BOUNCE THEM OFF THE WALLS AND "CRASH" IF THEY TOUCH.
785 ;
786 002546 016777 175554 175446 PART2: MOV DSTART,ADPC ; CLEAR SCREEN (R/W MODE).
787 002554 1S:
002554 104407 000000' BREAKS,BEGIN ;TEMPORARY RETURN TO MONITOR....
002560 104407 000000' BREAKS,BEGIN ;THEN CONTINUE AT NEXT INSTRUCTION.
788 002564 005777 175434 TST #DSK
789 002570 100371 HPL 1S
790 002572 005767 175464 TST LUTFLG
791 002576 001402 BEQ ZS
792 002600 004767 176022 JSR PC,GRASCL
793 002604 012767 002000 175500 2S: MOV #2000,T1 ; SETUP DEAD MAN TIMER <<VER B>>
794 002612 012767 002000 000536 MOV #200,L0LIM
795 002620 012767 001600 000532 MOV #1600,H1LIM ;SET LIMITS FOR ORIGINS.
796 002626 004767 000456 JSR PC,RAND
797 002632 010167 007340 MOV R1,RH ;NEW ORIGIN, RED BALL.
798 002636 004767 000446 JSR PC,RAND
799 002642 010167 007332 MOV R1,RG*2
800 002646 004767 000436 JSR PC,RAND
801 002652 010167 007332 MOV R1,GB ;NEW ORIGIN, GREEN BALL.
802 002656 004767 000426 JSR PC,RAND
803 002662 010167 007324 MOV R1,GB*2
804 002666 012767 000004 000462 MOV #4,L0LIM
805 002674 012767 000030 000456 MOV #30,H1LIM ;SET LIMITS FOR DELTAS.
806 002702 004767 000402 JSR PC,RAND
807 002706 010167 000624 MOV R1,DX1 ;BALL 1, DELTA X...
808 002712 004767 000372 JSR PC,RAND
809 002716 010167 000616 MOV R1,DX1+2 ;...AND Y.
810 002722 004767 000362 JSR PC,RAND
811 002726 010167 000610 MOV R1,DX2 ;BALL 2, DITTO.
812 002732 004767 000352 JSR PC,RAND
813 002736 010167 000602 MOV R1,DX2+2
814 ;
815 002742 016767 175360 175360 MOV DSTART,DLOOP
816 002750 062767 006210 175352 ADD #B1-DBEGIN,DLOOP ; INIT STARTING ADDRESS.
817 002756 032767 000002 175032 HIT #BIT1,SRI ; DYNAMIC MODE (DEFAULT) ??
818 002764 001007 BNE JS ; BR IF NOT SO.
819 002766 062767 000012 175334 ADD #B1A-B1,DLOOP ; NO, ADJUST STARTING ADDRESS...
820 002774 012767 164002 007256 MOV #SYNC+1,B4 ;...AND OPCODE FOR R/W MODE.
821 003002 000414 HR 4S
822 003004 016777 175320 175210 3S: MOV DLOOP,ADPC ; DYNAMIC, RECONFIGURE IM'S.
823 003012 005777 175206 TST #DSK ; WAIT FOR DPU STOP.
824 003016 100375 BPL -4
825 003020 062767 000036 175302 ADD #B2-B1,DLOOP ; ADJUST STARTING ADDRESS...
826 003026 012767 170340 007224 MOV #SWTCH:CLRMEM,B4 ;...AND OPCODE FOR DYNAM MODE.

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828 003034 012767 020040 007160 48:  MOV    #20040,B3      ; NULL (SPACE) THE CRASH TEXT.
829 003042 016767 007154 007154      MOV    B3,B3+2
830 003050 016767 007146 007150      MOV    B3,B3+4
831 003056 012777 003114' 175154    MOV    #STOP2,#STPV    ; SET STOP VECTOR...
832 003064 016767 175204 175204    MOV    FCD,FCD1
833 003072 006367 175200      ASL    FCD1            ;...EXTEND THE FRAME DELAY.
834 003076 006367 175174      ASL    FCD1
835 003102 016777 175222 175112 CNTNU:  MOV    DLOUP,#OPC     ; START/RESTART DISPLAY.
836 003110 104400 000000'      EXITS,BEGIN          ;EXIT TO MONITOR. MODULE WAIT FOR INTERRUPT.
837
838 ;
839 ; CONTINUE HERE ON STOP INTERRUPT.
840 ;
841 ; MOVE THE BALLS AROUND UNTIL THEY CRASH, THEN END-PASS.
842
841 003114      STOP2:
003114 000004 000000' 003122'  PIROS,BEGIN,18      ; QUEUE UP TO CONTINUE AT 18 AND RTI
842 003122 126727 007074 000040 18:  CMPB  B3,#40        ; ARE WE STILL RUNNING ??
843 003130 001405      BEQ    Z8            ; OR IF SO.
844 003132 005367 175140      DEC    FCD1         ; NO, WE'RE DEAD -- DISPLAY...
845 003136 001061      HNE   Z8            ;...THE MESSAGE FOR A WHILE...
846 003140 000167 177320      JMP    DUNE2        ;...AND SIGNAL ALL DONE.
847
848 003144 016767 007026 007000 28:  MOV    RB,ORB       ; OK, COPY THE ORIGINS...
849 003152 016767 007022 006774      MOV    RB+2,ORB+2   ;...SO WE CAN ERASE.
850 003160 016767 007024 006776      MOV    GB,UGB
851 003166 016767 007020 006772      MOV    GB+2,UGB+2
852 003174 016701 006776      MOV    RB,R1
853 003200 166701 007004      SUB    GB,R1
854 003204 100001      BPL    .+4
855 003206 005401      NEG    R1
856 003210 020127 000034      CMP    R1,#34
857 003214 101025      BHI   Z8            ; NO, WE'RE OK !!!
858 003216 016701 006756      MOV    RB+2,R1
859 003222 166701 006764      SUB    GB+2,R1
860 003226 100001      BPL    .+4
861 003230 005401      NEG    R1
862 003232 020127 000034      CMP    R1,#34
863 003236 101014      BHI   Z8            ; OR IF NOT, WE'RE STILL OK.
864
865 003240 012767 051103 006754      MOV    #"CR,B3      ; OTHERWISE...
866 003246 012767 051501 006750      MOV    #"AS,B3+2    ;...SAY "CRASH" !!!!
867 003254 012767 020110 006744      MOV    #"H ,B3+4
868 003262 004767 000416      JSR    PC,DJING
869 003266 000402      SKP2
870 003270 004767 000252      38:  JSR    PC,MOVE
871 003274 005367 175012      DEC    11
872 003300 001401      HEB   55
873 003302 000677      46:  BP   CNTNU
874 003304 000167 177154      56:  JMP    DUNE2
875
876 ;
877 ; RANDOM NUMBER GETTER.
878 ;
879 ;
880 003310      RAND:
003310 104417 000000'      RANDB,BEGIN
881 003314 016701 174534      MOV    RANNUM,R1
882 003320 042701 176000      BIC   #"C1777,R1

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883 003374 020167 000026      18:  CMP    R1,LULIM
884 003330 103003      BHIS  Z8
885 003332 066701 000020      ADD    LULIM,R1
886 003336 000772      BR    15
887 003340 020167 000014      28:  CMP    R1,HILIM
888 003344 101403      HLUS  Z8
889 003346 166701 000006      SUB    HILIM,R1
890 003352 000772      BR    23
891 003354 000207      38:  RTS   PC
892 003356 000000      LULIM: 0
893 003360 000000      HILIM: 0

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```

895
896          ;NON EXISTANCE MEMORY TRAP HANDLER
897
898 003362          TRP1:
899 003362 016737 174754 000004      MOV     TEMPC,##MEMERR      ;RESTORE NON EXIST MEM ADR
900 003370 016737 174744 000006      MOV     TEMPS,##MEMERR+2    ;RESTORE PS
901 003376 022626                CMP     (SP)+,(SP)+        ;ADJUST STACK
902 003400 000167 175760                JMP     SETUP
903
904 003404          TRP2:
905 003404 016737 174732 000004      MOV     TEMPC,##MEMERR      ;!!!TRP2 ADDED AT <<VER B>>!!!!
906 003412 016737 174722 000006      MOV     TEMPS,##MEMERR+2    ;RESTORE NON EXIST MEM ADR
907 003420 022626                CMP     (SP)+,(SP)+        ;ADJUST STACK
908 003422 016700 174652                MOV     ADDR,R0            ;GET CSP
909 003426 010067 174446                MOV     KU,CSHA           ;PUT INTO CSRA
910 003432 012767 000010 174446      MOV     #10,ERRIYP        ;LOAD ERROR CODE
911 003440 104405 000000' 000000      HDRS,BEGIN,NULL          ;NXM TIMEOUT,MODULE DROPPED
912 003446 104401 000000' 003466'     MSGS,BEGIN,2$           ;ASCII MESSAGE CALL
913 003454 046767 174624 174634      BIC     DUNON,TJ          ;REMOVE FROM LIST
914 003462 000167 176776                JMP     DUNE2             ;NEXT!!
915
916
917
918 003466          045          040          2$: .ASCIZ '% VSCB -- NXM TIMEOUT,MODULE DROPPED%'
919 003471          126          123          103
920 003474          102          060          040
921 003477          055          055          040
922 003502          116          130          115
923 003505          040          174          111
924 003510          115          105          117
925 003513          125          124          054
926 003516          115          117          104
927 003521          125          114          105
928 003524          040          104          122
929 003527          117          120          120
930 003532          105          104          045
931 003535          000
932
933          .EVEN
934
935          ;
936          ; ROUTINE TO MOVE THE BALLS AROUND ON THE SCREEN.
937          ;
938          ;
939 003536          000000 000000      DX1: 0,0          ;DELTA FOR BALL 1...
940 003542          000000 000000      DX2: 0,0          ;...AND 2.
941
942 003546          012701 012176'     MOVE: MOV     #R0,R1          ;RED BALL ORIGIN...
943 003552          012702 003536'     MOV     #DX1,R2          ;...AND DELTA POINTERS.
944 003556          004767 000010      JSR     PC,1$           ;
945 003562          012701 012210'     MOV     #R0,R1          ; GREEN BALL ORIGIN...
946 003566          012702 003542'     MOV     #DX2,R2          ;...AND DELTA POINTERS.
947
948 003572          061211'     1$: ADD     (R2),(R1)        ;OLD X + DELTA X.
949 003574          021127 000024      CMP     (R1),#24        ;HIT LEFT WALL ??
950 003600          101005                BHI     2$              ;NO
951 003602          012711 000024      MOV     #24,(R1)        ;YES, SET X AT MIN.....
952 003606          005412                NEG     (R2)            ;...AND REVERSE DELTA X.

```

```

939 003610          004767 000070          JSR     PC,DING          ;"DING"
940
941 003614          021127 001752          2$: CMP     (R1),#MAXX-24    ;HIT RIGHT WALL ??
942 003620          103405                BLU     5$              ;NO
943 003622          012711 001752          MOV     #MAXX-24,(R1)    ;YES, SET X AT MAX...
944 003626          005412                NEG     (R2)            ;...AND REVERSE DELTA X.
945 003630          004767 000050          JSR     PC,DING
946
947 003634          005721          3$: 1ST  (R1)+          ;BUMP POINTERS FOR Y'S.
948 003636          005722                TST     (R2)+
949 003640          061211                ADD     (R2),(R1)        ;OLD Y + DELTA Y.
950 003642          021127 000024      CMP     (R1),#24        ;HIT FLOOR ??
951 003646          101005                BHI     4$              ;NO
952 003650          012711 000024      MOV     #24,(R1)        ;YES, SET Y AT MIN...
953 003654          005412                NEG     (R2)            ;...AND REVERSE DELTA Y.
954 003656          004767 000022          JSR     PC,DING
955
956 003662          021127 001652          4$: CMP     (R1),#MAXY60-24  ;HIT CEILING ??
957 003666          103405                BLU     5$              ;NO
958 003670          012711 001652          MOV     #MAXY60-24,(R1) ;YES, SET Y AT MAX...
959 003674          005412                NEG     (R2)            ;...AND REVERSE DELTA Y.
960 003676          004767 000002          JSR     PC,DING
961
962 003702          000207          5$: PTS  PC            ;EXIT.
963
964 003704          032767 000004 174104  DING: BIT     #BIT2,SRI        ;IF BIT2=1 BELL OFF
965 003712          001003                BNE     1$              ;EXIT IF "BELL" DISABLED.
966 003714          104401 000000' 003724'     MSGS,BEGIN,BELL ;ASCII MESSAGE CALL
967 003722          000207          1$: RTS     PC
968 003724          007          000          BELL: .ASCIZ '<'>

```

```

970           ,SBTTL *** PART 1 -- DISPLAY CODE
971           ;
972           ; SMORGASHURD, ONE FRAME THAT HAS A BIT OF EVERYTHING.
973           ; FIRST, A DEC LOGD (SOMT UF), THEN VIOLATE THE EDGES
974           ; AT LOWER LEFT AND UPPER RIGHT.
975           ;
976           100000      DSUB = CHAR           ; USE CHAR MODE TO CALL SUBROUTINES.
977           ;                                     ; *** REMEMBER THAT THEY ***
978           ;                                     ; *** CANNOT BE NESTED ***
979           ; CHAR CODES 1 - 11, AND 16 - 77
980           ; ARE AVAILABLE AS CALL ARGUMENTS.
981           ; APPROPRIATE DISPATCH POINTERS MUST
982           ; BE SET IN TABLE "ACAT".
983
984 003726 176074 176474      DBEGIN: R0WRT1CH0, R0WRT1CH1
985 003732 177074 177474      R0WRT1CH2, R0WRT1CH3
986 003736 152000 012320'    SETCB, ACAT           ; SET CHAR BASE POINTER.
987 003742 170140 173000    CLRMEM, S10PM      ; CLEAR SCREEN RAM AND STOP
988
989 003746 114000 000540 000100 LOGD:  APNT, 540, 100
990 003754 103774          CHAR1ALL
991 003756 104 040 111      .ASCIZ /D I G I T A L/
992
993 003774 114000 000140 000140      APNT,140,140      ;EXCEED EDGES AT LOWER LEFT...
994 004002 110000          LVEC
995 004004 060200 000000          .WORD $1+$X, $Y
996 004010 040000 020200          .WORD $1+$X, $Y
997 004014 040200 000000          .WORD $1+$X, $Y
998 004020 040000 000200          .WORD $1+$X, $Y
999 004024 114000 001640 001540      APNT,1640,1540      ;...AND AT UPPER RIGHT.
1000 004032 110000          LVEC
1001 004034 040200 000000          .WORD $1+$X, $Y
1002 004040 040000 000300          .WORD $1+$X, $Y
1003 004044 060200 000000          .WORD $1+$X, $Y
1004 004050 040000 020300          .WORD $1+$X, $Y
1005 004054 173401          STOPI+1           ; END OF FRAME 1.
  
```

```

1007           ;
1008           ; NOW LONG, AND SHORT VECTORS, AND SOME RELATIVE POINTS.
1009           ;
1010 004056 114000 000500 000002      OCTGNS: APNT,500,2
1011 004064 113774          LVEC1ALL           ; 1 LARGE OCTAGON...
1012 004066 040576 000000          .WORD $1+$X, $Y
1013 004072 040436 000436          .WORD $1+$X, $Y
1014 004076 040000 000576          .WORD $1+$X, $Y
1015 004102 060436 000436          .WORD $1+$X, $Y
1016 004106 060576 000000          .WORD $1+$X, $Y
1017 004112 060436 020436          .WORD $1+$X, $Y
1018 004116 040000 020576          .WORD $1+$X, $Y
1019 004122 040436 020436          .WORD $1+$X, $Y
1020
1021 004126 114000 000160 000620      APNT,160,620      ;...AND 2 SMALL ONES.
1022 004134 100000 000001          DSUB, 1           ; CALL OCTAGON SUBROUTINE.
1023
1024 004140 114000 001520 000620      APNT,1520,620
1025 004146 100000 000001          DSUB, 1
1026
1027 004152 173402          STOPI+2           ; END OF FRAME 2.
1028
1029 004154          SUB.1:           ; SUBROUTINE FOR SMALL OCTAGON...
1030 004154 107774          SVECIALL          ;...CALLED VIA CHAR CODE 1.
1031 004156 057460          .WORD $1+$X+$Y
1032 004160 053456          .WORD $1+$X+$Y
1033 004162 040076          .WORD $1+$X+$Y
1034 004164 073456          .WORD $1+$X+$Y
1035 004166 077400          .WORD $1+$X+$Y
1036 004170 073556          .WORD $1+$X+$Y
1037 004172 040176          .WORD $1+$X+$Y
1038 004174 053556          .WORD $1+$X+$Y
1039 004176 130000          RPNT           ; 5 REL POINTS IN CENTER.
1040 004200 004050          .WORD $1+$X+$Y
1041 004202 044050          .WORD $1+$X+$Y
1042 004204 044000          .WORD $1+$X+$Y
1043 004206 064020          .WORD $1+$X+$Y
1044 004210 064120          .WORD $1+$X+$Y
1045 004212 044120          .WORD $1+$X+$Y
1046 004214 165000          DPOP
  
```

```
1048 ;  
1049 ; NEXT, SHOW GRAPH/HISTO Y IN UPPER LEFT.  
1050 ;  
1051 YVAL = 1240  
1052 004216 114000 000100 001240 GRY: APNT,100,YVAL  
1053 004224 113774 LVEC:ALL ;DRAW BASE LINE.  
1054 004226 040340 000000 I:140, 0  
1055 004232 020344 000000 MXY:144, 0  
1056 004236 174104 GX:14  
1057 004240 127774 GHY:ALL ;X INCH = 4 FOR GRAPH.  
1058 ; GRAPH Y MODE.  
1059 .REPT 71  
1060 O:YVAL  
1061 YVAL=YVAL+4  
1062 .ENDR  
004242 001240 O:YVAL  
004244 001244 O:YVAL  
004246 001250 O:YVAL  
004250 001254 O:YVAL  
004252 001260 O:YVAL  
004254 001264 O:YVAL  
004256 001270 O:YVAL  
004260 001274 O:YVAL  
004262 001300 O:YVAL  
004264 001304 O:YVAL  
004266 001310 O:YVAL  
004270 001314 O:YVAL  
004272 001320 O:YVAL  
004274 001324 O:YVAL  
004276 001330 O:YVAL  
004300 001334 O:YVAL  
004302 001340 O:YVAL  
004304 001344 O:YVAL  
004306 001350 O:YVAL  
004310 001354 O:YVAL  
004312 001360 O:YVAL  
004314 001364 O:YVAL  
004316 001370 O:YVAL  
004320 001374 O:YVAL  
004322 001400 O:YVAL  
004324 001404 O:YVAL  
004326 001410 O:YVAL  
004330 001414 O:YVAL  
004332 001420 O:YVAL  
004334 001424 O:YVAL  
004336 001430 O:YVAL  
004340 001434 O:YVAL  
004342 001440 O:YVAL  
004344 001444 O:YVAL  
004346 001450 O:YVAL  
004350 001454 O:YVAL  
004352 001460 O:YVAL  
004354 001464 O:YVAL  
004356 001470 O:YVAL  
004360 001474 O:YVAL  
004362 001500 O:YVAL  
004364 001504 O:YVAL  
004366 001510 O:YVAL
```

```
004370 001514 O:YVAL  
004372 001520 O:YVAL  
004374 001524 O:YVAL  
004376 001530 O:YVAL  
004400 001534 O:YVAL  
004402 001540 O:YVAL  
004404 001544 O:YVAL  
004406 001550 O:YVAL  
004410 001554 O:YVAL  
004412 001560 O:YVAL  
004414 001564 O:YVAL  
004416 001570 O:YVAL  
004420 001574 O:YVAL  
004422 001600 O:YVAL  
1062  
1063 YVAL = 1240  
1064 004424 150000 001240 HSY: SETHB,YVAL ; SET HISTO BASE LINE.  
1065 004430 114000 000130 001240 APNT,130,YVAL  
1066 004436 174110 GX:110 ;X INCH = 10 FOR HISTO.  
1067 004440 127774 GHY:ALL  
1068 .REPT 31  
1069 HST:YVAL  
1070 YVAL=YVAL+10  
1071 .ENDR  
004442 041240 HST:YVAL  
004444 041250 HST:YVAL  
004446 041260 HST:YVAL  
004450 041270 HST:YVAL  
004452 041300 HST:YVAL  
004454 041310 HST:YVAL  
004456 041320 HST:YVAL  
004460 041330 HST:YVAL  
004462 041340 HST:YVAL  
004464 041350 HST:YVAL  
004466 041360 HST:YVAL  
004470 041370 HST:YVAL  
004472 041400 HST:YVAL  
004474 041410 HST:YVAL  
004476 041420 HST:YVAL  
004500 041430 HST:YVAL  
004502 041440 HST:YVAL  
004504 041450 HST:YVAL  
004506 041460 HST:YVAL  
004510 041470 HST:YVAL  
004512 041500 HST:YVAL  
004514 041510 HST:YVAL  
004516 041520 HST:YVAL  
004520 041530 HST:YVAL  
004522 041540 HST:YVAL  
1072 004524 173403 STUP:13 ; END OF FRAME 3.
```

```

1074                                     ;
1075                                     ; NEXT, GRAPH/HISTO X IN LOWER RIGHT.
1076                                     ;
1077                                     XVAL = 1340
1078 004526 001340 001340 000100 GRX: APNT,XVAL,100
1079 004534 114000 LVECCIALI ;DRAW BASE LINE.
1080 004536 113774 000340 110, 340
1081 004542 000000 020344 0, MX11344
1082 004546 174104 GY114 ;Y INCR = 4 FOR GRAPH.
1083 004550 123774 GHX:ALL
1084 000071 .REPT 71
1085 01XVAL
1086 XVAL=XVAL+4
1087 .ENDR
004552 001340 01XVAL
004554 001344 01XVAL
004556 001350 01XVAL
004560 001354 01XVAL
004562 001360 01XVAL
004564 001364 01XVAL
004566 001370 01XVAL
004570 001374 01XVAL
004572 001400 01XVAL
004574 001404 01XVAL
004576 001410 01XVAL
004600 001414 01XVAL
004602 001420 01XVAL
004604 001424 01XVAL
004606 001430 01XVAL
004610 001434 01XVAL
004612 001440 01XVAL
004614 001444 01XVAL
004616 001450 01XVAL
004620 001454 01XVAL
004622 001460 01XVAL
004624 001464 01XVAL
004626 001470 01XVAL
004630 001474 01XVAL
004632 001500 01XVAL
004634 001504 01XVAL
004636 001510 01XVAL
004640 001514 01XVAL
004642 001520 01XVAL
004644 001524 01XVAL
004646 001530 01XVAL
004650 001534 01XVAL
004652 001540 01XVAL
004654 001544 01XVAL
004656 001550 01XVAL
004660 001554 01XVAL
004662 001560 01XVAL
004664 001564 01XVAL
004666 001570 01XVAL
004670 001574 01XVAL
004672 001600 01XVAL
004674 001604 01XVAL
004676 001610 01XVAL
  
```

```

004700 001614 01XVAL
004702 001620 01XVAL
004704 001624 01XVAL
004706 001630 01XVAL
004710 001634 01XVAL
004712 001640 01XVAL
004714 001644 01XVAL
004716 001650 01XVAL
004720 001654 01XVAL
004722 001660 01XVAL
004724 001664 01XVAL
004726 001670 01XVAL
004730 001674 01XVAL
004732 001700 01XVAL
1088
1089 001340 XVAL = 1340
1090 004734 150000 001340 HSR: SETHR,XVAL ; SET HISTO BASE LINE.
1091 004740 114000 001340 000130 APNT,XVAL,130
1092 004746 174110 GY110 ;Y INCR = 10 FOR HISTO.
1093 004750 123774 GHX:ALL
1094 000031 .REPT 31
1095 HST:XVAL
1096 XVAL=XVAL+10
1097 .ENDR
004752 041340 HST:XVAL
004754 041350 HST:XVAL
004756 041360 HST:XVAL
004760 041370 HST:XVAL
004762 041400 HST:XVAL
004764 041410 HST:XVAL
004766 041420 HST:XVAL
004770 041430 HST:XVAL
004772 041440 HST:XVAL
004774 041450 HST:XVAL
004776 041460 HST:XVAL
005000 041470 HST:XVAL
005002 041500 HST:XVAL
005004 041510 HST:XVAL
005006 041520 HST:XVAL
005010 041530 HST:XVAL
005012 041540 HST:XVAL
005014 041550 HST:XVAL
005016 041560 HST:XVAL
005020 041570 HST:XVAL
005022 041600 HST:XVAL
005024 041610 HST:XVAL
005026 041620 HST:XVAL
005030 041630 HST:XVAL
005032 041640 HST:XVAL
1098 005034 173404 STOP1+4 ; END OF FRAME 4.
  
```

```

1100 ;
1101 ; NEXT SHOW BIT MAP MODE 1 AT BOTTOM CENTER.
1102 ; AND BIT MAP MODE 0 AT TOP CENTER.
1103 ;
1104 ; DUN'T FORGET TO FILL THE BUFFER FIRST.
1105 ;
1106 005036 114000 000400 000240 B14: APNT,400,240 ; 4 ON 4 OFF FOR 340 PIXELS.
1107 005044 136340 BM14:340
1108 005046 005132' DBUFR
1109 005050 164000 DNOP
1110 ;
1111 005052 114000 000300 000340 B18: APNT,300,340 ; 2 ON 2 OFF FOR 400 PIXELS.
1112 005060 137400 BM18:400
1113 005062 005132' DBUFR
1114 005064 164000 DNOP
1115 ;
1116 005066 114000 000540 001340 B04: APNT, 540, 1340 ; 4 BIT X 32 SQUARE...
1117 005074 134010 BM04:M32:EX2 ;...EXPANDED X 2.
1118 005076 005132' DBUFR
1119 005100 164000 DNOP
1120 ;
1121 005102 114000 001040 001340 B08: APNT, 1040, 1340 ; 8 BIT X 32 SQUARE...
1122 005110 135010 BM08:M32:EX2 ;...EXPANDED X 2.
1123 005112 005132' DBUFR
1124 005114 173405 STUPI+5 ; END OF FRAME 5.
1125 ;
1126 ;
1127 ; FINALLY, AT DEAD-CENTER:
1128 ; VERTICAL COLOR BARS -- NUMBER OF COLORS/SHADES DEPENDANT
1129 ; UPON IMAGE MEMORY SIZE AND PRESENCE (OR ABSENCE) OF THE LUT.
1130 ; IF ALL 256. GREY-SCALE LEVELS ARE ACTIVE, WE'LL HAVE ONLY
1131 ; 1 VECTOR PER LEVEL !!!!!
1132 ; BUFFER REQUIRES <256.*5>+2 WORDS.
1133 ;
1134 ;
1135 005116 114000 000376 000476 BARS: APNT, HAFX-400, HAFY60-240
1136 005124 100000 000002 DSUB, 2 ; EXECUTE CODE IN BUFFER.
1137 005130 173406 STUPI+6 ; END OF FRAME 6.
1138 ;
1139 ; DISPLAY CODE BUFFER.
1140 ; SHAPED BY THE BIT MAP AND BARS ROUTINES.
1141 ;
1142 005132 SUB.2: ; SUBROUTINE CALL FOR "BARS".
1143 005132 DBUFR: .BLAW <256.*5>+2
  
```

```

1145 ;SBTTL *** PART 2 -- DISPLAY CODE
1146 ;
1147 ; DISPLAY FILES FOR BOUNCING BALLS.
1148 ; IMAGE MEMORIES ARE IN R/W MODE INITIALLY.
1149 ; IF R/W MODE SELECTED, USE B1A AS ENTRY.
1150 ; OTHERWISE, RECONFIGURE IM'S AT B1, AND USE B2 AS THE ENTRY.
1151 ;
1152 012136 176050 176434 B1: READ:CH0, WRIT:CH1 ; DYNAMIC MODE, RESET IM'S.
1153 012142 177050 177434 READ:CH2, WRIT:CH3
1154 012146 173000 STUPE
1155 ;
1156 012150 116000 B1A: APNT:NONE ; READ/WRITE MODE...
1157 012152 000000 000000 URB: 0,0 ;...ERASE OLD IMAGE...
1158 012156 100000 000003 DSUB, 3
1159 012162 114000 APNT
1160 012164 000000 000000 UGB: 0,0
1161 012170 100000 000003 DSUB, 3 ;...AND FALL THRU.
1162 ;
1163 012174 116500 B2: APNT:RED ; DYNAMIC MODE...
1164 012176 000000 000000 RB: 0,0 ;...REDRAW NEW IMAGE.
1165 012202 100000 000003 DSUB, 3
1166 012206 117140 APNT:GRN
1167 012210 000000 000000 GB: 0,0
1168 012214 100000 000003 DSUB, 3
1169 012220 103774 CHAR:WHILE
1170 012222 040 040 040 B3: .ASCII / ; "CRASH" IF THAT'S THE CASE.
1171 012225 040 040 040
1172 012230 117774 000000 000000 APNT:WHITE, 0, 0 ; FINALLY, REFRESH THE FRAME.
1173 012236 110000 LVEC
1174 012240 041776 000000 I+MAXX, 0
1175 012244 040000 001676 I+0, MAXY60
1176 012250 061776 000000 I+MAXY+MAXX, 0
1177 012254 040000 021676 I+0, MAXY+MAXY60
1178 ;
1179 012260 164002 B4: SYNC+1 ; "SWITCH:CLRMEM" IF DYNAMIC.
1180 012262 164000 DNOP, DNOP
1181 012266 173400 STUPE
1182 ;
1183 ; OCTAGON SUBROUTINE.
1184 ;
1185 012270 SUB.3: ; CALL VIA CHAR CODE 3.
1186 012270 SVEC ;DRAW CCW FROM LO LEFT CORNER.
1187 012272 .WORD SI+SX+SY
1188 012274 .WORD SI+SX+SY
1189 012276 .WORD SI+SX+SY
1190 012300 .WORD SI+SX+SY
1191 012302 .WORD SI+SX+SY
1192 012304 .WORD SI+SX+SY
1193 012306 .WORD SI+SX+SY
1194 012310 .WORD SI+SX+SY
1195 012312 .WORD SI+SX+SY
1196 012314 .WORD SI+SX+SY
1197 012316 DNOP
  
```

1198			.SBTTL	*** ASCII CHARACTER ADDRESS TABLE AND SUBPIX
1199			:	
1200			:	THE FOLLOWING 128 WORDS CONTAIN THE CHARACTER SUBPIX
1201			:	STARTING ADDRESSES. 0-37 AND 140-177 ARE NULL FOR NUM.
1202			:	(EXCEPT FOR 12 AND 15)
1203			:	
1204	012320	012720'	ACAT:	..NULL :0-37 ARE NULL (NLISTED).
1210				
1211		012420'	..SVPC=.	:SAVE PC, BACK UP AND...
1212		012344'	..ACAT<12*2>	
1213	012344	012722'	..LF	:...INSERT LINE FEED...
1214		012352'	..ACAT<15*2>	
1215	012352	012732'	..CR	:...AND CARRIAGE RETURN ADDRESSES.
1216		012420'	..SVPC	:RESTORE PC
1217				
1218	012420	012746'	..SPC	:SPACE
1219	012422	012754'	..EXC	:EXCLAM
1220	012424	012772'	..QUO	:DOUBLE QUOTE.
1221	012426	013010'	..NUM	:NUMBER SIGN
1222	012430	013036'	..DOL	:DOLLAR
1223	012432	013070'	..PCT	:PER CENT
1224	012434	013130'	..AND	:AMPERSAND
1225	012436	013170'	..QUO1	:SINGLE QUOTE
1226	012440	013202'	..LPAR	:LFT PAREN
1227	012442	013220'	..RPAR	:RT PAREN
1228	012444	013236'	..AST	:ASTERISK
1229	012446	013260'	..PLS	:PLUS
1230	012450	013276'	..CMA	:COMMA
1231	012452	013310'	..MNS	:MINUS
1232	012454	013322'	..DOT	:PERIOD
1233	012456	013332'	..SLSH	:SLASH
1234	012460	013346'	..00	
1235	012462	013400'	..11	
1236	012464	013420'	..22	
1237	012466	013452'	..33	
1238	012470	013512'	..44	
1239	012472	013532'	..55	
1240	012474	013562'	..66	
1241	012476	013614'	..77	
1242	012500	013634'	..88	
1243	012502	013704'	..99	
1244	012504	013734'	..COLN	:COLON
1245	012506	013752'	..SEMI	:SEMI COLON
1246	012510	013770'	..LANG	:LFT ANGLE
1247	012512	014004'	..EQ	:EQUALS
1248	012514	014022'	..RANG	:RT ANGLE
1249	012516	014034'	..QUES	:QUESTION
1250	012520	014062'	..ATS	:AT SIGN
1251	012522	014116'	..AA	
1252	012524	014142'	..BB	
1253	012526	014176'	..CC	
1254	012530	014224'	..DD	
1255	012532	014246'	..EE	
1256	012534	014254'	..FF	
1257	012536	014272'	..GG	
1258	012540	014310'	..HH	
1259	012542	014330'	..II	

1260	012544	014352'	..JJ	
1261	012546	014372'	..KK	
1262	012550	014410'	..LL	
1263	012552	014424'	..MM	
1264	012554	014442'	..NN	
1265	012556	014462'	..OO	
1266	012560	014512'	..PP	
1267	012562	014534'	..QQ	
1268	012564	014550'	..RR	
1269	012566	014564'	..SS	
1270	012570	014622'	..TT	
1271	012572	014640'	..UU	
1272	012574	014662'	..VV	
1273	012576	014702'	..WW	
1274	012600	014722'	..XX	
1275	012602	014746'	..YY	
1276	012604	014772'	..ZZ	
1277	012606	015014'	..LBRK	:LFT BRACKET
1278	012610	015032'	..BSLH	:BACK SLASH
1279	012612	015050'	..RBRK	:RT BRACKET
1280	012614	015066'	..CRT	:CARROT
1281	012616	015102'	..USC	:UNDERSCOME
1282	012620	012720'	..NULL	:140-177 ARE NULL (NLISTED).

```

1289 ; THESE ARE THE CHAR SUBPIX ROUTINES.
1290 ; EACH IS BUILT ON A 5 X 7 DOT MATRIX (ALA VFD5).
1291 ; <CR> WILL INVOKE A "STOP". CPU MUST CALCULATE THE START
1292 ; CD-ORDS FOR NEXT LINE, STUFF IN "..CRX AND ..CRX+2" AND
1293 ; RESUME, TO XCT PSUEDO-CRLF.
1294 ;
1295 ; THE FOLLOWING MACRO (SKY4) APPLIES A 4X SCALE FACTOR AT
1296 ; ASSEMBLY TIME TO YIELD A CHAR SIZE OF 30 X 40 (OCTAL)
1297 ; WHICH PRODUCES A MEDIUM SIZE CHAR FONT.
1298 ;
1299 ;
1300 ; .MACRO SKY4 A,B,C
1301 ; SKY A,B*4,C*4
1302 ; .ENDM SKY4
1303 012720 165000 ;.NULL: DPOP ;ALL UNDEFINED CHARS EXIT HERE.
1304
1305 ;***** CHAR SET IS .ALISTED *****
  
```

```

1879 .SHT1L *** 256 LEVEL LUMINANCE (GREY-SCALE) TABLE
1880 ;
1881 ; THIS IS A TABLE OF 256 (8 BITS) LUMINANCE VALUES
1882 ; WHICH SHOULD PRODUCE A LINEAR 12 BIT GREY-SCALE.
1883 ;
1884 ; ON THE GREY-SCALE, 36 = BLUE, 120 = RED, AND 230 = GREEN.
1885 ;
1886 015112 ;LUMTBL:
1887 015112 000000 000400 001000 0, 400, 1000, 1
1888 015120 000601
1888 015122 001400 000401 002000 1400, 401, 2000, 1001
1889 015130 001001
1889 015132 002400 000002 003000 2400, 2, 3000, 402
1890 015140 000402
1890 015142 003400 002401 004000 3400, 2401, 4000, 3
1891 015150 000003
1891 015152 003001 002020 005000 3001, 2020, 5000, 2420
1892 015160 002420
1892 015162 000004 005400 003002 4, 5400, 3002, 6000
1893 015170 000000
1893 015172 001040 006400 002403 1040, 6400, 2403, 5
1894 015200 000005
1894 015202 003003 002040 007400 3003, 2040, 7400, 5020 ; LEVEL 37
1895 015210 005020
1895 015212 002404 000006 000006 2404, 60, 6, 1441
1896 015220 001441
1896 015222 003004 002023 001042 3004, 2023, 1042, 2405
1897 015230 002405
1897 015232 000007 004440 002024 7, 4440, 2024, 1061
1898 015240 001061
1898 015242 000100 000010 000462 100, 10, 462, 2025
1899 015250 002025
1899 015252 001044 002407 000011 1044, 2407, 11, 501
1900 015260 000501
1900 015262 007440 001063 000120 7440, 1063, 120, 12
1901 015270 000012
1901 015272 000502 003500 001064 502, 3500, 1064, 2411
1902 015300 002411
1902 015302 005407 000503 002030 5407, 503, 2030, 1065 ; LEVEL 77
1903 015310 001065
1903 015312 000140 000014 000504 140, 14, 504, 2031
1904 015320 002031
1904 015322 001066 002413 000015 1066, 2413, 15, 505
1905 015330 000505
1905 015332 007444 002540 000160 7444, 2540, 160, 16
1906 015340 000016
1906 015342 003014 003504 002541 3014, 3504, 2541, 125
1907 015350 000125
1907 015352 000017 002160 002034 17, 2160, 2034, 200
1908 015360 000200
1908 015362 000126 003142 002161 126, 3142, 2161, 3506
1909 015370 003506
1909 015372 001072 001600 007066 1072, 1600, 7066, 2162
1910 015400 002162
1910 015402 002036 000220 001601 2036, 220, 1601, 3126 ; LEVEL 137
1911 015410 003126
1911 015412 002163 005161 002563 2163, 5161, 2563, 131
  
```


1912	015420	000131	000513	005162	4600,	513,	5162,	240	
	015422	004600							
	015430	000240							
1913	015432	000132	003146	002165	132,	3146,	2165,	1222	
	015440	001222							
1914	015442	004220	004074	004602	4220,	4074,	4602,	2166	
	015450	002166							
1915	015452	003513	000260	004075	3513,	260,	4075,	3132	
	015460	003132							
1916	015462	003640	001152	002477	3640,	1152,	2477,	3223	
	015470	003223							
1917	015472	006607	003567	000300	607,	3567,	300,	210	
	015500	000210							
1918	015502	005476	002243	005241	5476,	2243,	5241,	1154	; LEVEL 177
	015510	001154							
1919	015512	004152	004660	003153	4152,	4660,	3153,	2172	
	015520	002172							
1920	015522	003517	000320	004117	3517,	320,	4117,	646	
	015530	000646							
1921	015532	002173	001230	002555	2173,	1230,	2555,	157	
	015540	000157							
1922	015542	000647	007624	000340	647,	7624,	340,	2574	
	015550	002574							
1923	015552	005554	002321	002213	5554,	2321,	2213,	2703	
	015560	002703							
1924	015562	000267	003265	004612	267,	3265,	4612,	3647	
	015570	003647							
1925	015572	000360	001741	003266	360,	1741,	3266,	4631	
	015600	004631							
1926	015602	002177	005213	005703	2177,	5213,	5703,	1634	; LEVEL 237
	015610	001634							
1927	015612	000671	003651	005176	671,	3651,	5176,	272	
	015620	000272							
1928	015622	001617	006266	001362	1617,	6266,	1362,	4360	
	015630	004360							
1929	015632	000273	007232	000655	273,	7232,	655,	3653	
	015640	003653							
1930	015642	001237	004235	004743	1237,	4235,	4743,	1256	
	015650	001256							
1931	015652	007615	001312	005237	7615,	1312,	5237,	3345	
	015660	003345							
1932	015662	000731	007616	002712	731,	7616,	2712,	1747	
	015670	001747							
1933	015672	006235	002331	003656	6235,	2331,	3656,	4364	
	015700	004364							
1934	015702	000277	003275	002332	277,	3275,	2332,	5330	; LEVEL 277
	015710	005330							
1935	015712	002714	005712	003276	2714,	5712,	3276,	7745	
	015720	007745							
1936	015722	001370	004366	007364	1370,	4366,	7364,	3277	
	015730	003277							
1937	015732	002334	002371	004367	2334,	2371,	4367,	5714	
	015740	005714							
1938	015742	004751	006276	001372	4751,	6276,	1372,	2717	
	015750	002717							
1939	015752	005715	004752	006277	5715,	4752,	6277,	1373	
	015760	001373							

1940	015762	004371	007367	004753	4371,	7367,	4753,	2337	
	015770	002337							
1941	015772	001374	004372	005717	1374,	4372,	5717,	4754	
	016000	004754							
1942	016002	007752	001375	004373	7752,	1375,	4373,	1757	; LEVEL 337
	016010	001757							
1943	016012	004755	007753	005337	4755,	7753,	5337,	4374	
	016020	004374							
1944	016022	007372	003357	006355	7372,	3357,	6355,	7754	
	016030	007754							
1945	016032	005374	002776	005774	5374,	2776,	5774,	7337	
	016040	007337							
1946	016042	006356	005357	004376	6356,	5357,	4376,	7374	
	016050	007474							
1947	016052	004776	007756	006757	4776,	7756,	6757,	4377	
	016060	004377							
1948	016062	007375	006376	007757	7375,	6376,	7757,	2777	
	016070	002777							
1949	016072	006776	005777	006376	6776,	5777,	6376,	6377	
	016100	006377							
1950	016102	007776	006777	007377	7776,	6777,	7377,	7777	; LEVEL 377
	016110	007777							

```
1952          .SBTTL *** PATCH AREA AND END
1953          ;
1954          ; PATCHES GO HERE.
1955          ;
1956 01b112    PATCH: .BLAN 4U
1957
1958          LASTAD= .          ; END OF MODULE.
1959
1960          .SBTTL ***** TH - TH - TH - TH - THAT'S ALL FOLKS ! *****
1961          .END
000001
```

```
ACAT 012320P      CLRMEM= 170140      GHY = 12400U      MOVE 003546H      RES2 000060H
ACSR 000102H      CNTNU 003102R      GHASCL 000626H   MSGNS = 104403   RPNT = 130000
ADDBAS= 000010    CONFIG 00Q056R   GRN = 003140     MSGS = 104402   RSTRT 000112R
ADDR 000006H      CSRA 000100R    GRX 004526R      MSGS = 104401   R256 = 000100
ADDRX 000300R     CUIUM = 146016   GRY 004216R      MSX = 020000    K6 = 0000006
ADDR22= 001000    CUIUFF= 146012  GWBUFF= 104414   MSY = 000100    R7 = 0000007
ALL = 003774      CUIS = 146013   GXI = 174100     MXY = 020000    SAVDPU 000452R
APMT = 114000     CUOFF = 146040  GYI = 174100     M128 = 000002  SBADR 000102R
ASB 000106R      CUON = 146060   HAFX = 000776    M256 = 000003  SDPC 000252R
ASTAT 000104R     CURD = 146100   HAFY = 000776    M32 = 000000    SDSH 000254R
AWAS 000110R      DATCK= 104411   HAFY50= 000776  M64 = 000001    SDXK 000256R
BADPAD 000336H    DATERS= 104404  HAFY60= 000736  NEXT 001774R    SOXR 000260R
BARS 005116H      DBEGIN 003726R  HILIM 003360H    NUNE = 002000   SETCB = 152000
BEGIN 000000P     DHUFR 005132R  HPDCMT 000044R   NULL = 000000   SEHB = 150000
BELL 003724H      DEVON 000304R  HRDERS= 104405  OCTGNS 004056P  SETMEM= 170100
BIT0 = 000001     DING 003704R   HRDPAS 000050R  UGB 012164H     SEIRR = 000014
BIT1 = 000002     DIVCHK 000306R HSI = 040000     GNE 001042R     SETUP 001364R
BIT10 = 002000    DJMP = 160000   HSX 004734R      OPEN = 000000   SHADES 000674R
BIT11 = 004000    DLOOP 000330P  HSY 004424R      ORB 012152R     SIOFF = 171000
BIT12 = 010000    DNOP = 164000  HUES 002354R     UTGAS = 104420  SION = 171400
BIT13 = 020000    DUNE1 002454R  I = 040000U      PADUR 000322H   SKP1 = 000401
BIT14 = 040000    DONE2 002464R  ICONT 000036R   PART1 001632R   SKP2 = 000402
BIT15 = 100000    DPC 000222R    ICUUNT 000040P  PAKT2 002546R   SKP3 = 000403
BIT2 = 000004     DPOP = 165000  IDNUM 000122P    PASCNT 000034R SLDR 000266R
BIT3 = 000010     DRK 000224R    INIT 000030R    PASS 002536K   SLMR 000270R
BIT4 = 000020     DSR 000224R    INTK 000120H    PATCH 016112R  SLSR 000264R
BIT5 = 000040     DSTMT 000326R  JSMV 000242H    PIRGS = 000004  SPCFNT 000042K
BIT6 = 000100     DSUB = 100000  JSSV 000246R    POPSP = 005726  SUPERS= 104406
BIT7 = 000200     DVID1 000014R  JSU = 000000     POPSP2= 022626  SPPAS 000046K
BIT8 = 000400     DX = 000002    JS1 = 000400     PROTEC= 176000  SPUINT 000032H
BIT9 = 001000     DXR 000226R    JS2 = 001000     PRTY = 000000   SPSIZ = 000040
BLU = 002170      DX1 003536R    JS3 = 001400     PRTY0 = 000000  SM1 000016R
BLXIT 000756R     DX2 003542R   LASTAD= 016212R PRTY1 = 000040   SM2 000020R
BM04 = 134000     DYI = 000002   LDI = 002000     PRTY2 = 000100  SM3 000022R
BM08 = 135000     DYN1 = 000004  LDR 000234K      PRTY3 = 000140  SR4 000024R
BM14 = 136000     DYR 000230R   LDUN = 100000    PRTY4 = 000200  START 001000R
BM18 = 137000     EABITS 000324R LMR 000236R      PRTY5 = 000240  STAT 000026R
BREAKS= 104407    ENDITS= 104413 LUGU 003746R     PRTY6 = 000300  STOP = 172000
BR1 000012R      ENDS = 104410  LOLIM 003356H    PRTY7 = 000340  STOP1 = 173400
BR2 000013R      ERRTRP 000106R LREAD = 010000   PS = 177776     STOPN = 173000
BRDMS = 104421    EXITS = 104400  LSET = 010000    PS* = 177776   STOP1 002022R
B04 005066R      EXSM2 = 000050 LSR 000232R     PUSH = 005746  STOP2 003114H
B08 005102R      EXSM4 = 000060 LUMTBL 015112R   PUSH2 = 024646 STP1 000240R
B1 012136R        EX2 = 000010  LUIFLG 000262R  RAND 003310R   SUB.1 004154R
B1A 012150R       EX4 = 000020  LUTIM 001256H   RANDS = 104417 SUB.2 005132R
B14 005036R       FCD 000274R   LUTV 000250K    RANNUM 000054R SUB.3 012270R
B18 005052R       FCD1 000276R  LVEC = 110000    RB 012176R     SVEC = 104000
B2 012174H        FDIX 000272R  LO = 002000     RDWHT = 176074 SVRO 000062R
B3 012222P        FR2 000276R  MAP22= 104416   RDWHT1= 176076 SVR1 000064R
B4 012260R        FR3 002106R  MATCH 000344R    READ = 176050  SVR2 000066H
CANT 001564R       FR4 002116R   MAXX = 001776   RED = 002500   SVR3 000070R
CDATAS= 104412    FR5 002130R   MAXY = 001776   RELOC 000332R  SVR4 000072H
CHAR = 100000     FR6 002224R   MAXY50= 001776  RELOC 000334R  SVR5 000074H
CH0 = 000000      G6 012210R    MAXY60= 001676  RESM 002012R   SVR6 000076R
CH1 = 000400      GCOFF = 000100  MEMERR= 000004  RESTRA 001064H  SWE = 000010
CH2 = 001000      GETPAS= 104415 MODNAM 000000H  RESTRT 001026H SWITCH 000344R
CH3 = 001400      GHX = 120000  MODSP 000222R   RES1 000056R   SWTCH = 170200
```

SYNC = 164001	WDTU = 000114H	..CK	U12732R	..MM	014424H	..SS	014564R
SYSCNT 000052R	WHITE = 003774	..CRT	015066H	..MNS	013310R	..TT	014622R
TEMPC 000342R	WRT = 176034	..CRX	012740R	..NN	014442R	..USC	015102R
TEMPS 000340R	WRT1 = 176036	..DD	U14224H	..NULL	012720R	..UU	014640R
TIMOUT 000366R	XFLAG 000005R	..DUL	U13036R	..NUM	013010R	..VV	014662R
TOTV 000244R	XVAL = 001650	..DOT	013322R	..UU	U14462R	..WW	014702R
TRPDFN= 000022	YVAL = 001550	..EE	U14246R	..PCT	013070R	..XX	014722R
TRP1 003362R	Y1 = 000000	..EU	U14004R	..PLS	013260R	..YY	014746R
TRP2 003404R	SSVPC = 012420R	..EXC	U12754H	..PP	014512R	..ZZ	014772R
T0 000310R	SX = 002000	..FF	U14254H	..QU	014534H	..00	013346R
T1 000312R	SY = 000000	..GG	U14272R	..QUES	U14034R	..11	013400R
T2 000314H	..AA 014116R	..HH	014310R	..QUO	012772R	..22	013420H
T3 000316H	..AND 013130H	..II	U14330R	..QUO1	013170H	..33	013452R
UNXLI 000512R	..AST 013236R	..JJ	U14352R	..RANG	014022R	..44	013512R
VADDN 000320R	..ATS 014062R	..KK	U14372R	..RBHK	015050R	..55	013532R
VECBAS= 000020	..BH 014142R	..LANG	013770R	..RPAR	012220R	..66	013562R
VECTOR 000010H	..BSLH 015032H	..LBRK	U15014R	..RR	014550H	..77	013614R
VECTIX 000302R	..CC 014176R	..LF	U12722R	..SEMI	013752R	..88	013634R
WASADR 000104R	..CMA 013276R	..LL	U14410R	..SLSH	013332H	..99	013704R
WDFR 000116R	..COLN 013734R	..LPAR	013202R	..SPC	012746R		

. ABS. 000000 000
v16212 001

ERRORS DETECTED: 0

VIRTUAL MEMORY USED: 8480 WORDS (34 PAGES)
DYNAMIC MEMORY: 9786 WORDS (37 PAGES)
ELAPSED TIME: 00:02:47



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IDENTIFICATION

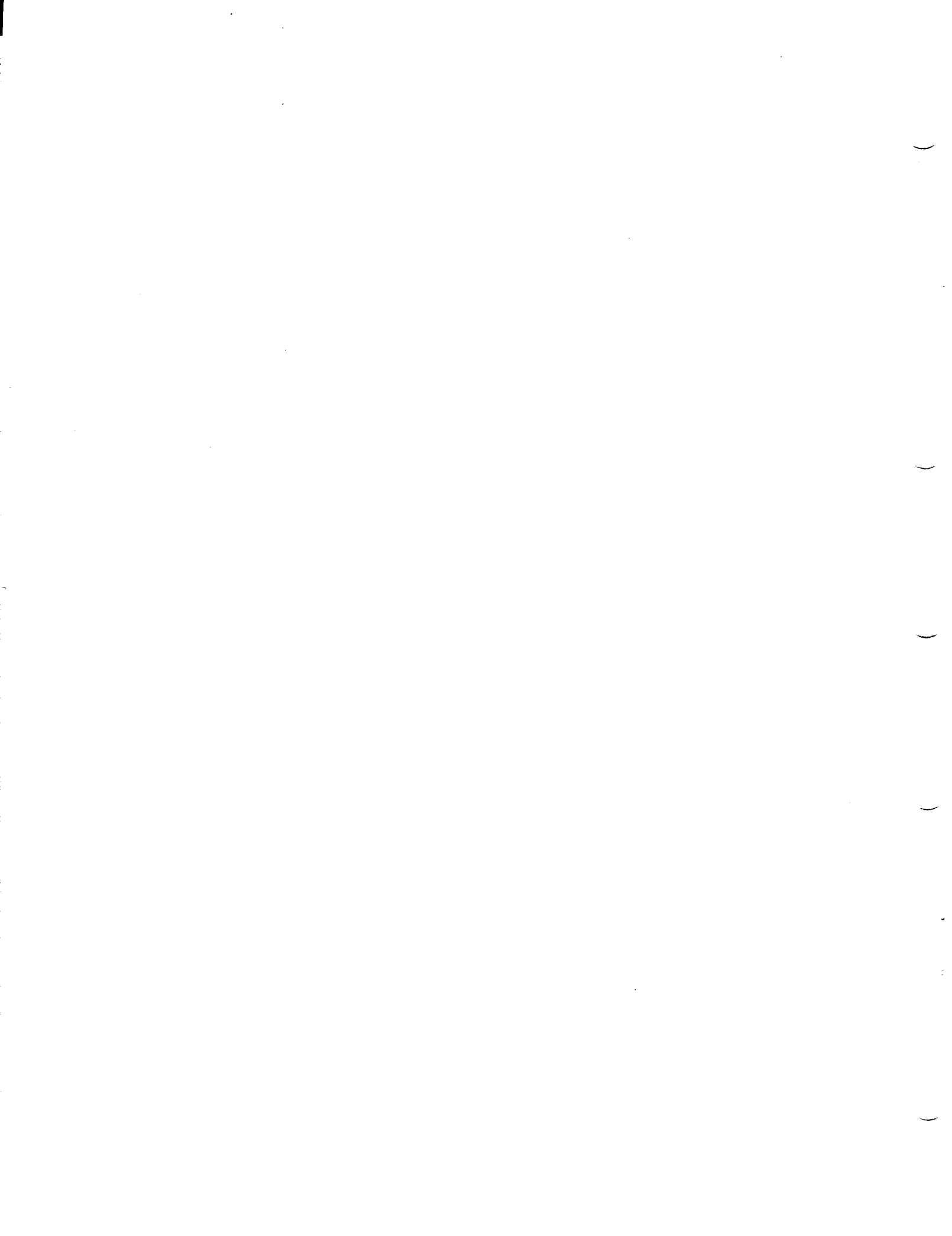
PRODUCT CODE: AC-S210A-MC
PRODUCT NAME: CXTMDA0 DEC/X11 TM78 MAGTAPE EXERCISER MODULE
DATE: JUNE 1,1980
MAINTAINER: DIAGNOSTIC ENGINEERING

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1.0 ABSTRACT

THE TMD IS AN IOMODX MODULE THAT CAN EXERCISE UP TO 8 TM78 CONTROLLERS (DRIVES) WITH UP TO 4 TU78 TAPE TRANSPORTS (SLAVES) ON A SINGLE TM78. EACH TU78 WILL BE EXERCISED BY DOING A WRITE, READ REVERSE, READ FORWARD AND AN IN-CORE COMPARE. THIS SEQUENCE OF FUNCTIONS WILL BE DEFINED AS A CYCLE. AN "END OF PASS" WILL BE REACHED AFTER 500 CYCLES, OR WHEN END OF TAPE (EOT) IS DETECTED. WHEN "END OF PASS" IS REACHED, A "TAPE MARK" FUNCTION IS EXECUTED BEFORE CONTINUING.

WHEN EOT IS DETECTED AND AFTER THE TAPEMARK IS WRITTEN, THE ERROR SUMMARY IS PRINTED, ALL TU78(S) ARE REWOUND, AND THE DENSITY CHANGED.

THIS MODULE UTILIZES THE AUTO REPOSITIONING FEATURES OF THE TM78 TO ATTEMPT RECOVERY FROM WRITE AND READ STATUS ERRORS. THEREFORE, RECOVERY SEQUENCES ARE AS DEFINED FOR THE TM78 OPERATIONAL MICRO CODE, AND RECOVERABLE/NON-RECOVERABLE ERROR REPORTS AND STATISTIC UPDATE ARE BASED SOLEY ON THE OPERATIONAL MICROCODE RETRY ALGORITHM RESULTS.

STATISTICS MAINTAINED FOR EACH TU78 ARE:

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RECOVERABLE READ ERRORS
UNRECOVERABLE READ ERRORS
RECOVERABLE WRITE ERRORS

READ STATUS ERRORS ARE NOT PRINTED ON A TRY BY TRY BASIS BUT RATHER THE FINAL OUTCOME OF EACH RETRY (RECOVERABLE OR NON-RECOVERABLE) IS PRINTED ON A RECORD BY RECORD BASIS.

WRITE STATUS ERRORS ARE NOT PRINTED ON A TRY BY TRY BASIS BUT ARE ALSO CLASSIFIED AS RECOVERABLE OR NON-RECOVERABLE. HOWEVER, A UNRECOVERABLE WRITE ERROR WILL CAUSE THE UNIT TO BE DROPPED AS THE OPERATIONAL MICRO CODE CANNOT CONTINUE, AND MAINTAIN A READABLE (ANSI STANDARD) TAPE.

2.0 REQUIREMENTS

HARDWARE: AT LEAST 1 TU78 SLAVE ON A TM78 CONTROLLER AND AN RH11/RH70.

SOFTWARE: TMB REQUIRES 6K BYTES OF STORAGE - (3K WORDS)

3.0 PASS DEFINITION

ONE PASS CONSISTS OF 500 CYCLES OF A WRITE, READ REVERSE, READ FORWARD, AND DATA CHECK OPERATIONS.

4.0 EXECUTION TIME

ONE PASS OF TMD TAKES APPROXIMATELY MINUTE.

5.0 CONFIGURATION REQUIREMENTS

DEFAULT PARAMETERS: DEVADR: 172400 VECTOR: 224 BR1: 5

REQUIRED PARAMETERS: NONE

6.0 DEVICE/OPTION SETUP

POWER UP ALL TU78'S AND MAKE READY AT LOAD POINT AND WRITE ENABLED.

7.0 MODULE OPERATION TEST SEQUENCE

REFER TO SECTION 13.0.

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8.0 DRIVER OPERATION OPTIONS

SR1 - THE DEFAULT FOR ALL OPTIONS IS ZERO (0).

BIT0 = 0: ALLOW DATA COMPARE

BIT0 = 1: INHIBIT DATA COMPARE

BIT1 = 0: MODULE WILL EXERCISE ALL TU78'S FOUND ON THE USER
SPECIFIED MASSBUS CONTROLLER.

BIT1 = 1: OPERATOR MUST SELECT THE TU78(S) IN ADDITION TO THE
TM78'S (DVID1) TO BE EXERCISED. SEE SECTION 10.0 FOR
DETAILS.

BIT2 = 0: REPORT RECOVERABLE/NON-RECOVERABLE ERRORS AS THEY OCCUR.

BIT2 = 1: DO NOT REPORT RECOVERABLE/NON-RECOVERABLE ERRORS AS
THEY OCCUR.

BIT3 = 0: PRINT ERROR SUMMARY AT EOT.

BIT3 = 1: DO NOT PRINT ERROR SUMMARY AT EOT.

BIT4 = 0: TEST GCR (6250 BPI)

BIT4 = 1: DO NOT TEST GCR (6250 BPI)

BIT5 = 0: TEST PE (1600 BPI)

BIT5 = 1: DO NOT TEST PE (1600 BPI)

9.0 SELECTING SLAVES

BIT 1 OF SR1 = 1

WHEN BIT 1 IS SET IN SR1, THE OPERATOR MUST SELECT THE TU78(S)
TO BE TESTED.

TO SELECT THE TU78(S) FOR A PARTICULAR TM78, A "1" MUST BE SET
IN THE APPROPRIATE BIT POSITION FOR THE DESIRED TU78. REFER TO
THE FOLLOWING TABLE.

UNIT TABLE FORMAT

T U 7 8 #

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	15	4	3	2	1	0	
580	+-----+-----+-----+-----+-----+-----+						
581	!	NOT USED	!	TM0	!	TM0	!
582	!		!	TU3	!	TU2	!
583	!		!	TU1	!	TU0	!
584	+-----+-----+-----+-----+-----+-----+						
585	!	NOT USED	!	TM1	!	TM1	!
586	!		!	TU3	!	TU2	!
587	!		!	TU1	!	TU0	!
588	+-----+-----+-----+-----+-----+-----+						
589	!	NOT USED	!	TM2	!	TM2	!
590	!		!	TU3	!	TU2	!
591	!		!	TU1	!	TU0	!
592	+-----+-----+-----+-----+-----+-----+						T
593	!	NOT USED	!	TM3	!	TM3	!
594	!		!	TU3	!	TU2	!
595	!		!	TU1	!	TU0	!
596	+-----+-----+-----+-----+-----+-----+						7
597	!	NOT USED	!	TM4	!	TM4	!
598	!		!	TU3	!	TU2	!
599	!		!	TU1	!	TU0	!
600	+-----+-----+-----+-----+-----+-----+						8
601	!	NOT USED	!	TM5	!	TM5	!
602	!		!	TU3	!	TU2	!
603	!		!	TU1	!	TU0	!
604	+-----+-----+-----+-----+-----+-----+						#
605	!	NOT USED	!	TM6	!	TM6	!
606	!		!	TU3	!	TU2	!
607	!		!	TU1	!	TU0	!
608	+-----+-----+-----+-----+-----+-----+						
609	!	NOT USED	!	TM7	!	TM7	!
610	!		!	TU3	!	TU2	!
611	!		!	TU1	!	TU0	!
612	+-----+-----+-----+-----+-----+-----+						V

REMEMBER THAT DVID1 MUST BE SET UP FOR THE PROPER TM78'S TO BE TESTED. IF THE TM78 IN NOT SELECTED IN DVID1, THE INFORMATION FOR THAT TM78 IN THE UNIT TABLE IS IGNORED.

BIT 1 OF SR1 = 0

THE DEC/X11 DRIVER WILL SEARCH FOR AND TEST ALL TU78 SLAVES THAT RESPOND READY, ON-LINE AND WRITE ENABLED. IN THIS MODE THE CONTENTS OF DVID1 IS NOT USED.

10.0 DUAL PORT OPTION

THE TM78 PRODUCT WILL ALLOW TWO RH11/RH70 CONTROLLERS (PORTS A AND B) TO BE CONNECTED, AND EACH TU78 HAS A FRONT PANEL SWITCH THAT SPECIFIES THE PORTS THAT HAVE ACCESS TO THAT TU. THE CHOICES ARE A, B, A AND B, OR NONE.

THE A ONLY, B ONLY, OR NONE CONFIGURATIONS POSE NO PROBLEM TO THIS DRIVER, BUT THE A AND B CONFIGURATION IS A PROBLEM. THE PROBLEM EXISTS BECAUSE THE TM78 DOES NOT ARBITRATE PORT ACCESS, AND PROVIDES NO PATH TO ALLOW SOFTWARE TO DO IT.

THEREFORE, THE BOTH MODE OF A TU78 IS ONLY USEABLE FOR WRITE ONLY UTILIZATION, OR FOR INTERLEAVED OPERATIONS SYNCHRONIZED THROUGH SOME EXTERNAL MECHANISM.

AS A RESULT DUAL MB ACCESS TO A SINGLE TU78 IS NOT SUPPORTED

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-----* ERRORS *  
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                2.1  
                *****  
                * *  
-----* DENSITY CONTROL*-----                3.1  
I * * I *****  
I ***** I * *  
I                2.2 I-----* ISSUE WRITE *-----  
I ***** I * * I  
I * * I ***** I  
I--* BOT CONTROL *-----I                3.2 I  
I * * I ***** I  
I ***** I * * ISSUE * I  
I                2.3 I-----* READ *-----I  
I ***** I * * FORWARD * I  
I * * I ***** I  
I--* WRITE CONTROL *-----I                3.3 I  
I * * I ***** I  
I ***** I * * ISSUE * I  
I                2.4 I-----* READ *-----I  
***** I ***** I * * REVERSE * I *****  
* SEQUENCE * I * * READ * I ***** I * INT *  
* CONTROL *--I--* FORWARD *-----I                3.4 I-----* CTL *  
* * I * * CONTROL * I ***** I * *  
***** I ***** I * * ISSUE * I *****  
I                2.5 I-----* TAPE *-----I  
I ***** I * * MARK * I  
I * * READ * I ***** I  
I--* REVERSE *-----I                3.5 I  
I * * CONTROL * I ***** I  
I ***** I * * * I  
I                2.6 I-----* ISSUE REWIND *-----I  
I ***** I * * * I  
I * * TAPE * I ***** I  
I--* MARK *-----I                3.6 I  
I * * CONTROL * I ***** I  
I ***** I * * * I  
I                2.7 I-----* ISSUE SENSE *-----  
I ***** I * * *  
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I--*REWIND CONTROL *-----  
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12.0 DATA STRUCTURES OF THE TM78 DEC/X11 DRIVER

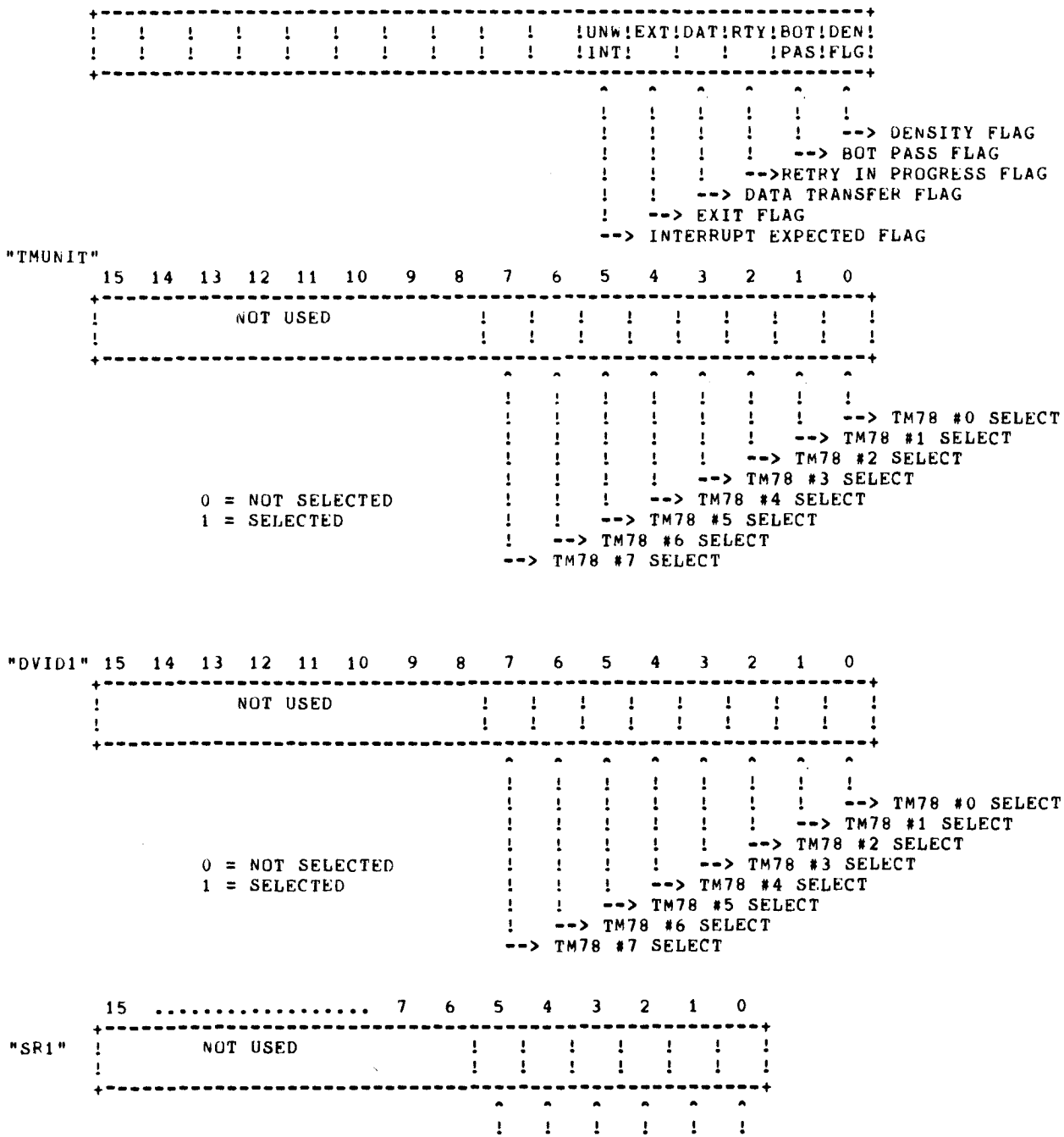
	15	0	
TMBIT	-----	+	TM78 # CONVERTED TO A BIT POSITION
TMBIT2	-----	+	TM78 # *2
TUBIT	-----	+	TU78 # CONVERTED TO A BIT POSITION
TUBIT2	-----	+	TU78 # *2
UCNT	-----	+	UNIT COUNT
TM78	-----	+	TM78 #
TU78	-----	+	TU78 #
EOTCNT	-----	+	EOT COUNT
TINTCD	-----	+	TERMINATION INTERRUPT CODE
ITM78	-----	+	INTERRUPTING TM78
ITU78	-----	+	INTERRUPTING TU78
BOTCNT	-----	+	# OF TU78'S AT BOT
SENSE	-----	+	SENSE COMMAND INFORMATION
CNDTI	-----	+	CLEAR ATTENTION INTERRUPT
ERRIDX	-----	+	MODULE ERROR INDEX
A16AD	-----	+	EXTENDED ADDRESS BITS 16 AND 17
PA18	-----*	+	LOWER 16 OF BUFFER ADDRESS
XMEM	-----*	+	EA BITS OF 18 BIT ADDRESS
PA22	-----*	+	LOWER 16 OF 22 BIT ADDRESS
EA22	-----*	+	EA BITS OF 22 BIT ADDRESS
FILE	-----	+	FILE NUMBER
RECORD	-----	+	RECORD NUMBER

* = THESE LOCATIONS MUST BE IN THIS ORDER

"FLAGS"

15 14 13 12 11 10 9 8 7 6 5 4 3 2 1 0

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```

860      ! ! ! ! ! --> 0=ALLOW DATA COMPARE
861      ! ! ! ! !      1=INHIBIT DATA COMPARE
862      ! ! ! ! ! --> 0=TEST AVAIL. TU78'S
863      ! ! ! ! !      1=TEST USER SEL. TU78'S
864      ! ! ! --> 0=REPORT ERRORS
865      ! ! !      1=DO NOT REPORT ERRORS
866      ! ! --> 0=STATISTICS AT EOT
867      ! !      1=NO STATISTICS AT EOT
868      ! --> 0=TEST GCR
869      !      1=DO NOT TEST GCR
870      -->0=TEST PE
871      1=DO NOT TEST PE
    
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UNIT TABLE

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		T U 7 8 #						
		←-----						
		15	4	3	2	1	0	
UTBL	!	NOT USED	! TM0	! TM0	! TM0	! TM0	! TM0	!
	!		! TU3	! TU2	! TU1	! TU0	!	!
	!	-----						!
	!	NOT USED	! TM1	! TM1	! TM1	! TM1	!	!
	!		! TU3	! TU2	! TU1	! TU0	!	!
	!	-----						!
	!	NOT USED	! TM2	! TM2	! TM2	! TM2	!	!
	!		! TU3	! TU2	! TU1	! TU0	!	T
	!	-----						!
	!	NOT USED	! TM3	! TM3	! TM3	! TM3	!	M
	!		! TU3	! TU2	! TU1	! TU0	!	!
	!	-----						!
	!	NOT USED	! TM4	! TM4	! TM4	! TM4	!	7
	!		! TU3	! TU2	! TU1	! TU0	!	8
	!	-----						!
	!	NOT USED	! TM5	! TM5	! TM5	! TM5	!	#
	!		! TU3	! TU2	! TU1	! TU0	!	!
	!	-----						!
	!	NOT USED	! TM6	! TM6	! TM6	! TM6	!	!
	!		! TU3	! TU2	! TU1	! TU0	!	!
	!	-----						!
	!	NOT USED	! TM7	! TM7	! TM7	! TM7	!	!
	!		! TU3	! TU2	! TU1	! TU0	!	!
	!	-----						V

0 = TU NOT SELECTED FOR TEST (USER SELECTED)
OR
TU NOT ONLINE-WRITE ENABLE-AVAILABLE (PROGRAM SELECTED)

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1 = TU SELECTED FOR TEST
 EOT TABLE

		T U 7 8 #										
		----->										
		15	4	3	2	1	0					
EOTTBL	!	NOT USED	!	TM0	!	TM0	!	TM0	!	TM0	!	!
	!		!	TU3	!	TU2	!	TU1	!	TU0	!	!
	!	----->										!
	!	NOT USED	!	TM1	!	TM1	!	TM1	!	TM1	!	!
	!		!	TU3	!	TU2	!	TU1	!	TU0	!	!
	!	----->										!
	!	NOT USED	!	TM2	!	TM2	!	TM2	!	TM2	!	!
	!		!	TU3	!	TU2	!	TU1	!	TU0	!	T
	!	----->										!
	!	NOT USED	!	TM3	!	TM3	!	TM3	!	TM3	!	M
	!		!	TU3	!	TU2	!	TU1	!	TU0	!	!
	!	----->										!
	!	NOT USED	!	TM4	!	TM4	!	TM4	!	TM4	!	7
	!		!	TU3	!	TU2	!	TU1	!	TU0	!	8
	!	----->										!
	!	NOT USED	!	TM5	!	TM5	!	TM5	!	TM5	!	#
	!		!	TU3	!	TU2	!	TU1	!	TU0	!	!
	!	----->										!
	!	NOT USED	!	TM6	!	TM6	!	TM6	!	TM6	!	!
	!		!	TU3	!	TU2	!	TU1	!	TU0	!	!
	!	----->										!
	!	NOT USED	!	TM7	!	TM7	!	TM7	!	TM7	!	!
	!		!	TU3	!	TU2	!	TU1	!	TU0	!	V
	!	----->										!

0 = TU NOT AT EOT

1 = TU AT EOT
 SCRATCH UNIT TABLE

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		T U 7 8 #										
		15	4	3	2	1	0					
SUTRL	!	NOT USED	!	TM0	!	TM0	!	TM0	!	TM0	!	!
	!		!	TU3	!	TU2	!	TU1	!	TU0	!	!
	!	NOT USED	!	TM1	!	TM1	!	TM1	!	TM1	!	!
	!		!	TU3	!	TU2	!	TU1	!	TU0	!	!
	!	NOT USED	!	TM2	!	TM2	!	TM2	!	TM2	!	!
	!		!	TU3	!	TU2	!	TU1	!	TU0	!	T
	!	NOT USED	!	TM3	!	TM3	!	TM3	!	TM3	!	M
	!		!	TU3	!	TU2	!	TU1	!	TU0	!	!
	!	NOT USED	!	TM4	!	TM4	!	TM4	!	TM4	!	7
	!		!	TU3	!	TU2	!	TU1	!	TU0	!	!
	!	NOT USED	!	TM5	!	TM5	!	TM5	!	TM5	!	8
	!		!	TU3	!	TU2	!	TU1	!	TU0	!	!
	!	NOT USED	!	TM6	!	TM6	!	TM6	!	TM6	!	#
	!		!	TU3	!	TU2	!	TU1	!	TU0	!	!
	!	NOT USED	!	TM7	!	TM7	!	TM7	!	TM7	!	!
	!		!	TU3	!	TU2	!	TU1	!	TU0	!	V

0 = TU NOT BEING TESTED OR DROPPED
1 = TU BEING TESTED

UNRECOVERABLE READ ERROR STATISTIC TABLE
"URREAD"

15	00	TM78#	TU78#
!	!	0	0
!	!	0	1
!	!	0	2
!	!	0	3
!	!	1	0
!	!	1	1
!	!	1	2

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		.	.
		.	.
		.	.
		.	.
		6	0
		6	1
		6	2
		6	3
		7	0
		7	1
		7	2
		7	3

INDEX = ((TM78*(8))+(TU78*(2)))

RECOVERABLE READ ERROR STATISTIC TABLE
 "RREAD"

15	00	TM78#	TU78#
		0	0
		0	1
		0	2
		0	3
		1	0
		1	1
		1	2
		1	3
		.	.
		.	.
		.	.
		.	.

1084	.	.
1085		
1086	6	0
1087		
1088	6	1
1089		
1090	6	2
1091		
1092	6	3
1093		
1094	7	0
1095		
1096	7	1
1097		
1098	7	2
1099		
1100	7	3
1101		

INDEX = ((TM78#(8))+(TU78#(2)))

RECOVERABLE WRITE ERROR STATISTIC TABLE

"RWRIT"

	15	00	TM78#	TU78#
1111				
1112			0	0
1113				
1114			0	1
1115				
1116			0	2
1117				
1118			0	3
1119				
1120			1	0
1121				
1122			1	1
1123				
1124			1	2
1125				
1126			1	3
1127				
1128			.	.
1129			.	.
1130			.	.
1131			.	.
1132			.	.
1133				
1134			6	0
1135				
1136			6	1
1137				
1138			6	2
1139				

1140	!	!	6	3
1141	!-----!	!	7	0
1142	!	!	7	1
1143	!-----!	!	7	2
1144	!	!	7	3
1145	!-----!	!		
1146	!	!		
1147	!-----!	!		
1148	!	!		
1149	+-----+	!		
1150				
1151				
1152				
1153				

INDEX = ((TM78*(8))+(TU78*(2)))

```

1155 000000' IOMODX <TMDA >.172400,224,5,0,0,1.,0,BUFIN,512.,512.
(1) 000000' MODULE 150000,TMDA .172400,224,5,0,0,1.,0,BUFIN,512.,512.
(2) .TITLE TMDA DEC/X11 SYSTEM EXERCISER MODULE
(2) ; DDXCUM VERSION 6 23-MAY-78
(2) .LIST BIN
(2) ;*****
(2) BEGIN:
(2) 000000' 046524 040504 040 MODNAM: .ASCII /TMDA / :MODULE NAME.
(2) 000005' 000 XFLAG: .BYTE UPEN ;USED TO KEEP TRACK OF WBUFF USAGE
(2) 000006' 172400 ADDR: 172400+0 ;1ST DEVICE ADDR.
(2) 000010' 000224 VECTOR: 224+0 ;1ST DEVICE VECTOR.
(2) 000012' 240 BR1: .BYTE PRTY5+0 ;1ST BR LEVEL.
(2) 000013' 000 BR2: .BYTE PRTY0+0 ;2ND BR LEVEL.
(2) 000014' 000001 DVID1: 0+1 ;DEVICE INDICATOR 1.
(2) 000016' 000000 SR1: OPEN ;SWITCH REGISTER 1
(2) 000020' 000000 SR2: OPEN ;SWITCH REGISTER 2
(2) 000022' 000000 SR3: OPEN ;SWITCH REGISTER 3
(2) 000024' 000000 SR4: OPEN ;SWITCH REGISTER 4
(2) ;*****
(2) 000026' 150000 STAT: 150000 ;STATUS WORD.
(2) 000030' 000252' INIT: START ;MODULE START ADDR.
(2) 000032' 000252' SPOINT: MODDSP ;MODULE STACK POINTER.
(2) 000034' 000000 PASCNT: 0 ;PASS COUNTER.
(2) 000036' 000001 ICONT: 1. ;# OF ITERATIONS PER PASS=1.
(2) 000040' 000000 ICONT: 0 ;LOC TO COUNT ITERATIONS
(2) 000042' 000000 SOFCNT: 0 ;LOC TO SAVE TOTAL SOFT ERRORS
(2) 000044' 000000 HRDCNT: 0 ;LOC TO SAVE TOTAL HARD ERRORS
(2) 000046' 000000 SOFPAS: 0 ;LOC TO SAVE SOFT ERRORS PER PASS
(2) 000050' 000000 HRDPAS: 0 ;LOC TO SAVE HARD ERRORS PER PASS
(2) 000052' 000000 SYSCNT: 0 ;# OF SYS ERRORS ACCUMULATED
(2) 000054' 000000 RANNUM: 0 ;HOLDS RANDOM # WHEN RAND MACRO IS CALLED
(2) 000056' 000000 CONFIG: ;RESERVED FOR MONITOR USE
(2) 000056' 000000 RES1: 0 ;RESERVED FOR MONITOR USE
(2) 000060' 000000 RES2: '0 ;RESERVED FOR MONITOR USE
(2) 000062' 000000 SVR0: OPEN ;LOC TO SAVE R0.
(2) 000064' 000000 SVR1: OPEN ;LOC TO SAVE R1.
(2) 000066' 000000 SVR2: OPEN ;LOC TO SAVE R2.
(2) 000070' 000000 SVR3: OPEN ;LOC TO SAVE R3.
(2) 000072' 000000 SVR4: OPEN ;LOC TO SAVE R4.
(2) 000074' 000000 SVR5: OPEN ;LOC TO SAVE R5.
(2) 000076' 000000 SVR6: OPEN ;LOC TO SAVE R6.
(2) 000100' 000000 CSRA: OPEN ;ADDR OF CURRENT CSR.
(2) 000102' SBADR: ;ADDR OF GOOD DATA, OR
(2) 000102' 000000 ACSR: OPEN ;CONTENTS OF CSR.
(2) 000104' WASADR: ;ADDR OF BAD DATA, OR
(2) 000104' 000000 ASTAT: OPEN ;STATUS REG CONTENTS.
(2) 000106' ERRTP: ;TYPE OF ERROR
(2) 000106' 000000 ASB: OPEN ;EXPECTED DATA.
(2) 000110' 000000 AWAS: OPEN ;ACTUAL DATA.
(2) 000112' 001566' RSTRT: RSTRT ;RESTART ADDRESS AFTER END OF PASS
(2) 000114' 000000 WDTO: OPEN ;WORDS TO MEMORY PER ITERATION
(2) 000116' 000000 WDFR: OPEN ;WORDS FROM MEMORY PER ITERATION
(2) 000120' 000000 INTR: OPEN ;# OF INTERRUPTS PER ITERATION
(2) 000122' 000000 IDNUM: 0 ;MODULE IDENTIFICATION NUMBER=0
(2) 000124' 011174' RBUFVA: BUFIN ;READ BUFFER VIRTUAL ADDRESS
(2) 000126' 000000 RBUFPA: OPEN ;READ BUFFER PHYSICAL ADDRESS

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(2) 000130' 000000 RBUFEA: OPEN ;READ BUFFER EA BITS
(2) 000132' 001000 RBUFSZ: 512. ;SIZE OF THE READ BUFFER
(2) 000134' 000000 WBUFPA: OPEN ;WRITE BUFFER PHYSICAL ADDRESS
(2) 000136' 000000 WBUFEA: OPEN ;WRITE BUFFER EA BITS
(2) 000140' 001000 WBUFRQ: 512. ;WRITE BUFFER SIZE REQUESTED
(2) 000142' 000000 WBUFSZ: OPEN ;WRITE BUFFER SIZE AVAILABLE
(2) 000144' 000000 CDERCT: OPEN ;CDATA/DATCK ERROR COUNT
(2) 000146' 000000 CDWDCT: OPEN ;CDATA/DATCK WORD COUNT
(2) 000150' 000000 FREE: OPEN ;RESERVED FOR FUTURE USE
(2) 000040 ;MODULE STACK STARTS HERE.
(2) .REPT SPSIZ
(2) .NLIST
(2) .WORD 0
(2) .LIST
(3) .ENDP
(2) 000252' MODDSP:
(2) ;*****
1156 .REM
1157 13.0 PDL FOR THE TM78 DEC/X11 DRIVER
1158 -----
1159
1160
1161 BEGINROUTINE MODULE 1.0 (DRIVER INITIALIZATION)
1162 : CLEAR "FLAGS"
1163 : CALL MODULE 1.1 (BUILD I/O REGISTER MAP)
1164 : CALL MODULE 1.2 (RH11/70 INITIALIZE)
1165 : CALL MODULE 1.3 (CHECK TM78 DRIVE TYPE)
1166 : IF TMUNIT = 0
1167 : : THEN-CLEAR RH11/70 REGISTER CS1
1168 : : PRINT ERROR 1
1169 : : DROP THIS DEC/X11 DRIVER (DEC/X11 ENDMOD)
1170 : : ELSE-CONTINUE
1171 : ENDIF
1172 : CALL MODULE 1.4 (BUILD TU78 UNIT TABLE)
1173 : IF UNIT COUNT = 0
1174 : : THEN-CLEAR RH11/70 REGISTER CS1
1175 : : PRINT ERROR 11
1176 : : DROP THIS DEC/X11 DRIVER (DEC/X11 ENDMOD)
1177 : : ELSE-CONTINUE
1178 : ENDIF
1179 : CALL MODULE 1.5 (INITIALIZE PROGRAM DATA STRUCTURES)
1180 : IF "SR1" BIT4 AND BIT5=1 (NO DENSITY SELECTED)
1181 : : THEN-CLEAR RH11/70 REGISTER CS1
1182 : : PRINT ERROR 12
1183 : : DROP THIS DEC/X11 DRIVER (DEC/X11 ENDMOD)
1184 : : ELSE-CONTINUE
1185 : ENDIF
1186 ENDROUTINE
1187
1188
1189
1190 000252' 005067 010216 START: CLR FLAGS ;CLEAR THE FLAGS
1191 000256' 004767 000144 CALL M1.1 ;CALL MODULE 1.1 (BUILD I/O REGISTER MAP)
1192 000262' 004767 000174 CALL M1.2 ;CALL MODULE 1.2 (RH11/70 INITIALIZE)
1193 000266' 004767 000200 CALL M1.3 ;CALL MODULE 1.3 (CHECK TM78 DRIVE TYPE)
1194 000272' 005767 010200 TST TMUNIT ;IF TMUNIT=0
1195 000276' 001011 BNE IS ;ELSE-CONTINUE

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1196 000300' 005077 010570          CLR    @CS1          ;THEN-CLEAR TH/70 REGISTER CS1
1197 000304' 012767 000001 010146    MOV    #1,ERRINX    ;PRINT ERROR 1
1198 000312' 004767 006316          CALL   M4.1
1199 000316' 104410 000000'          ENDS,BEGIN
1200 000322' 004767 000366          1S:   CALL   M1.4      ;CALL MODULE 1.4 (BUILD TU78 UNIT TABLE)
1201 000326' 005767 010106          TST   UCNT          ;IF UNIT COUNT=0
1202 000332' 001011 000000'          BNE   2S            ;ELSE-CONTINUE
1203 000334' 005077 010534          CLR    @CS1          ;THEN-CLEAR RH11/70 REGISTER CS1
1204 000340' 012767 000011 010112    MOV    #11,ERRINX   ;PRINT ERROR 11
1205 000346' 004767 006262          CALL   M4.1
1206 000352' 104410 000000'          ENDS,BEGIN
1207 000356' 004767 000762          2S:   CALL   M1.5      ;CALL MODULE 1.5 (INITIALIZE DATA STRUCTURES)
1208 000362' 016701 177430          MOV    SR1,R1
1209 000366' 042701 177717          BIC   #1-BIT4-BIT5,R1 ;
1210 000372' 022701 000060          CMP   #BIT4+BIT5,R1 ;IF NO DENSITY SELECTED
1211 000376' 001011 000000'          BNE   3S            ;ELSE-CONTINUE
1212 000400' 005077 010470          CLR    @CS1          ;THEN-CLEAR RH11/70 REGISTER CS1
1213 000404' 012767 000012 010046    MOV    #12,ERRINX   ;PRINT ERROR 12
1214 000412' 004767 006216          CALL   M4.1
1215 000416' 104410 000000'          ENDS,BEGIN
1216 000422' 000167 001142          3S:   JMP    M2.0      ;DEC/X11 RESTART CODE
    
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1218          .REM
1219          BEGINROUTINE  MODULE 1.1 (BUILD I/O REGISTER MAP)
1220          : GET DEC/X11 HEADER WORD "ADDR"
1221          : CLEAR LOOP COUNT
1222          : BGND0
1223          : : STORE ("ADDR"+LOOP COUNT) AT (RHTBL+LOOP COUNT)
1224          : : ACCESS THE UNIBUS ADDRESS ("ADDR"+LOOP COUNT)
1225          : : ADD 2 TO LOOP COUNT
1226          : : DO UNTIL LOOP COUNT = 100(8)
1227          : ENDD0
1228          ENDRROUTINE
1229
1230          -
1231
1232 000426' 016701 177354          M1.1: MOV    ADDR,R1      ;GET DEC/X11 HEADER WORD "ADDR"
1233 000432' 005711 000000'          TST   (R1)          ;CATCH WRONG DEVICE ADDRESS EARLY
1234 000434' 005002 000000'          CLR   R2            ;CLEAR THE LOOP COUNT
1235 000436' 010162 011074'          1S:   MOV    R1,RHTBL(R2) ;STORE ("ADDR"+LOOP COUNT) AT (RHTBL+LOOP COUNT)
1236 000442' 062701 000002          ADD   #2,R1         ;NEXT REGISTER
1237 000446' 062702 000002          ADD   #2,R2         ;ADD 2 TO THE LOOP COUNT
1238 000452' 022702 000100          CMP   #100,R2       ;DO UNTIL THE LOOP COUNT=100
1239 000456' 001367 000000'          BNE   1S            ;
1240 000460' 000207 000000'          RTS   PC            ;ENDROUTINE
    
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1242          .REM -
1243          BEGINROUTINE MODULE 1.2 (RH11/70 INITIALIZE)
1244          : SET THE "CLR" BIT IN RH11/70 CS2 (000040)
1245          ENDRROUTINE
1246
1247          -
1248
1249          000462' 052777 000040 010414 M1.2: BIS #BIT5,@CS2 ;SET THE CLEAR BIT IN RH11/70 CS2
1250          000470' 000207          RTS PC ;ENDROUTINE
1251
1252          .REM -
1253          BEGINROUTINE MODULE 1.3 (CHECK TM78 DRIVE TYPE)
1254          : CLEAR THE TM78#
1255          : IF BIT1 OF SR1 = 1 (USER SELECTED TU78'S)
1256          : : THEN - COPY "DVID1" TO "TMUNIT"
1257          : : ELSE - SET "TMUNIT" = 377(8)
1258          : ENDF
1259          : BGNDO
1260          : : IF BIT#(TM78#) OF "TMUNIT" = 0
1261          : : : THEN-CONTINUE
1262          : : : ELSE-LOAD RH11/70 CS2 REG WITH TM78#
1263          : : : SET TRE IN RH11/70 IN REG CS1
1264          : : : CLEAR REGISTER CS1
1265          : : : IF NED BIT IN RH11/70 REGISTER CS2 = 0
1266          : : : : THEN - IF BITS 8:0 OF RH11/70 REGISTER 26 = 101(8)
1267          : : : : : THEN-CONTINUE
1268          : : : : : ELSE-CLEAR BIT#(TM78#) OF "TMUNIT"
1269          : : : : : ENDF
1270          : : : : : ELSE - CLEAR BIT#(TM78#) OF "TMUNIT"
1271          : : : : : ENDF
1272          : : INCREMENT THE TM78#
1273          : : DO UNTIL THE TM78# = 10(8)
1274          : ENDDO
1275          ENDRROUTINE
1276
1277          -
1278
1279          000472' 005067 007744 M1.3: CLR TM78 ;CLEAR THE TM78#
1280          000476' 012767 000377 007772 MOV #377,TMUNIT ;SELECT ALL TM78'S
1281          000504' 632767 000002 177304 BIT #BIT1,SR1 ;IF SR1 BIT1 = 1 (USER SELECTED TU78'S)
1282          000512' 001403 BEQ 1$ ;ELSE - CONTINUE
1283          000514' 016767 177274 007754 MOV DVID1,TMUNIT ;THEN - COPY DEC/X11 VARIABLE "DVID1" TO "TMUNIT"
1284          000522' 004767 000734 1$: CALL TMCONV
1285          000526' 036767 007676 007742 BIT TMBIT,TMUNIT ;IF BIT#(TM78#) OF "TMUNIT"=0
1286          000534' 001433 BEQ 3$ ;THEN-CONTINUE
1287          000536' 016777 007700 010340 MOV TM78,@CS2 ;SELECT UNIT #
1288          000544' 000240 NOP ;WAIT A BIT
1289          000546' 012777 040000 010320 MOV #BIT14,@CS1 ;SET BIT TRE IN RH11/70 REGISTER CS1
1290          000554' 005077 010314 CLR @CS1 ;CLEAR BIT TRE IN RH11/70 REGISTER CS1
1291          000560' 032777 010000 010316 BIT #BIT12,@CS2 ;IF BIT NED IN RH11/70 REGISTER CS2 = 0
1292          000566' 001404 BEQ 2$ ;THEN - GO TO THEN
1293          000570' 046767 007634 007700 BIC TMBIT,TMUNIT ;ELSE - CLEAR BIT#(TM78#) OF "TMUNIT"
1294          000576' 000412 BR 3$ ;CONTINUE
1295          000600' 017701 010316 2$: MOV @DT,R1 ;IF BITS 8:0 OF RH11/70 REGISTER26=101
1296          000604' 042701 177000 BIC #177000,R1
1297          000610' 022701 000101 CMP #101,R1
    
```

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1298          000614' 001412 BEQ 4$ ;THEN-CONTINUE
1299          000616' 046767 007606 007652 BIC TMBIT,TMUNIT ;ELSE-CLEAR BIT#(TM78#) OF "TMUNIT"
1300          000624' 005267 007612 3$: INC TM78 ;INCREMENT THE TM78#
1301          000630' 022767 000010 007604 CMP #10,TM78 ;DO UNTIL THE TM78#=10
1302          000636' 001331 BNE 1$
1303          000640' 000424 BR 6$ ;EXIT
1304          000642' 012704 000500 4$: MOV #500,R4 ;SET LOOP COUNTER
1305          000646' 005777 010274 5$: TST @ST78 ;TM READY ?
1306          000652' 100764 BMI 3$ ;YES , CHECK NEXT TM78
1307          000654' 104407 000000' BREAKS,BEGIN ;TEMPORARY RETURN TO MONITOR....
1308          (1) 000660' 104407 000000' BREAKS,BEGIN ;THEN CONTINUE AT NEXT INSTRUCTION.
1309          000664' 005304 DEC R4 ;COUNT DOWN
1310          000666' 001367 BNE 5$ ;BR IF NOT TIMED OUT
1311          000670' 012767 000013 007562 MOV #13,ERRINX ;SET ERROR #13
1312          000676' 004767 005732 CALL M4.1 ;GO TO ERROR TYPE ROUTINE
1313          000702' 046767 007522 007566 BIC TMBIT,TMUNIT ;DROP THIS TM78
1314          000710' 000745 BR 3$ ;CHECK NEXT TM78
1314          000712' 000207 6$: RTS PC ;ENDROUTINE
    
```

```
1316 .REM  
1317 BEGINROUTINE MODULE 1.4 (BUILD TU78 UNIT TABLE)  
1318 : SET UP INTERRUPT VECTOR (MODULE 4.0) AND BR LEVEL  
1319 : IF SR1 BIT1=1 (USER SELECTED TU78'S)  
1320 : : THEN-COPY USER TABLE "UTBL" TO SCRATCH UNIT TABLE "SUTBL"  
1321 : : CLEAR TM78# "TM78"  
1322 : : CLEAR UNIT COUNT "UCNT"  
1323 : : BGNDO  
1324 : : : IF BIT*(TM78#) OF "TMUNIT" = 0  
1325 : : : : THEN-CONTINUE  
1326 : : : : ELSE-LOAD RH11/70 CS2 REGISTER WITH TM78#  
1327 : : : : CLEAR THE TU78#  
1328 : : : : BGNDO  
1329 : : : : : IF SCRATCH UNIT TABLE (TM78#.TU78#) = 1  
1330 : : : : : : THEN-CALL MODULE 3.6 (SENSE)  
1331 : : : : : : IF TERM. INT. CODE=DONE  
1332 : : : : : : : THEN-CONTINUE  
1333 : : : : : : : ELSE-CLEAR THE TU78 STATUS  
1334 : : : : : : : ENDIF  
1335 : : : : : : IF TU78 IS RDY.PRES.ONL.AVAIL AND NOT FPT  
1336 : : : : : : : THEN-INCREMENT UNIT COUNT  
1337 : : : : : : : ELSE-DROP TU78 - CLEAR BIT (SCRATCH UNIT TABLE(TM78#  
1338 : : : : : : : ENDIF  
1339 : : : : : : : ELSE-CONTINUE  
1340 : : : : : : : ENDIF  
1341 : : : : : : : INCREMENT THE TU78#  
1342 : : : : : : : DO UNTIL THE TU78# = 4  
1343 : : : : : : : ENDDO  
1344 : : : : : : : ENDIF  
1345 : : : : : : : INCREMENT THE TM78#  
1346 : : : : : : : DO UNTIL THE TM78# = 10(8)  
1347 : : : : : : : ENDDO  
1348 : : : : : : : ELSE-CLEAR THE TM78#  
1349 : : : : : : : CLEAR THE UNIT COUNT  
1350 : : : : : : : BGNDO  
1351 : : : : : : : IF BIT*(TM78#) OF TMUNIT = 0  
1352 : : : : : : : : THEN - CLEAR (SCRATCH UNIT TABLE(TM78*(2)))  
1353 : : : : : : : : CLEAR (UNIT TABLE(TM78*(2)))  
1354 : : : : : : : : ELSE - LOAD THE RH11/70 CS2 REGISTER WITH TM78#  
1355 : : : : : : : : CLEAR THE TU78#  
1356 : : : : : : : : BGNDO  
1357 : : : : : : : : : CALL MODULE 3.6 (SENSE)  
1358 : : : : : : : : : IF TERM. INT. CODE=DONE  
1359 : : : : : : : : : THEN-CONTINUE  
1360 : : : : : : : : : ELSE-CLEAR THE TU78 STATUS  
1361 : : : : : : : : : ENDIF  
1362 : : : : : : : : : IF TU78 IS RDY.PRES.ONL.AVAIL AND NOT FPT  
1363 : : : : : : : : : : THEN-SET BIT (SCRATCH UNIT TABLE(TM78#.TU78#))  
1364 : : : : : : : : : : SET BIT (UNIT TABLE(TM78#. TU78#))  
1365 : : : : : : : : : : INCREMENT UNIT COUNT  
1366 : : : : : : : : : : ELSE-CONTINUE  
1367 : : : : : : : : : : ENDIF  
1368 : : : : : : : : : : INCREMENT THE TU78#  
1369 : : : : : : : : : : DO UNTIL THE TU78# = 4  
1370 : : : : : : : : : : ENDDO  
1371 : : : : : : : : : : ENDIF
```

```
1372 : : : INCREMENT THE TM78#  
1373 : : : DO UNTIL THE TM78# = 10(8)  
1374 : : : ENDDO  
1375 : : : ENDIF  
1376 ENDROUTINE  
1377 -  
1378  
1379 000714' 016700 177070 M1.4: MOV VECTOR,R0 ;SET UP INTERRUPT VECTOR  
1380 000720' 012720 006314' MOV #M4.0,(R0)+  
1381 000724' 116720 177062 MOVBR BR1,(R0)+ ;SETUP BR LEVEL  
1382 000730' 105010 CLRBR (R0)  
1383 000732' 032767 000002 177056 BIT #BIT1,SR1 ;IF SR1 BIT1=1  
1384 000740' 001504 BEQ M1.4X ;ELSE-GO TO ELSE CODE  
1385 000742' 005001 CLR R1 ;SET LOOP COUNT  
1386 000744' 016161 010514' 010554' 1s: MOV UTBL(R1),SUTBL(R1) ;COPY UNIT TABLE TO SCRATCH UNIT TABLE  
1387 000752' 062701 000002 ADD #2,R1  
1388 000756' 022701 000020 CMP #20,R1  
1389 000762' 001370 BNE 1s  
1390 000764' 005067 007452 CLR TM78 ;CLEAR THE TM78#  
1391 000770' 005067 007444 CLR UCNT ;CLEAR THE UNIT COUNT  
1392 000774' 004767 000462 2s: CALL TMCONV  
1393 001000' 036767 007424 007470 BIT TMBIT,TMUNIT ;IF BIT*(TM78#) OF "TMUNIT"=0  
1394 001006' 001452 BEQ 7s ;THEN-CONTINUE  
1395 001010' 016777 007426 010066 MOV TM78,#CS2 ;ELSE-LOAD RH11/70 CS2 REGISTER WITH TM78#  
1396 001016' 005067 007422 CLR TU78 ;CLEAR THE TU78#  
1397 001022' 004767 000476 3s: CALL TUCONV  
1398 001026' 016701 007400 MOV TMBIT2,R1  
1399 001032' 036761 007376 010554' BIT TUBIT,SUTBL(R1) ;IF SCRATCH UNIT TABLE (TM78#.TU78#)=1  
1400 001040' 001427 BEQ 6s ;ELSE-CONTINUE  
1401 001042' 004767 0005164 CALL M3.6 ;CALL MODULE 3.6 (SENSE)  
1402 001046' 022767 000001 007374 CMP #DONE,TINTCD ;IF SENSE TERMINATION INTERRUPT CODE=DONE  
1403 001054' 001402 BEQ 4s ;THEN-CONTINUE  
1404 001056' 005067 007374 CLR SENSE ;ELSE-CLEAR SENSE  
1405 001062' 016701 007370 4s: MOV SENSE,R1  
1406 001066' 042701 017177 BIC #017177,R1  
1407 001072' 022701 160200 CMP #160200,R1 ;IF TU78 IS RDY.PRES.ONL.AVAIL AND NOT FPT  
1408 001076' 001406 BEQ 5s ;THEN-GO DO THEN  
1409 001100' 016701 007326 MOV TMBIT2,R1 ;ELSE-DROP TU78  
1410 001104' 046761 007324 010554' BIC TUBIT,SUTBL(R1) ;CLEAR BIT (SCRATCH UNIT TABLE(TM78#.TU78#))  
1411 001112' 000402 BR 6s  
1412 001114' 005267 007320 5s: INC UCNT ;INCREMENT THE UNIT COUNT  
1413 001120' 005267 007320 6s: INC TU78 ;INCREMENT THE TU78#  
1414 001124' 022767 000004 007312 CMP #4,TU78 ;DO UNTIL THE TU78#=4  
1415 001132' 001333 BNE 3s  
1416 001134' 005267 007302 7s: INC TM78 ;INCREMENT THE TM78#  
1417 001140' 022767 000010 007274 CMP #10,TM78 ;DO UNTIL THE TM78#=10  
1418 001146' 001312 BNE 2s  
1419 001150' 000474 BR M1.40T  
1420 001152' 005067 007264 M1.4X: CLR TM78 ;CLEAR THE TM78#  
1421 001156' 005067 007256 CLR UCNT ;CLEAR THE UNIT COUNT  
1422 001162' 004767 000274 1s: CALL TMCONV  
1423 001166' 016701 007240 MOV TMBIT2,R1  
1424 001172' 005061 010554' CLR SUTBL(R1) ;CLEAR THE SCRATCH UNIT TABLE FOR THIS TM78  
1425 001176' 005061 010514' CLR UTBL(R1) ;CLEAR THE UNIT TABLE FOR THIS TM78  
1426 001202' 036767 007222 007266 BIT TMBIT,TMUNIT ;IF BIT*(TM78#) OF TMUNIT = 0  
1427 001210' 001446 BEQ 4s ;THEN - CONTINUE
```



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1428 001212' 016777 007224 007664      MOV    TM78,PCS2      ;ELSE - LOAD THE RH11/70 CS2 REGISTER WITH TM78#
1429 001220' 005067 007220              CLR    TU78          ;CLEAR THE TU78#
1430 001224' 004767 000274              2s:   CALL  TUCONV          ;
1431 001230' 004767 004776              CALL  M3.6          ;CALL MODULE 3.6 (SENSE)
1432 001234' 022767 000001 007206      CMP    #DONE,TINTCD ;IF SENSE TERMINATION INTERRUPT CODE=DONE
1433 001242' 001402              BEQ    5s           ;THEN-CONTINUE
1434 001244' 005067 007206              CLR    SENSE        ;ELSE-CLEAR SENSE
1435 001250' 016701 007202              5s:   MOV    SENSE,R1
1436 001254' 042701 017177              BIC    #017177,R1
1437 001260' 022701 160200              CMP    #160200,R1   ;IF TU78 IS RDY.PRES.ONL.AVAIL AND NOT FPT
1438 001264' 001012              BNE    3s           ;ELSE-CONTINUE
1439 001266' 016701 007140              MOV    TMBIT2,R1
1440 001272' 056761 007136 010554'     BIS    TUBIT,SUTBL(R1) ;SET BIT SCRATCH UNIT TABLE(TM78#,TU78#)
1441 001300' 056761 007130 010514'     BIS    TUBIT,UTBL(R1) ;SET BIT UNIT TABLE(TM78#,TU78#)
1442 001306' 005267 007126              INC    UCNT          ;INCREMENT THE UNIT COUNT
1443 001312' 005267 007126              3s:   INC    TU78        ;INCREMENT THE TU78#
1444 001316' 022767 000004 007120      CMP    #4,TU78      ;DO UNTIL THE TU78#=4
1445 001324' 001337              BNE    2s           ;
1446 001326' 005267 007110              4s:   INC    TM78        ;INCREMENT THE TM78#
1447 001332' 022767 000010 007102      CMP    #10,TM78     ;DO UNTIL THE TM78#=10
1448 001340' 001310              BNE    1s           ;
1449 001342' 000207              M1.40T: RTS        PC      ;RETURN TO USER.
  
```

```

1451      .REM
1452      BEGINROUTINE  MODULE 1.5 (INITIALIZE PROGRAM DATA STRUCTURES)
1453      : CLEAR THE RECOVERABLE WRITE STATISTIC TABLE
1454      : CLEAR THE RECOVERABLE READ STATISTIC TABLE
1455      : CLEAR THE UNRECOVERABLE READ STATISTIC TABLE
1456      : CLEAR THE EOT TABLE
1457      : IF SR1 BIT4 = 0 (TEST GCR)
1458      : : THEN = SET BIT0 OF FLAGS (GCR)
1459      : : ELSE = CLEAR BIT0 OF FLAGS (PE)
1460      : ENDF
1461      : SET THE BOT PASS FLAG
1462      : CLEAR RECORD NUMBER
1463      : SET FILE NUMBER=1
1464      ENDRROUTINE
1465
1466      -
1467
1468 001344' 005001      M1.5: CLR    R1          ;CLEAR THE LOOP COUNT
1469 001346' 005061 010774' 1s:   CLR    RWRIT(R1)      ;CLEAR RECOVERABLE WRITE STATISTIC TABLE
1470 001352' 005061 010674'     CLR    RREAD(R1)       ;CLEAR RECOVERABLE READ STATISTIC TABLE
1471 001356' 005061 010574'     CLR    URREAD(R1)      ;CLEAR UNRECOVERABLE READ STATISTIC TABLE
1472 001362' 062701 000002     ADD    #2,R1           ;INCREMENT THE LOOP COUNT
1473 001366' 022701 000100     CMP    #64,,R1         ;DO UNTIL THE LOOP COUNT=64.
1474 001372' 001365     BNE    1s
1475 001374' 005001     CLR    R1             ;CLEAR THE LOOP COUNT
1476 001376' 005061 010534' 2s:   CLR    EOTTHL(R1)     ;CLEAR THE EOT TABLE
1477 001402' 062701 000002     ADD    #2,R1           ;INCREMENT THE LOOP COUNT
1478 001406' 022701 000020     CMP    #16,,R1        ;DO UNTIL THE LOOP COUNT=16
1479 001412' 001371     BNE    2s
1480 001414' 052767 000001 007052     BIS    #BIT0,FLAGS     ;SET THE DENSITY TO GCR
1481 001422' 032767 000020 176366     BIT    #BIT4,SR1       ;IF SR1 BIT4 = 0 (TEST GCR)
1482 001430' 001410     BEQ    3s             ;THEN = SET THE DENSITY TO GCR
1483 001432' 042767 000001 007034     BIC    #BIT0,FLAGS     ;ELSE = SET THE DENSITY TO PE
1484 001440' 005067 007040     CLR    RECRD          ;CLEAR RECORD NUMBER
1485 001444' 012767 000001 007030     MOV    #1,FILE         ;SET FILE NUMBER=1
1486 001452' 052767 000002 007014 3s:   BIS    #BIT1,FLAGS     ;SET THE BOT PASS FLAG
1487 001460' 000207     RTS                    ;ENDROUTINE
1488
1489 001462' 016701 006754      TMCONV: MOV   TM78,R1
1490 001466' 012702 000001     MOV    #1,R2
1491 001472' 005701              1s:   TST    R1
1492 001474' 001403              BEQ    2s
1493 001476' 005301              DEC    R1
1494 001500' 006302              ASL    R2
1495 001502' 000773              BR    1s
1496 001504' 010267 006720 2s:   MOV    R2,TMBIT
1497 001510' 016701 006726     MOV    TM78,R1
1498 001514' 006301              ASL    R1
1499 001516' 010167 006710     MOV    R1,TMBIT2
1500 001522' 000207     RTS                    PC
  
```

```
1502 001524' 016701 006714 TUCONV: MOV TU78,R1
1503 001530' 012702 000001 MOV #1,R2
1504 001534' 005701 1s: TST R1
1505 001536' 001403 BEQ 2s
1506 001540' 005301 DEC R1
1507 001542' 006302 ASL R2
1508 001544' 000773 BR 1s
1509 001546' 010267 006662 2s: MOV R2,TUBIT
1510 001552' 016701 006666 MOV TU78,R1
1511 001556' 006301 ASL R1
1512 001560' 010167 006652 MOV R1,TUBIT2
1513 001564' 000207 RTS PC
1514 .REM -
1515 BEGINROUTINE MODULE 2.0 (DRIVER SEQUENCE CONTROL)
1516 : BGND0
1517 : : ENABLE RH11/70 INTERRUPTS
1518 : : CLEAR INTERRUPT EXPECTED FLAG
1519 : : CLEAR THE TM78#
1520 : : INCREMENT RECORD NUMBER
1521 : : BGND0
1522 : : : GET WRITE BUFFER (DEC/X11 GWBUFF)
1523 : : : CLEAR THE TU78#
1524 : : : BGND0
1525 : : : : IF THE TU78 SELECTED BIT (SCRATCH UNIT TABLE(TM78#,TU78#)) = 0
1526 : : : : THEN-CONTINUE
1527 : : : : ELSE-IF BOT PASS FLAG NOT=0
1528 : : : : : THEN-CALL MODULE 2.7 (REWIND CONTROL)
1529 : : : : : ELSE-IF EOT FLAG=1(EOTTBL(TM78#,TU78#))
1530 : : : : : : THEN-CONTINUE
1531 : : : : : : ELSE-CALL MODULE 2.3 (WRITE CONTROL)
1532 : : : : : : IF TU78 SELECTED BIT (SCRATCH UNIT TABLE(TM78#,TU78#)
1533 : : : : : : : THEN-CALL MODULE 2.5 (READ REVERSE CONTROL)
1534 : : : : : : : ELSE-CONTINUE
1535 : : : : : : : ENDF
1536 : : : : : : IF TU78 SELECTED BIT (SCRATCH UNIT TABLE(TM78#,TU78#)
1537 : : : : : : : THEN-CALL MODULE 2.8 (COMPARE DATA)
1538 : : : : : : : CALL MODULE 2.4 (READ FORWARD CONTROL)
1539 : : : : : : : ELSE-CONTINUE
1540 : : : : : : : ENDF
1541 : : : : : : IF TU78 SELECTED BIT (SCRATCH UNIT TABLE(TM78#,TU78#)
1542 : : : : : : : THEN-CALL MODULE 2.8 (COMPARE DATA)
1543 : : : : : : : ELSE-CONTINUE
1544 : : : : : : : ENDF
1545 : : : : : : ENDF
1546 : : : : : ENDF
1547 : : : : ENDF
1548 : : : : INCREMENT THE TU78#
1549 : : : : DO UNTIL THE TU78# = 4
1550 : : : ENDDO
1551 : : : INCREMENT THE TM78# BY 1
1552 : : : DO UNTIL THE TM78# = 10(8)
1553 : : ENDDO
1554 : : CALL MODULE 2.10 (UNIT CONTROL)
1555 : : IF UNIT COUNT=0
1556 : : : THEN-CLEAR RH11/70 REGISTER CS1
1557 : : : PRINT ERROR 11
```

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1558 : : : DROP THIS DEC/X11 DRIVER (ENDMOD)
1559 : : : ELSE-CONTINUE
1560 : : ENDF
1561 : : IF BOT PASS FLAG=1
1562 : : : THEN-BGND0
1563 : : : : CALL MODULE 2.10 (UNIT CONTROL)
1564 : : : : CALL MODULE 2.2 (BOT CONTROL)
1565 : : : : DO UNTIL UNIT COUNT=0 OR UNIT COUNT=BOT COUNT
1566 : : : ENDDO
1567 : : : CLEAR THE EOT COUNT
1568 : : : CLEAR THE EOT TABLE
1569 : : : SET FILE NUMBER=1
1570 : : : CLEAR RECORD NUMBER
1571 : : : ELSE-CONTINUE
1572 : : ENDF
1573 : : CLEAR THE BOT PASS FLAG
1574 : : IF UNIT COUNT=EOT COUNT
1575 : : : THEN-SET BOT PASS FLAG
1576 : : : : CALL MODULE 2.9 (STATISTICS)
1577 : : : : CALL MODULE 2.1 (DENSITY CONTROL)
1578 : : : ELSE-CONTINUE
1579 : : ENDF
1580 : : IF RECORD NUMBER=500. OR BOT PASS FLAG=1
1581 : : : THEN-CALL MODULE 2.6 (TAPE MARK CONTROL)
1582 : : : INCREMENT FILE NUMBER
1583 : : : ELSE-CONTINUE
1584 : : ENDF
1585 : : DO UNTIL RECORD NUMBER=500. OR BOT PASS FLAG=1
1586 : : ENDDO
1587 : : CLEAR THE RECORD NUMBER
1588 : : END THE PASS (DEC/X11 ENDIT)
1589 ENDRoutine
1590
1591 -
1592 001566' 000240 RESTRT: NOP ;DEC/X11 RESTART TAG
1593 001570' 042767 M2.0: BIC #BITS,FLAGS ;CLEAR INTERRUPT EXPECTED FLAG
1594 001576' 012777 000100 006676 MOV #BIT6,RC51 ;ENABLE RH11/70 INTERRUPTS
1595 001604' 005067 006632 CLR TM78 ;CLEAR THE TM78#
1596 001610' 104414 000000' GWBUFFS, BEGIN ;GET WRITE BUFFER INFORMATION
1597 001614' 005267 006664 INC RECORD ;INCREMENT THE RECORD NUMBER
1598 001620' 005067 006620 1s: CLR TU78 ;CLEAR TU78#
1599 001624' 004767 177632 2s: CALL TUCONV
1600 001630' 004767 177670 CALL TUCONV
1601 001634' 016701 006572 MOV TUBIT2,R1
1602 001640' 036761 006570 010554' BIT TUBIT,SUTBL(R1) ;IF TU78 SELECTED BIT=0
1603 001646' 001451 BEQ 6s ;THEN-CONTINUE
1604 001650' 032767 000002 006616 BIT #BIT1,FLAGS ;ELSE-IF BOT PASS FLAG NOT=0
1605 001656' 001403 BEQ 3s
1606 001660' 004767 002450 CALL M2.7 ;THEN-CALL MODULE 2.7 (REWIND CONTROL)
1607 001664' 000442 BR 6s ;
1608 001666' 016701 006540 3s: MOV TMBIT2,R1 ;ELSE-IF EOT FLAG BIT=1
1609 001672' 036761 006536 010534' BIT TUBIT,EOTTBL(R1)
1610 001700' 001034 BNE 6s ;THEN-CONTINUE
1611 001702' 004767 000666 CALL M2.3 ;ELSE-CALL MODULE 2.3 (WRITE CONTROL)
1612 001706' 016701 006520 MOV TMBIT2,R1
1613 001712' 036761 006516 010554' BIT TUBIT,SUTBL(R1) ;IF TU SELECTED BIT=1
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1614 001720' 001402          BEQ      4S          ;ELSE=CONTINUE
1615 001722' 004767 001620    CALL     M2.5       ;THEN=CALL MODULE 2.5 (READ REVERSE CONTROL)
1616 001726' 016701 006500    4S:    MOV      TMBIT2,R1
1617 001732' 036761 006476    010554' BIT      TUBIT,SUTBL(R1) ;IF TU SELECTED BIT=1
1618 001740' 001404          BEQ      5S          ;ELSE=CONTINUE
1619 001742' 004767 002520    CALL     M2.8       ;THEN=CALL MODULE 2.8 (COMPARE DATA)
1620 001746' 004767 001224    CALL     M2.4       ;CALL MODULE 2.4 (READ FORWARD CONTROL)
1621 001752' 016701 006454    5S:    MOV      TMBIT2,R1
1622 001756' 036761 006452    010554' BIT      TUBIT,SUTBL(R1) ;IF TU SELECTED BIT=1
1623 001764' 001402          BEQ      6S          ;ELSE=CONTINUE
1624 001766' 004767 002474    CALL     M2.8       ;THEN=CALL MODULE 2.8 (COMPARE DATA)
1625 001772' 005267 006446    6S:    INC      TU78      ;INCREMENT THE TU78#
1626 001776' 022767 000004    006440  CMP      #4,TU78    ;DO UNTIL THE TU78#=4
1627 002004' 001307          BNE      2S
1628
1629 002006' 005267 006430          INC      TM78      ;INCREMENT THE TM78#
1630 002012' 022767 000010    006422  CMP      #10,TM78  ;DO UNTIL THE TM78#=10
1631 002020' 001277          BNE      1S
1632
1633 002022' 004767 003114          CALL     M2.10      ;CALL MODULE 2.10 (UNIT CONTROL)
1634 002026' 005767 006406          TST     UCNT       ;IF UNIT COUNT=0
1635 002032' 001011          BNE      7S          ;ELSE=CONTINUE
1636 002034' 012767 000011    006416  MOV      #11,ERRINX ;PRINT ERROR 11
1637 002042' 004767 004566          CALL     M4.1
1638 002046' 005077 007022          CLR     @CS1       ;THEN-CLEAR RH11/70 REGISTER CS1
1639 002052' 104410 000000'          ENDS,BEGIN
1640
1641 002056' 032767 000002    006410  7S:    BIT      #BIT1,FLAGS ;IF BOT PASS FLAG=1
1642 002064' 001432          BEQ     11S         ;ELSE=CONTINUE
1643 002066' 004767 003050    8S:    CALL     M2.10      ;CALL MODULE 2.10 (UNIT CONTROL)
1644 002072' 004767 000244          CALL     M2.2       ;CALL MODULE 2.2 (BOT CONTROL)
1645 002076' 005767 006336          TST     UCNT       ;DO UNTIL THE UNIT
1646 002102' 001404          BEQ     9S          ;COUNT=0 OR THE UNIT
1647 002104' 026767 006344    006326  CMP     BOTCNT,UCNT ;COUNT=THE BOT COUNT
1648 002112' 001365          BNE     8S
1649 002114' 005001          CLR     R1
1650 002116' 005067 006324          CLR     EOTCNT     ;CLEAR THE EOT COUNT
1651 002122' 005061 010534'    10S:   CLR     EOTBL(R1)  ;CLEAR THE EOT TABLE
1652 002126' 062701 000002          ADD     #2,R1
1653 002132' 022701 000020          CMP     #20,R1
1654 002136' 001371          BNE     10S
1655 002140' 012767 000001    006334  MOV     #1,FILE     ;SET FILE NUMBER=1
1656 002146' 005067 006332          CLR     RECORD     ;CLEAR RECORD NUMBER
1657
1658 002152' 042767 000002    006314  11S:   BIC     #BIT1,FLAGS ;CLEAR THE BOT PASS FLAG
1659 002160' 026767 006254    006260  CMP     UCNT,EOTCNT ;IF UNIT COUNT=EOT COUNT
1660 002166' 001007          BNE     12S        ;ELSE=CONTINUE
1661 002170' 052767 000002    006276  BIS     #BIT1,FLAGS ;SET THE BOT PASS FLAG
1662 002176' 004767 002316          CALL    M2.9       ;THEN=CALL MODULE 2.9 (STATISTICS)
1663 002202' 004767 000064          CALL    M2.1       ;CALL MODULE 2.1 (DENSITY CONTROL)
1664
1665 002206' 022767 000764    006270  12S:   CMP     #500,.RECORD ;IF RECORD NUMBER=500.
1666 002214' 001404          BEQ     13S
1667 002216' 032767 000002    006250  BIT     #BIT1,FLAGS ;OR BOT PASS FLAG=1
1668 002224' 001404          BEQ     14S        ;ELSE=CONTINUE
1669 002226' 004767 001660    13S:   CALL    M2.6       ;THEN=CALL MODULE 2.6 (TAPE MARK CONTROL)
    
```

```

1670 002232' 005267 006244          INC     FILE        ;INCREMENT THE FILE NUMBER
1671 002236' 022767 000764    006240  14S:   CMP     #500,.RECORD ;DO UNTIL RECORD NUMBER=500.
1672 002244' 001406          BEQ     15S
1673 002246' 032767 000002    006220  BIT     #BIT1,FLAGS ;OR BOT PASS FLAG=1
1674 002254' 001002          BNE     15S
1675 002256' 000167 177306          JMP     M2.0
1676 002262' 005067 006216          15S:   CLR     RECORD
1677 002266' 104413 000000'          ENDS,BEGIN
(1)
1678          ;SIGNAL END OF ITERATION.
1679          ;MONITOR SHALL TEST END OF PASS
1680          .REM
1681          BEGINROUTINE MODULE 2.1 (DENSITY CONTROL)
1682          : IF DENSITY FLAG=0
1683          : : THEN-IF BIT4 OF SR1 (TEST GCR SWITCH)=0
1684          : : : THEN-SET THE DENSITY FLAG (GCR)
1685          : : : ELSE=CONTINUE
1686          : : : ENDF
1687          : : ELSE-IF BIT5 OF SR1 (TEST PE SWITCH)=0
1688          : : : THEN-CLEAR THE DENSITY FLAG (PE)
1689          : : : ELSE=CONTINUE
1690          : : : ENDF
1691          : ENDF
1692          ENDRROUTINE
1693
1694 002272' 032767 000001    006174  M2.1:  BIT     #BIT0,FLAGS ;IF DENSITY FLAG=0
1695 002300' 001010          BNE     1S          ;ELSE-GO TO ELSE
1696 002302' 032767 000020    175506  BIT     #BIT4,SR1   ;THEN-IF BIT4 OF SR1=0
1697 002310' 001013          BNE     2S          ;ELSE=CONTINUE
1698 002312' 052767 000001    006154  BIS     #BIT0,FLAGS ;THEN-SET THE DENSITY FLAG
1699 002320' 000407          BR      2S
1700 002322' 032767 000040    175466  1S:    BIT     #BIT5,SR1   ;IF BIT5 OF SR1=0
1701 002330' 001003          BNE     2S          ;ELSE=CONTINUE
1702 002332' 042767 000001    006134  BIC     #BIT0,FLAGS ;THEN-CLEAR THE DENSITY FLAG
1703 002340' 000207          RTS     PC          ;RETURN
    
```

```

1705 .REM -
1706 BEGINROUTINE MODULE 2.2 (BOT CONTROL)
1707 : CLEAR THE BOT COUNT
1708 : CLEAR THE TM78#
1709 : BGND0
1710 : : CLEAR THE TU78#
1711 : : RGND0
1712 : : IF BIT SET (SCRATCH UNIT TABLE(TM78#, TU78#))
1713 : : : THEN-CALL MODULE 3.6 (SENSE)
1714 : : : SELECT TERMINATION INTERRUPT CODE
1715 : : : : DONE-IF BIT10 OF SENSE=1 (TU AT BOT)
1716 : : : : : THEN-INCREMENT BOT COUNT
1717 : : : : : ELSE-CONTINUE
1718 : : : : : ENDF
1719 : : : : : IF "TRE" BIT IN RH11/70 CS1 REGISTER=1
1720 : : : : : THEN-PRINT ERROR 10
1721 : : : : : ELSE-CONTINUE
1722 : : : : : ENDF
1723 : : : : : TM FAULT B-PRINT ERROR 7
1724 : : : : : DROP TM78-CLEAR ALL BITS (SCRATCH UNIT TABLE (TM78#))
1725 : : : : : ALL OTHERS-PRINT ERROR 2
1726 : : : : : DROP TU78-CLEAR BIT (SCRATCH UNIT TABLE(TM78#, TU78#))
1727 : : : : : ENDSELECT
1728 : : : : : ELSE-CONTINUE
1729 : : : : : ENDF
1730 : : : INCREMENT THE TU78#
1731 : : : DO UNTIL THE TU78#=4
1732 : : : ENDDO
1733 : : INCREMENT THE TM78#
1734 : : DO UNTIL THE TM78#=10(8)
1735 : : ENDDO
1736 ENDROUTINE
1737
1738 -
1739
1740 002342' 005067 006106 M2.2: CLR BUTCNT ;CLEAR BOT COUNT
1741 002346' 005067 006070 CLR TM78 ;CLEAR THE TM78#
1742 002352' 005067 006066 1s: CLR TU78 ;CLEAR THE TU78#
1743 002356' 004767 177100 2s: CALL TUCONV ;
1744 002362' 004767 177136 CALL TUCONV
1745 002366' 016701 006040 MOV TMBIT2,R1
1746 002372' 036761 006036 010554' BIT TUBIT,SUTBL(R1) ;IF BIT SET (SCRATCH UNIT TABLE(TM78#,TU78#))
1747 002400' 001460 BEQ 6S ;ELSE-CONTINUE
1748 002402' 004767 003624 CALL M3.6 ;THEN-CALL MODULE 3.6 (ISSUE SENSE)
1749 002406' 016701 006020 MOV TMBIT2,R1
1750 002412' 022767 000001 006030 CMP #DONE,TINTCD ;DONE?
1751 002420' 001020 BNE 4S ;NO-CONTINUE
1752 002422' 032767 002000 006026 BIT #BIT10,SENSE ;YES-IF TU AT BOT?
1753 002430' 001402 BEQ 3S ;ELSE-CONTINUE
1754 002432' 005267 006016 INC BUTCNT ;THEN-INCREMENT BOT COUNT
1755 002436' 032777 040000 006430 3s: BIT #BIT14,#CS1 ;IF 'TRE' BIT IN RH11/70 CS1=1
1756 002444' 001436 BEQ 6S ;ELSE-CONTINUE
1757 ;THEN-
1758 002446' 012767 000010 006004 MOV #10,ERRINX
1759 002454' 004767 004154 CALL M4.1
1760 002460' 000430 BR 6S
    
```

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1761 002462' 022767 000032 005760 4s: CMP #TMFB,TINTCD ;TM FAULT B?
1762 002470' 001012 BNE 5S ;NO-CONTINUE
1763 002472' 012767 000007 005760 MOV #7,ERRINX
1764 002500' 004767 004130 CALL M4.1
1765 002504' 016701 005722 MOV TMBIT2,R1 ;DROP TM78
1766 002510' 005061 010554' CLR SUTBL(R1)
1767 002514' 000412 BR 6S
1768 002516' 012767 000002 005734 5s: MOV #2,ERRINX
1769 002524' 004767 004104 CALL M4.1
1770 002530' 016701 005676 MOV TMBIT2,R1 ;DROP TU78
1771 002534' 046761 005674 010554' BIC TUBIT,SUTBL(R1)
1772 002542' 005267 005676 6s: INC TU78 ;INCREMENT THE TU78#
1773 002546' 022767 000004 005670 CMP #4,TU78 ;DO UNTIL THE TU78#=4
1774 002554' 001300 BNE 2S
1775
1776 002556' 005267 005660 INC TM78 ;INCREMENT THE TM78#
1777 002562' 022767 000010 005652 CMP #10,TM78 ;DO UNTIL THE TM78#=10
1778 002570' 001270 HNE 1S
1779 002572' 000207 RTS PC ;RETURN
    
```

```

1781 .REM
1782 BEGINROUTINE MODULE 2.3 (WRITE CONTROL)
1783 : CLEAR RETRY IN PROGRESS FLAG
1784 : CLEAR THE EXIT FLAG
1785 : RGND0
1786 : : CALL MODULE 3.1 (WRITE)
1787 : : SELECT TERMINATION INTERRUPT CODE
1788 : : : DONE-SET THE EXIT FLAG
1789 : : : IF RETRY IN PROGRESS FLAG=1
1790 : : : : THEN-UPDATE RECOVERABLE WRITE ERROR STATISTICS
1791 : : : : PRINT ERROR 3
1792 : : : : ELSE-CONTINUE
1793 : : : : ENDF
1794 : : : : IF "TRE" BIT IN RH11/70 CS1 REGISTER=1
1795 : : : : : THEN-PRINT ERROR 10
1796 : : : : : ELSE-CONTINUE
1797 : : : : ENDF
1798 : : : EOT -SET THE EOT FLAG (EOT TABLE (TM78#,TU78#))
1799 : : : SET THE EXIT FLAG
1800 : : : IF RETRY IN PROGRESS FLAG NOT=0
1801 : : : : THEN-UPDATE RECOVERABLE WRITE ERROR STATISTICS
1802 : : : : PRINT ERROR 3
1803 : : : : ELSE-CONTINUE
1804 : : : : ENDF
1805 : : : BAD TAPE-SET THE EXIT FLAG
1806 : : : : PRINT ERROR 4
1807 : : : : DROP THE TU78-CLEAR BIT (SCRATCH UNIT TABLE(TM78#, TU78#))
1808 : : : : RETRY-SET RETRY IN PROGRESS FLAG
1809 : : : : TM FAULT B-PRINT ERROR 7
1810 : : : : : DROP TM78-CLEAR ALL BITS (SCRATCH UNIT TABLE (TM78#))
1811 : : : : : SET THE EXIT FLAG
1812 : : : : : ALL OTHER-PRINT ERROR 2
1813 : : : : : DROP TU78-CLEAR BIT (SCRATCH UNIT TABLE(TM78#, TU78#))
1814 : : : : : SET THE EXIT FLAG
1815 : : : : ENDF
1816 : : : DO UNTIL THE EXIT FLAG NOT=0
1817 : : : ENDDO
1818 ENDRROUTINE
1819
1820
1821
1822 002574' 042767 000004 005672 M2.3: BIC #BIT2,FLAGS ;CLEAR RETRY IN PROGRESS FLAG
1823 002602' 042767 000020 005664 BIC #BIT4,FLAGS ;CLEAR EXIT FLAG
1824 002610' 004767 002444 18: CALL M3.1 ;CALL MODULE 3.1 (WRITE)
1825 002614' 016701 005612 MOV TMBIT2,R1
1826 002620' 022767 000001 005622 CMP #DONE,TINTCD ;DONE?
1827 002626' 001034 BNE 48 ;NO-CONTINUE
1828 002630' 052767 000020 005636 28: BIS #BIT4,FLAGS ;YES-SET THE EXIT FLAG
1829 002636' 032767 000004 005630 BIT #BIT2,FLAGS ;IF RETRY IN PROGRESS FLAG=1
1830 002644' 001413 BEQ 38 ;ELSE-CONTINUE
1831 002646' 016701 005560 MOV TMBIT2,R1
1832 002652' 066701 005560 ADD TUBIT2,R1 ;ADD IN THE TU DISPLACEMENT
1833 002656' 005261 010774' INC RWRIT(R1) ;THEN-UPDATE RECOVERABLE WRITE ERROR STATS.
1834 002662' 012767 000003 005570 MOV #3,ERRINX
1835 002670' 004767 003740 CALL M4.1
1836

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1837 002674' 032777 040000 006172 38: HIT #BIT14,#CS1 ;IF 'TRE' HIT IN RH11/70 CS1=1
1838 002702' 001530 BEQ 98 ;ELSE-CONTINUE
1839 002704' 012767 000010 005546 MOV #10,ERRINX
1840 002712' 004767 003716 CALL M4.1
1841 002716' 000522 BR 98
1842
1843 002720' 022767 000004 005522 48: CMP #EOT,TINTCD ;EOT?
1844 002726' 001026 BNE 58 ;NO-CONTINUE
1845 002730' 016701 005476 MOV TMBIT2,R1 ;SET THE EOT FLAG
1846 002734' 056761 005474 010534' BIS TUBIT,EOTBL(R1)
1847 002742' 052767 000020 005524 HIS #BIT4,FLAGS ;YES-SET THE EXIT FLAG
1848 002750' 032767 000004 005516 BIT #BIT2,FLAGS ;IF RETRY IN PROGRESS FLAG=1
1849 002756' 001502 BEQ 98 ;ELSE-CONTINUE
1850 002760' 066701 005452 ADD TUBIT2,R1 ;ADD IN THE TU DISPLACEMENT
1851 002764' 005261 010774' INC RWRIT(R1) ;THEN-UPDATE RECOVERABLE WRITE ERROR STATS.
1852 002770' 012767 000003 005462 MOV #3,ERRINX
1853 002776' 004767 003632 CALL M4.1
1854 003002' 000470 BR 98
1855
1856 003004' 022767 000027 005436 58: CMP #BTAPE,TINTCD ;BAD TAPE?
1857 003012' 001016 BNE 68 ;NO-CONTINUE
1858 003014' 052767 000020 005452 BIS #BIT4,FLAGS ;YES-SET THE EXIT FLAG
1859 003022' 012767 000004 005430 MOV #4,ERRINX
1860 003030' 004767 003600 CALL M4.1
1861 003034' 016701 005372 MOV TMBIT2,R1 ;DROP TU78
1862 003040' 046761 005370 010554' BIC TUBIT,SUTBL(R1)
1863 003046' 000446 BR 98
1864
1865 003050' 022767 000022 005372 68: CMP #RTRY,TINTCD ;RETRY?
1866 003056' 001004 BNE 78 ;NO-CONTINUE
1867 003060' 052767 000004 005406 BIS #BIT2,FLAGS ;YES-SET RETRY IN PROGRESS FLAG
1868 003066' 000436 BR 98
1869 003070' 022767 000032 005352 78: CMP #TMFB,TINTCD ;TM FAULT B?
1870 003076' 001015 BNE 88 ;NO-CONTINUE
1871 003100' 012767 000007 005352 MOV #7,ERRINX
1872 003106' 004767 003522 CALL M4.1
1873 003112' 016701 005314 MOV TMBIT2,R1 ;DROP TM78
1874 003116' 005061 010554' CLR SUTBL(R1)
1875 003122' 052767 000020 005344 HIS #BIT4,FLAGS ;SET THE EXIT FLAG
1876 003130' 000415 BR 98
1877
1878 003132' 052767 000020 005334 88: BIS #BIT4,FLAGS ;SET THE EXIT FLAG
1879 003140' 012767 000002 005312 MOV #2,ERRINX
1880 003146' 004767 003462 CALL M4.1
1881 003152' 016701 005254 MOV TMBIT2,R1 ;DROP TU78
1882 003156' 046761 005252 010554' BIC TUBIT,SUTBL(R1)
1883
1884 003164' 032767 000020 005302 98: BIT #BIT4,FLAGS ;DO UNTIL EXIT FLAG=1
1885 003172' 001606 BEQ 18
1886 003174' 000207 RTS PC ;RETURN

```

```

1888 .REM
1889 BEGINROUTINE MODULE 2.4 (READ FORWARD CONTROL)
1890 : CLEAR THE RETRY IN PROGRESS FLAG
1891 : CLEAR THE EXIT FLAG
1892 : CALL MODULE 3.2 (READ FORWARD)
1893 : BGNDO
1894 : : SELECT TERMINATION INTERRUPT CODE
1895 : : : DONE-SET THE EXIT FLAG
1896 : : : IF RETRY IN PROGRESS FLAG=1
1897 : : : THEN-UPDATE RECOVERABLE READ ERROR STATISTICS
1898 : : : : PRINT ERROR 5
1899 : : : : ELSE-CONTINUE
1900 : : : : ENDF
1901 : : : IF "TRE" BIT IN RH11/70 CS1 REGISTER=1
1902 : : : : THEN-PRINT ERROR 10
1903 : : : : ELSE-CONTINUE
1904 : : : : ENDF
1905 : : : RETRY-SET THE RETRY IN PROGRESS FLAG
1906 : : : CALL MODULE 3.2 (READ FORWARD)
1907 : : : READ OPP-SET THE RETRY IN PROGRESS FLAG
1908 : : : CALL MODULE 3.3 (READ REVERSE)
1909 : : : UNREADABLE-SET THE EXIT FLAG
1910 : : : : UPDATE UNRECOVERABLE READ ERROR STATISTICS
1911 : : : : PRINT ERROR 6
1912 : : : : TM FAULT B-PRINT ERROR 7
1913 : : : : DROP TM78-CLEAR ALL BITS (SCRATCH UNIT TABLE (TM78#))
1914 : : : : SET THE EXIT FLAG
1915 : : : : ALL OTHER-PRINT ERROR 2
1916 : : : : DROP THE TU78-CLEAR BIT (SCRATCH UNIT TABLE(TM78#, TU78#))
1917 : : : : SET THE EXIT FLAG
1918 : : : : ENDSELECT
1919 : : : DO UNTIL THE EXIT FLAG NOT=0
1920 : : : ENDDO
1921 ENDRROUTINE
1922
1923
1924
1925 003176' 042767 000004 005270 M2.4: BIC #BIT2,FLAGS ;CLEAR RETRY IN PROGRESS FLAG
1926 003204' 042767 000020 005262 BIC #BIT4,FLAGS ;CLEAR EXIT FLAG
1927 003212' 004767 002310 CALL M3.2 ;CALL MODULE 3.2 (READ FORWARD)
1928 003216' 016701 005210 MOV TMBIT2,R1
1929
1930 003222' 022767 000001 005220 15: CMP #DONE,TINTCD ;DONE?
1931 003230' 001034 BNE 35 ;NO-CONTINUE
1932 003232' 052767 000020 005234 BIS #BIT4,FLAGS ;YES-SET EXIT FLAG
1933 003240' 032767 000004 005226 BIT #BIT2,FLAGS ;IF RETRY IN PROGRESS=1
1934 003246' 001413 BEQ 28
1935 003250' 016701 005156 MOV TMBIT2,R1 ;THEN-UPDATE RECOVERABLE READ ERRORS
1936 003254' 066701 005156 ADD TUBIT2,R1 ;ADD IN THE TU DISPLACEMENT
1937 003260' 005261 010674' INC RREAD(R1)
1938 003264' 012767 000005 005166 MOV #5,ERRINX
1939 003272' 004767 003336 CALL M4.1
1940
1941 003276' 032777 040000 005570 25: BIT #BIT14,#CS1 ;IF TRE BIT IN RH11/70 CS1=1
1942 003304' 001513 BEQ 85 ;ELSE-CONTINUE
1943 003306' 012767 000010 005144 MOV #10,ERRINX
    
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1944 003314' 004767 003314 CALL M4.1
1945 003320' 000505 BR 85
1946 003322' 022767 000022 005120 35: CMP #RTRY,TINTCD ;RETRY
1947 003330' 001006 BNE 45 ;NO-CONTINUE
1948 003332' 052767 000004 005134 BIS #BIT2,FLAGS ;YES-SET RETRY IN PROGRESS FLAG
1949 003340' 004767 002162 CALL M3.2 ;CALL MODULE 3.2 (READ FORWARD)
1950 003344' 000473 BR 85
1951
1952 003346' 022767 000023 005074 45: CMP #RDOPP,TINTCD ;READ OPPOSITE
1953 003354' 001006 BNE 55 ;NO-CONTINUE
1954 003356' 052767 000004 005110 BIS #BIT2,FLAGS ;YES-SET RETRY IN PROGRESS FLAG
1955 003364' 004767 002414 CALL M3.3 ;CALL MODULE 3.3 (READ REVERSE)
1956 003370' 000461 BR 85
1957
1958 003372' 022767 000024 005050 55: CMP #UNREAD,TINTCD ;UNREADABLE?
1959 003400' 001017 BNE 65 ;NO-CONTINUE
1960 003402' 052767 000020 005064 BIS #BIT4,FLAGS ;YES-SET EXIT FLAG
1961 003410' 016701 005016 MOV TMBIT2,R1 ;UPDATE UNRECOVERABLE READ ERRORS
1962 003414' 066701 005016 ADD TUBIT2,R1 ;ADD IN THE TU DISPLACEMENT
1963 003420' 005261 010574' INC URREAD(R1)
1964 003424' 012767 000006 005026 MOV #6,ERRINX ;PRINT ERROR 6
1965 003432' 004767 003176 CALL M4.1
1966 003436' 000436 BR 85
1967
1968 003440' 022767 000032 005002 65: CMP #TMFB,TINTCD ;TM FAULT B?
1969 003446' 001015 BNE 75 ;NO-CONTINUE
1970 003450' 052767 000020 005016 BIS #BIT4,FLAGS ;YES-SET THE EXIT FLAG
1971 003456' 012767 000007 004774 MOV #7,ERRINX
1972 003464' 004767 003144 CALL M4.1
1973 003470' 016701 004736 MOV TMBIT2,R1 ;DROP TM78
1974 003474' 005061 010554' CLR SUTHL(R1)
1975 003500' 000415 BR 85
1976
1977 003502' 052767 000020 004764 75: BIS #BIT4,FLAGS ;SET THE EXIT FLAG
1978 003510' 012767 000002 004742 MOV #2,ERRINX
1979 003516' 004767 003112 CALL M4.1
1980 003522' 016701 004704 MOV TMBIT2,R1 ;DROP TU78
1981 003526' 004761 004702 010554' BIC TUBIT,SUTBL(R1)
1982
1983 003534' 032767 000020 004732 85: BIT #BIT4,FLAGS ;DO UNTIL EXIT FLAG=1
1984 003542' 001627 BEQ 15
1985 003544' 000207 RTS PC ;RETURN
    
```

```

1987 .REM
1988 BEGINROUTINE MODULE 2.5 (READ REVERSE CONTROL)
1989 : CLEAR THE RETRY IN PROGRESS FLAG
1990 : CLEAR THE EXIT FLAG
1991 : CALL MODULE 3.3 (READ REVERSE)
1992 : BGNDDO
1993 : : SELECT TERMINATION INTERRUPT CODE
1994 : : : DONE-SET THE EXIT FLAG
1995 : : : IF RETRY IN PROGRESS FLAG NOT=0
1996 : : : : THEN-UPDATE RECOVERABLE READ ERROR STATISTICS
1997 : : : : PRINT ERROR 5
1998 : : : : ELSE-CONTINUE
1999 : : : : ENDF
2000 : : : IF "TRE" BIT IN RH11/70 CS1 REGISTER=1
2001 : : : : THEN-PRINT ERROR 10
2002 : : : : ELSE-CONTINUE
2003 : : : : ENDF
2004 : : : RETRY-SET THE RETRY IN PROGRESS FLAG
2005 : : : CALL MODULE 3.3 (READ REVERSE)
2006 : : : READ OPP-SET THE RETRY IN PROGRESS FLAG
2007 : : : CALL MODULE 3.2 (READ FORWARD)
2008 : : : UNREADABLE-SET THE EXIT FLAG
2009 : : : UPDATE UNRECOVERABLE READ ERROR STATISTICS
2010 : : : : PRINT ERROR 6
2011 : : : : TM FAULT B-PRINT ERROR 7
2012 : : : : DROP TM78-CLEAR ALL BITS (SCRATCH UNIT TABLE (TM78#))
2013 : : : : SET THE EXIT FLAG
2014 : : : : ALL OTHER-PRINT ERROR 2
2015 : : : : DROP THE TU78-CLEAR BIT (SCRATCH UNIT TABLE(TM78#, TU78#))
2016 : : : : SET THE EXIT FLAG
2017 : : : : ENDSELECT
2018 : : : DO UNTIL THE EXIT FLAG NOT=0
2019 : : : ENDDO
2020 ENDRROUTINE
2021
2022
2023
2024 003546' 042767 000004 004720 M2.5: BIC #BIT2,FLAGS ;CLEAR RETRY IN PROGRESS FLAG
2025 003554' 042767 000020 004712 BIC #BIT4,FLAGS ;CLEAR EXIT FLAG
2026 003562' 004767 002216 CALL M3.3 ;CALL MODULE 3.3 (READ REVERSE)
2027
2028 003566' 022767 000001 004654 1s: CMP #DONE,TINTCD ;DONE?
2029 003574' 001034 BNE 3s ;NO-CONTINUE
2030 003576' 052767 000020 004670 BIS #BIT4,FLAGS ;YES-SET EXIT FLAG
2031 003604' 032767 000004 004662 BIT #BIT2,FLAGS ;IF RETRY IN PROGRESS=1
2032 003612' 001413 BEQ 2s
2033 003614' 016701 004612 MOV TMBIT2,R1 ;THEN-UPDATE RECOVERABLE READ ERRORS
2034 003620' 066701 004612 ADD TUBIT2,R1 ;ADD IN THE TU DISPLACEMENT
2035 003624' 005261 010674' INC RREAD(R1)
2036 003630' 012767 000005 004622 MOV #5,ERRINX
2037 003636' 004767 002772 CALL M4.1
2038
2039 003642' 032777 040000 005224 2s: BIT #BIT14,#CS1 ;IF THE BIT IN RH11/70 CS1=1
2040 003650' 001513 BEQ 8s ;ELSE-CONTINUE
2041 003652' 012767 000010 004600 MOV #10,ERRINX
2042 003660' 004767 002750 CALL M4.1

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2043 003664' 000505 BR 8s
2044 003666' 022767 000022 004554 3s: CMP #RTRY,TINTCD ;RETRY
2045 003674' 001006 BNE 4s ;NO-CONTINUE
2046 003676' 052767 000004 004570 BIS #BIT2,FLAGS ;YES-SET RETRY IN PROGRESS FLAG
2047 003704' 004767 002074 CALL M3.3 ;CALL MODULE 3.3 (READ REVERSE)
2048 003710' 000473 BR 8s
2049
2050 003712' 022767 000023 004530 4s: CMP #ROPP,TINTCD ;READ OPPOSITE
2051 003720' 001006 BNE 5s ;NO-CONTINUE
2052 003722' 052767 000004 004544 BIS #BIT2,FLAGS ;YES-SET RETRY IN PROGRESS FLAG
2053 003730' 004767 001572 CALL M3.2 ;CALL MODULE 3.2 (READ FORWARD)
2054 003734' 000461 BR 8s
2055
2056 003736' 022767 000024 004504 5s: CMP #UNREAD,TINTCD ;UNREADABLE?
2057 003744' 001017 BNE 6s ;NO-CONTINUE
2058 003746' 052767 000020 004520 BIS #BIT4,FLAGS ;YES-SET EXIT FLAG
2059 003754' 016701 004452 MOV TMBIT2,R1 ;UPDATE UNRECOVERABLE READ ERRORS
2060 003760' 066701 004452 ADD TUBIT2,R1 ;ADD IN THE TU DISPLACEMENT
2061 003764' 005261 010574' INC URREAD(R1)
2062 003770' 012767 000006 004462 MOV #6,ERRINX ;PRINT ERROR 6
2063 003776' 004767 002632 CALL M4.1
2064 004002' 000436 BR 8s
2065
2066 004004' 022767 000032 004436 6s: CMP #TMFB,TINTCD ;TM FAULT B?
2067 004012' 001015 BNE 7s ;NO-CONTINUE
2068 004014' 052767 000020 004452 BIS #BIT4,FLAGS ;YES-SET THE EXIT FLAG
2069 004022' 012767 000007 004430 MOV #7,ERRINX
2070 004030' 004767 002600 CALL M4.1
2071 004034' 016701 004372 MOV TMBIT2,R1 ;DROP TM78
2072 004040' 005061 010554' CLR SUTBL(R1)
2073 004044' 000415 BR 8s
2074
2075 004046' 052767 000020 004420 7s: BIS #BIT4,FLAGS ;SET THE EXIT FLAG
2076 004054' 012767 000002 004376 MOV #2,ERRINX
2077 004062' 004767 002546 CALL M4.1
2078 004066' 016701 004340 MOV TMBIT2,R1 ;DROP TU78
2079 004072' 046761 004336 010554' BIC TUBIT,SUTBL(R1)
2080
2081 004100' 032767 000020 004366 8s: BIT #BIT4,FLAGS ;DO UNTIL EXIT FLAG=1
2082 004106' 001627 BEQ 1s
2083 004110' 000207 RTS PC ;RETURN

```

```

2085 .REM -
2086 BEGINROUTINE MODULE 2.6 (TAPE MARK CONTROL)
2087 : CLEAR THE TM78#
2088 : HGND0
2089 : : CLEAR THE TU78#
2090 : : HGND0
2091 : : : IF BIT (SCRATCH UNIT TABLE(TM78#, TU78#)) = 1, AND
2092 : : : BIT (EOT TABLE(TM78#, TU78#)) = 0
2093 : : : : THEN-CALL MODULE 3.4 (TAPE MARK)
2094 : : : : SELECT TERMINATION INTERRUPT CODE
2095 : : : : : DONE-IF "TRE" BIT IN RH11/70 CS1 REGISTER=1
2096 : : : : : : THEN-PRINT ERROR 10
2097 : : : : : : ELSE-CONTINUE
2098 : : : : : : ENDF
2099 : : : : : TM FAULT B-PRINT ERROR 7
2100 : : : : : DROP TM78-CLEAR ALL BITS (SCRATCH UNIT TABLE (TM78#))
2101 : : : : : ALL OTHERS-PRINT ERROR 2
2102 : : : : : : DROP TU78-CLEAR BIT (SCRATCH UNIT TABLE(TM78#, TU78#))
2103 : : : : : ENDF
2104 : : : : : ELSE-CONTINUE
2105 : : : : ENDF
2106 : : : : INCREMENT THE TU78#
2107 : : : : DO UNTIL THE TU78#=4
2108 : : : : ENDDO
2109 : : : : INCREMENT THE TM78#
2110 : : : : DO UNTIL THE TM78#=10(8)
2111 : : : : ENDDO
2112 ENDRROUTINE
2113 -
2114
2115
2116 004112' 005067 004324 M2.6: CLR TM78 ;CLEAR THE TM78#
2117 004116' 005067 004322 1s: CLR TU78 ;CLEAR THE TU78#
2118 004122' 004767 175334 2s: CALL TMCONV ;
2119 004126' 004767 175372 CALL TUCONV
2120 004132' 016701 004274 MOV TMBIT2,R1
2121 004136' 036761 004272 010554' BIT TUBIT,SUTBL(R1) ;IF BIT SET (SCRATCH UNIT TABLE(TM78#,TU78#))
2122 004144' 001456 BEQ 5s ;ELSE-CONTINUE
2123 004146' 036761 004262 010534' BIT TUBIT,EDTTBL(R1) ;IF BIT (EOT TABLE(TM78#, TU78#)) = 0
2124 004154' 001052 BNE 5s ;ELSE - CONTINUE
2125 004156' 004767 001664 CALL M3.4 ;CALL MODULE 3.4 (TAPE MARK CONTROL)
2126 004162' 016701 004244 MOV TMBIT2,R1
2127 004166' 022767 000001 004254 CMP #DONE,TINTCD ;DONE?
2128 004174' 001012 BNE 3s ;NO-CONTINUE
2129 004176' 032777 040000 004670 BIT #BIT14,CS1 ;YES-IF TRE BIT IN RH11/70 CS1=1
2130 004204' 001436 BEQ 5s ;ELSE-CONTINUE
2131 004206' 012767 000010 004244 MOV #10,ERRINX
2132 004214' 004767 002414 CALL M4.1
2133 004220' 000430 BR 5s
2134
2135 004222' 022767 000032 004220 3s: CMP #TMFB,TINTCD ;TM FAULT B?
2136 004230' 001012 BNE 4s ;NO-CONTINUE
2137 004232' 012767 000007 004220 MOV #7,ERRINX ;YES - PRINT ERROR 7
2138 004240' 004767 002370 CALL M4.1
2139 004244' 016701 004162 MOV TMBIT2,R1 ;DROP TM78
2140 004250' 005061 010554' CLR SUTBL(R1)

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2141 004254' 000412 BR 5s
2142
2143 004256' 012767 000002 004174 4s: MOV #2,ERRINX ;PRINT ERROR 2
2144 004264' 004767 002344 CALL M4.1
2145 004270' 016701 004136 MOV TMBIT2,R1 ;DROP TU78
2146 004274' 046761 004134 010554' BIC TUBIT,SUTBL(R1)
2147
2148 004302' 005267 004136 5s: INC TU78 ;INCREMENT THE TU78#
2149 004306' 022767 000004 004130 CMP #4,TU78 ;DO UNTIL THE TU78#=4
2150 004314' 001302 BNE 2s
2151
2152 004316' 005267 004120 INC TM78 ;INCREMENT THE TM78#
2153 004322' 022767 000010 004112 CMP #10,TM78 ;DO UNTIL THE TM78#=10
2154 004330' 001272 BNE 1s
2155 004332' 000207 RTS PC ;RETURN

```



```

2157 .REM -
2158 BEGINROUTINE MODULE 2.7 (REWIND CONTROL)
2159 : CALL MODULE 3.5 (REWIND)
2160 : SELECT TERMINATION INTERRUPT
2161 : : DONE-IF "TRE" BIT IN RH11/70 CS1 REGISTER=1
2162 : : : THEN-PRINT ERROR 10
2163 : : : ELSE-CONTINUE
2164 : : : ENDIF
2165 : : REWINDING-CONTINUE
2166 : : : IF "TRE" BIT IN RH11/70 CS1 REGISTER=1
2167 : : : : THEN-PRINT ERROR 8
2168 : : : : ELSE-CONTINUE
2169 : : : : ENDIF
2170 : : : TM FAULT 8-PRINT ERROR 7
2171 : : : : DROP TM78-CLEAR ALL BITS (SCRATCH UNIT TABLE (TM78#))
2172 : : : ALL OTHERS-PRINT ERROR 2
2173 : : : : DROP TU78-CLEAR BIT (SCRATCH UNIT TABLE(TM78#, TU78#))
2174 : : : ENDSELECT
2175 : ENDROUTINE
2176 -
2177
2178
2179 004334' 004767 001610 M2.7: CALL M3.5 ;CALL MODULE 3.5 (REWIND CONTROL)
2180 004340' 022767 000001 004102 CMP #DONE,TINTCD ;DONE?
2181 004346' 001012 BNE 2$ ;NO-CONTINUE
2182 004350' 032777 040000 004516 1$: BIT #BIT14,0CS1 ;YES-IF IN RH11/70 CS1#1
2183 004356' 001442 BEQ 4$ ;ELSE-CONTINUE
2184 : ;THEN-PRINT ERROR 8
2185 004360' 012767 000010 004072 MOV #10,ERRINX
2186 004366' 004767 002242 CALL M4.1
2187 004372' 000434 BR 4$
2188
2189 004374' 022767 000007 004046 2$: CMP #REW,TINTCD ;REWINDING
2190 004402' 001762 BEQ 1$ ;YES-CHEAT-USE DONE CODE
2191 004404' 022767 000032 004036 CMP #TMFB,TINTCD ;NO-FAULT B?
2192 004412' 001012 BNE 3$ ;NO-CONTINUE
2193 004414' 012767 000007 004036 MOV #7,ERRINX ;YES - PRINT ERROR 7
2194 004422' 004767 002206 CALL M4.1
2195 004426' 016701 004000 MOV TMBIT2,R1 ;DROP TM78
2196 004432' 005061 010554' CLR SUBL(R1)
2197 004436' 000412 BR 4$
2198 004440' 012767 000002 004012 3$: MOV #2,ERRINX ;PRINT ERROR 2
2199 004446' 004767 002162 CALL M4.1
2200 004452' 016701 003754 MOV TMBIT2,R1 ;DROP TU78
2201 004456' 046761 003752 010554' BIC TMBIT,SUBL(R1)
2202 004464' 000207 4$: RTS PC ;RETURN
  
```

```

2204 .REM -
2205 BEGINROUTINE MODULE 2.8 (DATA COMPARE)
2206 : IF INHIBIT DATA COMPARE FLAG (BIT0 OF SR1)=1
2207 : : THEN-CONTINUE
2208 : : ELSE-IF READ WAS A READ REVERSE
2209 : : : THEN-COMPARE WRITE BUFFER WITH READ BUFFER (DEC/X11 CKDATA)
2210 : : : ELSE-CONTINUE
2211 : : : ENDIF
2212 : ENDIF
2213 ENDROUTINE
2214 -
2215
2216
2217 004466' 032767 000001 173322 M2.8: BIT #BIT0,SR1 ;IF INHIBIT DATA COMPARE FLAG=1
2218 004474' 001010 BNE 1$ ;THEN-CONTINUE
2219 004476' 022767 000077 004002 CMP #RREV,RDFCN ;ELSE-WAS IT A READ REVERSE?
2220 004504' 001404 BEQ 1$ ;YES - THEN NO DATA COMPARE
2221 004506' 104412 000000' 000126' CDATAS,BEGIN,RBUFGA ; REQUEST FOR MONITOR TO CHECK DATA
2222 (1) 004514' 004516' .+2 ; IF ERROR, CONTINUE
2222 004516' 000207 1$: RTS PC ;RETURN
2223
2224 :NOTE: IF ANYONE TRIES TO ADD A DATA COMPARE ON A READ REVERSE
2225 : OPERATION THERE WILL HAVE TO BE SOME WORK DONE WITH THE GETPA
2226 : AND MAP22 MACROS TO MAKE THE ADDRESSES APPEAR CORRECT FOR THE
2227 : READ REVERSE DATA COMPARE. THE REASON BEING THAT THE DATA
2228 : COMPARE MACRO UTILIZES THE "RBUFGA" AND "RBUFEA" WORDS IN THE
2229 : HEADER AS A POINTER TO THE READ BUFFER. HOWEVER, ON A READ
2230 : REVERSE OPERATION THESE WORDS POINT TO THE END OF THE BUFFER,
2231 : AND THE DATA COMPARE MACRO ASSUMES THAT "RBUFGA" AND "RBUFEA"
2232 : A STARTING BUFFER ADDRESSES.
  
```

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2234 .REM -
2235 BEGINROUTINE MODULE 2.9 (STATISTIC PRINTOUT)
2236 : IF BIT3 OF SR1=0
2237 : : THEN-PRINT STATISTIC HEADER LINE
2238 : : CLEAR TM78#
2239 : : BGNDD
2240 : : : CLEAR THE TU78#
2241 : : : BGNDD
2242 : : : : IF BIT SET (UNIT TABLE(TM78#, TU78#))
2243 : : : : : THEN-LET INDEX=((TM78*(8))+(TU78*(2)))
2244 : : : : : GET (RECOVERABLE READ ERROR TABLE (INDEX))
2245 : : : : : GET (RECOVERABLE WRITE ERROR TABLE (INDEX))
2246 : : : : : GET UNRECOVERABLE READ ERROR TABLE (INDEX))
2247 : : : : : PRINT TM78#, TU78#, REC. READ, UN-REC. READ, REC. WRITE
2248 : : : : : ELSE-CONTINUE
2249 : : : : : ENDIF
2250 : : : : INCREMENT THE TU78#
2251 : : : : DO UNTIL THE TU78#=4
2252 : : : : ENDDO
2253 : : : : INCREMENT THE TM78#
2254 : : : : DO UNTIL THE TM78#=10(8)
2255 : : : : ENDDO
2256 : : ELSE-CONTINUE
2257 : ENDIF
2258 ENDROUTINE
2259 -
2260
2261 004520' 032767 000010 173270 M2.9: BIT #BIT3,SR1 ;IF BIT3 OF SR1=0
2262 004526' 001103 BNE 48 ;ELSE-CONTINUE
2263 004530' 104401 000000' 004740' MSGS,BEGIN,STATHD ;ASCII MESSAGE CALL
2264 004536' 005067 003700 CLR TM78 ;CLEAR THE TM78#
2265 004542' 005067 003676 1S: CLR TU78 ;CLEAR THE TU78#
2266 004546' 004767 174710 CALL TMCNVV
2267 004552' 004767 174746 2S: CALL TUCNVV
2268 004556' 016701 003650 MOV TMBIT2,R1
2269 004562' 036761 003646 010514' BIT TUBIT,UTBL(R1) ;IF BIT SET (UNIT TABLE(TM78#,TU78#))
2270 004570' 001446 BEQ 3S ;ELSE-CONTINUE
2271 004572' 016702 003634 MOV TMBIT2,R2 ;THEN-LET INDEX=((TM78*(8))+(TU78*(2)))
2272 004576' 006302 ASL R2 ;TIMES 4
2273 004600' 006302 ASL R2 ;TIMES 8
2274 004602' 066702 003630 ADD TUBIT2,R2 ;ADD TU78# X 2
2275 004606' 016267 010674' 003664 MOV RREAD(R2),TEMP ;GET THE RECOVERABLE READS
2276 ;*****
2277 (1) ;CONVERT TEMP TO ASCII AND
2278 (1) ;STORE AT STRR
2279 (1) 004614' 104421 000000' 010500' BTODS,BEGIN,TEMP,STRR
2280 (1) 004622' 005072' ;*****
2281 (1) ;CONVERT THE RECOVERABLE WRITES
2282 (1) 004624' 016267 010774' 003646 MOV RWRIT(R2),TEMP ;GET THE RECOVERABLE WRITES
2283 (1) ;*****
2284 (1) ;CONVERT TEMP TO ASCII AND
2285 (1) ;STORE AT STRW
2286 (1) 004632' 104421 000000' 010500' BTODS,BEGIN,TEMP,STRW
2287 (1) 004640' 005131' ;*****
2288 (1) ;CONVERT THE UNRECOVERABLE READS
2289 (1) 004642' 016267 010574' 003630 MOV URREAD(R2),TEMP ;GET THE UNRECOVERABLE READS

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2280 ;*****
2281 (1) ;CONVERT TEMP TO ASCII AND
2282 (1) ;STORE AT STURR
2283 (1) 004650' 104421 000000' 010500' BTODS,BEGIN,TEMP,STURR
2284 (1) 004656' 005111' ;*****
2285 (1) ;CONVERT TM78 TO ASCII AND
2286 (1) ;STORE AT STTM
2287 (1) 004660' 104420 000000' 010442' OTOAS,BEGIN,TM78,STTM
2288 (1) 004666' 005046' ;*****
2289 (1) ;CONVERT TU78 TO ASCII AND
2290 (1) ;STORE AT STTU
2291 (1) 004670' 104420 000000' 010444' OTOAS,BEGIN,TU78,STTU
2292 (1) 004676' 005057' ;*****
2293 (1) ;ASCII MESSAGE CALL
2294 2283 004700' 104401 000000' 005044' MSGS,BEGIN,STATLN ;ASCII MESSAGE CALL
2295 2284 004706' 005267 003532 3S: INC TU78 ;INCREMENT THE TU78#
2296 2285 004712' 022767 000004 003524 CMP #4,TU78 ;DO UNTIL THE TU78#=4
2297 2286 004720' 001314 BNE 2S
2298 2287 004722' 005267 003514 INC TM78 ;INCREMENT THE TM78#
2299 2288 004726' 022767 000010 003506 CMP #10,TM78 ;DO UNTIL THE TM78#=10
2300 2289 004734' 001302 BNE 1S
2301 2290 004736' 000207 RTS PC ;RETURN
2302 2291 004740' 020045 020040 046524 4S: STATHD: .ASCII '% TM78# TU78# REC. READS UN-REC. READS '
2303 004746' 034067 020043 020040
2304 004754' 052040 033525 021470
2305 004762' 020040 020040 042522
2306 004770' 027103 051040 040505
2307 004776' 051504 020040 020040
2308 005004' 047125 051055 041505
2309 005012' 020056 042522 042101
2310 005020' 020123 020040 040
2311 2292 005025' 122 041505 020056 .ASCIZ 'REC. WRITES %'
2312 005032' 051127 052111 051505
2313 005040' 022440 000
2314 2293 005044' 005044' .EVEN
2315 2294 005044' 020040 .ASCII ' '
2316 2295 005046' 030060 030060 030060 STATLN: .ASCII '000000'
2317 2296 005054' 020040 040 SITM: .ASCII ' '
2318 2297 005057' 060 030060 030060 SITU: .ASCII '000000'
2319 005064' 060
2320 2298 005065' 040 020040 020040 .ASCII ' '
2321 2299 005072' 030060 030060 027060 STRR: .ASCII '00000.'
2322 2300 005100' 020040 020040 020040 .ASCII ' '
2323 005106' 020040 040
2324 2301 005111' 060 030060 030060 STURR: .ASCII '00000.'
2325 005116' 056
2326 2302 005117' 040 020040 020040 .ASCII ' '
2327 005124' 020040 020040 040
2328 2303 005131' 060 030060 030060 STRW: .ASCII '00000.'
2329 005136' 056
2330 2304 005137' 045 000 .ASCIZ '% '
2331 2305 005142' .EVEN

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2307 .REM -
2308 BEGINROUTINE MODULE 2.10 (UNIT CONTROL)
2309 : CLEAR THE UNIT COUNT
2310 : CLEAR THE EOT COUNT
2311 : CLEAR THE TM78#
2312 : BGNDO
2313 : : CLEAR THE TU78#
2314 : : BGNDO
2315 : : : IF BIT SET (SCRATCH UNIT TABLE(TM78#, TU78#))
2316 : : : THEN-INCREMENT THE UNIT COUNT
2317 : : : ELSE-CONTINUE
2318 : : : ENDF
2319 : : : IF BIT SET (EOT TABLE(TM78#, TU78#))
2320 : : : THEN-INCREMENT THE EOT COUNT
2321 : : : ELSE-CONTINUE
2322 : : : ENDF
2323 : : : INCREMENT THE TU78#
2324 : : : DO UNTIL THE TU78#=4
2325 : : : ENDDO
2326 : : : INCREMENT THE TM78#
2327 : : : DO UNTIL THE TM78#=10(8)
2328 : : : ENDDO
2329 ENDROUTINE
2330 -
2331
2332 005142' 005067 003272 M2.10: CLR UCNT ;CLEAR THE UNIT COUNT
2333 005146' 005067 003274 CLR EOTCNT ;CLEAR THE EOT COUNT
2334 005152' 005067 003264 CLR TM78 ;CLEAR THE TM78 #
2335 005156' 005067 003262 1s: CLR TU78 ;CLEAR THE TU78#
2336 005162' 004767 174274 CALL TMCONV
2337 005166' 004767 174332 2s: CALL TUCONV
2338 005172' 016701 003234 MOV TMBIT2,R1 ;
2339 005176' 036761 003232 010554' BIT TUBIT,SUTBL(R1) ;IF SCRATCH UNIT TABLE (TM78#,TU78#)=1
2340 005204' 001402 BEQ 3s ;ELSE-CONTINUE
2341 005206' 005267 003226 INC UCNT ;THEN-INCREMENT THE UNIT COUNT
2342 005212' 036761 003216 010534' 3s: BIT TUBIT,EOTBL(R1) ;IF EOT TABLE (TM78#,TU78#)=1
2343 005220' 001402 BEQ 4s ;ELSE-CONTINUE
2344 005222' 005267 003220 INC EOTCNT ;THEN-INCREMENT THE EOT COUNT
2345 005226' 005267 003212 4s: INC TU78 ;INCREMENT THE TU78#
2346 005232' 022767 000004 003204 CMP #4,TU78 ;DO UNTIL THE TU78#=4
2347 005240' 001352 BNE 2s
2348 005242' 005267 003174 INC TM78 ;INCREMENT THE TM78#
2349 005246' 022767 000010 003166 CMP #10,TM78 ;DO UNTIL THE TM78#=10
2350 005254' 001340 BNE 1s
2351 005256' 000207 RTS PC ;RETURN
    
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2353 .REM -
2354 BEGINROUTINE MODULE 3.1 (WRITE)
2355 : LOAD THE RH11/70 CS2 REGISTER WITH THE TM78#
2356 : LOAD THE RH11/70 CS1 REGISTER WITH THE "TRE" BIT (040000)
2357 : CLEAR THE RH11/70 CS1 REGISTER
2358 : LOAD THE RH11/70 WC REGISTER WITH 2'S COMPLEMENT OF BUFFER SIZE (DEC/X11 WBUF SZ)
2359 : LOAD THE RH11/70 BC REGISTER WITH (WBUF SZ*2)
2360 : CLEAR THE TM78 DATA TRANSFER INTERRUPT CODE
2361 : LOAD THE RH11/70 REGISTER 14 WITH (TU78# * FORMAT * RECORD COUNT * SKIP COUNT)
2362 : IF 22 BIT ADDRESSING AVAILABLE (DEC/X11 BIT9 OF RES1)
2363 : : THEN-CONVERT THE 18 BIT WRITE BUFFER ADDRESS TO 22 BITS (DEC/X11 MAP22)
2364 : : : LOAD THE RH70 BA REGISTER
2365 : : : LOAD THE RH70 EXTENDED BA REGISTER
2366 : : : ELSE-LOAD THE RH11 BA REGISTER
2367 : : : ENDF
2368 : SET THE DATA TRANSFER AND INTERRUPT EXPECTED FLAGS
2369 : IF DENSITY FLAG=1 (GCR)
2370 : : THEN-LOAD RH11/70 CS1 WITH (IE * WRITE GCR FUNCTION CODE * A16 * A17)
2371 : : : ELSE-LOAD RH11/70 CS1 WITH (IE * WRITE PE FUNCTION CODE * A16 * A17)
2372 : : : ENDF
2373 : WAIT FOR AN INTERRUPT (DEC/X11 EXIT)
2374 ENDROUTINE
2375 -
2376
2377 005260' 016777 003156 003616 M3.1: MOV TM78,@CS2 ;LOAD THE RH11/70 CS2 REGISTER WITH THE TM78#
2378 005266' 016777 040000 003600 MOV BIT14,@CS1 ;LOAD THE RH11/70 CS1 REGISTER WITH THE TRE BIT
2379 005274' 005077 003574 CLR @CS1 ;CLEAR RH11/70 CS1
2380 005300' 016777 172636 003570 MOV WBUF SZ,@WC ;LOAD THE RH11/70 WC REGISTER WITH
2381 005306' 005477 003564 NEG @WC ;THE 2'S COMPLEMENT OF BUFFER SIZE
2382 005312' 016777 172624 003562 MOV WBUF SZ,@BC ;LOAD THE RH11/70 BC REGISTER WITH
2383 005320' 006377 003556 ASL @BC ;THE WORD COUNT * 2
2384 005324' 005077 003556 CLR @XFRINT ;CLEAR THE DATA TRANSFER INTERRUPT CODE
2385 005330' 016777 003110 003552 MOV TU78,@TC ;LOAD RH11/70 REGISTER 14 WITH TU78#, FORMAT REC. CNT. S
2386 005336' 032767 001000 172512 BIT #ADDR22,RES1 ;IF 22 BIT ADDRESSING AVAILABLE
2387 005344' 001430 BEQ 1s ;GO DO ELSE
2388 005346' 016767 172562 003110 MOV WBUF PA,PA18 ;THEN-CONVERT THE 18 BIT
2389 005354' 016767 172556 003104 MOV WBUF EA,XMEM ;PHYSICAL ADDRESS TO 22 BITS
2390 005362' 104416 000000' 010464' MAP22s, BEGIN,PA18 ; GET 22-BIT ADDR FROM 18-BIT ADDR
2391 005370' 016777 003074 003502 MOV PA22,@BA ;LOAD THE RH70 BA REGISTER
2392 005376' 016777 003070 003564 MOV EA22,@EBA ;LOAD THE RH70 EBA REGISTER
2393 005404' 016767 003062 003050 MOV EA22,A16A17 ;SAVE ADDRESS BITS A16 AND A17
2394 005412' 042767 177774 003042 BIC #177774,A16A17
2395 005420' 000367 003036 SWAB A16A17
2396 005424' 000416 BR 2s ;
2397 005426' 016777 172502 003444 1s: MOV WBUF PA,@BA ;ELSE-LOAD THE RH11 BA REGISTER
2398 005434' 016767 172476 003020 MOV WBUF EA,A16A17 ;CALCULATE ADDRESS BITS A16 AND A17
2399 005442' 006367 003014 ASL A16A17 ;SHIFT EXTENDED ADDRESS
2400 005446' 006367 003010 ASL A16A17 ;BITS TO BIT POSITIONS
2401 005452' 006367 003004 ASL A16A17 ;8 AND 9
2402 005456' 006367 003000 ASL A16A17 ;
2403 005462' 052767 000050 003004 2s: BIS #BIT3:BITS,FLAGS ;SET THE DATA TRANSFER AND INTERRUPT EXPECTED FLAGS
2404 005470' 032767 000001 002776 BIT #BIT0,FLAGS ;IF DENSITY FLAG=1 (GCR)
2405 005476' 001403 BEQ 3s ;GO DO ELSE
2406 005500' 012701 000163 MOV #GCR:BIT6,R1 ;THEN-LOAD WRITE GCR FUNCTION CODE AND IE BIT
2407 005504' 000402 BR 4s ;
2408 005506' 012701 000161 3s: MOV #PE:BIT6,R1 ;ELSE-LOAD WRITE PE FUNCTION CODE AND IE BIT
    
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2409 005512' 056701 002744 4s: BIS A16A17,R1 ;FOR IN EXTENDED ADDRESS BITS
2410 005516' 010177 003352 MOV R1,PCSI ;ISSUE THE COMMAND
2411 005522' 104400 000000' EXITS,BEGIN ;EXIT TO MONITOR. MODULE WAIT FOR INTERRUPT.
2412
2413
2414 .REM
2415 BEGINROUTINE MODULE 3.2 (READ FORWARD)
2416 : LOAD THE RH11/70 CS2 REGISTER WITH THE TM78#
2417 : LOAD THE RH11/70 CS1 REGISTER WITH THE "TRE" BIT (040000)
2418 : CLEAR THE RH11/70 CS1 REGISTER
2419 : LOAD THE RH11/70 WC REGISTER WITH 2'S COMPLEMENT OF BUFFER SIZE (DEC/X11 WBUFVSZ)
2420 : LOAD THE RH11/70 BC REGISTER WITH (WBUFVSZ*2)
2421 : CLEAR THE TM78 DATA TRANSFER INTERRUPT CODE
2422 : LOAD THE RH11/70 REGISTER 14 WITH (TU78# * FORMAT * RECORD COUNT * SKIP COUNT)
2423 : CONVERT THE READ BUFFER ADDRESS TO 18 BITS (DEC/X11 GETPA)
2424 : IF 22 BIT ADDRESSING AVAILABLE (DEC/X11 BIT9 OF RES1)
2425 : : THEN-CONVERT 18 BIT READ BUFFER ADDRESS TO 22 BITS (DEC/X11 MAP22)
2426 : : : LOAD THE RH70 BA REGISTER
2427 : : : LOAD THE RH70 EXTENDED BA REGISTER
2428 : : ELSE-LOAD THE RH11 BA REGISTER
2429 : ENDF
2430 : SET THE DATA TRANSFER AND INTERRUPT EXPECTED FLAGS
2431 : LOAD RH11/70 CS1 WITH (IE * READ FORWARD FUNCTION CODE * A17 * A18)
2432 : WAIT FOR AN INTERRUPT (DEC/X11 EXIT)
2433 ENDRROUTINE
2434
2435
2436 005526' 012767 000071 002752 M3.2: MOV #RFD,RDFCN
2437 005534' 012767 011174' 172362 MOV #BUFV, RBUFVA
2438
2439 005542' 016777 002674 003334 MRDCOM: MOV TM78,PCSI ;LOAD THE RH11/70 CS2 REGISTER WITH THE TM78#
2440 005550' 012777 040000 003316 #BIT14,PCSI ;LOAD THE RH11/70 CS1 REGISTER WITH THE TRE BIT
2441 005556' 005077 003312 CLR PCSI ;CLEAR THE RH11/70 CS1 REGISTER
2442 005562' 016777 172354 003306 MOV WBUFVSZ,WBC ;LOAD THE RH11/70 WC REGISTER WITH THE
2443 005570' 005477 003302 #BC ;2'S COMPLEMENT OF BUFFER SIZE
2444 005574' 016777 172342 003300 MOV WBUFVSZ,#BC ;LOAD THE RH11/70 BC REGISTER WITH BUFFER SIZE
2445 005602' 006377 003274 ASL #BC ;WORD COUNT * 2
2446 005606' 005077 003274 CLR #XFRINT ;CLEAR THE RM78 DATA TRANSFER INTERRUPT CODE
2447 005612' 016777 002626 003270 MOV TU78,#TC ;LOAD RH11/70 REG. 14 WITH TU78#,FMT,REC-CNT,SKIP-CNT
2448 005620' 104415 000000' 000124' GETPA,BEGIN, RBUFVA ;GET PHYSICAL ADDRESS FROM 16-BIT RBUFVA
2449 005626' 032767 001000 172222 BIT #ADDR22,RES1 ;IF 22 BIT ADDRESSING AVAILABLE
2450 005634' 001430 BEQ IS ;GO DO ELSE
2451 005636' 016767 172264 002620 MOV RBUFPA,PA18 ;THEN-LOAD UP THE 18 BIT
2452 005644' 016767 172260 002614 MOV RBUFEA,XMEM ;ADDRESS FOR CONVERSION
2453 005652' 104416 000000' 010464' MAP22s. BEGIN,PA18 ; GET 22-BIT ADDR FROM 18-BIT ADDR
2454 005660' 016777 002604 003212 MOV PA22,#BA ;LOAD THE RH70 BA REGISTER
2455 005666' 016777 002600 003274 MOV EA22,#EBA ;LOAD THE RH70 EBA REGISTER
2456 005674' 016767 002572 002560 MOV EA22,A16A17 ;CALCULATE A16 + A17 FOR LATER
2457 005702' 042767 177774 002552 BIC #177774,A16A17 ;USE IN CS1
2458 005710' 000367 002546 SWAB A16A17 ;
2459 005714' 000416 BR ZS ;
2460 005716' 016777 172204 003154 1s: MOV RBUFPA,#BA ;ELSE-LOAD THE RH11 BA REGISTER
2461 005724' 016767 172200 002530 MOV RBUFEA,A16A17 ;CALCULATE A16 + A17 FOR LATER
2462 005732' 006367 002524 ASL A16A17 ;USE IN CS1
2463 005736' 006367 002520 ASL A16A17 ;
2464 005742' 006367 002514 ASL A16A17 ;

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2465 005746' 006367 002510 ASL A16A17 ;
2466 005752' 052767 000050 002514 2s: BIS #BIT3!BITS,FLAGS ;SET THE DATA TRANSFER AND INTERRUPT EXPECTED FLAGS
2467 005760' 016701 002522 MOV RDFCN,R1 ;GET THE READ COMMAND
2468 005764' 052701 000100 BIS #BIT6,R1 ;FOR IN THE IE BIT
2469 005770' 056701 002466 BIS A16A17,R1 ;FOR IN THE EXTENDED ADDRESS BITS
2470 005774' 010177 003074 MOV R1,PCSI ;ISSUE THE COMMAND
2471 006000' 104400 000000' EXITS,BEGIN ;EXIT TO MONITOR. MODULE WAIT FOR INTERRUPT.
2472
2473 .REM
2474 BEGINROUTINE MODULE 3.3 (READ REVERSE)
2475 : LOAD THE RH11/70 CS2 REGISTER WITH THE TM78#
2476 : LOAD THE RH11/70 CS1 REGISTER WITH THE "TRE" BIT (040000)
2477 : CLEAR THE RH11/70 CS1 REGISTER
2478 : LOAD THE RH11/70 WC REGISTER WITH 2'S COMPLEMENT OF BUFFER SIZE (DEC/X11 WBUFVSZ)
2479 : LOAD THE RH11/70 BC REGISTER WITH (WBUFVSZ*2)
2480 : CLEAR THE TM78 DATA TRANSFER INTERRUPT CODE
2481 : LOAD THE RH11/70 REGISTER 14 WITH (TU78# * FORMAT * RECORD COUNT * SKIP COUNT)
2482 : CONVERT THE READ BUFFER ADDRESS + "RBUFVSZ" TO 18 BITS (DEC/X11 GETPA)
2483 : IF RH70 SWITCH=0 (RH70)
2484 : : THEN-CONVERT 18 BIT READ BUFFER ADDRESS TO 22 BITS (DEC/X11 MAP22)
2485 : : : LOAD THE RH70 BA REGISTER
2486 : : : LOAD THE RH70 EXTENDED BA REGISTER
2487 : : ELSE-LOAD THE RH11 BA REGISTER
2488 : ENDF
2489 : SET THE DATA TRANSFER AND INTERRUPT EXPECTED FLAGS
2490 : LOAD RH11/70 CS1 WITH (IE * READ REVERSE FUNCTION CODE * A17 * A18)
2491 : WAIT FOR AN INTERRUPT (DEC/X11 EXIT)
2492 ENDRROUTINE
2493
2494
2494 006004' 012767 000077 002474 M3.3: MOV #RREV,RDFCN
2495 006012' 012767 011174' 172104 MOV #BUFV, RBUFVA
2496 006020' 066767 172116 172076 ADD WBUFVSZ,RBUFVA
2497 006026' 066767 172110 172070 ADD WBUFVSZ,RBUFVA
2498 006034' 162767 000002 172062 SUB #2,RBUFVA ;START ONE WORD BEFORE END
2499 006042' 000167 177474 JMP MRDCOM

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2501 .REM -
2502 BEGINROUTINE MODULE 3.4 (TAPE MARK)
2503 : LOAD THE RH11/70 CS2 REGISTER WITH THE TM78#
2504 : LOAD THE RH11/70 CS1 REGISTER WITH THE "TRE" BIT (040000)
2505 : CLEAR THE RH11/70 CS1 REGISTER
2506 : CLEAR THE DATA TRANSFER FLAG
2507 : SET THE INTERRUPT EXPECTED FLAG
2508 : IF DENSITY FLAG=1 (GCR)
2509 : : THEN-LOAD RH11/70 REGISTER (40+(TU78**2)) WITH WTM GCR FUNCTION CODE
2510 : : ELSE-LOAD RH11/70 REGISTER (40+(TU78**2)) WITH WTM PE FUNCTION CODE
2511 : ENDIF
2512 : LOAD RH11/70 CS1 REGISTER WITH (IE)
2513 : WAIT FOR AN INTERRUPT (DEC/X11 EXIT)
2514 ENDRROUTINE
2515 -
2516
2517 006046' 016777 002370 003030 M3.4: MOV TM78,@CS2 ;LOAD THE RH11/70 CS2 REGISTER WITH THE TM78#
2518 006054' 012777 040000 003012 MOV #BIT14,@CS1 ;LOAD THE RH11/70 CS1 REGISTER WITH THE TRE BIT
2519 006062' 005077 003006 CLR @CS1 ;CLEAR THE RH11/70 CS1 REGISTER
2520 006066' 042767 000010 002400 BIC #BIT3,FLAGS ;CLEAR THE DATA TRANSFER FLAG
2521 006074' 052767 000040 002372 BIS #BITS,FLAGS ;SET THE INTERRUPT EXPECTED FLAG
2522 006102' 016701 002336 MOV TU78,R1 ;GET THE TU78#
2523 006106' 006301 ASL R1 ;MULTIPLY BY 2
2524 006110' 032767 000001 002356 BIT #BIT0,FLAGS ;IF DENSITY FLAG=1
2525 006116' 001404 BEQ 1S ;GO DO ELSE
2526 006120' 012771 000017 011134' MOV #GCRFM,@M00(R1) ;THEN-LOAD RH11/70 REGISTER (40+(TU78**2)) WITH WTM GCR
2527 006126' 000403 BR 2S
2528 006130' 012771 000015 011134' 1S: MOV #PEFM,@M00(R1) ;ELSE=LOAD RH11/70 REGISTER (R0+(TU78**2)) WITH WTM PE F
2529 006136' 012777 000100 002730 2S: MOV #BIT6,@CS1 ;LOAD RH11/70 CS1 WITH THE IE BIT
2530 006144' 104400 000000' EXITS,BEGIN ;EXIT TO MONITOR. MODULE WAIT FOR INTERRUPT.

```

```

2532 .REM -
2533 BEGINROUTINE MODULE 3.5 (REWIND)
2534 : LOAD THE RH11/70 CS2 REGISTER WITH THE TM78#
2535 : LOAD THE RH11/70 CS1 REGISTER WITH THE "TRE" BIT (040000)
2536 : CLEAR THE RH11/70 CS1 REGISTER
2537 : CLEAR THE DATA TRANSFER FLAG
2538 : SET THE INTERRUPT EXPECTED FLAG
2539 : LOAD THE RH11/70 REGISTER (40+(TU78**2)) WITH REWIND FUNCTION CODE
2540 : LOAD THE RH11/70 CS1 REGISTER WITH (IE)
2541 : WAIT FOR AN INTERRUPT (DEC/X11 EXIT)
2542 ENDRROUTINE
2543 -
2544
2545 006150' 016777 002266 002726 M3.5: MOV TM78,@CS2 ;LOAD THE RH11/70 CS2 REGISTER WITH TM78#
2546 006156' 012777 040000 002710 MOV #BIT14,@CS1 ;LOAD THE RH11/70 CS1 REGISTER WITH THE TRE BIT
2547 006164' 005077 002704 CLR @CS1 ;CLEAR THE RH11/70 CS1 REGISTER
2548 006170' 042767 000010 002276 BIC #BIT3,FLAGS ;CLEAR THE DATA TRANSFER FLAG
2549 006176' 052767 000040 002270 BIS #BITS,FLAGS ;SET THE INTERRUPT EXPECTED FLAG
2550 006204' 016701 002234 MOV TU78,R1 ;GET THE TU78#
2551 006210' 006301 ASL R1 ;MULTIPLY BY 2
2552 006212' 012771 000007 011134' MOV #REW,@M00(R1) ;LOAD RH11/70 REGISTER (40+(TU78**2)) WITH THE REWIND FU
2553 006220' 012777 000100 002646 MOV #BIT6,@CS1 ;LOAD RH11/70 REGISTER CS1 WITH THE IE BIT
2554 006226' 104400 000000' EXITS,BEGIN ;EXIT TO MONITOR. MODULE WAIT FOR INTERRUPT.

```

```

2556 .REM -
2557 BEGINROUTINE MODULE 3.6 (SENSE)
2558 : LOAD THE RH11/70 CS2 REGISTER WITH THE TM78#
2559 : LOAD THE RH11/70 CS1 REGISTER WITH THE "TRE" BIT (040000)
2560 : CLEAR THE RH11/70 CS1 REGISTER
2561 : CLEAR THE DATA TRANSFER FLAG
2562 : SET THE INTERRUPT EXPECTED FLAG
2563 : LOAD THE RH11/70 REGISTER (40+(TU78**2)) WITH SENSE FUNCTION CODE
2564 : LOAD RH11/70 CS1 REGISTER WITH (IE)
2565 : WAIT FOR AN INTERRUPT (DEC/X11 EXIT)
2566 ENDROUTINE
2567 -
2568
2569 006232' 016777 002204 002644 M3.6: MOV TM78,#CS2 ;LOAD THE RH11/70 CS2 REGISTER WITH TM78#
2570 006240' 012777 040000 002626 MOV #BIT14,#CS1 ;LOAD THE RH11/70 CS1 REGISTER WITH THE TRE BIT
2571 006246' 005077 002622 CLR #CS1 ;CLEAR THE RH11/70 CS1 REGISTER
2572 006252' 042767 000010 002214 BIC #BIT3,FLAGS ;CLEAR THE DATA TRANSFER FLAG
2573 006260' 052767 000040 002206 BIS #BIT5,FLAGS ;SET THE INTERRUPT EXPECTED FLAG
2574 006266' 016701 002152 MOV TU78,R1 ;GET THE TU78#
2575 006272' 006301 ASL R1 ;MULTIPLY BY 2
2576 006274' 012771 000011 011134' MOV #SEN,#M00(R1) ;LOAD THE RH11/70 REGISTER (R0+(TU78**2)) WITH THE SENSE
2577 006302' 012777 000100 002564 MOV #BIT6,#CS1 ;LOAD RH11/70 REGISTER CS1 WITH THE IE BIT
2578 006310' 104400 000000' EXITS,BEGIN ;EXIT TO MONITOR. MODULE WAIT FOR INTERRUPT.

```

```

2580 .REM -
2581 BEGINROUTINE MODULE 4.0 (INTERRUPT)
2582 -
2583
2584
2585
2586 006314' 042777 000100 002552 M4.0: BIC #BIT6,#CS1 ;CLEAR THE RH11/70 IE BIT
2587 006322' 016767 002546 171550 MOV CS1,CSR ;COPY CSR ADDRESS FOR ERROR PRINTOUT
2588 006330' 017767 002540 171544 MOV #CS1,ACSR ;COPY CONTENTS OF CS1 TO HEADER
2589 006336' 017767 002542 171540 MOV #CS2,ASTAT ;COPY CS2 CONTENTS
2590 006344' 032767 000040 002122 BIT #BIT5,FLAGS ;INTERRUPT EXPECTED?
2591 006352' 001007 BNE Z6 ;YES-GO PROCESS
2592 006354' 017777 002532 002530 MOV #AS,#AS ;NO-CLEAR PENDING INTERRUPTS
2593 006362' 012777 000100 002504 1s: MOV #BIT6,#CS1 ;ENABLE-RH11/70 INTERRUPTS
2594 006370' 000002 RTI ;RETURN
2595
2596 006372' 032767 000010 002074 2s: BIT #BIT3,FLAGS ;DATA TRANSFER INTERRUPT EXPECTED
2597 006400' 001406 BEQ Z6 ;NO-
2598 006402' 042767 000040 002064 BIC #BIT5,FLAGS ;YES-CLEAR INTERRUPT EXPECTED FLAG
2599 -----
(1) 006410' 000004 000000' 006532' PIRQS,BEGIN,M4.0C ; QUEUE UP TO CONTINUE AT M4.0C AND RTI
(1) -----
2600
2601 006416' 017767 002470 002066 3s: MOV #AS,ASREG ;COPY THE RH11/70 AS REGISTER
2602 006424' 036767 002000 002060 BIT TMBIT,ASREG ;EXPECTED TM?
2603 006432' 001004 BNE Z6 ;YES-GO CHECK TU
2604 006434' 016777 002052 002450 4s: MOV ASREG,#AS ;NO-CLEAR THE INTERRUPT(S)
2605 006442' 000747 BR Z6
2606
2607 006444' 016777 001772 002432 5s: MOV TM78,#CS2 ;SELECT INTERRUPTING TM78
2608 006452' 017767 002454 002030 MOV #M0INT,M0INR ;SAVE INTERRUPT CODE
2609 006460' 017767 002430 001770 MOV #TUSTAT,SENSE ;SAVE SENSE INFO.
2610 006466' 017767 002440 001756 MOV #M0INT,ITU78 ;GET THE TU78 #
2611 006474' 000367 001752 SWAB ITU78
2612 006500' 042767 177774 001744 BIC #177774,ITU78
2613 006506' 026767 001732 001736 CMP TU78,ITU78 ;EXPECTED TU?
2614 006514' 001347 HNE Z6 ;NO-GO CLEAR INTERRUPT
2615 006516' 042767 000040 001750 BIC #BIT5,FLAGS ;YES-CLEAR INTERRUPT EXPECTED FLAG
2616 -----
(1) 006524' 000004 000000' 006532' PIRQS,BEGIN,M4.0C ; QUEUE UP TO CONTINUE AT M4.0C AND RTI
(1) -----
2617
2618 006532' 032767 000010 001734 M4.0C: BIT #BIT3,FLAGS ;IF DATA TRANSFER FLAG=1
2619 006540' 001412 BEQ Z6 ;GO DO ELSE
2620 006542' 016777 001674 002334 MOV TM78,#CS2 ;THEN-LOAD TM78 # TO RH11/70
2621 006550' 017767 002332 001672 MOV #XFRINT,TINTCD ;COPY DATA TRANSFER INTERRUPT
2622 006556' 042767 177700 001664 BIC #177700,TINTCD ;CODE TO TERMINATION INTERRUPT CODE
2623 006564' 000411 BR Z6
2624
2625 006566' 016767 001716 001654 1s: MOV M0INR,TINTCD ;GET THE NON DATA
2626 006574' 042767 177700 001646 BIC #177700,TINTCD ;TRANSFER INTERRUPT CODE
2627 006602' 016777 001622 002302 MOV TMBIT,#AS
2628 006610' 042767 000010 001656 2s: BIC #BIT3,FLAGS ;CLEAR DATA TRANSFER FLAG
2629 006616' 042767 000040 001650 BIC #BIT5,FLAGS ;CLEAR INTERRUPT EXPECTED FLAG
2630 006624' 012777 000100 002242 MOV #BIT6,#CS1 ;ENABLE RH11/70 INTERRUPTS
2631 006632' 000207 RTS PC ;RETURN

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```
2633 .REM -
2634 ;*BEGINROUTINE MODULE 4.1 (ERRORS)
2635 -
2636
2637 006634' 032767 000004 171154 M4.1: BIT #BIT2,SRI ;IF BIT2 OF SRI=0 (PRINT ERROR MESSAGES)
2638 006642' 001063 HNE 35 ;ELSE = GET OUT
2639 006644' 032767 000001 001622 BIT #BIT0,FLAGS ;IF GCR DENSITY?
2640 006652' 001407 BEG 15 ;ELSE=GO DO PE
2641 006654' 012702 000107 MOV #107,R2 ;THEN=LOAD GCR CHARACTERS
2642 006660' 012703 000103 MOV #103,R3
2643 006664' 012704 000122 MOV #122,R4
2644 006670' 000406 BR 25
2645 006672' 012702 000120 1S: MOV #120,R2 ;ELSE=LOAD PE CHARACTERS
2646 006676' 012703 000105 MOV #105,R3
2647 006702' 012704 000040 MOV #040,R4
2648 006706' 110267 000354 2S: MOVH R2,TYPE1 ;STORE THE FORMAT
2649 006712' 110267 000442 MOVH R2,TYPE2 ;TYPE IN THE
2650 006716' 110267 000521 MOVH R2,TYPE3 ;ERROR PRINTOUT
2651 006722' 110267 000601 MOVH R2,TYPE4 ;CHARACTER STRINGS
2652 006726' 110367 000335 MOVH R3,TYPE1+1
2653 006732' 110367 000423 MOVH R3,TYPE2+1
2654 006736' 110367 000502 MOVH R3,TYPE3+1
2655 006742' 110367 000562 MOVH R3,TYPE4+1
2656 006746' 110467 000316 MOVH R4,TYPE1+2
2657 006752' 110467 000404 MOVH R4,TYPE2+2
2658 006756' 110467 000463 MOVH R4,TYPE3+2
2659 006762' 110467 000543 MOVH R4,TYPE4+2
2660 006766' 005067 CLR ERRTP ;CLEAR ERRTYPE
2661 006772' 016702 001462 MOV ERRINX,R2 ;GET ERROR INDEX
2662 006776' 006302 ASL R2 ;MULTIPLY BY 2
2663 007000' 004772 007014' CALL @M4.1TB(R2) ;GO PRINT PROPER ERROR
2664 007004' 104401 000000' 010334' MSGS,BEGIN,CRLF ;ASCII MESSAGE CALL
2665 007012' 000207 3S: RTS PC ;RETURN
2666
2667 007014' 000000 M4.1TB: .WORD 0 ;UNDEFINED
2668 007016' 007044' .WORD ERR1 ;NO TM78 UNITS AVAILABLE
2669 007020' 007114' .WORD ERR2 ;UNEXPECTED INTERRUPT CODE
2670 007022' 007216' .WORD ERR3 ;RECOVERABLE WRITE ERROR
2671 007024' 007302' .WORD ERR4 ;UNRECOVERABLE WRITE ERROR
2672 007026' 007374' .WORD ERR5 ;RECOVERABLE READ ERROR
2673 007030' 007456' .WORD ERR6 ;UNRECOVERABLE READ ERROR
2674 007032' 007542' .WORD ERR7 ;TM FAULT INTERRUPT
2675 007034' 007614' .WORD ERR10 ;MASSBUS STATUS ERROR
2676 007036' 007760' .WORD ERR11 ;NO TU78 UNITS AVAILABLE
2677 007040' 010030' .WORD ERR12 ;BOTH DENSITIES DISABLED
2678 007042' 010100' .WORD ERR13 ;TM78 NOT READY
2679
2680 007044' ERR1:
(1) ;*****
(1) 007044' 104405 000000' 000000 HRDERS,BEGIN,NULL ;
(1) ;*****
2681 007052' 104401 000000' 007062' MSGS,BEGIN,ERMS1 ;ASCII MESSAGE CALL
2682 007060' 000207 RTS PC ;RETURN
2683 007062' 047516 052040 033515 ERMS1: .ASCIZ 'NO TM78 UNITS AVAILABLE%'
007070' 020070 047125 052111
007076' 020123 053101 044501
```

```
007104' 040514 046102 022505
007112' 000
2684 007114' .EVEN
2685
2686 007114' ERR2:
(1) ;*****
(1) 007114' 104405 000000' 000000 HRDERS,BEGIN,NULL ;
(1) ;*****
2687 ;CONVERT TINTCD TO ASCII AND
(1) ;STORE AT ERMS2A
(1) 007122' 104420 000000' 010450' OTOAS,BEGIN,TINTCD,ERMS2A
(1) 007130' 007205'
(1) ;*****
2688 007132' 104401 000000' 007152' MSGS,BEGIN,ERMS2 ;ASCII MESSAGE CALL
2689 007140' 004767 001014 CALL UNITS ;GO PRINT TM78#-TU78#
2690 007144' 004767 001110 CALL DROPTU ;GO DROP UNIT MESSAGE
2691 007150' 000207 RTS PC ;RETURN
2692 007152' 047125 054105 042520 ERMS2: .ASCIZ 'UNEXPECTED INTERRUPT CODE '
007160' 052103 042105 044440
007166' 052116 051105 052522
007174' 052120 041440 042117
007202' 020105 040
2693 007205' 060 030060 030060 ERMS2A: .ASCIZ '000000%'
007212' 022460 000
2694 007216' .EVEN
2695
2696 007216' ERR3:
(1) ;*****
(1) 007216' 104406 000000' 000000 SOFERS,BEGIN,NULL ;
(1) ;*****
2697 007224' 104401 000000' 007244' MSGS,BEGIN,ERMS3 ;ASCII MESSAGE CALL
2698 007232' 004767 000722 CALL UNITS ;PRINT THE TM78#-TU78#
2699 007236' 004767 001074 CALL RECFIL ;PRINT RECORD NUMBER FILE NUMBER
2700 007242' 000207 RTS PC ;RETURN
2701 007244' 042522 047503 042526 ERMS3: .ASCIZ 'RECOVERABLE WRITE '
007252' 040522 046102 020105
007260' 051127 052111 020105
2702 007266' 041507 122 TYPE1: .ASCIZ 'GCR'
2703 007271' 040 051105 047522 .ASCIZ ' ERROR%'
007276' 022522 000
2704 007302' .EVEN
2705
2706 007302' ERR4:
(1) ;*****
(1) 007302' 104405 000000' 000000 HRDERS,BEGIN,NULL ;
(1) ;*****
2707 007310' 104401 000000' 007334' MSGS,BEGIN,ERMS4 ;ASCII MESSAGE CALL
2708 007316' 004767 000636 CALL UNITS ;PRINT THE TM78#-TU78#
2709 007322' 004767 001010 CALL RECFIL ;PRINT RECORD NUMBER FILE NUMBER
2710 007326' 004767 000726 CALL DROPTU ;PRINT DROP MESSAGE
2711 007332' 000207 RTS PC ;RETURN
2712 007334' 047125 042522 047503 ERMS4: .ASCIZ 'UNRECOVERABLE WRITE '
007342' 042526 040522 046102
007350' 020105 051127 052111
007356' 020105
```

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2713 007360' 041507 122 TYPE2: .ASCII 'GCR'  
2714 007363' 040 051105 047522 .ASCIZ ' ERROR%'  
007370' 022522 000  
007374' .EVEN  
2715  
2716  
2717 007374' ERR5: ;*****  
(1) ;SOFERS,BEGIN,NULL ;  
(1) 007374' 104406 000000' 000000 ;*****  
2718 007402' 104401 000000' 007422' MSGS,BEGIN,ERMS5 ;ASCII MESSAGE CALL  
2719 007410' 004767 000544 CALL UNITS ;PRINT THE TM78*-TU78*  
2720 007414' 004767 000716 CALL RECFIL ;PRINT RECORD NUMBER FILE NUMBER  
2721 007420' 000207 RTS PC ;RETURN  
2722 007422' 042522 047503 042526 ERMS5: .ASCII 'RECOVERABLE READ '  
007430' 040522 046102 020105  
007436' 042522 042101 040  
2723 007443' 107 051103 TYPE3: .ASCII 'GCR'  
2724 007446' 042440 051122 051117 .ASCIZ ' ERROR%'  
007454' 000045 .EVEN  
2725  
2726  
2727 007456' ERR6: ;*****  
(1) ;HDRS,BEGIN,NULL ;  
(1) 007456' 104405 000000' 000000 ;*****  
2728 007464' 104401 000000' 007504' MSGS,BEGIN,ERMS6 ;ASCII MESSAGE CALL  
2729 007472' 004767 000462 CALL UNITS ;PRINT THE TM78*-TU78*  
2730 007476' 004767 000634 CALL RECFIL ;PRINT RECORD NUMBER FILE NUMBER  
2731 007502' 000207 RTS PC ;RETURN  
2732 007504' 047125 042522 047503 ERMS6: .ASCII 'UNRECOVERABLE READ '  
007512' 042526 040522 046102  
007520' 020105 042522 042101  
007526' 040  
2733 007527' 107 051103 TYPE4: .ASCII 'GCR'  
2734 007532' 042440 051122 051117 .ASCIZ ' ERROR%'  
007540' 000045 .EVEN  
2735  
2736  
2737 007542' ERR7: ;*****  
(1) ;HDRS,BEGIN,NULL ;  
(1) 007542' 104405 000000' 000000 ;*****  
2738 007550' 104401 000000' 007570' MSGS,BEGIN,ERMS7 ;ASCII MESSAGE CALL  
2739 007556' 004767 000376 CALL UNITS ;PRINT TM78*-TU78*  
2740 007562' 004767 000520 CALL DROPTM ;DROP THE TM78  
2741 007566' 000207 RTS PC ;RETURN  
2742 007570' 046524 043040 052501 ERMS7: .ASCIZ 'TM FAULT INTERRUPT%'  
007576' 052114 044440 052116  
007604' 051105 052522 052120  
007612' 000045 .EVEN  
2743  
2744  
2745 007614' ERR10: ;*****  
(1) ;SOFERS,BEGIN,NULL ;  
(1) 007614' 104406 000000' 000000 ;*****  
(1)
```

```
2746 007622' 017767 001246 000650 MOV @CS1,TEMP ;GET THE CONTENTS OF CS1  
2747 ;*****  
(1) ;CONVERT TEMP TO ASCII AND  
(1) 007630' 104420 000000' 010500' OTOAS,BEGIN,TEMP,ERM10A ;STORE AT ERM10A  
(1) 007636' 007731' ;*****  
(1) ;CONVERT TEMP TO ASCII AND  
2748 007640' 017767 001240 000632 OTOAS,BEGIN,TEMP,ERM10B ;STORE AT ERM10B  
2749 ;*****  
(1) ;CONVERT TEMP TO ASCII AND  
(1) 007646' 104420 000000' 010500' OTOAS,BEGIN,TEMP,ERM10B ;STORE AT ERM10B  
(1) 007654' 007747' ;*****  
(1) ;MSGS,BEGIN,ERMS10 ;ASCII MESSAGE CALL  
2750 007656' 104401 000000' 007676' CALL UNITS ;PRINT TM78*-TU78*  
2751 007664' 004767 000270 CALL RECFIL ;PRINT RECORD NUMBER FILE NUMBER  
2752 007670' 004767 000442 RTS PC ;RETURN  
2753 007674' 000207  
2754 007676' 040515 051523 052502 ERMS10: .ASCII 'MASSBUS STATUS ERROR CS1 = '  
007704' 020123 052123 052101  
007712' 051525 042440 051122  
007720' 051117 041440 030523  
007726' 036440 040  
2755 007731' 060 030060 030060 ERM10A: .ASCII '000000 CS2 = '  
007736' 020060 041440 031123  
007744' 036440 040  
2756 007747' 060 030060 ERM10B: .ASCIZ '000000%'  
007754' 022460 000  
2757 .EVEN  
2758  
2759 007760' ERR11: ;*****  
(1) ;HDRS,BEGIN,NULL ;  
(1) 007760' 104405 000000' 000000 ;*****  
2760 007766' 104401 000000' 007776' MSGS,BEGIN,ERMS11 ;ASCII MESSAGE CALL  
2761 007774' 000207 RTS PC ;RETURN  
2762 007776' 047516 052040 033525 ERMS11: .ASCIZ 'NO TU78 UNITS AVAILAB%'  
010004' 020070 047125 052111  
010012' 020123 053101 044501  
010020' 040514 046102 022505  
010026' 000  
2763 010030' .EVEN  
2764  
2765 010030' ERR12: ;*****  
(1) ;HDRS,BEGIN,NULL ;  
(1) 010030' 104405 000000' 000000 ;*****  
2766 010036' 104401 000000' 010046' MSGS,BEGIN,ERMS12 ;ASCII MESSAGE CALL  
2767 010044' 000207 RTS PC ;RETURN  
2768 010046' 047502 044124 042040 ERMS12: .ASCIZ 'BOTH DENSITIES DISABLED%'  
010054' 047105 044523 044524  
010062' 051505 042040 051511  
010070' 041101 042514 022504  
010076' 000  
2769 010100' .EVEN  
2770 010100' ERR13:
```



```
(1) ;*****  
(1) 010100' 104405 000000' 000000 HDRS,BEGIN,NULL ;  
(1) ;*****  
2771 010106' 104401 000000' 010126' MSGS,BEGIN,ERMS13 ;ASCII MESSAGE CALL  
2772 010114' 004767 000040 CALL UNITS ;PRINT UNITS  
2773 010120' 004767 000162 CALL DROPTM ;PRINT DRIPPING TM78  
2774 010124' 000207 RTS PC ;RETURN  
2775 010126' 046524 034067 051040 ERMS13: .ASCIZ/IM78 READY BIT TIME OUT%/  
010134' 040505 054504 041040  
010142' 052111 052040 046511  
010150' 020105 052517 022524  
010156' 000  
2776 010160' 010160' .EVEN  
2777 010160' 016767 000256 000312 UNITS: MOV TM78,TEMP ;GET THE TM78#  
2778 ;*****  
(1) ;CONVERT TEMP TO ASCII AND  
(1) ;STORE AT UNITM  
(1) 010166' 104420 000000' 010500' UTOAS,BEGIN,TEMP,UNITM  
(1) 010174' 010231' ;*****  
(1) ;GET THE TU78#  
2779 010176' 016767 000242 000274 MOV TU78,TEMP ;GET THE TU78#  
2780 ;*****  
(1) ;CONVERT TEMP TO ASCII AND  
(1) ;STORE AT UNITTU  
(1) 010204' 104420 000000' 010500' UTOAS,BEGIN,TEMP,UNITTU  
(1) 010212' 010247' ;*****  
(1) ;ASCII MESSAGE CALL  
2781 010214' 104401 000000' 010224' MSGS,BEGIN,UNITMS ;ASCII MESSAGE CALL  
2782 010222' 000207 RTS PC ;RETURN  
2783 ;*****  
2784 010224' 046524 034067 040 UNITS: .ASCII 'TM78 '  
2785 010231' 060 030060 030060 UNITM: .ASCII '000000 TU78 '  
010236' 020060 020040 052524  
010244' 034067 040  
2786 010247' 060 030060 030060 UNITTU: .ASCIZ '000000 %'  
010254' 020060 000045  
2787 .EVEN  
2788 DROPTU:  
(1) 010260' 104401 000000' 010270' MSGS,BEGIN,MSDTU ;ASCII MESSAGE CALL  
2789 010266' 000207 RTS PC ;RETURN  
2790 ;*****  
2791 010270' 052524 034067 042040 MSDTU: .ASCIZ 'TU78 DROPPED%'  
010276' 047522 050120 042105  
010304' 000045  
2792 .EVEN  
2793 DROPTM:  
2794 010306' 104401 000000' 010316' MSGS,BEGIN,MSDTM ;ASCII MESSAGE CALL  
(1) 010306' 000207 RTS PC ;RETURN  
2795 010314' 000207 ;*****  
2796 ;ASCII MESSAGE CALL  
2797 010316' 046524 034067 042040 MSDTM: .ASCIZ 'IM78 DROPPED%'  
010324' 047522 050120 042105  
010332' 000045  
2798 .EVEN  
2799  
2800 010334' 000045 CHLF: .ASCIZ '%'
```

```
2801 .EVEN  
2802  
2803 010336' RECFIL: ;*****  
(1) ;CONVERT RECORD TO ASCII AND  
(1) ;STORE AT RECMSG  
(1) 010336' 104421 000000' 010504' BTODS,BEGIN,RECORD,RECMSG  
(1) 010344' 010377' ;*****  
2804 ;CONVERT FILE TO ASCII AND  
(1) ;STORE AT FILMSG  
(1) 010346' 104421 000000' 010502' BTODS,BEGIN,FILE,FILMSG  
(1) 010354' 010417' ;*****  
2805 010356' 104401 000000' 010366' MSGS,BEGIN,RFMSG ;ASCII MESSAGE CALL  
2806 010364' 000207 RTS PC ;RETURN  
2807 ;*****  
2808 010366' 042522 047503 042122 RFMSG: .ASCII 'RECORD# '  
010374' 020043 040  
2809 010377' 060 030060 030060 RECMSG: .ASCII '00000. FILE# '  
010404' 020056 020040 044506  
010412' 042514 020043 040  
2810 010417' 060 030060 030060 FILMSG: .ASCIZ '00000.%'  
010424' 022456 000  
2811 010430' .EVEN
```

2813		.SBTTL	PROGRAM VARIABLE	
2814	010430'	000000	TMBIT: .WORD 0	:TM78 # CONVERTED TO A BIT POSITION
2815	010432'	000000	TMBIT2: .WORD 0	:TM78 #2
2816	010434'	000000	TUBIT: .WORD 0	:TU78 # CONVERTED TO A BIT POSITION
2817	010436'	000000	TUBIT2: .WORD 0	:TU78 #2
2818	010440'	000000	UCNT: .WORD 0	:UNIT COUNT (NUMBER OF TU78'S ACTIVE)
2819	010442'	000000	TM78: .WORD 0	:TM78 #
2820	010444'	000000	TU78: .WORD 0	:TU78 #
2821	010446'	000000	EOTCNT: .WORD 0	:EOT COUNT (NUMBER OF TU78'S AT EOT)
2822	010450'	000000	TINTCU: .WORD 0	:TERMINATION INTERRUPT CODE
2823	010452'	000000	ITU78: .WORD 0	:INTERRUPTING TU78 #
2824	010454'	000000	BOTCNT: .WORD 0	:BOT COUNT (NUMBER OF TU78'S AT BOT)
2825	010456'	000000	SENSE: .WORD 0	:SENSE COMMAND INFO
2826	010460'	000000	ERRINX: .WORD 0	:ERROR INDEX
2827	010462'	000000	A16A17: .WORD 0	:EXTENDED ADDRESS BITS 16 AND 17
2828	010464'	000000	PA18: .WORD 0	:LOWER 16 BITS OF 18 BIT MEMORY ADDRESS
2829	010466'	000000	XMEM: .WORD 0	:EXTENDED ADDRESS BITS OF AN 18 BIT ADDRESS
2830	010470'	000000	PA22: .WORD 0	:LOWER 16 BITS OF 22 BIT MEMORY ADDRESS
2831	010472'	000000	EA22: .WORD 0	:EXTENDED ADDRESS BITS OF A 22 BIT ADDRESS
2832	010474'	000000	FLAGS: .WORD 0	:FLAGS
2833	010476'	000000	TMUNIT: .WORD 0	:TM78 UNITS
2834	010500'	000000	TEMP: .WORD 0	:TEMP STORAGE
2835	010502'	000000	FILE: .WORD 0	:FILE NUMBER
2836	010504'	000000	RECORD: .WORD 0	:RECORD NUMBER
2837	010506'	000000	RDFCN: .WORD 0	:READ FUNCTION CODE
2838	010510'	000000	MONINR: .WORD 0	:COPY OF MOTION INTERRUPT REGISTER
2839	010512'	000000	ASREG: .WORD 0	:COPY OF ATTENTION SUMMARY REGISTER

2841		.SBTTL	UNIT TABLE	
2842				
2843	010514'	000000	UTBL: .WORD 0	:TM78 = 0 TU78 0 - 3
2844	010516'	000000	.WORD 0	:TM78 = 1 TU78 0 - 3
2845	010520'	000000	.WORD 0	:TM78 = 2 TU78 0 - 3
2846	010522'	000000	.WORD 0	:TM78 = 3 TU78 0 - 3
2847	010524'	000000	.WORD 0	:TM78 = 4 TU78 0 - 3
2848	010526'	000000	.WORD 0	:TM78 = 5 TU78 0 - 3
2849	010530'	000000	.WORD 0	:TM78 = 6 TU78 0 - 3
2850	010532'	000000	.WORD 0	:TM78 = 7 TU78 0 - 3
2851				
2852		.SBTTL	EOT TABLE	
2853				
2854	010534'	000000	EOTBL: .WORD 0	:TM78 = 0 TU78 0 - 3
2855	010536'	000000	.WORD 0	:TM78 = 1 TU78 0 - 3
2856	010540'	000000	.WORD 0	:TM78 = 2 TU78 0 - 3
2857	010542'	000000	.WORD 0	:TM78 = 3 TU78 0 - 3
2858	010544'	000000	.WORD 0	:TM78 = 4 TU78 0 - 3
2859	010546'	000000	.WORD 0	:TM78 = 5 TU78 0 - 3
2860	010550'	000000	.WORD 0	:TM78 = 6 TU78 0 - 3
2861	010552'	000000	.WORD 0	:TM78 = 7 TU78 0 - 3
2862				
2863		.SBTTL	SCRATCH UNIT TABLE	
2864				
2865	010554'	000000	SUTBL: .WORD 0	:TM78 = 0 TU78 0 - 3
2866	010556'	000000	.WORD 0	:TM78 = 1 TU78 0 - 3
2867	010560'	000000	.WORD 0	:TM78 = 2 TU78 0 - 3
2868	010562'	000000	.WORD 0	:TM78 = 3 TU78 0 - 3
2869	010564'	000000	.WORD 0	:TM78 = 4 TU78 0 - 3
2870	010566'	000000	.WORD 0	:TM78 = 5 TU78 0 - 3
2871	010570'	000000	.WORD 0	:TM78 = 6 TU78 0 - 3
2872	010572'	000000	.WORD 0	:TM78 = 7 TU78 0 - 3

.SBTTL UNRECOVERABLE READ STATISTIC TABLE			
2874			
2875			
2876	010574'	000000	UPREAD: .WORD 0
2877	010576'	000000	:TM78 0 TU78 0
2878	010600'	000000	.WORD 0 :TM78 0 TU78 1
2879	010602'	000000	.WORD 0 :TM78 0 TU78 2
2880	010604'	000000	.WORD 0 :TM78 0 TU78 3
2881	010606'	000000	.WORD 0 :TM78 1 TU78 0
2882	010610'	000000	.WORD 0 :TM78 1 TU78 1
2883	010612'	000000	.WORD 0 :TM78 1 TU78 2
2884	010614'	000000	.WORD 0 :TM78 1 TU78 3
2885	010616'	000000	.WORD 0 :TM78 2 TU78 0
2886	010620'	000000	.WORD 0 :TM78 2 TU78 1
2887	010622'	000000	.WORD 0 :TM78 2 TU78 2
2888	010624'	000000	.WORD 0 :TM78 2 TU78 3
2889	010626'	000000	.WORD 0 :TM78 3 TU78 0
2890	010630'	000000	.WORD 0 :TM78 3 TU78 1
2891	010632'	000000	.WORD 0 :TM78 3 TU78 2
2892	010634'	000000	.WORD 0 :TM78 3 TU78 3
2893	010636'	000000	.WORD 0 :TM78 4 TU78 0
2894	010640'	000000	.WORD 0 :TM78 4 TU78 1
2895	010642'	000000	.WORD 0 :TM78 4 TU78 2
2896	010644'	000000	.WORD 0 :TM78 4 TU78 3
2897	010646'	000000	.WORD 0 :TM78 5 TU78 0
2898	010650'	000000	.WORD 0 :TM78 5 TU78 1
2899	010652'	000000	.WORD 0 :TM78 5 TU78 2
2900	010654'	000000	.WORD 0 :TM78 5 TU78 3
2901	010656'	000000	.WORD 0 :TM78 6 TU78 0
2902	010660'	000000	.WORD 0 :TM78 6 TU78 1
2903	010662'	000000	.WORD 0 :TM78 6 TU78 2
2904	010664'	000000	.WORD 0 :TM78 6 TU78 3
2905	010666'	000000	.WORD 0 :TM78 7 TU78 0
2906	010670'	000000	.WORD 0 :TM78 7 TU78 1
2907	010672'	000000	.WORD 0 :TM78 7 TU78 2
2908			.WORD 0 :TM78 7 TU78 3

.SBTTL RECOVERABLE READ STATISTIC TABLE			
2909			
2910			
2911	010674'	000000	RREAD: .WORD 0
2912	010676'	000000	:TM78 0 TU78 0
2913	010700'	000000	.WORD 0 :TM78 0 TU78 1
2914	010702'	000000	.WORD 0 :TM78 0 TU78 2
2915	010704'	000000	.WORD 0 :TM78 0 TU78 3
2916	010706'	000000	.WORD 0 :TM78 1 TU78 0
2917	010710'	000000	.WORD 0 :TM78 1 TU78 1
2918	010712'	000000	.WORD 0 :TM78 1 TU78 2
2919	010714'	000000	.WORD 0 :TM78 1 TU78 3
2920	010716'	000000	.WORD 0 :TM78 2 TU78 0
2921	010720'	000000	.WORD 0 :TM78 2 TU78 1
2922	010722'	000000	.WORD 0 :TM78 2 TU78 2
2923	010724'	000000	.WORD 0 :TM78 2 TU78 3
2924	010726'	000000	.WORD 0 :TM78 3 TU78 0
2925	010728'	000000	.WORD 0 :TM78 3 TU78 1
2926	010730'	000000	.WORD 0 :TM78 3 TU78 2
2927	010732'	000000	.WORD 0 :TM78 3 TU78 3
2928	010734'	000000	.WORD 0 :TM78 4 TU78 0
2929	010736'	000000	.WORD 0 :TM78 4 TU78 1
2930	010740'	000000	.WORD 0 :TM78 4 TU78 2

2930	010742'	000000	.WORD 0	:TM78 4 TU78 3
2931	010744'	000000	.WORD 0	:TM78 5 TU78 0
2932	010746'	000000	.WORD 0	:TM78 5 TU78 1
2933	010750'	000000	.WORD 0	:TM78 5 TU78 2
2934	010752'	000000	.WORD 0	:TM78 5 TU78 3
2935	010754'	000000	.WORD 0	:TM78 6 TU78 0
2936	010756'	000000	.WORD 0	:TM78 6 TU78 1
2937	010760'	000000	.WORD 0	:TM78 6 TU78 2
2938	010762'	000000	.WORD 0	:TM78 6 TU78 3
2939	010764'	000000	.WORD 0	:TM78 7 TU78 0
2940	010766'	000000	.WORD 0	:TM78 7 TU78 1
2941	010770'	000000	.WORD 0	:TM78 7 TU78 2
2942	010772'	000000	.WORD 0	:TM78 7 TU78 3
2943				

.SBTTL RECOVERABLE WRITE STATISTIC TABLE			
2944			
2945			
2946	010774'	000000	RWRIT: .WORD 0
2947	010776'	000000	:TM78 0 TU78 0
2948	011000'	000000	.WORD 0 :TM78 0 TU78 1
2949	011002'	000000	.WORD 0 :TM78 0 TU78 2
2950	011004'	000000	.WORD 0 :TM78 0 TU78 3
2951	011006'	000000	.WORD 0 :TM78 1 TU78 0
2952	011010'	000000	.WORD 0 :TM78 1 TU78 1
2953	011012'	000000	.WORD 0 :TM78 1 TU78 2
2954	011014'	000000	.WORD 0 :TM78 1 TU78 3
2955	011016'	000000	.WORD 0 :TM78 2 TU78 0
2956	011020'	000000	.WORD 0 :TM78 2 TU78 1
2957	011022'	000000	.WORD 0 :TM78 2 TU78 2
2958	011024'	000000	.WORD 0 :TM78 2 TU78 3
2959	011026'	000000	.WORD 0 :TM78 3 TU78 0
2960	011030'	000000	.WORD 0 :TM78 3 TU78 1
2961	011032'	000000	.WORD 0 :TM78 3 TU78 2
2962	011034'	000000	.WORD 0 :TM78 3 TU78 3
2963	011036'	000000	.WORD 0 :TM78 4 TU78 0
2964	011040'	000000	.WORD 0 :TM78 4 TU78 1
2965	011042'	000000	.WORD 0 :TM78 4 TU78 2
2966	011044'	000000	.WORD 0 :TM78 4 TU78 3
2967	011046'	000000	.WORD 0 :TM78 5 TU78 0
2968	011050'	000000	.WORD 0 :TM78 5 TU78 1
2969	011052'	000000	.WORD 0 :TM78 5 TU78 2
2970	011054'	000000	.WORD 0 :TM78 5 TU78 3
2971	011056'	000000	.WORD 0 :TM78 6 TU78 0
2972	011060'	000000	.WORD 0 :TM78 6 TU78 1
2973	011062'	000000	.WORD 0 :TM78 6 TU78 2
2974	011064'	000000	.WORD 0 :TM78 6 TU78 3
2975	011066'	000000	.WORD 0 :TM78 7 TU78 0
2976	011070'	000000	.WORD 0 :TM78 7 TU78 1
2977	011072'	000000	.WORD 0 :TM78 7 TU78 2
2978			.WORD 0 :TM78 7 TU78 3
2979	011074'		
2980	011074'	000000	RHTBL: .WORD 0
2981	011076'	000000	CS1: .WORD 0
2982	011100'	000000	WC: .WORD 0
2983	011102'	000000	BA: .WORD 0
2984	011104'	000000	BC: .WORD 0
2985	011106'	000000	CS2: .WORD 0
			XFRINT: .WORD 0

2986	011110'	000000	TC:	.WORD	0	;REGISTER +14
2987	011112'	000000	AS:	.WORD	0	;REGISTER +16
2988	011114'	000000	TUSTAT:	.WORD	0	;REGISTER +20
2989	011116'	000000	DB:	.WORD	0	;REGISTER +22
2990	011120'	000000	D11:	.WORD	0	;REGISTER +24
2991	011122'	000000	DT:	.WORD	0	;REGISTER +26
2992	011124'	000000	SN:	.WORD	0	;REGISTER +30
2993	011126'	000000	D12:	.WORD	0	;REGISTER +32
2994	011130'	000000	D13:	.WORD	0	;REGISTER +34
2995	011132'	000000	MOINT:	.WORD	0	;REGISTER +36
2996	011134'	000000	MO0:	.WORD	0	;REGISTER +40
2997	011136'	000000	MO1:	.WORD	0	;REGISTER +42
2998	011140'	000000	MO2:	.WORD	0	;REGISTER +44
2999	011142'	000000	MO3:	.WORD	0	;REGISTER +46
3000	011144'	000000	AD78:	.WORD	0	;REGISTER +50
3001	011146'	000000	ST78:	.WORD	0	;REGISTER +52
3002	011150'	000000		.WORD	0	;REGISTER +54
3003	011152'	000000		.WORD	0	;REGISTER +56
3004	011154'	000000		.WORD	0	;REGISTER +60
3005	011156'	000000		.WORD	0	;REGISTER +62
3006	011160'	000000		.WORD	0	;REGISTER +64
3007	011162'	000000		.WORD	0	;REGISTER +66
3008	011164'	000000		.WORD	0	;REGISTER +70
3009	011166'	000000		.WORD	0	;REGISTER +72
3010	011170'	000000	EBA:	.WORD	0	;REGISTER +74
3011	011172'	000000	CS3:	.WORD	0	;REGISTER +76
3012						
3013	011174'	002000	RUFIN:	.BLKB	1024.	;1024 BYTE READ BUFFER

3015			.SHTTL	PROGRAM DEFINITIONS		
3016			:			
3017			:	INTERRUPT CODE DEFINITIONS		
3018			:			
3019			:			
3020	000027		BTAPE	=	000027	;BAD TAPE
3021	000001		DONE	=	000001	;DONE
3022	000004		EDT	=	000004	;END OF TAPE
3023	000023		RDOPP	=	000023	;READ OPPOSITE
3024	000022		RTRY	=	000022	;RETRY
3025	000032		TMFB	=	000032	;TM FAULT B
3026	000024		UNREAD	=	000024	;UNREADABLE
3027						
3028			:			
3029			:	COMMAND CODE DEFINITIONS		
3030			:			
3031			:			
3032	000063		WGCR	=	000063	;WRITE GCR
3033	000061		WPE	=	000061	;WRITE PE
3034	000017		GCRIM	=	000017	;WRITE GCR TAPE MARK
3035	000015		PETM	=	000015	;WRITE PE TAPE MARK
3036	000007		REW	=	000007	;REWIND
3037	000011		SEN	=	000011	;SENSE
3038	000071		RFWD	=	000071	;READ FORWARD
3039	000077		RREV	=	000077	;READ REVERSE

M4.1TR	007014R	1980*	1939*	1944*	1965*	1972*	1979*	2037*	2042*	2063*	2070*	2077*	2132*	2138*
NULL =	000000	2144*	2186*	2194*	2199*	2637*								
OPEN =	000000	2663*	2667*											
OTDAS =	104420	1155*	2680	2686	2696	2706	2717	2727	2737	2745	2759	2765	2770	
PASCNT	000034R	1155*	2281	2282	2687	2747	2749	2778	2780					
PA18	010464R	1155*	2390	2388*	2390	2451*	2453	2828*						
PA22	010470R	2391	2454	2830*										
PFTM =	000015	2528	3035*											
PIHDS =	000004	1155*	2599	2616										
POPSP =	005726	1155*												
POPSP2 =	022626	1155*												
PRTY =	000000	1155*												
PRTY0 =	000000	1155*												
PRTY1 =	000040	1155*												
PRTY2 =	000100	1155*												
PRTY3 =	000140	1155*												
PRTY4 =	000200	1155*												
PRTY5 =	000240	1155*												
PRTY6 =	000300	1155*												
PRTY7 =	000340	1155*												
PS =	177776	1155*												
PSW =	177776	1155*												
PUSH =	005746	1155*												
PIUSH2 =	024646	1155*												
RANDS =	104417	1155*												
RANRUM	000054R	1155*												
RHUFPA	000130R	1155*	2452	2461										
RRUFPA	000126R	1155*	2221	2451	2460									
RRUFPSZ	000132R	1155*												
RRUFVA	000124R	1155*	2437*	2448	2495*	2496*	2497*	2498*						
RDFCN	010506R	2219	2436*	2467	2494*	2637*								
RDOPP =	000023	1952	2050	3023*										
RECF1L	010336R	2699*	2709*	2720*	2730*	2752*	2803*							
RECM5G	010377R	2803	2809*											
RFCDRD	010504R	1484*	1597*	1656*	1665	1671	1676*	2803	2836*					
RESTRT	001566R	1155	1592*											
RES1	000056R	1155*	2386	2449										
RES2	000060R	1155*												
RFW =	000007	2189	2552	3036*										
RFMSG	010366R	2805	2808*											
RFWD =	000071	2436	303H*											
RHTRL	011074R	1235*	2979*											
RREAD	010674R	1470*	1937*	2035*	2275	2911*								
RREV =	000077	2219	2494	3039*										
RSTRT	000112R	1155*												
RTRY =	000022	1865	1946	2044	3024*									
RWRIT	010774R	1469*	1833*	1851*	2277	2946*								
SBADR	000102R	1155*												
SEN =	000011	2576	3037*											
SENSF	010456R	1404*	1405	1434*	1435	1752	2609*	2825*						
SN	011124R	2992*												
SOPCNT	000042R	1155*												
SOPERS =	104406	1155*	2696	2717	2745									
SOPPAS	000046R	1155*												

SPOINT	000032R	1155*												
SPSIZ =	000040	7*	1155											
SR1	000016R	1155*	1208	1281	1383	1481	1696	1700	2217	2261	2637			
SR2	000020R	1155*												
SR3	000022R	1155*												
SR4	000024R	1155*												
START	000252R	1155	1190*											
STAT	000026R	1155*												
STATHD	004740R	2263	2291*											
STATLN	005044R	2283	2294*											
STHR	005072R	2276	2299*											
STRW	005131R	2278	2303*											
STTM	005046R	2281	2295*											
STTU	005057R	2282	2297*											
STUKR	005111R	2280	2301*											
ST78	011146R	1305	3001*											
SUTBL	010554R	1386*	1399	1410*	1424*	1440*	1602	1613	1617	1622	1746	1766*	1771*	1862*
		1874*	1882*	1974*	1981*	2072*	2079*	2121	2140*	2146*	2196*	2201*	2339	2865*
SVR0	000062R	1155*												
SVR1	000064R	1155*												
SVR2	000066R	1155*												
SVR3	000070R	1155*												
SVR4	000072R	1155*												
SVR5	000074R	1155*												
SVR6	000076R	1155*												
SYS CNT	000052H	1155*												
TC	011110R	2385*	2447*	2986*										
TEMP	010500R	2275*	2276	2277*	2278	2279*	2280	2746*	2747	2748*	2749	2777*	2778	2779*
		2780	2834*											
TINTCD	010450R	1402	1432	1750	1761	1826	1843	1856	1865	1869	1930	1946	1952	1958
		1968	2028	2044	2050	2056	2066	2127	2135	2180	2189	2191	2621*	2622*
		2625*	2626*	2687	2822*									
TMHIT	010430R	1285	1293	1299	1312	1393	1426	1496*	2602	2627	2814*			
TMBIT2	010432R	1398	1409	1423	1439	1499*	1601	1608	1612	1616	1621	1745	1749	1765
		1770	1825	1831	1845	1861	1873	1881	1928	1935	1961	1973	1980	2033
		2059	2071	2078	2120	2126	2139	2145	2195	2200	2268	2271	2338	2815*
TMCONV	001462R	1284*	1392*	1422*	1489*	1599*	1743*	2118*	2266*	2336*				
TMFH =	000032	1761	1869	1968	2066	2135	2191	3025*						
TMUNIT	010476R	1194	1280*	1283*	1285	1293*	1299*	1312*	1393	1426	2833*			
TM78	010442R	1279*	1287	1300*	1301	1390*	1395	1416*	1417	1420*	1428*	1446*	1447	1489
		1497	1595*	1629*	1630	1741*	1776*	1777	2116*	2152*	2153	2264*	2281	2287*
		2288	2334*	2348*	2349	2377	2439	2517	2545	2569	2607	2620	2777	2819*
TRPDFD =	000022	1155*												
TUBIT	010434R	1399	1410	1440	1441	1509*	1602	1609	1613	1617	1622	1746	1771	1846
		1862	1882	1981	2079	2121	2123	2146	2201	2269	2339	2342	2816*	
TUBIT2	010436R	1512*	1832	1850	1936	1962	2034	2060	2274	2817*				
TUCONV	001524R	1397*	1430*	1502*	1600*	1744*	2119*	2267*	2337*					
TUSTAT	011114R	2609	2988*											
TU78	010444R	1396*	1413*	1414	1429*	1443*	1444	1502	1510	1598*	1625*	1626	1742*	1772*
		1773	2117*	2148*	2149	2265*	2282	2284*	2285	2335*	2345*	2346	2385	2447
		2522	2550	2574	2613	2779	2820*							
TYPE1	007266R	2648*	2652*	2656*	2702*									
TYPE2	007360R	2649*	2653*	2657*	2713*									
TYPE3	007443R	2650*	2654*	2658*	2723*									
TYPE4	007527R	2651*	2655*	2659*	2733*									
UCNT	010440R	1201	1391*	1412*	1421*	1442*	1634	1645	1647	1659	2332*	2341*	2818*	

UNITMS	010224R	2781	2784*														
UNITS	010160R	2689*	2698*	2708*	2719*	2729*	2739*	2751*	2772*	2777*							
UNITTM	010231P	277*	2785*														
UNITTU	010247H	2780	2786*														
UNHEAD=	000024	1958	2056	3026*													
URHEAD	010574H	1471*	1963*	2061*	2279	2876*											
UTRL	010514R	1386	1425*	1441*	2269	2843*											
VECTOR	000010H	1155*	1379														
WASADR	000104H	1155*															
WHUFFA	000136R	1155*	2389	2398													
WHUFPA	000134R	1155*	2388	2397													
WHUFRQ	000140R	1155*															
WHUFSZ	000142R	1155*	2380	2382	2442	2444	2496	2497									
WC	011076R	2380*	2381*	2442*	2443*	2981*											
WDFR	000116R	1155*															
WDTO	000114R	1155*															
WGCR	= 000063	2406	3032*														
WPF	= 000061	2408	3033*														
XFLAG	000005R	1155*															
XFRINT	011106R	2384*	2446*	2621	2985*												
XMEM	010406R	2389*	2452*	2829*													
.	= 013174R	2221	2293*	2305*	2684*	2694*	2704*	2715*	2757*	2763*	2769*	2776*	2811*	3013*			

BKMOD	95*																
BREAK	185*	1307															
BTOD	204*	2276	2278	2280	2803	2804											
CKDATA	240*	2221															
DATAACK	249*																
DATERR	138*																
DFSEVN	272*	1155															
DSEVNT	282*	1155															
END	175*																
ENDIT	166*	1677															
ENDMOD	171*	1199	1206	1215	1639												
EQUATS	288*	1155															
EXIT	120*	2411	2471	2530	2554	2578											
GETPA	231*	2448															
GWRUFF	219*	1596															
HRDER	128*	2680	2686	2706	2727	2737	2759	2765	2770								
IOMOD	91*																
IOMODP	115*																
IOMODR	111*																
IOMODX	107*	1155															
MAP22	235*	2390	2453														
MODULF	8*	1155															
MSG	154*	2263	2283	2664	2681	2688	2697	2707	2718	2728	2738	2750	2760	2766	2771		
.	2781	2788	2794	2805													
MSGN	158*																
MSGS	162*																
NRKMUD	103*																
OTDA	190*	2281	2282	2687	2747	2749	2778	2780									
PIRG	179*	2599	2616														
RAND	124*																
SBKMUD	99*																
SOFEF	144*	2696	2717	2745													
.	AHS.	000000	000														
.		013174	001														

ERRORS DETECTED: 0
 DEFAULT GLOBALS GENERATED: 0

XTMDA0,XTMDA0/CRF=DDXCOM.P11,XTMDA0.P11
 RUN-TIME: 4 8 .7 SECONDS
 RUN-TIME RATIO: 47/14=3.4
 CORF USED: 10K (20 PAGES)

Component Part Number : AF-9210A-M1

DIGITAL EQUIPMENT CORPORATION
DIAGNOSTIC ENGINEERING PATCH ORDERPAGE 1 OF 1
Form : B

Component Part Description : ECO TALLY 01

ECO Purpose : Patch

Approved : Yes

Diagnostic Package Code : CXTMD-A1

-----PRODUCT IDENTIFICATION-----

Patch Level : 1

Product Date : 23-Sep-80

Description : CXTMDA1 DEC/X11 TM78 MODULE

1ST Copyright year : 1980

Last Copyright Year : 1980

Expanded Description :

-----PRODUCT COMPONENTS AFFECTED-----

-----PRODUCTS OBSOLETE-----

-----PROBLEM/DESCRIPTION-----

UNEXPECTED INTERRUPT CODE 00000 WHEN RUNNING MONITOR
C ON AN 11/40.

-----SOLUTION/CORRECTION-----

CORRECT TYPE ERROR IN WRITE SUBROUTINE MODULE 3.1.
MOVE BIT 14, * CS1 SHOULD BE MOV #BIT 14, * CS1.
THIS IS A CONDITIONAL PATCH.

-----DEPO PATCH AREA-----

Loc	From	To	Mnemonic	Loc	From	To	Mnemonic
5266	16777	12777					

-----PRODUCT CHARACTERISTICS-----

Run Time (seconds) 1st Pass : 75 2nd Pass : 75 ACT Sequence Number : 1351 Multimedia Affected : *No

Processors : 05 10 20 34 35 40 44 45 50 55 60 70

Operational Environment : 02 03 04 06 50

Device Affected : TM78 TU78

Problem Reports Affected :

Kit Number : 7J129

-----AUTHORIZATION-----

Author	: R. COOKE	Maintaining Group	: JO DIA
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Product Engineer	:	Field Service Engineer	:
Manufacturing Engineer	: R. SACINO	Release Engineer	: M. HANSEN
Charge Number	: V980-05346	Coordination Number	: MC3796



.REM -

IDENTIFICATION

PRODUCT CODE: AC-S081B-MC
PRODUCT NAME: CXRMD80 RP04/5/6 RM02/3/5 MOD
PRODUCT DATE: JANUARY 1982
MAINTAINER: DEC/X11 SUPPORT GROUP

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1. ABSTRACT

RMD IS AN IOMODX WHICH WILL EXERCISE UP TO 8 RP04/5/6 OR RM02/3/5 DISK DRIVES ON AN RH11/PH70 CONTROLLER, OR DUAL-PORTED BETWEEN TWO SUCH CONTROLLERS. IN SINGLE-PORT MODE, THE ENTIRE SURFACE OF EACH DISK WILL BE TESTABLE (RANDOMLY OR SYSTEMATICALLY).

IN DUAL-PORT MODE, THE "A" CONTROLLER MODULE WILL ACCESS ONLY THE LOW HALF OF THE DISK, AND THE "B" CONTROLLER MODULE WILL ACCESS ONLY THE HIGH HALF OF THE DISK; NO COMMUNICATION WILL BE ATTEMPTED BETWEEN THE TWO PORTS. ONE COPY OF THIS MODULE MUST BE CONFIGURED FOR EACH PORT.
SINGLE/DUAL PORT MODE WILL BE AUTOMATICALLY SELECTED ON THE BASIS OF THE RPD REGISTER OF EACH SELECTED DRIVE.
DURING TESTING, ANY BAD SECTOR FOUND WILL BE STOKED AUTOMATICALLY AND WITHOUT OPERATOR INTERVENTION INTO THE MODULE'S BADSPOT TABLE. A MESSAGE CAN BE PRINTED TO NOTIFY THE OPERATOR OF THE BADSPOT ADDRESS (SEE OPERATOR OPTIONS). SECTORS LISTED IN THE TABLE WILL NOT BE TESTED ON ANY DRIVE.
THE WBUFRO WORD IN THE HEADER MAY BE CHANGED AT WILL TO ALTER THE MODULE'S TRANSFER SIZE (AND OF COURSE A AND B-PORT SIZES NEED NOT MATCH); THIS WILL NOT ALTER THE EFFECTIVENESS OF THE BADSPOT TABLE OR CAUSE THE MODULE TO ATTEMPT ACCESS OF OUT-OF-RANGE DISK ADDRESSES. WITHIN THIS FRAMEWORK, THE MODULES WILL ACCESS AS MUCH OF THE DISK AS POSSIBLE. RANDOM SEEKS WILL BE USED UNLESS THIS OPTION IS Deselected IN SRI.

2. REQUIREMENTS

HARDWARE: 1 TO 8 RP04/5/6 OR RM02/3/5 DRIVES ON 1 OR 2 RH11/RH70 CONTROLLERS

STORAGE: RMD REQUIRES:
DECIMAL WORDS: 2196
OCTAL WORDS: 4224
OCTAL BYTES: 10450

3. PASS DEFINITION

ONE PASS CONSISTS OF 300 ITERATIONS.
AN ITERATION CONSISTS OF THE FOLLOWING STEPS EXECUTED ON EACH SELECTED DRIVE:

- A. WRITE DATA TO DISK. IN DUAL-PORT MODE, PORT "A" MODULE WILL TEST THE LOW HALF OF THE DISK (CYLINDERS 0 TO 407 FOR RP06, CYLINDERS 0 TO 205 FOR RP04/5), PORT "B" WILL EXERISE THE HIGH HALF (CYLINDERS 206 TO 410 FOR RP04/5, CYLINDERS 408 TO 814 FOR RP06). IN SINGLE-PORT MODE, THE MODULE WILL ACCESS THE ENTIRE DISK SURFACE (CYLINDERS 0 TO 410 FOR RP04/5, CYLINDERS 0 TO 814 FOR RP06).
- B. WRITE-CHECK DATA JUST WRITTEN.
- C. READ 256 WORDS (1 SECTOR) INTO MODULE READ BUFFER.
- D. DO IN-CORE COMPARE OF READ BUFFER WITH FIRST 256 WORDS OF WRITE BUFFER.
- E. RELEASE THE DRIVE FOR OTHER PORT TO TEST.

4. EXECUTION TIME

ONE PASS OF RMD RUNNING ALONE ON A PDP-11/70 TAKES ABOUT A MINUTE.

5. CONFIGURATION REQUIREMENTS

DEFAULT PARAMETERS:
DVA=176700 VCI=254 BR1=5 DVC=1

REQUIRED PARAMETERS:
SRI MUST BE SET UP TO INDICATE WHICH PORT (A/B) THIS COPY OF THE MODULE IS TO TEST, IF ANY DRIVES ARE DUAL-PORTED. ALSO, BIT 7 MUST BE SET IF THE CONTROLLER IS AN RH70, AND CLEARED IF IT IS NOT AN RH70.

6. DEVICE/OPTION SETUP

MAKE SURE ALL DRIVES ARE POWERED UP, WRITE ENABLED, AND READY.
FOR DUAL-PORTED DRIVES: MAKE SURE CONTROLLER SELECT SWITCH IS IN A/B POSITION. IF DRIVE IS NOT CYCLED UP WITH SWITCH IN CORRECT POSITION, PLACE SWITCH CORRECTLY, AND DISABLE THEN RE-ENABLE DRIVE USING DRIVE DISABLE SWITCH.

7. OPERATOR OPTIONS

SRI

- HIT 2
CLEAR(0):
TYPE OUT DATA LATE ERRORS (HARD ERROR) AND COUNT THEM INTERNALLY.
SET(1):
COUNT DATA LATE ERRORS INTERNALLY, BUT DO NOT TYPE OUT.
- HIT 4
CLEAR(0):
THIS COPY OF RMD TESTS A-PORT ON ANY DUAL-PORTED DRIVES.
SET(1):
THIS COPY OF RMD TESTS B-PORT ON ANY DUAL-PORTED DRIVES.
- HIT 5
CLEAR(0):
TEST DISKS WITH RANDOM SEEKS.
SET(1):
DISABLE RANDOM SEEKS. INCREMENT SECTOR BY ONE EACH CYCLE.
- BIT 6
CLEAR(0):
DO NOT TYPE OUT HADSPOTS.
SET(1):
ALWAYS TYPE OUT ANY HADSPOT NOT ALREADY RECORDED ON BADSPOT TABLE.

HIT 7
CLPAR(0):
CONTROLLER IS RH11.
SET(1):
CONTROLLER IS RH70 (HAS RMBAE AND RHCS3)

8. NON-STANDARD PRINTOUTS

A. MOST PRINTOUTS HAVE THE STANDARD FORMATS.

B. ERROR MESSAGE DUMP RH11 REGISTERS IN THE FOLLOWING ORDER:

RHCS1	RHWC	RHBA	RPDA	RHCS2	KPDS	RPER1	RPAS	
RPLA	RHDB	KPMK	RPDI	KPSN	KPWF	KPDC	RPCC	
RPER2	RPER3	RPEC1	RPEC2					

; REVISION DATE: 2-APR-80 ;WMRMP
;
; THIS MODULE HAS BEEN MODIFIED TO SUPPORT NOT ;WMRMP
; JUST THE RP04/5/6 BUT ALSO THE RM02/3/5 SINGLE AND ;WMRMP ;RM05
; DUAL PORT. THE MODIFICATIONS ARE MARKED BY THE ;WMRMP
; REFERENCE 'WMRMP', RM05, AND RM05. ;WMRMP
;
; THE ORDER OF THE REGISTERS WILL VARY SLIGHTLY ;WMRMP
; BETWEEN AN RM AND AN RP. THE DIFFERENCES ARE AS ;WMRMP
; FOLLOWS: ;WMRMP

	FOR RP	ADDRESS	FOR RM	
	-----	-----	-----	
	RPCC	776736	RMHR	
	RPER2	776740	RMER2	
	RPER3	776742	RMER2	

C. THE BAD SECTOR MESSAGE (WHEN ENABLED) LOOKS LIKE:
'DRIVE X: HEADSPOT AT (OCTAL) CYL: XX, TRK: XX, SEC: XX'
WHERE 'X' IS AN OCTAL DIGIT. NO ERROR IS ASSOCIATED WITH THIS MESSAGE.

```
*****
*
* EDIT: BY: DATE: REASON:
*
* H.E.F. - 1-JAN-82 - FIX PROBLEMS LISTED IN AIDS REPORT
* - # CC0001217 ( 1. RM AS LOAD MEDIA?
* - 2. DUAL PORT / WRITE CHECK RETRIES
* - EXCEEDED; 3. SYSTEM ERRORS ON RH11
* - CONTROLLER ON AN 11/70; 4. INCORRECT
* - HANDLING OF BAD SPOTS ON LAST SECTOR.
* - 5. DUAL-PORT MAX ERRORS ).
*
* H.E.F. - 1-JAN-82 - ADD SUPPORT FOR RM05. THIS WAS ADDED
* - EVEN THOUGH AN RM05 MODULE (RMCA)
* - EXISTS BECAUSE SOMETIMES WE HAVE
* - RP04/5/6 AND RM05'S ON THE SAME
* - CONTROLLER.
*
* H.E.F. - 1-JAN-82 - AUTO-HANDLING OF BAD SECTOR ERRORS *
```

* - ON RM02/3/5. *

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.SRTTL MODULE HEADER BLOCK ;FDH001
000000' IUMGDY <RMDR >,176700,254,5,0,0,1500,104,RBUF,256,,256. ;MMRMRP ;RMDb5
000000' RMDULE 150000,RMDb ,176700,254,5,0,0,1500,104,RBUF,256,,256.
;TITLE RMDb DEC/X11 SYSTEM EXERCISER MODULE
;UDXCOM VERSION 6.3 14-AUG-81
;LIST FIN
*****
000000' HEGIN:
000000' 046522 041104 040 MODNAM: .ASCII /RMDR / ;MODULE NAME.
000005' 000 XFLAG: .BYTE OPEN ;USED TO KEEP TRACK OF WBUF USAGE
000006' 176700 ADDP: 176700+0 ;1ST DEVICE ADDR.
000010' 000254 VECTOR: 254+0 ;1ST DEVICE VECTOR.
000012' 240 BR1: .BYTE PR1Y5+0 ;1ST BR LEVEL.
000013' 000 BR2: .BYTE PR1Y0+0 ;2ND BR LEVEL.
000014' 000001 DVID1: 0+1 ;DEVICE INDICATOR 1.
000016' 000000 SP1: OPEN ;SWITCH REGISTER 1
000020' 000000 SP2: OPEN ;SWITCH REGISTER 2
000022' 000000 SP3: OPEN ;SWITCH REGISTER 3
000024' 000000 SP4: OPEN ;SWITCH REGISTER 4
*****
000026' 150000 STAT: 150000 ;STATUS WORD.
000030' 002374' INIT: START ;MODULE START ADDR.
000032' 000252' SPUINT: MODSP ;MODULE STACK POINTER.
000034' 000000 PASCNT: 0 ;PASS COUNTER.
000036' 001500 ICOUNT: 1500 ;# OF ITERATIONS PER PASS=1500
000040' 000000 SOFCNT: 0 ;LOC TO COUNT ITERATIONS
000042' 000000 HRDCNT: 0 ;LOC TO SAVE TOTAL SOFT ERRORS
000044' 000000 SOFPAS: 0 ;LOC TO SAVE TOTAL HARD ERRORS
000046' 000000 HRDPAS: 0 ;LOC TO SAVE SOFT ERRORS PER PASS
000050' 000000 SYSCNT: 0 ;LOC TO SAVE HARD ERRORS PER PASS
000052' 000000 RANNUM: 0 ;# OF SYS ERRORS ACCUMULATED
000054' 000000 CONFIG: ;HOLDS RANDOM # WHEN RAND MACRO IS CALLED
000056' 000000 RES1: 0 ;RESERVED FOR MONITOR USE
000060' 000000 RES2: 0 ;RESERVED FOR MONITOR USE
000062' 000000 SVR0: OPEN ;LOC TO SAVE R0.
000064' 000000 SVR1: OPEN ;LOC TO SAVE R1.
000066' 000000 SVR2: OPEN ;LOC TO SAVE R2.
000070' 000000 SVR3: OPEN ;LOC TO SAVE R3.
000072' 000000 SVR4: OPEN ;LOC TO SAVE R4.
000074' 000000 SVR5: OPEN ;LOC TO SAVE R5.
000076' 000000 SVR6: OPEN ;LOC TO SAVE R6.
000100' 000000 CSRA: OPEN ;ADDR OF CURRENT CSR.
000102' SRADR: ;ADDR OF GOOD DATA, DR
000102' 000000 ACSR: OPEN ;CONTENTS OF CSR.
000104' WASADR: ;ADDR OF BAD DATA, DR
000104' 000000 ASTAT: OPEN ;STATUS REG CONTENTS.
000106' ERRIYP: ;TYPE OF ERROR
000106' 000000 ASB: OPEN ;EXPECTED DATA.
000110' 000000 AAS: OPEN ;ACTUAL DATA.
000112' 002604' RSTRT: RSTRT ;RESTART ADDRESS AFTER END OF PASS
000114' 000000 WDIR: OPEN ;WORDS TO MEMORY PER ITERATION
000116' 000000 WDFR: OPEN ;WORDS FROM MEMORY PER ITERATION
000120' 000000 INTR: OPEN ;# OF INTERRUPTS PER ITERATION
000122' 000104 IONUM: 104 ;MODULE IDENTIFICATION NUMBER=104
000124' 001242' RBUFVA: RBUF ;READ BUFFER VIRTUAL ADDRESS

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000126' 000000 RBUFPA: OPEN ;READ BUFFER PHYSICAL ADDRESS
000130' 000000 RBUFEA: OPEN ;READ BUFFER EA BITS
000132' 000400 RBUFSZ: 256. ;SIZE OF THE READ BUFFER
000134' 000000 WBUFPA: OPEN ;WRITE BUFFER PHYSICAL ADDRESS
000136' 000000 WBUFEA: OPEN ;WRITE BUFFER EA BITS
000140' 000400 WBUFSZ: 256. ;WRITE BUFFER SIZE REQUESTED
000142' 000000 WBUFSZ: OPEN ;WRITE BUFFER SIZE AVAILABLE
000144' 000000 CDRECT: OPEN ;C/DATA/DATCK ERROR COUNT
000146' 000000 CDWDCT: OPEN ;C/DATA/DATCK WORD COUNT
000150' 000000 FREE: OPEN ;RESERVED FOR FUTURE USE
000040' .KEPT SPSIZ ;MODULE STACK STARTS HERE.
;LIST
;WORD 0
;LIST
;ENDR
MODSP:
*****
;
;
;MMRMRP
;MMRMRP

```

304									
305	000252'								:DRB001
306									:DRB001
307									:DRB001
308									:DRB001
309	000252'	177777							:DRB001
310	000254'	177777							:DRB001
311	000256'	177777							:DRB001
312	000260'	177777							:DRB001
313	000262'	177777							:DRB001
314	000264'	177777							:DRB001
315	000266'	177777							:DRB001
316	000270'	177777							:DRB001
317	000272'	177777							:DRB001
318	000274'	177777							:DRB001
319	000276'	177777							:DRB001
320	000300'	177777							:DRB001
321	000302'	177777							:DRB001
322	000304'	177777							:DRB001
323	000306'	177777							:DRB001
324	000310'	177777							:DRB001
325	000312'	177777							:DRB001
326	000314'	177777							:DRB001
327	000316'	177777							:DRB001
328	000320'	177777							:DRB001
329	000322'	177777							:DRB001
330	000324'	177777							:DRB001
331	000326'	177777							:DRB001
332	000330'	177777							:DRB001
333	000332'	177777							:DRB001
334	000334'	177777							:DRB001
335	000336'	177777							:DRB001
336	000340'	177777							:DRB001
337	000342'	177777							:DRB001
338	000344'	177777							:DRB001
339	000346'	177777							:DRB001
340	000350'	177777							:DRB001
341	000352'	177777							:DRB001
342	000354'	177777							:DRB001
343	000356'	177777							:DRB001
344	000360'	177777							:DRB001
345	000362'	177777							:DRB001
346	000364'	177777							:DRB001
347	000366'	177777							:DRB001
348	000370'	177777							:DRB001
349	000372'	177777							:DRB001
350	000374'	177777							:DRB001
351	000376'	177777							:DRB001
352	000400'	177777							:DRB001
353	000402'	177777							:DRB001
354	000404'	177777							:DRB001
355	000406'	177777							:DRB001
356	000410'	177777							:DRB001
357	000412'	177777							:DRB001
358	000414'	177777							:DRB001
359	000416'	177777							:DRB001

360	000420'	177777							:DRB001
361	000422'	177777							:DRB001
362	000424'	177777							:DRB001
363	000426'	177777							:DRB001
364	000430'	177777							:DRB001
365	000432'	177777							:DRB001
366	000434'	177777							:DRB001
367	000436'	177777							:DRB001
368	000440'	177777							:DRB001
369	000442'	177777							:DRB001
370	000444'	177777							:DRB001
371	000446'	177777							:DRB001
372	000450'	177777							:DRB001
373	000452'	177777							:DRB001
374	000454'	177777							:DRB001
375	000456'	177777							:DRB001
376	000460'	177777							:DRB001
377	000462'	177777							:DRB001
378	000464'	177777							:DRB001
379	000466'	177777							:DRB001
380	000470'	177777							:DRB001
381	000472'	177777							:DRB001
382	000474'	177777							:DRB001
383	000476'	177777							:DRB001
384	000500'	177777							:DRB001
385	000502'	177777							:DRB001
386	000504'	177777							:DRB001
387	000506'	177777							:DRB001
388	000510'	177777							:DRB001
389	000512'	177777							:DRB001
390	000514'	177777							:DRB001
391	000516'	177777							:DRB001
392	000520'	177777							:DRB001
393	000522'	177777							:DRB001
394	000524'	177777							:DRB001
395	000526'	177777							:DRB001
396	000530'	177777							:DRB001
397	000532'	177777							:DRB001
398	000534'	177777							:DRB001
399	000536'	177777							:DRB001
400	000540'	177777							:DRB001
401	000542'	177777							:DRB001
402	000544'	177777							:DRB001
403	000546'	177777							:DRB001
404	000550'	177777							:DRB001
405	000552'	177777							:DRB001
406	000554'	177777							:DRB001
407	000556'	177777							:DRB001
408	000560'	177777							:DRB001
409	000562'	177777							:DRB001
410	000564'	177777							:DRB001
411	000566'	177777							:DRB001
412	000570'	177777							:DRB001
413	000572'	177777							:DRB001
414	000574'	177777							:DRB001
415	000576'	177777							:DRB001

416	000600'	177777	177777	;TRACK/SECTOR ADDRESS	;DRB001
417	000602'	177777	177777	;CYLINDER ADDRESS	;DRB001
418	000604'	177777	177777	;TRACK/SECTOR ADDRESS	;DRB001
419	000606'	177777	177777	;CYLINDER ADDRESS	;DRB001
420	000610'	177777	177777	;TRACK/SECTOR ADDRESS	;DRB001
421	000612'	177777	177777	;CYLINDER ADDRESS	;DRB001
422	000614'	177777	177777	;TRACK/SECTOR ADDRESS	;DRB001
423	000616'	177777	177777	;CYLINDER ADDRESS	;DRB001
424	000620'	177777	177777	;TRACK/SECTOR ADDRESS	;DRB001
425	000622'	177777	177777	;CYLINDER ADDRESS	;DRB001
426	000624'	177777	177777	;TRACK/SECTOR ADDRESS	;DRB001
427	000626'	177777	177777	;CYLINDER ADDRESS	;DRB001
428	000630'	177777	177777	;TRACK/SECTOR ADDRESS	;DRB001
429	000632'	177777	177777	;CYLINDER ADDRESS	;DRB001
430	000634'	177777	177777	;TRACK/SECTOR ADDRESS	;DRB001
431	000636'	177777	177777	;CYLINDER ADDRESS	;DRB001
432	000640'	177777	177777	;TRACK/SECTOR ADDRESS	;DRB001
433	000642'	177777	177777	;CYLINDER ADDRESS	;DRB001
434	000644'	177777	177777	;TRACK/SECTOR ADDRESS	;DRB001
435	000646'	177777	177777	;CYLINDER ADDRESS	;DRB001
436	000650'	177777	177777	;TRACK/SECTOR ADDRESS	;DRB001
437	000652'	177777	177777	;CYLINDER ADDRESS	;DRB001
438	000654'	177777	177777	;TRACK/SECTOR ADDRESS	;DRB001
439	000656'	177777	177777	;CYLINDER ADDRESS	;DRB001
440	000660'	177777	177777	;TRACK/SECTOR ADDRESS	;DRB001
441	000662'	177777	177777	;CYLINDER ADDRESS	;DRB001
442	000664'	177777	177777	;TRACK/SECTOR ADDRESS	;DRB001
443	000666'	177777	177777	;CYLINDER ADDRESS	;DRB001
444	000670'	177777	177777	;TRACK/SECTOR ADDRESS	;DRB001
445	000672'	177777	177777	;CYLINDER ADDRESS	;DRB001
446	000674'	177777	177777	;TRACK/SECTOR ADDRESS	;DRB001
447	000676'	177777	177777	;CYLINDER ADDRESS	;DRB001
448	000700'	177777	177777	;TRACK/SECTOR ADDRESS	;DRB001
449	000702'	177777	177777	;CYLINDER ADDRESS	;DRB001
450	000704'	177777	177777	;TRACK/SECTOR ADDRESS	;DRB001
451	000706'	177777	177777	;CYLINDER ADDRESS	;DRB001
452	000710'	177777	177777	;TRACK/SECTOR ADDRESS	;DRB001
453	000712'	177777	177777	;CYLINDER ADDRESS	;DRB001
454	000714'	177777	177777	;TRACK/SECTOR ADDRESS	;DRB001
455	000716'	177777	177777	;CYLINDER ADDRESS	;DRB001
456	000720'	177777	177777	;TRACK/SECTOR ADDRESS	;DRB001
457	000722'	177777	177777	;CYLINDER ADDRESS	;DRB001
458	000724'	177777	177777	;TRACK/SECTOR ADDRESS	;DRB001
459	000726'	177777	177777	;CYLINDER ADDRESS	;DRB001
460	000730'	177777	177777	;TRACK/SECTOR ADDRESS	;DRB001
461	000732'	177777	177777	;CYLINDER ADDRESS	;DRB001
462	000734'	177777	177777	;TRACK/SECTOR ADDRESS	;DRB001
463	000736'	177777	177777	;CYLINDER ADDRESS	;DRB001
464	000740'	177777	177777	;TRACK/SECTOR ADDRESS	;DRB001
465	000742'	177777	177777	;CYLINDER ADDRESS	;DRB001
466	000744'	177777	177777	;TRACK/SECTOR ADDRESS	;DRB001
467	000746'	177777	177777	;CYLINDER ADDRESS	;DRB001
468	000750'	177777	177777	;TRACK/SECTOR ADDRESS	;DRB001
469	000752'	177777	177777	;CYLINDER ADDRESS	;DRB001
470	000754'	177777	177777	;TRACK/SECTOR ADDRESS	;DRB001
471	000756'	177777	177777	;CYLINDER ADDRESS	;DRB001

472	000760'	177777	177777	;TRACK/SECTOR ADDRESS	;DRB001
473	000762'	177777	177777	;CYLINDER ADDRESS	;DRB001
474	000764'	177777	177777	;TRACK/SECTOR ADDRESS	;DRB001
475	000766'	177777	177777	;CYLINDER ADDRESS	;DRB001
476	000770'	177777	177777	;TRACK/SECTOR ADDRESS	;DRB001
477	000772'	177777	177777	;CYLINDER ADDRESS	;DRB001
478	000774'	177777	177777	;TRACK/SECTOR ADDRESS	;DRB001
479	000776'	177777	177777	;CYLINDER ADDRESS	;DRB001
480	001000'	177777	177777	;TRACK/SECTOR ADDRESS	;DRB001
481	001002'	177777	177777	;CYLINDER ADDRESS	;DRB001
482	001004'	177777	177777	;TRACK/SECTOR ADDRESS	;DRB001
483	001006'	177777	177777	;CYLINDER ADDRESS	;DRB001
484	001010'	177777	177777	;TRACK/SECTOR ADDRESS	;DRB001
485	001012'	177777	177777	;CYLINDER ADDRESS	;DRB001
486	001014'	177777	177777	;TRACK/SECTOR ADDRESS	;DRB001
487	001016'	177777	177777	;CYLINDER ADDRESS	;DRB001
488	001020'	177777	177777	;TRACK/SECTOR ADDRESS	;DRB001
489	001022'	177777	177777	;CYLINDER ADDRESS	;DRB001
490	001024'	177777	177777	;TRACK/SECTOR ADDRESS	;DRB001
491	001026'	177777	177777	;CYLINDER ADDRESS	;DRB001
492	001030'	177777	177777	;TRACK/SECTOR ADDRESS	;DRB001
493	001032'	177777	177777	;CYLINDER ADDRESS	;DRB001
494	001034'	177777	177777	;TRACK/SECTOR ADDRESS	;DRB001
495	001036'	177777	177777	;CYLINDER ADDRESS	;DRB001
496	001040'	177777	177777	;TRACK/SECTOR ADDRESS	;DRB001
497	001042'	177777	177777	;CYLINDER ADDRESS	;DRB001
498	001044'	177777	177777	;TRACK/SECTOR ADDRESS	;DRB001
499	001046'	177777	177777	;CYLINDER ADDRESS	;DRB001
500	001050'	177777	177777	;TRACK/SECTOR ADDRESS	;DRB001
501	001052'			;CYLINDER ADDRESS	;DRB001
502					;DRB001
503	000004	DLATE	= HIT2		;DRB001
504	000020	BPORT	= B14		;DRB001
505	000040	NORDAD	= B15		;DRB001
506	000100	BDSPI	= B16		;DRB001
507	000260	RH70	= B17		;DRB001
508	137400	HRDCS2	= 137400		;DRB001
509	177777	HRDER1	= 177777		;DRB001
510	167777	HRDER2	= 167777		;DRB001
511	140153	HRDEF3	= 140153		;DRB001
512					;DRB001
513	001052'	000000	000000	S: 0,0,0,0,0,0,0,0,0,0,0,0,0,0,0,0,0,0,0,0	;DRB001
514	001060'	000000	000000		;DRB001
515	001066'	000000	000000		;DRB001
516	001074'	000000	000000		;DRB001
517	001102'	000000	000000		;DRB001
518	001110'	000000	000000		;DRB001
519	001116'	000000	000000		;DRB001
520	001124'	000000			;DRB001
521					;DRB001
522					;DRB001
523	001126'	000000		;DO NOT CHANGE THE ORDER OF THE NEXT 4 LOCATIONS	;DRB001
524	001130'	000000		;NEEDED FOR MAP22 ROUTINE	;DRB001
525	001132'	000000		PA18: 0	;DRB001
526	001134'	000000		XMEM: 0	;DRB001
527				PA22: 0	;DRB001
				EA22: 0	;DRB001


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528 001136' 000252'      NXTBBK: BADSP1      ; NEXT OPEN BADSPOT IN TABLE      ;DRR001
529                                ;                                ;DRR001
530 001140' 000000      CYLLIM: 0            ; UPPER DISK LIMIT (DEPENDS ON DISK TYPE) ;DRR001
531                                ;                                ;DRR001
532                                ; THE FOLLOWING ARE SOME ADDITIONAL LOCATIONS ;DRR001
533                                ; THAT WERE ADDED TO ALLOW THIS MODULE TO ;DRR001
534                                ; SUPPORT THE RP04/5/6 AND THE RM02/3 ;DRR001
535                                ; ;DRR001
536 001142' 000000      TYPFLG: 0            ; DRIVE TYPE FLAG. RP04/5/6 = BIT 0=1 ;RMB05
537                                ; RM02/3 = BIT 1=1 ;RMB05
538                                ; RM05 = BIT 2=1 ;RMB05
539 001144' 000000      TRKLM1: 0           ; UPPER TRACK LIMIT (DEPENDS ON DISK TYPE) ;DRR001
540 001146' 000000      TRKLM2: 0           ; UPPER TRACK LIMIT +1 (DEPENDS ON DISK TYPE) ;DRR001
541 001150' 000000      SECLM1: 0           ; UPPER SECTOR LIMIT (DEPENDS ON DISK TYPE) ;DRR001
542 001152' 000000      SECLM2: 0           ; UPPER SECTOR LIMIT +1 (DEPENDS ON DISK TYPE) ;DRR001
543 001154' 000000      CYLMSK: 0           ; MASK USED TO DETERMINE DISK MAX CYL ADDRESS ;DRR001
544 001156' 000000      TRKMSK: 0           ; MASK USED TO DETERMINE DISK MAX TRK ADDRESS ;DRR001
545 001160' 000000      SECMSK: 0           ; MASK USED TO DETERMINE DISK MAX SEC ADDRESS ;DRR001
546                                ; ;DRR001
547                                ; END OF EXTRA LOCATIONS TO HANDLE RP'S AND RM'S ;DRR001
548                                ; ;DRR001
549 001162' 000000      CYLNDR: 0           ; ABSOLUTE CYLINDER ADDRESS OF CURRENT OP. ;DRR001
550 001164' 000000      CYL: 0             ; RELATIVE DISK ADDRESS ;DRR001
551 001166' 000000      DSKADR: 0           ; FUP REFERENCING TRACK/SECTOR AS WORD ;DRR001
552 001168' 000000      SECTOR: .BYTE 0    ; SECTOR ADDRESS OF CURRENT OPERATION ;DRR001
553 001167' 000000      TRACK: .BYTE 0     ; TRACK ADDRESS OF CURRENT OPERATION ;DRR001
554                                ; ;DRR001
555 001170' 000000      BADCYL: 0           ; FROM DRIVE LOCATION REGISTERS, ON FINDING BADSPOT ;DRR001
556 001172' 000000      BADSEC: 0           ; FROM RPA ON BADSPOT OCCURANCE ;DRR001
557 001174' 000000      BADTRK: 0           ; FROM RPA+1 ON BADSPOT ;DRR001
558                                ; ;DRR001
559 001176' 000000      LASCYL: 0           ; LAST CYLINDER WHICH CAN BE WRITTEN ;DRR001
560 001200' 000000      LASTTRK: 0         ; LAST TRACK WHICH CAN BE WRITTEN ;DRR001
561 001202' 000000      LASSEC: 0         ; LAST SECTOR WHICH CAN BE WRITTEN ;DRR001
562                                ; ;DRR001
563 001204' 000000      SIZTRK: 0           ; NUMBER OF TRACKS IN MBPFSZ ;DRR001
564 001206' 000000      SIZSEC: 0         ; REMAINDER OF MBPFSZ, IN SECTORS ;DRR001
565 001210' 000000      FLAG: 0           ; ;DRR001
566 001212' 000000      DEVICE: 0         ; ;DRR001
567 001214' 000000      DRIVE: 0          ; ;DRR001
568 001216' 000000      UNITNO: 0         ; ;DRR001
569                                ; ;DRR001
570 001220' 000000      DLTCNT: 0         ; ;DRR001
571 001222' 000000      FUNC: 0           ; ;DRR001
572 001224' 000000      LIMR: 0           ; ;DRR001
573 001226' 000000      ZERO: 0          ; ;DRR001
574 001230' 000000      FERADR: 0         ; ;DRR001
575 001232' 000000      CLK: 0           ; ;DRR001
576 001234' 000000      TRY: 0           ; ;DRR001
577 001236' 000000      STORE: 0         ; ;DRR001
578 001240' 000000      HRDRER: 0         ; ;DRR001
579 001242' 000400      RBUF: .BLK* 256.  ; ;DRR001
580                                ; ;DRR001
581 002242' 000000      TABLE: 0         ; ;DRR001
582 002242' 001052'      S                ; ;DRR001
583 002244' 001054'      S+2             ; ;DRR001

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584 002246' 001056'      S+4
585 002250' 001060'      S+6
586 002252' 001062'      S+10
587 002254' 001064'      S+12
588 002256' 001066'      S+14
589 002260' 001070'      S+16
590 002262' 001072'      S+20
591 002264' 001074'      S+22
592 002266' 001076'      S+24
593 002270' 001100'      S+26
594 002272' 001102'      S+30
595 002274' 001104'      S+32
596 002276' 001106'      S+34
597 002300' 001110'      S+36
598 002302' 001112'      S+40
599 002304' 001114'      S+42
600 002306' 001116'      S+44
601 002310' 001120'      S+46
602 002312' 001122'      S+50
603 002314' 001124'      S+52
604                                ; ;DRR001
605 002316' 177777      177777      ;**=1 ;DRR001
606 002320' 000000      RHCS1: 0
607 002322' 000000      RHWC: 0
608 002324' 000000      RHBA: 0
609 002326' 000000      RPA: 0
610 002330' 000000      RHCS2: 0
611 002332' 000000      RPDS: 0
612 002334' 000000      RPER1: 0
613 002336' 000000      RPAS: 0
614 002340' 000000      RPLA: 0
615 002342' 000000      RHDB: 0
616 002344' 000000      RPMN: 0
617 002346' 000000      RFDI: 0
618 002350' 000000      RPSN: 0
619 002352' 000000      RPOF: 0
620 002354' 000000      RPDC: 0
621 002356' 000000      RPCC: 0
622 002360' 000000      RPER2: 0
623 002362' 000000      RPER3: 0
624 002364' 000000      RPEC1: 0
625 002366' 000000      RPEC2: 0
626 002370' 000000      RHBAE: 0
627 002372' 000000      RHCS3: 0
628                                ; ;DRR001
629                                ;**=4 ;DRR001
630 002374' 012767 000400 175512      .SBTTL MODULE CDFP
631 002402' 012767 000400 175506      START: MUV #256.,WDTO ;256 WORDS TO MEM/ITERATION
632 002410' 012767 000004 175502      MUV #256.,WDFP ;256 WORDS FROM MEM/ITERATION
633 002416' 012701 000252'      MUV #4,INTH ;4 INTERRUPTS/ITERATION
634 002422' 022721 177777      MUV #BADSP,T,K1 ;BAD SPOT TABLE
635 002426' 001375      4S: CMP #-(R1)+ ;END YET ?
636 002430' 005741      BNE 4S ;REV D
637 002432' 010167 176500      TST -(R1) ;REV D
638 002436' 016767 175352 176546      MUV R1,NXTBBK ;RESET POINTER ;REV D
639 002444' 123727 000041 000011      MUV DVIDI,DEVICE ;GET DRIVES TO TEST
        CMPB #*41,R1 ;IF RP IS LOAD MEDIUM THEN

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640 002452' 001404  
641 002454' 123727 000041 000016 14S: HEQ 13S ;BEGIN ;RMD8  
642 002462' 001024 CMFB #*1,#16 ; SEE IF RM IS LOAD MEDIUM ;RMD8  
643 002464' 113700 000040 13S: BNE JS ;RMD8  
644 002470' 012701 000001 MOV #*40,R0 ; GET LOAD-DEVICE NUMBER  
645 002474' 105700 1S: MOV #1,R1 ; INITIALIZE DEVICE MASK  
646 002476' 001403 TSTB R0 ; WHILE NOT POINTING AT LOAD-DEVICE DO  
647 002500' 006301 HEQ 2S ; BEGIN  
648 002502' 105300 ASL R1 ; SHIFT MASK TO NEXT DEVICE  
649 002504' 000773 BR R0 ; KEEP TRACK OF SHIFTING  
650 002506' 130167 176500 2S: BIFB R1,DEVICE ; IF LOAD DEVICE IS SELECTED THEN  
651 002512' 001410 HEQ 3S ; BEGIN  
652 002514' 113767 000040 176474 MOVH #*40,UNITNO ; MOVE LOAD-DEVICE NUMBER TO DRIVE  
653 002522' 004767 002726 JSR PC,DROP ; DROP THE LOAD-DEVICE  
654 ; END  
655 002526' 104403 000000' 012504' MSGNS,BEGIN,LUMED ;ASCII MESSAGE CALL WITH COMMON HEADER  
656 002534' 3S: ;END  
657 002534' 005067 176424 CLR CIL ;START AT CYLINDER 0  
658 002540' 012767 000377 176420 MOV #377,DSKADR ;START AT TRACK 0, SECTOR 0 ;RMD8  
659 ;(* INCREMENT BEFORE TEST *)  
660 002546' 004767 005210 JSR PC,SETUP ;CLEAR THE RM  
661 002552' 004767 005112 JSR PC,RESET ;RH70 ?  
662 002556' 032767 000200 175232 BIT #RH70,SK1 ;RMD8  
663 002564' 001604 BNE SS ; NO DON'T PRINT BAE & CSJ ;RMD8  
664 002566' 012767 177777 177516 MOV #*1,TABLE+50 ; SHORTEN TEMP STORAGE ;RMD8  
665 002574' 000403 BR R0 ; GOTO RSTART ;RMD8  
666 002576' 012767 001122' 177506 5S: MOV #S+50,TABLE+50 ; SET UP TO POINT TO DATA IF REG EXISTS ;RMD8  
667 002604' 104415 000000' 000124' GEIPAS,BEGIN, RBUFEA ;GET PHYSICAL ADDRESS FROM 16-BIT RBUFEA  
668 ;  
669 002612' 012767 177777 176370 LOOP1: MOV #*1,UNITNO ;PRE-SET UNIT NUMBER  
670 002620' 104414 000000' GNBUFFS, BEGIN ;GET WRITE BUFFER INFORMATION  
671 ;***-1 ;RMD8  
672 ;  
673 ;  
674 002624' LOOP2: ;RMD8  
675 002624' 004767 000756 3S: JSR PC,PICKDR ;GO PICK A DRIVE  
676 002630' 103407 BCS 1S ;RETURNS HERE IF ALL DRIVES DONE ;RMD8  
677 002632' 004767 001574 JSR PC,RTLIM ;GET DISK LIMITS FOR THIS TYPE OF DISK ;RMD8  
678 002636' 005067 176372 CLR TR1 ;ELSE CLR RE TRY COUNT ;RMD8  
679 002642' 004767 000022 JSR PC,CYCLE ;GO DO A CYCLE ON THIS DRIVE  
680 002646' 000766 BR LOOP2 ;DO IT TO NEXT DRIVE  
681 ;  
682 002650' 005767 176336 1S: TST DEVICE ;ANYBODY LEFT TO CHECK?  
683 002654' 001002 BNE JS ;RMD8  
684 002656' 104410 000000' ENDS,BEGIN ;RMD8  
685 002662' 2S: ;  
686 002662' 104413 000000' ENDIS,BEGIN ;SIGNAL END OF ITERATION.  
687 ;MONITOR SHALL TEST END OF PASS  
688 002666' 000751 BR LOOP1 ;RMD8  
689 ;  
690 002670' CYCLE: ;RMD8  
691 ;  
692 002670' 004767 001004 JSR PC,PICKAK ;SELECT A SECTOR TO TEST ***-4 ;RMD8  
693 002674' 004567 000026 JSR R5,WRITE ;GO WRITE A BLOCK ;RMD8  
694 002700' 004567 000116 JSR R5,WRITECK ;GO DO WRITE CHECK  
695 002704' 004567 000206 JSR R5,READ ;GO READ A BLOCK
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696 002710' 104412 000000' 000126' CDATA,BEGIN,RBUFEA ; REQUEST FOR MONITOR TO CHECK DATA  
697 002716' 002720' .+2 ; IF ERROR, CONTINUE  
698 002720' 004767 000266 JSR PC,RELEASE ;RELEASE THE DRIVE ;RMD8  
699 002724' 000207 RTS PC ;END CYCLE ;RMD8  
700 ;  
701 ;  
702 ;  
703 ; MACRO LINEUP EA BITS ; ***-6b ;RMD8  
704 ; LINEUP EA BITS FOR RHCS1  
705 ; .NLIST  
706 ; MOV EA BITS,R0 ; GET EXTENDED MEMORY BITS  
707 ; ASL R0 ; SHIFT 4 PLACES TO THE LEFT  
708 ; ASL R0 ; TO LINE UP WITH RHCS1  
709 ; ASL R0 ;  
710 ; MOV R0,XMEM ; SAVE THE SHIFTED BITS  
711 ; .LIST  
712 ; ENDM LINEUP  
713 ;  
714 ;  
715 002726' 012767 000161 176266 WRITE: MOV #161,FUNC ; LOAD WRITE FUNCTION  
716 002734' 012767 002726' 176266 MOV #WRITE,FERADR ;SAVE WHERE WE WERE  
717 002742' 016746 175174 MOV #BUFSZ,-(SP) ;GET WRITE SIZE  
718 002746' 005416 NEG (SP) ;NEGATE IT  
719 002750' 012677 177346 MOV (SP)+,RRHC ; LOAD WORD COUNT  
720 002754' 016777 175154 177342 MOV #BUFEA,RRHBA ; LOAD BUFFER ADDRESS  
721 002762' 016777 176200 177336 MOV DSKADR,RRPDA ; LOAD DISK ADDRESS  
722 002770' 016777 176166 177356 MOV CYLADR,RRPDC ; LOAD CYLINDER ADDRESS ;RMD8  
723 ;  
724 003016' 000167 000302 LINEUP #BUFEA ; LINE UP EA BITS FOR RHCS1  
725 003022' 012767 000171 GO ; CONTINUE  
726 003030' 012767 003022' 176172 WRITE: MOV #151,FUNC ; LOAD WRITE-CHECK FUNCTION  
727 003036' 016746 175100 MOV #WRITE,FERADR ;SAVE WHERE WE WERE  
728 003042' 005416 MOV #BUFSZ,-(SP) ;GET WRITE SIZE  
729 003044' 012677 177252 NEG (SP) ;NEGATE IT  
730 003050' 016777 175060 177246 MOV (SP)+,RRHC ; LOAD WORD COUNT  
731 003056' 016777 176104 177242 MOV #BUFEA,RRHBA ; LOAD BUFFER ADDRESS  
732 003064' 016777 176072 177262 MOV DSKADR,RRPDA ; LOAD DISK ADDRESS ;RMD8  
733 ;  
734 003112' 000167 000206 LINEUP #BUFEA ; LINE UP EA BITS FOR RHCS1  
735 003116' 012767 000171 GO ; CONTINUE  
736 003124' 012767 003116' 176076 READ: MOV #171,FUNC ; LOAD READ FUNCTION  
737 003132' 016746 174774 MOV #READ,FERADR ;SAVE WHERE WE WERE  
738 003136' 005416 MOV #BUFSZ,-(SP) ;GET READ SIZE  
739 003140' 012677 177156 NEG (SP) ;NEGATE IT  
740 003144' 016777 174756 177152 MOV (SP)+,RRHC ; LOAD WORD COUNT  
741 003152' 016777 176100 177146 MOV #BUFEA,RRHBA ; LOAD BUFFER ADDRESS  
742 003160' 016777 175776 177166 MOV DSKADR,RRPDA ; LOAD DISK ADDRESS ;RMD8  
743 ;  
744 003206' 000167 000112 LINEUP #BUFEA ; LINE UP EA BITS FOR RHCS1  
745 003212' 016777 176000 177110 JMP GO ; CONTINUE  
746 003220' 012777 000013 177072 RELEASE: MOV UNITNO,RRHCS2 ;SET UP UNIT TO RELEASE ;RMD8  
747 003226' 104407 000000' MOV #13,RRHCS1 ;EXECUTE RELEASE COMMAND ;RMD8  
748 003232' 104407 000000' BREAKS,BEGIN ;TEMPORARY RETURN TO MONITOR...  
749 003236' 000207 RTS PC ;THEN CONTINUE AT NEXT INSTRUCTION.  
750 ;IT'S RELEASED ;RMD8  
751 ;***-33 ;RMD8
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752 003240' 016777 175752 177062 CLEAR: MOV UNITNO,RRHCS2 ; LOAD UNIT ADDRESS
753 003246' 012777 000011 177044 MOV #11,RRHCS1 ; ISSUE A DRIVE CLEAR
754 003254' 000240 NOP ;WAIT
755 003256' 000240 NOP ;FOR DRIVE CLEAR TO FINISH
756 003260' 012777 000023 177032 MOV #23,RRHCS1 ;ISSUE A PACK ACK
757 003266' 105777 177026 1S: LSTH RRHCS1
758 003272' 100401 BR 1S ; NO, WAIT TILL DONE
759 003274' 000774 BR 1S ;CLEAR AS BIT
760 003276' 017746 177034 2S: MOV RRPAS, -(SP)
761 003302' 012677 177030 MOV (SP)+,RRPAS
762 003306' 012777 040000 177004 MOV #B1114,RRHCS1 ; CLEAR ANY CONTROLLER ERRORS
763 003314' 012777 010000 177030 MOV #BIT12,RRHCS1 ; SET BIT FOR 11 FURMAT
764 003322' 000205 RTS ; RETURN
765
766 003324' 016777 175666 176776 GO: MOV UNITNO,RRHCS2 ; LOAD UNIT SELECT
767 003332' 032767 001000 174516 BIT #ADDR22,PES1 ;22 BIT SUPPORT? ;DRH001
768 003340' 001434 BEQ 1S ;NO ;DRB001
769 003342' 017767 176756 175556 MOV RRHHA,PA16 ;GET 18 BIT ADDR
770 003350' 006267 175554 ASH XMEM ;SHIFT EA BITS TO POSITION 4,5
771 003354' 006267 175550 ASH XMEM
772 003360' 006267 175544 ASH XMEM
773 003364' 006267 175540 ASH XMEM
774 003370' 104416 000000' 001126' MAP22S, BEGIN,PA16 ; GET 22-BIT ADDR FROM 18-BIT ADDR
775 003376' 016777 175530 176720 MOV PA22,RRHHA ;LOAD BA REG
776 003404' 016777 175524 176756 MOV EA22,RRHFAE ;LOAD BAE REG
777 003412' 042767 000034 175514 BIC #34,EA22 ;CLEAR UNWANTED BITS
778 003420' 006367 175510 SWAB EA22 ;LOAD INTO BITS 8,9
779 003424' 016767 175504 175476 MOV EA22,XMEM ;LOAD XMEM TO SET INTO FUNCTION CODE
780 003432' 056767 175472 175562 1S: LLS XMEM,FUNC ; LOAD EXTENDED MEMORY BITS
781 003440' 016777 175556 176652 MOV FUNC,RRHCS1 ; EXECUTE THE FUNCTION
782 003446' 104400 000000' EXITS,BEGIN ;EXIT TO MONITOR. MODULE WAIT FOR INTERRUPT.
783 003452' 010046 NTRUP1: MOV R0, -(SP) ;SAVE R0
784 003454' 016700 175536 MOV UNITNO,R0
785 003460' 011600 003670' MOV BITTAB(R0),R0
786 003464' 017746 176646 MOV RRPAS, -(SP)
787 003470' 040016 BIC R0, (SP)
788 003472' 012677 176640 MOV (R0)+,RRPAS
789 003476' 012600 MOV (SP)+,R0
790
791 003500' 000004 000000' 003506' ;-----
792 ;PIRUS,BEGIN,1S ; QUEUE UP TO CONTINUE AT 1S AND RTI
793 ;-----
794 003506' 004767 003336 1S: JSR PC,ERRORS ;GO CHECK FOR ERRORS
795 003512' 103401 MCS ZS ;ERRORS DETECTED ;DRH001
796 003514' 000205 RTS R5 ;OTHERWISE, RETURN OK
797 003516'
798
799 003516' 105767 175516 2S: LSTH RRDEER ;DON'T RETRY ON HARD ERROR REV D
800 003522' 100422 BR 1S ;DRB001
801 003524' 005267 175504 INC TR1 ;COUNT AN ERROR
802 003530' 026727 175500 000003 CMP TR1,#3 ;TOO MANY FOR THIS CYCLE? ;DRB001
803 003536' 002007 AGE 5S ;IF IF SO
804 003540' 004567 003750 JSR R5,READY ; MAKE SURE WE HAVE THE DISK ;RMDB
805 003544' 103402 MCS 6S ;RMDB
806 003546' 004767 003544 JSR PC,NOTRDY ;RMDB
807 003552' 000177 175452 6S: JMP RRRPADR ;RE-EXECUTE THE DRIVER ROUTINE ;DRB001 ;RMDB
  
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808 003556' 104403 000000' 012216' 5S: ASGNS,BEGIN,EXCED
809 003564' 004767 003370 JSR PC,BAD ;UPDATE BAD BLOCK INFO.
810 003570' 012605 MOV (SP)+,R5 ;RESTORE R5
811 ;***-7 ;DRB001
812 003572' 004767 000192 3S: JSR PC,PICKRA ;TRY A DIFFERENT BLOCK
813 003576' 005367 175414 DEC UNITNO ;WANT TO RE-DO SAME DRIVE WERE ON
814 003602' 000167 177016 JMP LOOP2 ;GO DO IT
815 ;***-2 ;DRB001
816 ;
817 ;-----
818 ;
819 ;
820 ;***-17 ;DRB001
821 003606'
822 003606' 005267 175404 PICKRA: INC UNITNO ;POINT TO NEXT DRIVE
823 003612' 026727 175400 000010 CMP UNITNO,RR. ;DONE LOOKING?
824 003620' 001002 BNE ZS ;IF NO, ELSE
825 003622' 000261 BEC 5S ;SET CARRY FOR NO-MORE-DRIVES ;DRB001
826 003624' 000420 BR 5S ;EXIT ROUTINE ;DRB001
827 003626' 016700 175364 2S: MOV UNITNO,R0 ;USE AS AN INDEX
828 003632' 136067 003670' 175352 BITB BITTAB(R0),OVICE ;TEST THIS DEVICE?
829 003640' 001001 BNE 3S ;IF YES, ELSE
830 003642' 000761 BF 1S ;IF NO, ELSE
831 003644'
832 ;***-1 ;DRB001
833 003644' 004567 003644 JSR R5,READY ;SEE IF DRIVE IS READY
834 003650' 103402 MCS 4S ;IF IT WAS READY ;DRB001
835 003652' 004767 003440 JSR PC,NOIRDY ;ELSE GO CLEAR IT AND CHECK AGAIN
836 003656' 004767 000670 4S: JSR PC,GETDVT ;IS THIS LEGAL DRIVE TYPE? ;DRH001
837 003662' 103751 BCS 1S ;ILLEGAL TYPE: DROPPED. TRY NEXT ;DRB001
838 003664' 000241 CLC ;RETURN, NEXT DRIVE AVAILABLE ;DRB001
839 003666' 000207 5S: RTS PC ;GO HOME ;DRB001
840 003670' 001001 BITTAB: 1001
841 003672' 004004 4004
842 003674' 020020 20020
843 003676' 100100 100100
844
845
846 003700' PICKRA: ;***-11 ;DRB001
847 ;(* FUNCTION: SELECT NEXT DISK ADDRESS TO TEST. *) ;DRB001
848 ;(* AVOID WRITING OVER KNOWN BAD SPOTS OR DISK'S BAD *) ;DRB001
849 ;(* SPOT FILE (IF OPERATOR TELLS US IT'S THERE). CYL *) ;DRB001
850 ;(* IS "RELATIVE ADDRESS", MAX RANGE 0-410 (RP04/S) OR *) ;DRB001
851 ;(* 0-407 (RP06); THIS IS MOVED TO CYLNR AND INCR= *) ;DRB001
852 ;(* MENTED TO SECOND HALF IF THIS COPY OF RP0 IS IN *) ;DRB001
853 ;(* PORT H MODE. *) ;DRB001
854
855 003700' 032767 000040 174110 BII #NORAND,SRI ;IF SEQUENTIAL SEEKS THEN ;DRB001
856 003706' 001403 BEQ 1S ;BEGIN ;DRB001
857 003710' 105267 175252 INCH SECTUR ; SECTOR := SECTOR + 1 ;DRB001
858 003714' 000432 BR 2S ;END ;DRB001
859 003716' 1S: ;ELSE (* RANDOM SEEKS *) ;DRB001
860 ;BEGIN (* FUDGE 3 UN-RELATED *) ;DRB001
861 ; (* NUMBERS FROM 1 RAND CALL *) ;DRB001
862 003716' 104417 000000' RANDS,BEGIN
863 003722' 016700 174126 MOV RANNUM,R0 ; R0 := RANNUM ;DRB001
  
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864 003726' 046700 175222 BIC CYLMSK,R0 ; R0 := R0 MOD 1024. ;DRB001;WMRMRP
865 003732' 010067 175226 MOV R0,CYL ; CYL := R0 ;DRB001
866 003736' 016700 174112 MOV MAXNUM,R0 ; "RECHARGE" R0 ;DRB001
867 003742' 003000 SWAB R0 ; R0<0:8> := R0<8:8> ;DRB001
868 003744' 046700 175206 BIC TRKMSK,R0 ; R0 := R0 MOD 32. ;DRB001;WMRMRP
869 003750' 110067 175212 MOV R0,SECTOR ; SECTOR := R0 ;DRB001
870 003754' 016700 174074 MOV MAXNUM,R0 ; AND AGAIN ;DRB001
871 003760' 000241 CLC ; CARRY := 0 ;DRB001
872 003762' 006100 ROL R0 ; CARRY := R0<15:1> ;DRB001
873 003764' 006100 ROL R0 ; ;DRB001
874 003766' 006100 ROL R0 ; ;DRB001
875 003770' 006100 ROL R0 ; ;DRB001
876 003772' 046700 175162 BIC SECMSK,R0 ; R0 := (R0<0:13> * 8.) + R0<13:13> ;DRB001;WMRMRP
877 ; (* NOTE: THE FORMULAE USED ARE *) ;DRB001
878 ; (* ABSOLUTELY IRRELEVANT *) ;DRB001
879 003776' 110067 175165 MOV R0,TRACK ; TRACK := R0 ;DRB001
880 004002' ; END ;DRB001
881 004002' 126767 175160 175140 28: CMPB SECTOR,SECLIM ;IF SECTOR TOO GREAT THEN ;DRB001;WMRMRP
882 004010' 003412 BLE JS ;BEGIN ;DRB001
883 004012' 105267 175151 INCB TRACK ; TRACK := TRACK + 1 ;DRB001
884 004016' 016700 175144 MOV SECTOR,R0 ; R0 := SECTOR ;DRB001
885 004022' 016701 175124 MOV SECLIM,R1 ; R1 := # OF SECTORS IN TRACK ;DRB001;WMRMRP
886 004026' 004767 001370 JSR PC,MODULUS ; R0 := R0 MOD R1 ;DRB001
887 004032' 110067 175130 MOV R0,SECTOR ; SECTOR := R0 ;DRB001
888 004036' ; END ;DRB001
889 ;(* NOW, CUT VALUES DOWN TO LEGAL RANGES FOR CURRENT DRIVE TYPE *) ;DRB001
890 004036' 126767 175125 175100 35: CMPB TRACK,TRK LIM ;IF TRACK TOO GREAT THEN ;DRB001;WMRMRP
891 004044' 003412 BLE AS ;BEGIN ;DRB001
892 004046' 005267 175112 INC CYL ; CYL := CYL + 1 ;DRB001
893 004052' 116700 175111 MOV TRACK,R0 ; R0 := TRACK ;DRB001
894 004056' 016701 175064 MOV TRK LIM,R1 ; R1 := TRK LIMIT + 1 ;DRB001;WMRMRP
895 004062' 004767 001334 JSR PC,MODULUS ; R0 := R0 MOD R1 ;DRB001
896 004066' 110067 175075 MOV R0,TRACK ; TRACK := R0 ;DRB001
897 004072' ; END ;DRB001
898 004072' 026767 175066 175040 45: CMP CYL,CYL LIM ;IF CYL GT # OF CYLINDERS IN RANGE THEN ;DRB001
899 004100' 002420 BLT JS ;BEGIN ;DRB001
900 004102' 032767 000040 173706 BIT #NUMAND,SK1 ; IF SEQUENTIAL SEEKS THEN ;DRB001
901 004110' 001404 BEQ DS ; BEGIN ;DRB001
902 004112' 005067 175046 CLR CYL ; RESET CYLINDER ;DRB001
903 004116' 005067 175044 CLR DSKADR ; TRACK := 0, SECTOR := 0 ;DRB001
904 004122' ; ELSE ;DRB001
905 ; BEGIN ;DRB001
906 004122' 016700 175036 MOV CYL,R0 ; R0 := CYL ;DRB001
907 004126' 016701 175006 MOV CYLLIM,R1 ; R1 := CYLLIM ;DRB001
908 004132' 004767 001264 JSR PC,MODULUS ; R0 := R0 MOD R1 ;DRB001
909 004136' 010067 175022 MOV R0,CYL ; CYL := R0 ;DRB001
910 004142' ; END ;DRB001
911 004142' ; END ;DRB001
912 004142' 016767 175016 175012 55: MOV CYL,CYLNDR ;CYLNDR := CYL ;DRB001
913 004150' 032767 000020 173640 BIT #RPORT,SR1 ;IF RPORT AND DUAL PORT THEN ;DRB001
914 004156' 001407 BEQ SS ;BEGIN ;DRB001
915 004160' 032767 000001 175022 BIT #BIT0,FLAG ; ;DRB001
916 004166' 001403 BEQ SS ; ;DRB001
917 004170' 066767 174744 174764 ADD CYLLIM,CYLNDR ; INCREMENT TO SECOND HALF OF DISK ;DRB001
918 004176' ; END ;DRB001
919 004176' 026767 174760 174772 CMP CYLNDR,LASCYL ;IF CYLNDR GT LASCYL THEN ;DRB001

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920 004204' 003431 BLE ZUS ;BEGIN ;PREV. DO ;DRB001
921 004206' 016767 174764 174746 MOV LASCYL,CYLNDR ; CYLNDR := LASCYL (* TRUNCATE *) ;DRB001
922 004214' 032767 000040 173574 BIT #RNUMAND,SK1 ; IF SEQUENTIAL THEN ;DRB001
923 004222' 001404 BEQ JS ; BEGIN ;DRB001
924 004224' 005067 174734 CLR CYL ; RESET CYLINDER ;DRB001
925 004230' 005067 174732 CLR DSKADR ; TRACK := 0, SECTOR := 0 ;DRB001
926 004234' ; END ;DRB001
927 004234' 126767 174727 174736 108: CMPB TRACK,LASTRK ; IF TRACK GT LASTRKK THEN ;DRB001
928 004242' 003412 BLE JS ; BEGIN ;DRB001
929 004244' 116767 174730 174715 MOVB LASTRK,TRACK ; TRACK := LASTRK ;DRB001
930 004252' 126767 174710 174722 CMPB SECTOR,LASSEC ; IF SECTOR GT LASSEC THEN ;DRB001
931 004260' 003403 BLE JS ; BEGIN ;DRB001
932 004262' 116767 174714 174676 MOVB LASSEC,SECTOR ; SECTOR := LASSEC ;DRB001
933 004270' ; END ;DRB001
934 004270' ; END ;DRB001
935 ; ;DRB001
936 004270' 208: ; ;DRB001
937 ; ;DRB001
938 ;(* NOW, CHECK BADSPOT TABLE *) ;DRB001
939 004270' 012700 000252' MOV #BADSP,R0 ;R0 := ADDRESS OF TABLE ;DRB001
940 004274' 016701 174636 MOV #ATRN,R1 ;R1 := END OF TABLE ;DRB001
941 004300' 116702 174662 MOVB SECTOR,R2 ;R2 := SECTOR ;DRB001
942 004304' 116703 174657 MOVB TRACK,R3 ;R3 := TRACK ;DRB001
943 004310' 016704 174646 MOV CYLNDR,R4 ;R4 := CYLNDR ;DRB001
944 ;(* CALCULATE UPPER LIMIT OF THIS #RITE *) ;DRB001
945 004314' 066702 174666 ADD SIZEC,R2 ;R2 := R2 + # OF SECTORS IN WHITE ;DRB001
946 004320' 020267 174624 CMP R2,SECLIM ;IF R2 GT SECTOR LIM1 THEN ;DRB001;WMRMRP
947 004324' 003401 BLE JS ;BEGIN ;DRB001
948 004326' 005203 INC R3 ; R3 := R3 + 1 (INCR. UPPER TRACK) ;DRB001
949 ; ;DRB001
950 004330' 138: ; END ;DRB001
951 004330' 066703 174650 ADD SIZEC,R3 ;R3 := R3 + TRKSIZ ;DRB001
952 004334' 020367 174604 CMP R3,TRK LIM ;IF R3 GT TRACK LIMIT THEN ;DRB001;WMRMRP
953 004340' 003401 BLE JS ;BEGIN ;DRB001
954 004342' 005204 INC R4 ; R4 := R4 + 1 (INCR. UPPER CYLINDER) ;DRB001
955 ; ;DRB001
956 004344' 148: ; END ;DRB001
957 004344' 020001 158: ; WHILE R0 LT R1 DO ;DRB001
958 004346' 002030 BGE JS ;BEGIN ;DRB001
959 004350' 026710 174606 CMP CYLNDR,(R0) ; IF CYLNDR LE (R0) AND R4 GE (R0) ;DRB001
960 004354' 003022 LFS ; AND TRACK LE 3(R0) AND R3 GE 3(R0) ;DRB001
961 004356' 020410 CMP R4,(R0) ; AND SECTOR LE 2(R0) AND R2 GE 2(R0) THEN;DRB001
962 004360' 002420 BLT JS ; ;DRB001
963 004362' 126760 174601 000003 CMPB TRACK,3(R0) ; ;DRB001
964 004370' 003014 BGI JS ; ;DRB001
965 004372' 120360 000003 CMPB R3,3(R0) ; ;DRB001
966 004376' 002411 BLT JS ; ;DRB001
967 004400' 126760 174562 000002 CMPB SECTOR,2(R0) ; ;DRB001
968 004406' 003005 RCT JS ; ;DRB001
969 004410' 120260 000002 CMPB R2,2(R0) ; ;DRB001
970 004414' 002402 BLT JS ; BEGIN ;DRB001
971 004416' 000167 177256 JKP PICBK ; TRY ANOTHER SECTOR ;DRB001
972 004422' 178: ; END ;DRB001
973 004422' 062700 000004 ADD #4,R0 ; INCREMENT TO NEXT BAD SPOT ;DRB001
974 004426' 000746 BR JS ; END ;DRB001
975 004430' 168: ; ;DRB001

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976 004430' 000207      MIS      PC      ;CPU GU HOME!!!!      ;DRB001
977                      ;                      ;                      ;DRB001
978                      ;                      ;                      ;DRB001
979 004432'            WRTLIM:  ;(* WE ALLOW USER TO CHANGE #BUFSZ "AT WILL"--THEREFORE *) ;DRB001
980                      ;(* WE DO NOT AUTOMATICALLY KNOW THE TRANSFER SIZE. *) ;DRB001
981                      ;(* COMPUTATION IS MADE EASIER, FURTHERMORE, IF WE HAVE *) ;DRB001
982                      ;(* THE SIZE HADNEN INTO TRACKS AND SECTORS (SINCE *) ;DRB001
983                      ;(* #BUFSZ IS ONE WORD, THE TRANSFER CAN'T BE GREATER *) ;DRB001
984                      ;(* THAN 12 TRACKS). THIS ROUTINE ALSO CALCULATES THE *) ;DRB001
985                      ;(* UPPER LIMITS ON DISK WRITES AS NOT TO OVERFLOW *) ;DRB001
986                      ;(* HIGH CYLINDER. *) ;DRB001
987                      ;                      ;                      ;DRB001
988                      ;                      ;                      ;DRB001
989 004432' 016700 173504      MOV      #BUFSZ,R0      ;R0 := ALLOCATED WRITE BUFFER ;DRB001
990 004436' 000300      SWAB     R0              ;R0 := R0 / 256. ;DRB001
991 004440' 042700      BIC      #177400,R0      ; (* R0 IS #BUFSZ IN SECTORS *) ;DRB001
992 004444' 005067 174534      CLR      $IZTRK        ;$IZTRK := 0 ;DRB001
993 004450' 020067 174476      1S:    CMP      R0,SECLM1    ;$HILE KU GT SEC LIMIT DO ;DRB001;WMRMRP
994 004454' 002405      BLI     ZS              ;$BEGIN ;DRB001
995 004456' 105267 174522      INCB     $IZTRK        ; $IZTRK := $IZTRK + 1 ;DRB001
996 004462' 166700 174464      SUB     SECLM1,R0      ; DECREMENT SECTORS BY 1 TRACK ;DRB001;WMRMRP
997 004466' 000770      BR      1S              ;END ;DRB001
998 004470' 010067 174512      2S:    MOV      R0,$IZSEC    ;$IZSEC := REMAINDER ;DRB001
999 004474' 016701 174444      MOV     TRKLM1,R1      ;R1 := HIGH TRACK ;DRB001;WMRMRP
1000 004500' 016702 174444      MOV     SECLM1,R2      ;R2 := HIGH SECTOR ;DRB001;WMRMRP
1001 004504' 160002      SUB     R0,R2            ;R2 := R2 - $IZSEC ;DRB001
1002 004506' 005702      TST     R2              ;IF R2 LT 0 THEN ;DRB001
1003 004510' 002003      MGE     J5              ;$BEGIN ;DRB001
1004 004512' 066702 174434      ADD     SECLM1,R2      ; INCREMENT BACK UP ;DRB001;WMRMRP
1005 004516' 005301      DEC     R1              ; CAN'T FIT THAT LAST TRACK... ;DRB001
1006 004520'          ;                      ;                      ;DRB001
1007 004526' 166701 174460      SUB     $IZTRK,R1      ;R1 := R1 - $IZTRK ;DRB001
1008 004524' 005701      ISF     R1              ;IF R1 LT 0 THEN ;DRB001
1009 004526' 002004      BGE     4S              ;$BEGIN ;DRB001
1010 004530' 066701 174412      ADD     TRKLM1,R1      ; INCREMENT R1 BACK UP ;DRB001;WMRMRP
1011 004534' 005367 174436      DEC     LASCYL        ; LASCYL := LASCYL - 1 ;DRB001
1012 004540'          ;                      ;                      ;DRB001
1013 004540' 010167 174434      MOV     R1,LASTRK      ;LASTRK := HIGHEST TRACK WE CAN WRITE ;DRB001
1014 004544' 010267 174432      MOV     R2,LASSEC      ;LASSEC := HIGHEST SECTOR WE CAN WRITE ;DRB001
1015 004550' 000207      RTS     PC              ;END OF ROUTINE ;DRB001
1016 004552'          GETDVT:  ;                      ;                      ;DRB001
1017                      ;(* FIND WHICH TYPE OF DRIVE THIS IS. IF RP04/5, SET CYLLIM *) ;DRB001
1018                      ;(* TO 206. IF RP06, SET CYLLIM TO 408. DROP THE DRIVE IF *) ;DRB001
1019                      ;(* IT IS NOT A RP04/5/6 OR RM02/3 *) ;DRB001
1020                      ;                      ;                      ;DRB001
1021 004552' 016777 174440 175550      MOV     UNFINO,WRHCS2  ;$SELECT THE DRIVE ;DRB001
1022                      ;                      ;                      ;DRB001
1023                      ; THE FOLLOWING RPDT CHECKS WERE ;WMRMRP ;RMD5
1024                      ; INCLUDED FOR RM05 SUPPORT ;WMRMRP ;RMD5
1025                      ;                      ;WMRMRP ;RMD5
1026 004560' 027727 175562 024027 525S:  CMP     @RPDT,#24027    ; CHECK FOR RM05 DUAL PORT ;WMRMRP ;RMD5
1027 004566' 001012      BNE     51S            ; NO ;WMRMRP ;RMD5
1028 004570' 052767 000001 174412 511S:  BIS     #B10,FLAG      ; SET DUAL PORT BIT ;WMRMRP ;RMD5
1029 004576' 012767 000633 174334      MOV     #411,,CYLLIM  ; SET CYLINDER LIMIT = 411. ;RMD5
1030 004604' 012767 001465 174364      MOV     #21,,LASCYL   ; SET LASCYL = 21. (DISK HIGH LIM1) ;RMD5
1031 004612' 000415      BR      514S          ; NOW GO FINISH UP ;RMD5
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1032 004614' 027727 175526 020027 512S:  CMP     @RPDT,#20027    ; CHECK FOR RM05 SINGLE PORT ;RMD5
1033 004622' 001042      BNE     515S          ; NO ;RMD5
1034 004624' 042767 000001 174356 513S:  BIS     #B10,FLAG      ; SET TO SINGLE PORT ;RMD5
1035 004632' 012767 001466 174300      MOV     #822,,CYLLIM  ; SET CYLINDER TO 822. ;RMD5
1036 004640' 012767 001465 174330      MOV     #21,,LASCYL   ; SET DISK HIGH LIMIT TO 21. ;RMD5
1037 004646' 012767 000004 174266 514S:  MOV     #4,,TYPEFLG    ; SET DRIVE TYPE FLAG TO RM05 ;RMD5
1038 004654' 012767 176000 174272      MOV     #176000,CYLSK  ; SET MASK FOR RM05 CYLINDER ;RMD5
1039 004662' 012767 177740 174266      MOV     #177740,TRKMSK ; SET MASK FOR RM05 TRACK ;RMD5
1040 004670' 012767 177740 174262      MOV     #177740,SECMASK ; SET MASK FOR RM05 SECTOR MASK ;RMD5
1041 004676' 012767 000022 174240      MOV     #18,,TRKLM1   ; SET TRACK LIMIT FOR RM05 ;RMD5
1042 004704' 012767 000023 174234      MOV     #19,,TRKLM1   ; SET TRACK LIMIT +1 FOR RM05 ;RMD5
1043 004712' 012767 000037 174230      MOV     #31,,SECLM1   ; SET SECTOR LIMIT FOR RM05 ;RMD5
1044 004720' 012767 000040 174224      MOV     #32,,SECLM1   ; SET SECTOR LIMIT FOR RM05 ;RMD5
1045 004726' 000473      BR      52S           ; GO TO 2S IN 2 BR'S, 1 IS TOO FAR ;RMD5
1046                      ;                      ;                      ;RMD5
1047 004730'          515S:  ;                      ;                      ;RMD5
1048                      ;                      ;                      ;RMD5
1049                      ; THE FOLLOWING RPDT CHECKS WERE ;WMRMRP
1050                      ; INCLUDED FOR RM02/3 SUPPORT ;WMRMRP
1051                      ;                      ;WMRMRP
1052 004730' 027727 175412 024024 25S:    CMP     @RPDT,#24024    ; CHECK FOR RM03 DUAL PORT ;WMRMRP
1053 004736' 001404      BEQ     11S           ; YES ;WMRMRP
1054 004740' 027727 175402 024025      CMP     @RPDT,#24025    ; CHECK FOR RM02 DUAL PORT ;WMRMRP
1055 004746' 001012      BNE     12S           ; NO ;WMRMRP
1056 004750' 052767 000001 174232 11S:    BIS     #B10,FLAG      ; SET DUAL PORT BIT ;WMRMRP
1057 004756' 012767 000633 174154      MOV     #411,,CYLLIM  ; SET CYLINDER LIMIT = 411. ;WMRMRP
1058 004764' 012767 001465 174204      MOV     #21,,LASCYL   ; SET LASCYL = 21. (DISK HIGH LIM1) ;WMRMRP
1059 004772' 000421      BR      14S          ; NOW GO FINISH UP ;WMRMRP
1060 004774' 027727 175346 020024 12S:    CMP     @RPDT,#20024    ; CHECK FOR RM03 SINGLE PORT ;WMRMRP
1061 005002' 001404      BEQ     13S           ; YES ;WMRMRP
1062 005004' 027727 175336 020025      CMP     @RPDT,#20025    ; CHECK FOR RM02 SINGLE PORT ;WMRMRP
1063 005012' 001042      BNE     15S           ; NO ;WMRMRP
1064 005014' 042767 000001 174166 13S:    BIS     #B10,FLAG      ; SET TO SINGLE PORT ;WMRMRP
1065 005022' 012767 001466 174110      MOV     #822,,CYLLIM  ; SET CYLINDER TO 822. ;WMRMRP
1066 005030' 012767 001465 174140      MOV     #21,,LASCYL   ; SET DISK HIGH LIMIT TO 21. ;WMRMRP
1067 005036' 012767 000002 174076 14S:    MOV     #2,,TYPEFLG    ; SET DRIVE TYPE FLAG TO RM02/3 ;RMD5
1068 005044' 012767 176000 174102      MOV     #176000,CYLSK  ; SET MASK FOR RM02/3 CYLINDER ;WMRMRP
1069 005052' 012767 177740 174076      MOV     #177740,TRKMSK ; SET MASK FOR RM02/3 TRACK ;WMRMRP
1070 005060' 012767 177770 174072      MOV     #177770,SECMASK ; SET MASK FOR RM02/3 SECTOR MASK ;WMRMRP
1071 005066' 012767 000004 174050      MOV     #4,,TRKLM1    ; SET TRACK LIMIT FOR RM02/3 ;WMRMRP
1072 005074' 012767 000005 174044      MOV     #5,,TRKLM1    ; SET TRACK LIMIT +1 FOR RM02/3 ;WMRMRP
1073 005102' 012767 000037 174040      MOV     #31,,SECLM1   ; SET SECTOR LIMIT FOR RM02/3 ;WMRMRP
1074 005110' 012767 000040 174034      MOV     #32,,SECLM1   ; SET SECTOR LIMIT FOR RM02/3 ;WMRMRP
1075 005116' 000537      BR      52S           ; WE GOT THE STUFF TO DO IT NOW - RETURN ;WMRMRP
1076                      ;                      ;                      ;WMRMRP
1077 005120'          15S:  ;                      ;                      ;WMRMRP
1078                      ;                      ;                      ;WMRMRP
1079 005120' 027727 175222 024024      CMP     @RPDT,#24022    ; IF IT IS AN RP06 DUAL PORT THEN ;DRB001
1080 005126' 001012      BNE     1S            ;$BEGIN ;DRB001
1081 005130' 052767 000001 174052      BIS     #B10,FLAG      ; DUAL PORT ;DRB001
1082 005136' 012767 000627 173774      MOV     #407,,CYLLIM  ; CYLLIM := 408. ;DRB001
1083 005144' 012767 001455 174024      MOV     #13,,LASCYL   ; LASCYL := DISK HIGH LIM ;DRB001
1084 005152' 000471      BR      5S            ; GO FINISH UP ;DRB001;WMRMRP
1085 005154'          ;                      ;                      ;DRB001
1086 005154' 027727 175166 024021 1S:    CMP     @RPDT,#24021    ; IF IT IS RP04/5 DUAL PORT THEN ;DRB001
1087 005162' 001404      BEQ     3S            ;                      ;DRB001
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1098 005164' 027727 175156 024020      CME      @RP0T,@24020      ;
1099 005172' 001012                      BNE      4S                ;BEGIN
1090 005174'                      3S:
1091 005174' 052767 000001 174006      BIS      @B10,FLAG        ; DUAL PORT
1092 005202' 012767 000315 173730      MOV      #205.,CYLLIM    ; CYLLIM := 206.
1093 005210' 012767 000631 173760      MOV      #409.,LASCYL    ; LASCYL := DISK HIGH LIM
1094 005216' 000447                      BR       5S                ;END
1095 005220'                      4S:
1096 005220' 027727 175122 020022      CMP      @RP0T,@2002Z    ;IF RP06 SINGLE PORT THEN
1097 005226' 001012                      BNE      0S                ;BEGIN
1098 005230' 042767 000001 173752      BIC      @B10,FLAG        ; SINGLE PORT
1099 005236' 012767 001456 173674      MOV      #14.,CYLLIM    ; CAN USE ENTIRE DISK
1100 005244' 012767 001455 173724      MOV      #13.,LASCYL    ; LASCYL := DISK HIGH LIMIT
1101 005252' 000431                      BR       5S                ; GO FINISH UP
1102 005254'                      6S:
1103 005254' 027727 175066 020021      CMP      @RP0T,@2002Z    ;IF RP04/5 SINGLE PORT THEN
1104 005262' 001404                      BEO      8S                ;
1105 005264' 027727 175056 020020      CMP      @RP0T,@20020    ;
1106 005272' 001012                      BNE      9S                ;
1107 005274'                      8S:
1108 005274' 042767 000001 173706      BIC      @B10,FLAG        ; SINGLE PORT
1109 005302' 012767 000632 173630      MOV      #10.,CYLLIM    ; USE ENTIRE DISK
1110 005310' 012767 000631 173660      MOV      #409.,LASCYL    ; LASCYL := DISK HIGH LIMIT
1111 005316' 000407                      BR       5S                ; GO FINISH UP
1112 005320'                      9S:
1113
1114 005320' 004767 000130                      JSR      PC,DR0P          ; DR0P THE DRIVE
1115 005324' 104403 000000' 012306'      MSGNS,BEGIN,BADTYP      ;ASCII MESSAGE CALL WITH COMMON HEADER
1116 005332' 000261                      SEC
1117 005334' 000431                      BR       7S                ; ERROR RETURN
1118 005336'                      5S:
1119
1120
1121
1122 005336' 012767 000001 173576      MOV      #1,TYPEFLG      ; SET DRIVE TYPE FLAG TO RP
1123 005344' 012767 176000 173602      MOV      #176000,CYLSK   ; SET MASK FOR RP04/5/6 CYL
1124 005352' 012767 177740 173576      MOV      #177740,TRKSK   ; SET MASK FOR RP04/5/6 TRACK
1125 005360' 012767 177740 173572      MOV      #177740,SECSK   ; SET MASK FOR RP04/5/6 SECTOR
1126 005366' 012767 000022 173550      MOV      #18.,TRKLLM1    ; SET THE TRACK LIMIT FOR THE RP'S
1127 005374' 012767 000023 173544      MOV      #19.,TRKLLM1    ; SET THE TRACK LIMIT +1 FOR THE RP'S
1128 005402' 012767 000025 173540      MOV      #21.,SECLM1     ; SET THE SECTOR LIMIT FOR THE RP'S
1129 005410' 012767 000026 173534      MOV      #22.,SECLM1     ; SET THE SECTOR LIMIT +1 FOR THE RP'S
1130
1131
1132
1133 005416'                      2S:
1134 005416' 000241                      CLC
1135 005420'                      7S:
1136 005420' 000207                      RIS      PC
1137 005422'                      MODLUS:
1138
1139
1140
1141 005422' 020001                      1S:
1142 005424' 002402                      CMP      R0,R1            ;WHILE R0 GE R1 DO
1143 005426' 160100                      BLT      2S                ;BEGIN
1144
1145
1146
1147
1148
1149
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1152
1153
1154
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1156
1157
1158
1159
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1161
1162
1163
1164
1165 005500' 104420 000000' 001216'      @T0AS,BEGIN,UNITNO,ADR1
1166 005506' 012516'
1167
1168 005510' 000207                      RTS      PC
1169
1170
1171
1172
1173
1174
1175
1176
1177
1178
1179
1180
1181
1182
1183 005512' 005067 000464                      EROR: CLR      DLTFLG          ;INITIALIZE DLT FLAG
1184 005516' 104403 000000' 012470'      MSGNS,BEGIN,ASTER      ;ASCII MESSAGE CALL WITH COMMON HEADER
1185 005524' 005067 173510                      CLR      @RDERR          ;RESET ERROR INDICATOR
1186 005530' 032767 100000 173324      BIT      @BIT15,S+10    ;
1187 005536' 001417                      BEQ      14S              ;
1188 005540' 005267 173454                      LAC      @DLTCW1        ;
1189 005544' 052767 000001 000430      BIS      #1,DLTFLG      ;SET FLAG FOR DATA LATE
1190 005552' 032767 000004 172236      BII      @DLATE,SR1     ;
1191 005560' 001006                      BNE      14S              ;
1192 005562' 052767 177777 173450      RIS      #-1,@RDERR     ;
1193 005570' 104403 000000' 012222'      MSGNS,BEGIN,DLTERR     ;ASCII MESSAGE CALL WITH COMMON HEADER
1194 005576' 032777 020000 174514      BIT      @BIT13,@RHC51  ;MSSBUS CONTROL PARITY ERR?
1195 005604' 001406                      BEQ      5S                ;
1196 005606' 052767 177777 173424      BIS      #-1,@RDERR     ;SET HARD ERR
1197 005614' 104403 000000' 012174'      MSGNS,BEGIN,MCPLERR    ;ASCII MESSAGE CALL WITH COMMON HEADER
1198 005622' 032767 137400 173232      BIT      @HRDCS2,S+10   ;ANY ERRORS ?
1199 005630' 001402                      BEQ      1S

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1144 005430' 000774                      BR       1S                ;END
1145 005432'                      2S:
1146 005432' 000207                      RTS      PC                ;THAT'S ALL
1147
1148 005434' 012700 001242'      CLRBB: MOV      @RBUF,R0    ;CLEAR RBUF BUFFER
1149 005440' 016701 172466      MOV      @RBUFZ,R1        ;GET ITS ADDR AND SIZE
1150 005444' 005020      CLRUM: CLR      (R0)+      ;CLEAR ANOTHER
1151 005446' 005301      DEC      R1                ;COUNT ANOTHER
1152 005450' 001375      BNE      @CLRUM          ;BR BACK TILL DONE
1153 005452' 000207      RTS      PC
1154
1155 005454' 012701 000001      DROP: MOV      #1,R1        ; INITIALIZE DROP PICKER
1156 005460' 016700 173532      MOV      @UNITNO,R0       ; GET THE DRIVE NUMBER
1157 005464' 001403      BEQ      2S                ; IF DRIVE 0 GO DROP IT
1158 005466' 006301      1S:  ASL      R1            ; POINT TO NEXT DRIVE
1159 005470' 005300      DEC      R0                ; IS THIS THE ONE ?
1160 005472' 001375      BNE      1S                ; NO, LOOK AGAIN
1161 005474' 040167 173512      2S:  BIC      #1,@DVICE      ; DROP THE DRIVE
1162
1163
1164
1165 005500' 104420 000000' 001216'      @T0AS,BEGIN,UNITNO,ADR1
1166 005506' 012516'
1167
1168 005510' 000207                      RTS      PC
1169
1170
1171
1172
1173
1174
1175
1176
1177
1178
1179
1180
1181
1182
1183 005512' 005067 000464                      EROR: CLR      DLTFLG          ;INITIALIZE DLT FLAG
1184 005516' 104403 000000' 012470'      MSGNS,BEGIN,ASTER      ;ASCII MESSAGE CALL WITH COMMON HEADER
1185 005524' 005067 173510                      CLR      @RDERR          ;RESET ERROR INDICATOR
1186 005530' 032767 100000 173324      BIT      @BIT15,S+10    ;
1187 005536' 001417                      BEQ      14S              ;
1188 005540' 005267 173454                      LAC      @DLTCW1        ;
1189 005544' 052767 000001 000430      BIS      #1,DLTFLG      ;SET FLAG FOR DATA LATE
1190 005552' 032767 000004 172236      BII      @DLATE,SR1     ;
1191 005560' 001006                      BNE      14S              ;
1192 005562' 052767 177777 173450      RIS      #-1,@RDERR     ;
1193 005570' 104403 000000' 012222'      MSGNS,BEGIN,DLTERR     ;ASCII MESSAGE CALL WITH COMMON HEADER
1194 005576' 032777 020000 174514      BIT      @BIT13,@RHC51  ;MSSBUS CONTROL PARITY ERR?
1195 005604' 001406                      BEQ      5S                ;
1196 005606' 052767 177777 173424      BIS      #-1,@RDERR     ;SET HARD ERR
1197 005614' 104403 000000' 012174'      MSGNS,BEGIN,MCPLERR    ;ASCII MESSAGE CALL WITH COMMON HEADER
1198 005622' 032767 137400 173232      BIT      @HRDCS2,S+10   ;ANY ERRORS ?
1199 005630' 001402                      BEQ      1S

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1200 005632' 004767 000346
1201 005636' 032767 040000 173220 1S: JSR PC,CHKCS2 ;GO SEE THE BITS
1202 005644' 001451 HIF #B1114,S+12 ;ANY SET IN THE ERROR REG'S ?
1203 005646' 032767 177777 173212 BEW 4S
1204 005654' 001402 BIT #HRDEK1,S+14
1205 005656' 004767 000464 JSR PC,CHKERR1
1206 005662' 032767 000006 173252 2S: HIT #B,TYPFLG ; TEST TO SEE IF THIS IS AN RM02/3/05 ;RMD5
1207 005670' 001413 BEW 30S ; NO - THEN CHECK RPER2 & RPER3 ;RMD5
1208 005672' 032767 176210 173214 BIT #176210,S+42 ; RM - CHECK RPER2 HARD ERROR BITS ;#MRMRP
1209 005700' 001433 BEW 4S ; NO ERRORS IN RPER2 - CONTINUE ;#MRMRP
1210 005702' 052767 177777 173330 BIS #=1,HRDEK ; SET THE HARD ERROR FLAG ;#MRMRP
1211 005710' 104403 000000' 012320' MSGNS,BEGIN,EMZERR ;ASCII MESSAGE CALL WITH COMMON HEADER
1212 005716' 000424 BF 4S ; CONTINUE ;#MRMRP
1213 005720' 032767 167777 173164 30S: BIT #HRDEK2,S+40
1214 005726' 001406 BEW 3S ;ANY ERRORS IN ER2
1215 005730' 052767 171777 173302 FLS #=1,HRDEK
1216 005736' 104403 000000' 012320' MSGNS,BEGIN,EMZERR ;ASCII MESSAGE CALL WITH COMMON HEADER
1217 005744' 032767 140153 173142 3S: BIT #HRDEK3,S+42 ;TEST ER3
1218 005752' 001406 BEW 4S
1219 005754' 052767 177777 173256 BIS #=1,HRDEK ;SET HARD ERROR INDICATOR
1220 005762' 104403 000000' 012324' MSGNS,BEGIN,EMZERR ;ASCII MESSAGE CALL WITH COMMON HEADER
1221 005770' 032767 040000 173064 4S: BIT #B114,S+10 ;WCE ?
1222 005776' 001406 BEW 6S
1223 006000' 052767 000002 000174 BIS #2,DLIFLG ;SET FLAG FOR SUFT TO BE USED IF DLT ALSO OCCURE
1224 006006' 104403 000000' 012330' MSGNS,BEGIN,WCEERR ;ASCII MESSAGE CALL WITH COMMON HEADER
1225 006014' 032767 040000 173030 6S: BIT #B114,S ;IRE ?
1226 006022' 001406 BEW 7S
1227 006024' 052767 000002 000150 BIS #2,DLIFLG ;SET SOFT FLAG
1228 006032' 104403 000000' 012166' MSGNS,BEGIN,FKERR ;ASCII MESSAGE CALL WITH COMMON HEADER
1229 006040' 032767 100000 173004 7S: BIT #B115,S ;SC ?
1230 006046' 001406 BEW 8S
1231 006050' 052767 000002 000124 BIS #2,DLIFLG ;SET FLAG
1232 006056' 104403 000000' 012334' MSGNS,BEGIN,SCERR ;ASCII MESSAGE CALL WITH COMMON HEADER
1233 006064' 005767 172772 8S: IST S+10
1234 006070' 100405 CHL 10S
1235 006072' 016767 174244 173136 MOV #RHD,STORE
1236 006100' 005067 174236 CLR RHD
1237 006104' 005767 173130 10S: IST HRDEK ;ANNY HARD ERROR'S ?
1238 006110' 100414 BNE 12S ;IF TRUE THEN BR
1239 006112' 032767 000001 000062 BIT #1,DLIFLG ;DLT ? #DLATE IN SRI MUST BE SET IF TRUE
1240 006120' 001404 BEW 22S
1241 006122' 022767 000003 000052 CMP #3,DLIFLG ;I: (SUFT & DLI NOT COUNTED) ?
1242 006130' 001007 BNE 11S
1243 006132' 22S:
1244 ;*****
1245 006132' 104406 000000' 002242' SUPPNS,BEGIN,IMBLE ;
1246 ;*****
1247 006140' 000403 MH 11S ;
1248 006142' 12S:
1249 ;*****
1250 006142' 104405 000000' 002242' HDRS,BEGIN,PARLE ;
1251 ;*****
1252 006150' 005767 174166 11S: IST RHD ;
1253 006154' 001003 BNE 9S ;
1254 006156' 016767 173054 174156 MOV STORE,RHD ;
1255 006164' 004767 001126 JSR PC,NUTRDY ;

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1256
1257 006170' 104403 000000' 012470' MSGNS,BEGIN,ASTER ;ASCII MESSAGE CALL WITH COMMON HEADER
1258 006176' 000261 SEC
1259 006200' 000207 RTS PC
1260
1261 006202' 000000 DLTFLG: .WORD 0 ;DLT FLAG TO BE USED FOR SOFT ERROR
1262 ; & INHIBIT DLT COMPARISONS
1263
1264 006204' 052767 177777 173026 CHKCS2: FLS #=1,HRDEK ;SET HARD ERROR
1265 006212' 104403 000000' 012340' MSGNS,BEGIN,CS2HD ;ASCII MESSAGE CALL WITH COMMON HEADER
1266 006220' 032767 020000 172634 1S: BIT #B113,S+10 ;UPE ?
1267 006226' 001403 BEW 2S
1268 006230' 104403 000000' 012344' MSGNS,BEGIN,UPER ;ASCII MESSAGE CALL WITH COMMON HEADER
1269 006236' 032767 010000 172616 2S: BIT #B112,S+10 ;MED ?
1270 006244' 001403 BEW 3S
1271 006246' 104403 000000' 012350' MSGNS,BEGIN,FEDE ;ASCII MESSAGE CALL WITH COMMON HEADER
1272 006254' 032767 004000 172600 3S: BIT #B111,S+10 ;NXM ?
1273 006262' 001403 BEW 4S
1274 006264' 104403 000000' 012354' MSGNS,BEGIN,NXM ;ASCII MESSAGE CALL WITH COMMON HEADER
1275 006272' 032767 002000 172562 4S: BIT #B110,S+10
1276 006300' 001403 BEW 5S
1277 006302' 104403 000000' 012360' MSGNS,BEGIN,PGERR ;ASCII MESSAGE CALL WITH COMMON HEADER
1278 006310' 032767 001000 172544 5S: BIT #B19,S+10 ;MAF ?
1279 006316' 001403 BEW 6S
1280 006320' 104403 000000' 012364' MSGNS,BEGIN,MXPER ;ASCII MESSAGE CALL WITH COMMON HEADER
1281
1282 006326' 032767 000400 172526 6S: BIT #B18,S+10
1283 006334' 001403 BEW 7S
1284 006336' 104403 000000' 012370' MSGNS,BEGIN,MUPER ;ASCII MESSAGE CALL WITH COMMON HEADER
1285 006344' 000207 RTS PC
1286
1287 006346' 104403 000000' 012374' CHKERR1:
1288 006354' 032767 100000 172504 MSGNS,BEGIN,ERRHD ;ASCII MESSAGE CALL WITH COMMON HEADER
1289 006362' 001406 BIT #B115,S+14 ;DATA CHECK ERROR ?
1290 006364' 052767 000002 177610 BEW 1S
1291 006372' 104403 000000' 012400' BIS #2,DLIFLG ;SET SOFT ERR
1292 006400' 032767 000020 172460 MSGNS,BEGIN,UCKER ;ASCII MESSAGE CALL WITH COMMON HEADER
1293 006406' 001423 BIT #B14,S+14 ;FORMAT ERROR ?
1294 006410' 104403 BEW 2S
1295 006416' 016767 000000' 012404' MSGNS,BEGIN,FERER ;ASCII MESSAGE CALL WITH COMMON HEADER
1296 006424' 042767 077757 172604 MOV S+14,STORE ;SAVE ER1
1297 006432' 022767 100020 172576 BIC #7757,STORE
1298 006440' 001003 CMP #B115 ! B14,STORE
1299 006442' 052767 177777 172570 BNE 21S
1300 006450' 052767 000002 172524 21S: BIS #=1,HRDEK ;SET HARD ERROR INDICATOR
1301 ;SET SOFT ERR
1302
1303 006456' 032767 000400 172402 2S: BIT #B18,S+14 ;HCRC ?
1304 006464' 001406 BEW 3S
1305 006466' 052767 000002 177506 BIS #2,DLIFLG ;SET SOFT ERR
1306 006474' 104403 000000' 012410' MSGNS,BEGIN,HCRC ;ASCII MESSAGE CALL WITH COMMON HEADER
1307 006502' 032767 000200 172356 3S: BIT #B17,S+14 ;HEADER COMP ERR ?
1308 006510' 001420 BEW 5S
1309 006512' 104403 000000' 012414' MSGNS,BEGIN,HCE ;ASCII MESSAGE CALL WITH COMMON HEADER
1310 006520' 016767 172342 172510 MOV S+14,STORE
1311 006526' 042767 177177 172502 BIC #17717,STORE ;SAVE ONLY HCRC & HCE

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1312 006534' 022767 000600 172474 CMP #000,STURE
1313 006542' 001403 BEQ 55
1314 006544' 052767 171777 172466 HIS #-1,HKDERH ;SOFT ERROR IF BOTH SET
1315 006552' 032767 071157 172306 5s: BIF #17157,S+14 ;SET HKD ERR
1316 006560' 001001 BNE 65 ;ONLY HARD'S LEFT
1317 006562' 000207 RTS PC
1318
1319 006564' 052767 171777 172446 6s: HJS #-1,HKDERH ;SET HKD ERR
1320 006572' 032767 040000 172266 HIT #B114,S+14 ;UNS ?
1321 006600' 001403 BEQ 75
1322 006602' 104403 000000' 012420' MSGNS,BEGIN,UNSEF ;ASCII MESSAGE CALL WITH COMMON HEADER
1323 006610' 032767 020000 172250 7s: HIT #B113,S+14 ;OPI ?
1324 006616' 001403 BEQ 85
1325 006620' 104403 000000' 012424' MSGNS,BEGIN,OPIER ;ASCII MESSAGE CALL WITH COMMON HEADER
1326 006626' 032767 010000 172232 8s: BIF #B112,S+14 ;DIE ?
1327 006634' 001403 BEQ 95
1328 006636' 104403 000000' 012430' MSGNS,BEGIN,DTER ;ASCII MESSAGE CALL WITH COMMON HEADER
1329 006644' 032767 040000 172214 9s: BIF #B111,S+14 ;WLE ?
1330 006652' 001403 BEQ 105
1331 006654' 104403 000000' 012434' MSGNS,BEGIN,WLER ;ASCII MESSAGE CALL WITH COMMON HEADER
1332 006662' 032767 002000 172176 10s: BIF #B110,S+14 ;IAE ?
1333 006670' 001403 BEQ 205
1334 006672' 104403 000000' 012440' MSGNS,BEGIN,IAER ;ASCII MESSAGE CALL WITH COMMON HEADER
1335 006700' 032767 001000 172160 20s: HIT #B19,S+14 ;
1336 006706' 001403 BEQ 175 ;
1337 006710' 104403 000000' 012474' MSGNS,BEGIN,AUE ;ASCII MESSAGE CALL WITH COMMON HEADER
1338 006716' 032767 000040 172142 17s: BIF #B15,S+14 ;
1339 006724' 001403 BEQ 115 ;
1340 006726' 104403 000000' 012500' MSGNS,BEGIN,WCF ;ASCII MESSAGE CALL WITH COMMON HEADER
1341 006734' 032767 000100 172124 11s: BIF #B16,S+14 ;ECH ?
1342 006742' 001405 BEQ 125
1343 006744' 104403 000000' 012444' MSGNS,BEGIN,ECHR ;ASCII MESSAGE CALL WITH COMMON HEADER
1344 006752' 004767 000202 JSR PC,HAD
1345 006756' 032767 000010 172102 12s: BIF #B13,S+14 ;PAR ?
1346 006764' 001403 BEQ 135
1347 006766' 104403 000000' 012450' MSGNS,BEGIN,PAFR ;ASCII MESSAGE CALL WITH COMMON HEADER
1348 006774' 032767 000004 172064 13s: BIF #B12,S+14 ;KMR ?
1349 007002' 001403 BEQ 145
1350 007004' 104403 000000' 012454' MSGNS,BEGIN,RMRR ;ASCII MESSAGE CALL WITH COMMON HEADER
1351 007012' 032767 000002 172046 14s: BIF #B11,S+14 ;ILR ?
1352 007020' 001403 BEQ 155
1353 007022' 104403 000000' 012460' MSGNS,BEGIN,ILRR ;ASCII MESSAGE CALL WITH COMMON HEADER
1354 007030' 032767 000001 172030 15s: BIF #B10,S+14 ;ILF ?
1355 007036' 001403 BEQ 165
1356 007040' 104403 000000' 012464' MSGNS,BEGIN,ILFR ;ASCII MESSAGE CALL WITH COMMON HEADER
1357 007046' 000207 RTS PC
1358
1359 ; R E V D END OF UPDATE
1360
1361
1362
1363 ;////////////////////////////////////
1364 ;////////////////////////////////////
1365 ;////////////////////////////////////
1366
1367 007050' ERRORS: ;DRB001

1368 007050' 005777 173244 TST #RHCSI ;IF NO ERROR THEN ;DRB001
1369 007054' 100402 BFI 15 ;BEGIN ;DRB001
1370 007056' 000241 CLC ; SET NO ERROR ;DRB001
1371 007060' 000207 HIS PC
1372
1373
1374 007062' 032767 000006 172052 1s: BIT #6,TYPFLG ;ELSE ;DRB001
1375 007070' 001413 BEQ 45 ; IF DRIVE TYPE IS RM02/3/ OR RM05 ;RMDB5
1376 007072' 032777 100000 173262 BIT #100000,#KPER3 ; (BR IF NOT RM) ;RMD8
; AND IF BIT 15 (HSE) SET IN RMEP2 ;RMD61
; (NOTE: KPER3 = HMEK2 SFE DOCUMENTATION) ;RMDB1
1377 BEQ 45 ; (BR IF NOT HSE) ;RMD6
1378 007100' 001407 CLC ; THEN DON'T COUNT THIS AS AN ERROR ;RMD8
1379 007102' 000241 JSR PC,BAD ; THIS IS A BAD SECTOR SO ENTER IT ;RMD1
1380 007104' 004767 000050 ADD #4,R6 ; ADJUST SP FOR RETURN TO LOOP2 ;RMD61
1381 007110' 062706 000004 JMP LOOP2 ; GO ON NEXT COMPLETE CYCLE ;RMD61
1382 007114' 000167 173504
1383
1384
1385 007120' 000261 4s: SEC ;SET C BIT TO INDICATE ERROR ;RMD6
1386 007122' 005000 CLR R0 ;INIT INDEX
1387 007124' 016701 173170 MOV #RHCSI,R1 ; BEGIN OF REGISTER POINTERS ;DRB001
1388 007130' 3s: ; REPEAT ;DRB001
; BEGIN ;DRB001
1389 MOV (R1)+,(R0) ; MOV REGISTER TO TEMPORARY ;DRB001
1390 007134' 005720 TST (R0)+ ; ADVANCE POINTER ;DRB001
1391 007136' 022760 171777 002242' CMP #-1,TABLE(R0) ;END ? REV D ;RMRMRP
1392 007144' 001401 BEQ 335 ;YES END OF TABLE ;JMJ01
1393 007146' 000776 BR 35 ; NO GO DO IT AGAIN ;JMJ01
1394 007150' 004767 000476 33s: JSR PC,ENSUHI ; GET SOME TRIVIAL INFO ;DRB001
1395
1396
1397
1398
1399 007154' 000167 176332 JMP EROR ; REV. 00 ;RCP
1400
1401 007160' BAD:
1402 007160' 11s: ; BEGIN ;DRB001
1403 007160' 116767 172002 172004 MOV# SECTOR,BADSEC ; SET CURRENT SECTOR ;MK002
1404 007166' 116767 171775 172000 MOV# TRACK,BADTRK ; SET CURRENT TRACK ;MK002
1405 007174' 016767 171762 171766 MOV CYLNDR,BADCYL ; SET CURRENT CYLINDER ;MK002
1406 007202' 032767 000100 170606 BIF #RDSPI,SP1 ;IF OPERATOR WANTS TYPEOUT ;MK002
1407 007210' 001423 BEQ 125 ;ELSE BRANCH AROUND MSG ;MK002
;*****
;CONVERT UNITNO TO ASCII AND ;*****
;STORE AT ADDR1
1411 007212' 104420 000000' 001216' OTUAS,BEGIN,UNITNO,ADR1 ;STORE AT ADDR1
1412 007220' 012516'
;*****
;CONVERT BADCYL TO ASCII AND ;*****
;STORE AT CYLNO
1417 007222' 104420 000000' 001170' OTUAS,BEGIN,BADCYL,CYLNO ;STORE AT CYLNO
1418 007230' 010562'
;*****
;CONVERT BADTRK TO ASCII AND ;*****
;STORE AT TRKNO
1423 007232' 104420 000000' 001174' OTUAS,BEGIN,BADTRK,TRKNO


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1424 007240' 010600'
1425
1426
1427
1428
1429 007242' 104420 000000' 001172'
1430 007250' 010616'
1431
1432 007252' 104403 000000' 012232'
1433 007260'
1434 007260' 026727 171652 001052'
1435 007266' 002012
1436 007270' 016700 171642
1437 007274' 016720 171670
1438 007300' 116720 171666
1439 007304' 116720 171664
1440 007310' 010067 171622
1441 007314'
1442 007314' 000207
1443
1444
1445
1446
1447
1448 007316' 012767 077777 171706
1449 007324'
1450 007324' 032777 004000 172766
1451 007332' 001406
1452 007334' 010046
1453 007336' 012600
1454 007340' 032777 004000 172752
1455 007346' 001017
1456 007350'
1457 007350' 104407 000000'
1458 007354' 104407 000000'
1459 007360' 104407 000000'
1460 007364' 104407 000000'
1461 007370' 005367 171636
1462 007374' 001453
1463 007376' 104403 000000' 012256'
1464 007404' 000427
1465 007406' 004567 173626
1466 007412' 004567 000076
1467 007416' 000435
1468 007420' 004767 000226
1469 007424' 005000
1470 007426' 016701 172666
1471 007432' 012160 001052'
1472 007436' 005720
1473 007440' 022760 002242'
1474 007446' 001371
1475 007450' 012767 000006 170430
1476
1477 007456' 104405 000000' 002242'
1478
1479

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1480 007464' 004767 173522
1481 007470' 004767 175760
1482 007474' 104403 000000' 012274'
1483 007502' 016706 170324
1484 007506' 000167 173112
1485 007512' 000207
1486
1487
1488
1489 007514' 016777 171476 172606
1490 007522' 017700 172572
1491 007526' 032760 004000
1492 007532' 001433
1493 007534' 017700 172572
1494 007540' 105700
1495 007542' 100027
1496 007544' 032700 000100
1497 007550' 001424
1498 007552' 032700 000400
1499 007556' 001421
1500 007560' 032700 004000
1501 007564' 001016
1502 007566' 032700 010000
1503 007572' 001413
1504 007574' 032700 040000
1505 007600' 001010
1506 007602' 005700
1507 007604' 100406
1508 007606' 032777 004000 172504
1509 007614' 001402
1510
1511 007616' 000261
1512 007620' 000401
1513 007622' 000241
1514 007624' 000205
1515
1516
1517
1518 007626' 014167 170254
1519 007632' 010167 170244
1520 007636' 014267 170246
1521 007642' 010267 170236
1522 007646' 005721
1523 007650' 005722
1524
1525 007652' 016767 172442 170220
1526 007660' 017767 172434 170214
1527 007666' 000207
1528
1529
1530 007670'
1531 007670' 012777 000040 172432
1532 007676' 012767 077777 171326
1533 007704' 105777 172410
1534 007710' 100417
1535 007712' 104407 000000'

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1536 007716' 104407 000000'          BREAKS,BEGIN          ;THEN CONTINUE AT NEXT INSTRUCTION.
1537 007722' 005367 171304          DEC      CLK          ; WAIT SOME MORE ?
1538 007726' 001366          BNE     IS          ; YES
1539 007730' 012767 000003 170150    MOV     #3,ERRTYP    ;CONTROLLER NOT READY
1540                                     ;*****
1541 007736' 104405 000000' 000000    HWDERS,BEGIN,NULL    ; CONTROLLER NOT READY
1542                                     ;*****
1543 007744' 104410 000000'          ENDS,BEGIN          ;
1544 007750' 017746 172362          2S:    MOV     @RFPAS,=(SP)
1545 007754' 012677 172356          MOV     (SP)+,@RFPAS
1546 007760' 000207          RTS     PC          ; RETURN
1547                                     ;
1548                                     ;-----
1549
1550 007762' 016700 170020          SETUP: MOV     ADDR,R0          ; GET DEVICE ADDRESS
1551 007766' 010067 172326          MOV     R0,RHCS1     ; GENERATE REGISTER ADDRESSES
1552 007772' 062700 000002          ADD     #2,R0
1553 007776' 010067 172320          MOV     R0,RHNC
1554 010002' 062700 000002          ADD     #2,R0
1555 010006' 010067 172312          MOV     R0,RHBA
1556 010012' 062700 000002          ADD     #2,R0
1557 010016' 010067 172304          MOV     R0,RPDA
1558 010022' 062700 000002          ADD     #2,R0
1559 010026' 010067 172276          MOV     R0,RHCS2
1560 010032' 062700 000002          ADD     #2,R0
1561 010036' 010067 172270          MOV     R0,RPUS
1562 010042' 062700 000002          ADD     #2,R0
1563 010046' 010067 172262          MOV     R0,RPEP1
1564 010052' 062700 000002          ADD     #2,R0
1565 010056' 010067 172254          MOV     R0,RFAS
1566 010062' 062700 000002          ADD     #2,R0
1567 010066' 010067 172246          MOV     R0,RPLA
1568 010072' 062700 000002          ADD     #2,R0
1569 010076' 010067 172240          MOV     R0,RHDM
1570 010102' 062700 000002          ADD     #2,R0
1571 010106' 010067 172232          MOV     R0,RMPR
1572 010112' 062700 000002          ADD     #2,R0
1573 010116' 010067 172224          MOV     R0,RPUT
1574 010122' 062700 000002          ADD     #2,R0
1575 010126' 010067 172216          MOV     R0,RPSW
1576 010132' 062700 000002          ADD     #2,R0
1577 010136' 010067 172210          MOV     R0,RPOF
1578 010142' 062700 000002          ADD     #2,R0
1579 010146' 010067 172202          MOV     R0,RPDC
1580 010152' 062700 000002          ADD     #2,R0
1581 010156' 010067 172174          MOV     R0,RPCC
1582 010162' 062700 000002          ADD     #2,R0
1583 010166' 010067 172166          MOV     R0,RPER2
1584 010172' 062700 000002          ADD     #2,R0
1585 010176' 010067 172160          MOV     R0,RPER3
1586 010202' 062700 000002          ADD     #2,R0
1587 010206' 010067 172152          MOV     R0,RPEC1
1588 010212' 062700 000002          ADD     #2,R0
1589 010216' 010067 172144          MOV     R0,RPEC2
1590
1591 010222' 032767 001000 167626    BIT     @ADDR22,RFS1 ;DO WE HAVE 22-BIT ADDR SUPPORT?

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***?
 ;DRB001
 ;DRB001

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1592 010230' 001415          BEM     IS          ;NO
1593 010232' 032767 000200 167556    FII     #RH70,SR1    ;IS THIS AN RH70
1594 010240' 001411          BEQ     IS          ;NO
1595 010242' 062700 000002          ADD     #2,R0
1596 010246' 010067 172116          MOV     R0,RHBAE
1597 010252' 062700 000002          ADD     #2,R0
1598 010256' 010067 172110          MOV     R0,RHCS3
1599
1600 010262' 000403          BR      2S          ;DRB001
1601 010264' 042767 001000 167564    1S:    FIC     @ADDR22,RFS1 ; CAN'T USE 22 BIT ADDRESSING
1602 010272' 016700 167512          MOV     VECTOR,R0    ; GET VECTOR ADDRESS
1603 010276' 012720 003452'          2S:    MOV     #NTRUPT,(R0)+ ; SET POINTER JUST IN CASE
1604 010302' 116710 167504          MOVVB  BR1,(R0)      ; SET PRIORITY
1605 010306' 000207          RTS     PC          ; RETURN
1606

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***?
 ;DRB001
 ;DRB001

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1607 .SPTTL MODULE MESSAGES
1608
1609 010310' 042440 051122 051117 MES1: .ASCIZ ' ERROR%' ;**=40 ;DRB001
1610 010316' 000045 ;DRB001
1611 010320' 050040 051101 052111 MES2: .ASCIZ ' PARITY' ;DRB001
1612 010326' 000131 ;DRB001
1613 010330' 042040 044522 047526 MES3: .ASCIZ ' DRIVE ' ;DRB001
1614 010336' 000040 ;DRB001
1615 010340' 046440 051501 041123 MES4: .ASCIZ ' MASSBUS' ;DRB001
1616 010346' 051525 000 ;DRB001
1617 010351' 040 040504 040524 MES5: .ASCIZ ' DATA' ;DRB001
1618 010356' 000 ;DRB001
1619 010357' 040 042522 051124 MES6: .ASCIZ ' RETRY EXCEEDED%' ;DRB001
1620 010364' 020131 054105 042503 ;DRB001
1621 010372' 042105 042105 000045 ;DRB001
1622 010400' 046040 052101 000105 MES7: .ASCIZ ' LATE' ;DRB001
1623 010406' 047516 020124 042522 MES8: .ASCIZ ' NOT READY%' ;DRB001
1624 010414' 042101 022531 000 ;DRB001
1625 010421' 040 047503 046125 MES9: .ASCIZ ' COULD NOT GET' ;DRB001
1626 010426' 020104 047516 020124 ;DRB001
1627 010434' 042507 000124 ;DRB001
1628 010440' 042040 047522 050120 MES10: .ASCIZ ' DROPPED' ;DRB001
1629 010446' 042105 000 ;DRB001
1630 010451' 072 047040 052117 MES11: .ASCIZ ': NOT AN RPU4/5/6 OR AN RM02/3/5%' ;DRB001;WMRMRP
1631 010456' 040440 020116 050122 ;DRB001
1632 010464' 032066 032457 033057 ;DRB001
1633 010472' 047440 020122 047101 ;DRB001
1634 010500' 051040 030115 027462 ;DRB001
1635 010506' 027463 022465 000 ;DRB001
1636 010513' 040 051124 047101 MES12: .ASCIZ ' TRANSFER' ;DRB001
1637 010520' 043123 051105 000 ;DRB001
1638 010525' 045 000 ;DRB001
1639 010527' 072 041040 042101 MES13: .ASCIZ '%' ;DRB001
1640 010534' 050123 052117 040440 MES15: .ASCIZ ' BADSPUT AT (OCTAL) CYL: ' ;DRB001
1641 010542' 020124 047450 052103 ;DRB001
1642 010550' 046101 020051 054503 ;DRB001
1643 010556' 035114 000040 ;DRB001
1644 010562' 000002 ;DRB001
1645 010564' 020040 020040 020054 CYLNO: .BLKB 2 ;DRB001
1646 010572' 051124 035113 000040 MES16: .ASCIZ ' , TRK: ' ;DRB001
1647 010600' 000004 ;DRB001
1648 010604' 020040 020054 042523 ;DRB001
1649 010612' 035103 000040 MES17: .ASCIZ ' , SEC: ' ;DRB001
1650 010616' 000004 ;DRB001
1651 010622' 020040 000045 SECNO: .BLKB 4 ;DRB001
1652 010626' 052440 045516 047516 MES18: .ASCIZ ' %' ;DRB001
1653 010634' 047127 000 MES19: .ASCIZ ' UNKNOWN' ;DRB001
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1663 010637' 110 051101 020104 MES20: .ASCIZ ' HARD ERROR(S) SET IN RPER2 %'
1664 010644' 051105 047522 024122
1665 010652' 024523 051440 052105
1666 010660' 044440 020116 050122
1667 010666' 051105 020062 022440
1668 010674' 000
1669 010675' 110 051101 020104 MES21: .ASCIZ ' HARD ERROR(S) SET IN RPER3 %'
1670 010702' 051105 047522 024122
1671 010710' 024523 051440 052105
1672 010716' 044440 020116 050122
1673 010724' 051105 020063 022440
1674 010732' 000040
1675 010734' 051127 052111 020105 MES22: .ASCIZ ' WRITE CHECK ERROR %'
1676 010742' 044103 041505 020113
1677 010750' 051105 047522 020122
1678 010756' 022440 000040
1679 010762' 041523 051440 052105 MES23: .ASCIZ ' SC SET %'
1680 010770' 020040 000045
1681 010774' 044124 020105 047506 MES24: .ASCIZ ' THE FOLLOWING ERROR BITS ARE SET IN RPCS2 %'
1682 011002' 046114 053517 047111
1683 011010' 020107 051105 047522
1684 011016' 020122 044502 051524
1685 011024' 040440 042522 051440
1686 011032' 052105 044440 020116
1687 011040' 050122 051503 020062
1688 011046' 020045 020045 000
1689 011053' 125 044516 052502 MES25: .ASCIZ ' URIBUS PARITY ERROR %'
1690 011060' 020123 040520 044522
1691 011066' 054524 042440 051122
1692 011074' 051117 022440 000040
1693 011102' 047516 020116 054105 MES26: .ASCIZ ' NON EXISTANT DRIVE %'
1694 011110' 051511 043524 052116
1695 011116' 042040 044522 042526
1696 011124' 022440 000040
1697 011130' 054116 020115 022440 MES27: .ASCIZ ' X% %'
1698 011136' 000
1699 011137' 120 047522 051107 MES28: .ASCIZ ' PROGRAM ERROR PGE %'
1700 011144' 046501 042440 051122
1701 011152' 051117 020040 043520
1702 011160' 020105 022440 000040
1703 011166' 044515 051523 042105 MES29: .ASCIZ ' MISSED TRANSFER %'
1704 011174' 052040 040522 051516
1705 011202' 042506 020122 022440
1706 011210' 020040 000
1707 011213' 115 051501 041123 MES30: .ASCIZ ' MASSBUS DATA LINE PARITY ERROR %'
1708 011220' 051525 042040 052101
1709 011226' 020101 044514 042516
1710 011234' 050040 051101 052111
1711 011242' 020131 051105 047522
1712 011250' 020122 022440 000040
1713 011256' 044124 020105 047506 MES31: .ASCIZ ' THE FOLLOWING BITS ARE SET IN RPER1 % %'
1714 011264' 046114 053517 047111
1715 011272' 020107 044502 051524
1716 011300' 040440 042522 051440
1717 011306' 052105 044440 020116
1718 011314' 050122 051105 020061

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1719	011322'	022440	022440	000040					
1720	011330'	040504	040524	041440	MES32:	.ASCIZ	'DATA CHECK ERROR % '		
1721	011336'	042510	045503	042440					
1722	011344'	051122	051117	020040					
1723	011357'	020045	000						
1724	011355'	106	051117	040515	MES33:	.ASCIZ	'FORMAT ERROR % '		
1725	011362'	020124	051105	047522					
1726	011370'	020122	022440	020040					
1727	011376'	000							
1728	011377'	040	042510	042101	MES34:	.ASCIZ	'HEADER CRC ERROR % '		
1729	011404'	051105	041440	041522					
1730	011412'	042440	051122	051117					
1731	011420'	022440	000040						
1732	011424'	042510	042101	051105	MES35:	.ASCIZ	'HEADER COMPARE ERROR % '		
1733	011432'	041440	046517	040520					
1734	011440'	042522	042440	051122					
1735	011446'	051117	020040	020045					
1736	011454'	000							
1737	011455'	104	044522	042526	MES36:	.ASCIZ	'DRIVE UNSAFE % '		
1738	011462'	052440	051516	043101					
1739	011470'	020105	022440	000040					
1740	011476'	050117	051105	052101	MES37:	.ASCIZ	'OPERATION INCOMPLETE % '		
1741	011504'	047511	020116	047111					
1742	011512'	047503	050115	042514					
1743	011520'	042524	022440	000040					
1744	011526'	051104	053111	020105	MES38:	.ASCIZ	'DRIVE TIMING ERROR % '		
1745	011534'	044524	044515	043516					
1746	011542'	042440	051122	051117					
1747	011550'	022440	000040						
1748	011554'	051127	052111	020105	MES39:	.ASCIZ	'WRITE LOCK ERROR % '		
1749	011562'	047514	045503	042440					
1750	011570'	051122	051117	022440					
1751	011576'	000040							
1752	011600'	047111	040526	044514	MES40:	.ASCIZ	'INVALID ADDRESS ERROR % '		
1753	011606'	020104	042101	051164					
1754	011614'	051505	020123	051105					
1755	011622'	047522	020122	020045					
1756	011630'	000							
1757	011631'	105	041503	044040	MES41:	.ASCIZ	'ECC HARD ERROR % '		
1758	011636'	051101	020104	051105					
1759	011644'	047522	020122	020045					
1760	011652'	000							
1761	011653'	120	051101	052111	MES42:	.ASCIZ	'PARITY ERROR % '		
1762	011660'	020131	051105	047522					
1763	011666'	020122	020045	000					
1764	011673'	122	043505	051511	MES43:	.ASCIZ	'REGISTER MODIFICATION REFUSED % '		
1765	011700'	042524	020122	047515					
1766	011706'	044504	044506	040503					
1767	011714'	044524	047117	051040					
1768	011722'	043105	051525	042105					
1769	011730'	020040	020045	000					
1770	011735'	111	046114	043505	MES44:	.ASCIZ	'ILLEGAL REGISTER % '		
1771	011742'	046101	051040	043505					
1772	011750'	051511	042524	020122					
1773	011756'	020045	000						
1774	011761'	111	046114	043505	MES45:	.ASCIZ	'ILLEGAL FUNCTION % '		

1775	011766'	046101	043040	047125					
1776	011774'	052103	047511	020116					
1777	012002'	020045	000						
1778	012005'	040	022445	020045	MES46:	.ASCIZ	'***** % '		
1779	012012'	020040	025052	025052					
1780	012020'	025052	025052	025052					
1781	012026'	025052	025052	025052					
1782	012034'	025052	025052	025052					
1783	012042'	025052	025052	025052					
1784	012050'	025052	025052	025052					
1785	012056'	025052	022440	020045					
1786	012064'	000							
1787	012065'	101	042104	042522	MES47:	.ASCIZ	'ADDRESS OVERFLOW ERROR % '		
1788	012072'	051523	047440	042526					
1789	012100'	043122	047514	020127					
1790	012106'	051105	047522	020122					
1791	012114'	020045	000						
1792	012117'	127	044522	042524	MES48:	.ASCIZ	'WRITE LOCK FAILURE % '		
1793	012124'	046040	041517	020113					
1794	012132'	040506	046111	051125					
1795	012140'	020105	020045	000					
1796	012145'	040	040055	047514	MES49:	.ASCIZ	' - LOAD MEDIA % '		
1797	012152'	042101	046440	042105					
1798	012160'	040511	022440	000040					
1799									
1800									
1801									
1802									
1803									
1804									
1805									
1806									
1807									
1808									
1809	012166'	010513'			TRERR:	MES12			DRB001
1810	012170'	010310'				MES1			DRB001
1811	012172'	177777				177777			DRB001
1812									DRB001
1813	012174'	010340'			MCPERR:	MES4			DRB001
1814	012176'	010320'				MES2			DRB001
1815	012200'	010310'				MES1			DRB001
1816	012202'	177777				177777			DRB001
1817									DRB001
1818	012204'	010340'			MDPERR:	MES4			DRB001
1819	012206'	010351'				MES5			DRB001
1820	012210'	010320'				MES2			DRB001
1821	012212'	010310'				MES1			DRB001
1822	012214'	177777				177777			DRB001
1823									DRB001
1824	012216'	010357'			EXCED:	MES6			DRB001
1825	012220'	177777				177777			DRB001
1826									DRB001
1827	012222'	010351'			DLTERR:	MES5			DRB001
1828	012224'	010400'				MES7			DRB001
1829	012226'	010310'				MES1			DRB001
1830	012230'	177777				177777			DRB001

1831				
1832	012232'	010330'		
1833	012234'	012523'	BADMES: MES3	;DRB001
1834	012236'	010527'	NUMB	;DRB001
1835	012240'	010564'	MES15	;DRB001
1836	012242'	010604'	MES16	;DRB001
1837	012244'	010622'	MES17	;DRB001
1838	012246'	177777	MES18	;DRB001
1839			177777	;DRB001
1840	012250'	010626'		;DRB001
1841	012252'	010310'	UNKNM: MES19	;DRB001
1842	012254'	177777	MES1	;DRB001
1843			177777	;DRB001
1844	012256'	010330'		;DRB001
1845	012260'	010406'	NUT: MES3	;DRB001
1846	012262'	177777	MESH	;DRB001
1847			177777	;DRB001
1848	012264'	010421'		;DRB001
1849	012266'	010330'	TOUT: MES9	;DRB001
1850	012270'	010525'	MES3	;DRB001
1851	012272'	177777	MES13	;DRB001
1852			177777	;DRB001
1853	012274'	010330'		;DRB001
1854	012276'	012523'	UNP: MES3	;DRB001
1855	012300'	010440'	NUMB	;DRB001
1856	012302'	010525'	MES10	;DRB001
1857	012304'	177777	MES13	;DRB001
1858			177777	;DRB001
1859	012306'	010330'		;DRB001
1860	012310'	012523'	BADTYP: MES3	;DRB001
1861	012312'	010440'	NUMB	;DRB001
1862	012314'	010451'	MES10	;DRB001
1863	012316'	177777	MES11	;DRB001
1864			177777	;DRB001
1865				
1866			////////////////////	
1867			////////////////////	
1868			////////////////////	
1869			////////////////////	
1870			PK E V D UPDATES	
1871	012320'	010637'	ER2ERR: MES20	
1872	012322'	177777	177777	
1873				
1874	012324'	010675'	ER3ERR: MES21	
1875	012326'	177777	177777	
1876				
1877	012330'	010734'	WCEERR: MES22	
1878	012332'	177777	177777	
1879				
1880	012334'	010762'	SCERR: MES23	
1881	012336'	177777	177777	
1882				
1883	012340'	010774'	CS2HD: MES24	
1884	012342'	177777	177777	
1885				
1886	012344'	011053'	UPER: MES25	

1887	012346'	177777		177777
1888				
1889	012350'	011102'	NEDEP: MES26	
1890	012352'	177777	177777	
1891				
1892	012354'	011130'	NXM: MES27	
1893	012356'	177777	177777	
1894				
1895	012360'	011137'	PGERR: MES28	
1896	012362'	177777	177777	
1897				
1898	012364'	011166'	MXPER: MES29	
1899	012366'	177777	177777	
1900				
1901	012370'	011213'	MDPER: MES30	
1902	012372'	177777	177777	
1903				
1904	012374'	011256'	ER1PD: MES31	
1905	012376'	177777	177777	
1906				
1907	012400'	011330'	DCKER: MES32	
1908	012402'	177777	177777	
1909				
1910	012404'	011355'	FERER: MES33	
1911	012406'	177777	177777	
1912				
1913	012410'	011377'	HCRC: MES34	
1914	012412'	177777	177777	
1915				
1916	012414'	011424'	HCE: MES35	
1917	012416'	177777	177777	
1918				
1919	012420'	011455'	UNSER: MES36	
1920	012422'	177777	177777	
1921				
1922	012424'	011476'	OPIER: MES37	
1923	012426'	177777	177777	
1924				
1925	012430'	011526'	DTEF: MES38	
1926	012432'	177777	177777	
1927	012434'	011554'	WLER: MES39	
1928	012436'	177777	177777	
1929	012440'	011600'	IAER: MES40	
1930	012442'	177777	177777	
1931				
1932	012444'	011631'	ECHK: MES41	
1933	012446'	177777	177777	
1934				
1935	012450'	011653'	PARR: MES42	
1936	012452'	177777	177777	
1937				
1938	012454'	011673'	RMRR: MES43	
1939	012456'	177777	177777	
1940				
1941	012460'	011735'	ILRR: MES44	
1942	012462'	177777	177777	

BKMOD	1#																
BREAK	1#	747	1456	1459	1535												
BTOD	1#																
CKDATA	1#	696															
CLKSP	1#																
DATAACK	1#																
DATEERR	1#																
DFSEVN	1#	302															
DSEVNT	1#	302															
END	1#	684	1543														
ENDTT	1#	685															
ENDMOD	1#																
EQUATS	1#	302															
EXIT	1#	782															
GETPA	1#	669															
GWBUFF	1#	672															
HRDER	1#	1248	1476	1540													
IOMOD	1#																
IUMODP	1#																
IOMODR	1#																
IOMODX	1#	230															
LINEUP	702#	723	733	743													
MAP22	1#	774															
MODULE	1#	231															
MSG	1#																
MSGN	1#	655	1115	1184	1193	1197	1211	1216	1220	1224	1228	1232	1257	1265	1268		
		1271	1274	1277	1280	1284	1287	1292	1295	1306	1309	1322	1325	1328	1331	1334	
		1337	1340	1343	1347	1350	1354	1356	1432	1463	1482						
MSGS	1#																
NBKMUD	1#																
OTUA	1#	1162	1408	1414	1420	1426											
PIRJ	1#	790															
RAND	1#	862															
SBKMUD	1#																
SOFER	1#	1244															

. ABS. 000000 000
 012576 001

ERRORS DETECTED: 0
 DEFAULT GLOBALS GENERATED: 0

XRMDB0, XRMDB0.SEG/SOL/CRF/DOC=DDXCUM, XRMDB0
 RUN-TIME: 2.5 .8 SECONDS
 RUN-TIME RATIO: 36/9=4.1
 CORE USED: 11K (22 PAGES)

DOCUMENT PAGES: 47

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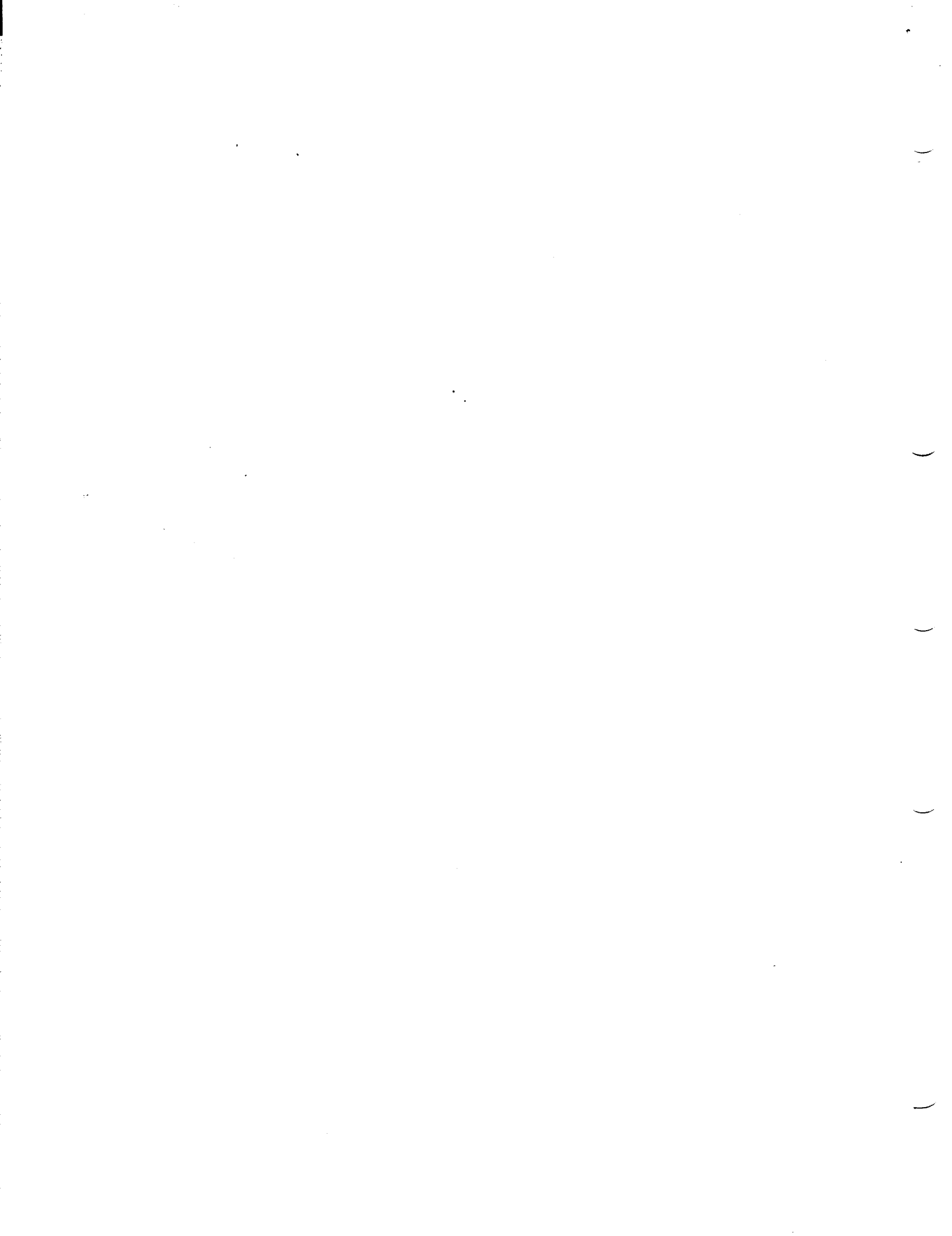
IDENTIFICATION

PRODUCT CODE: AC-S077A-MC
PRODUCT NAME: CXRMCA0 RM05/3/2 MODULE
DATE CREATED: APRIL 1980
MAINTAINER: CX DIAGNOSTIC GROUP
AUTHOR: MIKE LEAVITT

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1. ABSTRACT

CXRMCAO IS AN IOMODX THAT EXERCISES RM05, RM03 AND RM02 DISK DRIVES ON AN RH70 OR RH11 CONTROLLER. IT EXERCISES THE DRIVES BY DOING SEEKS, WRITES, WRITE-CHECKS, READS, AND IN-CORE COMPARISONS. ALL ERRORS DETECTED ARE REPORTED ON THE CONSOLE TTY.

2. REQUIREMENTS

PDP-11 PROCESSOR
PROGRAM LOADING DEVICE
TERMINAL
RH11 OR RH70 CONTROLLER
1 TO 8 DISK DRIVES (ANY COMBINATION OF RM05'S, RM03'S OR RM02'S)

TEST MEDIA:

THE RM05, RM03 OR RM02 DISK PACK MUST BE FORMATTED IN 16 BIT DATA FORMAT.

LOAD DEVICE:

ANY DEVICE THAT IS SUPPORTED BY THE XXDP SOFTWARE PACKAGE.

3. PASS DEFINITION

ONE PASS OF THE CXRMCAO MODULE CONSISTS OF 200 CYCLES OF THE FOLLOWING TEST SEQUENCE FROM STEP 1 TO STEP 9.

1. SELECT A STARTING BLOCK ADDRESS. (SEE SECTION 10.)
2. SELECT A DRIVE FOR TEST.
3. IF NO DRIVE IS SELECTED, DROP THE CXRMCAO MODULE.
4. DO AN EXPLICIT SEEK TO A CYLINDER. IF ERRORS, REPORT AND RETRY UP TO RETRY LIMIT.
5. DO A WRITE DATA - IF ERRORS, REPORT AND RETRY UP TO RETRY LIMIT.
6. DO A WRITE CHECK DATA - IF ERRORS, REPORT AND RETRY UP TO RETRY LIMIT.
7. DO A READ DATA - IF ERRORS, REPORT AND RETRY UP TO RETRY LIMIT.
8. DO AN IN-CORE DATA COMPARE - IF ERRORS, REPORT.
9. IF COMPLETED CYCLE OF ALL DRIVES, GO TO STEP 1, ELSE STEP 2.

NOTE: THE RETRY LIMIT IS 3 TIMES. THE MODULE CLEARS THE ERROR, BEFORE EACH RETRY. IF THE MODULE FAILS TO CLEAR THE ERROR, THE MODULE WILL BE DROPPED.

4. EXECUTION TIME

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ONE PASS OF CXRMCA0 RUNNING ALONE ON A PDP-11/70 TAKES APPROXIMATELY 20 SECONDS.

5. CONFIGURATION REQUIREMENTS

DEFAULT PARAMETERS:

DEVADR: 176700, VECTOR: 254, BR1: 5, DEVCNT: 1

REQUIRED PARAMETERS:

NONE

6. DEVICE/OPTION SETUP

MAKE CERTAIN THAT ALL DRIVES ARE POWERED UP, WRITE ENABLED, AND READY

7. OPERATION OPTIONS

MEANING OF SR1:

BIT02 = 1 COUNT DATA LATE ERRORS, BUT DON'T TYPE THEM.
BIT02 = 0 COUNT AND TYPE DATA LATE ERRORS.

BIT04 = 1 PORT B SELECTED
BIT04 = 0 PORT A SELECTED

BIT10 = 1 SELECT RANDOM BLOCK ADDRESSING.
BIT10 = 0 SELECT SEQUENTIAL BLOCK ADDRESSING.

BIT12 = 1 DUAL PORT OPERATION.
BIT12 = 0 SINGLE PORT OPERATION.

BIT15 = 1 32 REGISTERS ON RH70
BIT15 = 0 22 REGISTERS ON RH70

NOTE: BIT04 HAS NO MEANING IN SINGLE PORT OPERATION.

8. NON-STANDARD PRINTOUTS

A. MOST PRINTOUTS HAVE THE STANDARD FORMATS DESCRIBED IN THE DEC/X11 DOCUMENT.

B. ERROR MESSAGES DUMP THE CONTENTS OF THE 20 OR 22 REGISTERS. (DEPENDING IF AN RH11 OR RH70 IS USED)

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IN THE FOLLOWING ORDER:

RMCS1	RMWC	RMBA	RMDA	RMCS2	RMDS	RMER1	RMA5
RMLA	RMDB	RMMR1	RMDT	RMSN	RMOF	RMDC	RMHR
RMMR2	RMER2	RMEC1	RMEC2	*RMBAE	*RMCS3		

* PRINTED ON ERROR REPORT IF USING AN RH70 CONTROLLER

9. DUAL PORT SETUP:

TO RUN A DUAL PORT SYSTEM, SK1 HAS TO BE MODIFIED TO INDICATE TO THE MODULE, WHICH PORT THE MODULE IS LOCATED ON. SEE SECTION 7. FOR SK1 OPTIONS.

THE CONTROLLER SELECT SWITCH ON THE RM DRIVE MUST BE IN THE A/B POSITION. THIS SWITCH IS ACTIVATED WHEN THE DRIVE IS CYCLED UP. IF THE SWITCH WAS NOT IN THIS POSITION WHEN DRIVE WAS POWERED UP, THE FOLLOWING STEPS MUST BE TAKEN. PLACE THE SWITCH IN THE A/B POSITION, TURN THE "ON-LINE" SWITCH TO OFF-LINE, THEN TURN THE "ON-LINE" SWITCH TO ON-LINE. THIS WILL PUT THE RM DRIVE IN THE DUAL PORT MODE.

10. BLOCK ADDRESS

EACH SECTOR ON THE RM05, RM03 OR RM02 PACK IS CALLED A BLOCK.

AT THE BEGINNING OF EACH TEST CYCLE, THE CXRMCA0 MODULE SELECTS A STARTING BLOCK ADDRESS SEQUENTIALLY OR RANDOMLY FOR EACH DATA TRANSFER.

WHEN BIT10 OF SK1 IS RESET(0), CXRMCA0 MODULE SELECTS THE BLOCK ADDRESS SEQUENTIALLY FROM BLOCK 0 TO THE LAST BLOCK.

WHEN BIT10 OF SK1 IS SET(1), CXRMCA0 MODULE SELECTS THE BLOCK ADDRESS RANDOMLY BETWEEN BLOCK 0 AND THE LAST BLOCK.

IN SINGLE PORT OPERATION, CXRMCA0 MODULE ACCESSES EVERY BLOCK ON THE TRACKS AS FOLLOWS:

RM03/2 = 0 THRU 4.
RM05 = 0 THRU 18.

IN DUAL PORT OPERATION, CXRMCA0 MODULE ON THE PORT A ACCESSES EVERY BLOCK ON THE TRACKS AS FOLLOWS:

RM03/2 = 0 THRU 2.
RM05 = 0 THRU 9.

IN DUAL PORT OPERATION, CXRMCA0 MODULE ON THE PORT B ACCESSES EVERY BLOCK ON THE TRACKS AS FOLLOWS:

RM03/2 = 3. THRU 4.
RM05 = 10. THRU 18.

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NOTE: THE MANUFACTURES/USERS BAD SECTOR FILE WILL NEVER
 BE ACCESSED IN ANY WAY BY THIS MODULE.

11. BAD SECTOR TABLE

32. LOCATIONS STARTING AT THE LABEL "BADSPT" ARE RESERVED
 FOR 16. BAD SECTOR ENTRIES BY THE USER. EACH BAD SECTOR IS
 SPECIFIED THROUGH ENTERING ITS CYLINDER ADDRESS TO THE
 FIRST WORD AND ITS TRACK AND SECTOR ADDRESS TO THE SECOND
 WORD; THE HIGH BYTE ON THE SECOND WORD IS THE TRACK ADDRESS
 WHILE THE LOW BYTE ON THE SECOND WORD IS THE SECTOR ADDRESS.
 WHEN THE MODULE DETECTS A DATA TRANSFER ERROR, ON ANY OF
 THESE SPOTS, THE MODULE WILL SKIP OVER THE BAD SECTOR AND
 START ANOTHER TEST CYCLE.

THE MODULE ALSO DETECTS BAD SECTORS THROUGH THE "BSE" ERROR BIT
 (BIT15 OF RMER2). WHENEVER THE "BSE" BIT IS SET AFTER EXECUTING
 A DATA TRANSFER COMMAND, THE MODULE WILL IGNORE THE ERROR.

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5          .TITLE  HMCA DEC/X11 SYSTEM EXERCISE MODULE
          ;          DUXCOM  VERSION 6      23-MAY-78
          ;          .LIST  BIN
          ;*****
          REGIN:
000000          122          115          103  MUDNAM: .ASC11 /HMCA / MODULE NAME.
000005          000          XFLAG: .BYTE  OPEN          ;USED TO KEEP TRACK OF WBUF USAGE
000006          176700      ADDR: 176700+0          ;1ST DEVICE ADDR.
000010          000254      VECTOH: 254+0          ;1ST DEVICE VECTOR.
000012          240          DR1: .BYTE  PHYS+0          ;1ST DR LEVEL.
000013          000          DR2: .BYTE  PHIO+0          ;2ND DR LEVEL.
000014          000001      DVID1: 0+1          ;DEVICE INDICATOR 1.
000016          000000      SR1:  OPEN          ;SWITCH REGISTER 1
000020          000000      SR2:  OPEN          ;SWITCH REGISTER 2
000022          000000      SR3:  OPEN          ;SWITCH REGISTER 3
000024          000000      SR4:  OPEN          ;SWITCH REGISTER 4
          ;*****
000026          150000      STA1: 150000          ;STATUS WORD.
000030          001670      INIT: START          ;MODULE STACK ADDR.
000032          000252      SPOIN1: MUDSP          ;MODULE STACK POINTER.
000034          000000      PASCNT: 0          ;PASS COUNTER.
000036          000310      ICOUNT: 200.          ;# OF ITERATIONS PER PASS=200.
000040          000000      ICGUNT: 0          ;LOC TO COUNT ITERATIONS
000042          000000      SUPCNT: 0          ;LOC TO SAVE TOTAL SOFI ERRORS
000044          000000      HRCNT: 0          ;LOC TO SAVE TOTAL HARD ERRORS
000046          000000      SUPPAS: 0          ;LOC TO SAVE SOFI ERRORS PER PASS
000050          000000      HRPPAS: 0          ;LOC TO SAVE HARD ERRORS PER PASS
000052          000000      SISCN1: 0          ;# OF SYS ERRORS ACCUMULATED
000054          000000      RANNUM: 0          ;HOLDS RANDOM # WHEN RANU MACRO IS CALLED
000056          000000      CUNFIG: 0          ;RESERVED FOR MONITOR USE
000060          000000      RES1: 0          ;RESERVED FOR MONITOR USE
000062          000000      RES2: 0          ;RESERVED FOR MONITOR USE
000064          000000      SVR0:  OPEN          ;LOC TO SAVE R0.
000066          000000      SVR1:  OPEN          ;LOC TO SAVE R1.
000070          000000      SVR2:  OPEN          ;LOC TO SAVE R2.
000072          000000      SVR3:  OPEN          ;LOC TO SAVE R3.
000074          000000      SVR4:  OPEN          ;LOC TO SAVE R4.
000076          000000      SVR5:  OPEN          ;LOC TO SAVE R5.
000078          000000      SVR6:  OPEN          ;LOC TO SAVE R6.
000100          000000      CSRA:  OPEN          ;ADDR OF CURRENT CSR.
000102          000000      SBAHM: 0          ;ADDR OF GOOD DATA, OR
000104          000000      ACSM:  OPEN          ;CONTENTS OF CSM.
000106          000000      WASALR: 0          ;ADDR OF BAD DATA, OR
000108          000000      ASTAT:  OPEN          ;STATUS REG CONTENTS.
000110          000000      ERRTYP: 0          ;TYPE OF ERROR
000112          002130      ASB:  OPEN          ;EXPECTED DATA.
000114          000000      AWAS:  OPEN          ;ACTUAL DATA.
000116          000000      RSTR1:  RESTR1          ;RESTART ADDRESS AFTER END OF PASS
000118          000000      ADTO:  OPEN          ;WORDS TO MEMORY PER ITERATION
000120          000000      DRFK:  OPEN          ;WORDS FROM MEMORY PER ITERATION
000122          000000      INTR:  OPEN          ;# OF INTERRUPTS PER ITERATION
000124          000000      IDNUM: 0          ;MODULE IDENTIFICATION NUMBER=0
000126          000000      RBUFVA: RBUF          ;READ BUFFER VIRTUAL ADDRESS
000128          000000      RBUFPA:  OPEN          ;READ BUFFER PHYSICAL ADDRESS
000130          000000      RBUFEA:  OPEN          ;READ BUFFER EA HITS
000132          000400      RBUFSZ: 256.          ;SIZE OF THE READ BUFFER
000134          000000      RBUFPA:  OPEN          ;WRITE BUFFER PHYSICAL ADDRESS
    
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000136          000000      WBUFEA:  OPEN          ;WRITE BUFFER EA BITS
000140          000400      WBUFSZ: 256.          ;WRITE BUFFER SIZE REQUESTED
000142          000000      CDEKCT:  OPEN          ;WRITE BUFFER SIZE AVAILABLE
000144          000000      CDWDCT:  OPEN          ;CDATA/DATCK ERROR COUNT
000146          000000      FREE:  OPEN          ;CDATA/DATCK WORD COUNT
000150          000000      .REPT          SPSIZ          ;RESERVED FOR FUTURE USE
000252          MUDSP:          ;MODULE STACK STARTS HERE.
          ;*****
6 000252          BADSPT:
12 000252          .WORD 177777
000254          .WORD 177777
000256          .WORD 177777
000260          .WORD 177777
000262          .WORD 177777
000264          .WORD 177777
000266          .WORD 177777
000270          .WORD 177777
000272          .WORD 177777
000274          .WORD 177777
000276          .WORD 177777
000300          .WORD 177777
000302          .WORD 177777
000304          .WORD 177777
000306          .WORD 177777
000310          .WORD 177777
000312          .WORD 177777
000314          .WORD 177777
000316          .WORD 177777
000320          .WORD 177777
000322          .WORD 177777
000324          .WORD 177777
000326          .WORD 177777
000330          .WORD 177777
000332          .WORD 177777
000334          .WORD 177777
000336          .WORD 177777
000340          .WORD 177777
000342          .WORD 177777
000344          .WORD 177777
000346          .WORD 177777
000350          .WORD 177777
14
15          ;DO NOT CHANGE THE ORDER OF THE NEXT 4 LOCATIONS, THEY ARE
16          ;INCLUDED FOR MAP22 ROUTINE.
17 000352          000000      PA1: 0
18 000354          000000      XMEM: 0
19 000356          000000      PA2: 0
20 000360          000000      EA2: 0
21
22 000362          000000      DRDRV: 0          ;DRUP DRIVE = 1
23 000364          000000      FLAG70: 0          ;BIT15 SET INDICATES THAT AN 11/70 PRESENT
24 000366          000000      CYLIND: 0          ;CYLINDER ADDRESS FOR DRIVER
25 000370          000000      BSKADR: 0          ;COMPOSITE TRACK AND SECTOR ADDRESS FOR DRIVER
26 000372          000000      DVICE: 0          ;DEVICE NUMBER
27 000374          000000      UNITNO: 0          ;# OF UNIT BEING TESTED
28 000376          000000      BADSEC: 0          ;BAD SECTOR FLAG, USER BAD SECTOR DETECTED = -1.
    
```

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29
30 000400 000000      DLICNT: 0          ;BASE = 100000
31 000402 000000      FUNC: 0           ;DATA LATE ERROR COUNT
32 000404 000000      ZEMJ: 0          ;FUNCTION TO BE PERFORMED
33 000406 000000      FEHADK: 0
34 000410 000000      CLK: 0           ;USED IN TIMEOUT LOOPS
35 000412 000000      TRY: 0
36 000414 000000      DRVTIP: 0        ;DRIVE TYPE FLAG, RM02 = 5, RM03 = 4 AND RM05 = 7
37
38 000416              KBUF: .BLKW 256.
39
40
41
42 001416 001614'      TABLE: S+NCS1
43 001420 001616'      S+NMC
44 001422 001620'      S+NBA
45 001424 001622'      S+NDA
46 001426 001624'      S+NCS2
47 001430 001626'      S+ADS
48 001432 001630'      S+NEM1
49 001434 001632'      S+NAS
50 001436 001634'      S+NLA
51 001440 001636'      S+NDB
52 001442 001640'      S+NMR1
53 001444 001642'      S+NDT
54 001446 001644'      S+NSN
55 001450 001646'      S+NDF
56 001452 001650'      S+NDC
57 001454 001652'      S+NHH
58 001456 001654'      S+NMR2
59 001460 001656'      S+NER2
60 001462 001660'      S+NEC1
61 001464 001662'      S+NEC2
62 001466 001664'      S+NBAE
63 001470 001666'      S+NCS3
64 001472 177777
65
66 001474 000000      RMCS1: 0          ;CONTROL STATUS REGISTER #1
67 001476 000000      RMWC: 0          ;WORD COUNT REGISTER
68 001500 000000      RMBA: 0          ;BUS ADDRESS REGISTER
69 001502 000000      RMDA: 0          ;DESIRED TRACK/SECTOR ADDRESS REGISTER
70 001504 000000      RMCS2: 0         ;CONTROL STATUS REGISTER #2
71 001506 000000      RMDS: 0          ;DRIVE STATUS REGISTER
72 001510 000000      RMEK1: 0         ;ERROR REGISTER #1
73 001512 000000      RMAS: 0          ;ATTENTION SUMMARY REGISTER
74 001514 000000      RMLA: 0          ;LOOK AHEAD REGISTER
75 001516 000000      RMDB: 0          ;DATA BUFFER REGISTER
76 001520 000000      RMM1: 0          ;MAINTENANCE REGISTER #1
77 001522 000000      RMDT: 0          ;DRIVE TYPE REGISTER
78 001524 000000      RMSN: 0          ;SERIAL NUMBER REGISTER
79 001526 000000      RMDF: 0          ;OFFSET REGISTER
80 001530 000000      RMDC: 0          ;DESIRED CYLINDER ADDRESS REGISTER
81 001532 000000      RMR1: 0          ;HOLDING REGISTER
82 001534 000000      RMM2: 0          ;MAINTENANCE REGISTER #2
83 001536 000000      RMR2: 0          ;ERROR REGISTER #2
84 001540 000000      RMEC1: 0         ;ECC POSITION REGISTER
85 001542 000000      RMEC2: 0         ;ECC PATTERN REGISTER

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86 001544 000000      RMBAL: 0          ;BUS ADDRESS EXTENSION REGISTER (RM70)
87 001546 000000      RMCS3: 0         ;CONTROL STATUS REGISTER #3 (RM70)
88
89 001550 000406'      XFEHAD: FEHADK
90 001552 177777      177777
91
92
93
94 000000              ;REGISTER ADDRESS INDEX VALUE
95 000002              NCS1 = 0
96 000004              NMC = 2
97 000006              NBA = 4
98 000010              NDA = 6
99 000012              NCS2 = 10
100 000014             NDS = 12
101 000016             NEM1 = 14
102 000020             NAS = 16
103 000022             NLA = 20
104 000024             NDB = 22
105 000026             NMR1 = 24
106 000030             NDT = 26
107 000032             NSN = 30
108 000034             NDF = 32
109 000036             NDC = 34
110 000040             NHH = 36
111 000042             NMR2 = 40
112 000044             NER2 = 42
113 000046             NEC1 = 44
114 000050             NEC2 = 46
115 000052             NBAE = 50
116
117
118
119 000000              ;DISK ADDRESS EQUATES FOR LIMITS TABLES
120 000002              FT = 0          ;FIRST TRACK
121 000004              LT = 2          ;LAST TRACK
122 000006              LS = 4          ;LAST SECTOR
123 000010              LC = 6          ;LAST CYLINDER
124 000012              BST = 10        ;BAD SECTOR TRACK (DEC 144 FILE)
125 000014              SEC = 12        ;CURRENT SECTOR
126 000016              TRK = 14        ;CURRENT TRACK
127
128
129
130
131
132
133
134
135
136
137 001554 000000      TAB03: .WORD 0          ;FIRST TRACK
138 001556 000000      .WORD 2          ;LAST TRACK
139 001560 000037      .WORD 31         ;LAST SECTOR
140 001562 001466      .WORD 822.        ;LAST CYLINDER
141 001564 000004      .WORD 4.          ;BAD SECTOR TRACK (DEC 144 FILE)
142 001566 000000      .WORD 0          ;CURRENT SECTOR ADDRESS

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143 001570 000000          .A0FD  0          ;CURRENT TRACK ADDRESS
144 001572 000000          .A0RD  0          ;CURRENT CYLINDER ADDRESS
145
146 001574 000000          TAB05: .A0FD  0          ;FIRST TRACK
147 001576 000000          .A0MD  0          ;LAST TRACK
148 001600 000037          .A0RD  31.         ;LAST SECTION
149 001602 001466          .A0RD  822.        ;LAST CYLINDER
150 001604 000022          .A0RD  18.         ;BAD SECTION TRACK (DEC 144 FILE)
151 001606 000000          .A0RD  0          ;CURRENT SECTION ADDRESS
152 001610 000000          .A0FD  0          ;CURRENT TRACK ADDRESS
153 001612 000000          .A0RD  0          ;CURRENT CYLINDER ADDRESS
154
155 001614          SI      .BLKW  22.         ;STORE THE RHYM REGISTER CONTENTS
156
157          ;CONTROL STATUS REGISTER #1
158
159          SC      = 100000        ;SPECIAL CONDITION
160          TRE      = 400000        ;TRANSFER ERROR
161          MCPB     = 200000        ;CONTROL BUS PARITY ERROR
162          DVA      = 4000         ;DRIVE AVAILABLE
163          MDY      = 200          ;CONTROL READY
164          GO       = 1            ;GO BIT
165
166          ;CONTROL STATUS REGISTER #2
167
168          CLR      = 40            ;MASSBUS CLEAR COMMAND
169          IR       = 100          ;SIL0 INPUT READY
170          OR       = 200          ;SIL0 OUTPUT READY
171          MDPE     = 400          ;MASS BUS PARITY
172          MXF      = 1000         ;MISSED TRANSFER ERROR
173          PGE      = 2000         ;PROGRAM ERROR (RH)
174          NEM      = 4000         ;NON-EXISTENCE MEMORY
175          NED      = 10000        ;NON-EXISTENCE DRIVE
176          UPE      = 20000        ;UNIBUS PARITY ERROR
177          WCE      = 40000        ;WRITE CHECK ERROR
178          DIL      = 100000       ;DATA LATE ERROR
179
180          ;RM DRIVE STATUS REGISTER
181
182          OM       = 1            ;OFFSET MODE
183          VV       = 100          ;VOLUME VALID
184          DRY      = 200          ;DRIVE READY
185          DPR      = 400          ;DRIVE PRESENT
186          PGM      = 1000         ;PROGRAMMABLE STATE
187          LBI      = 2000         ;LAST BLOCK TRANSFERRED
188          WKL      = 4000         ;WRITE LOCK
189          MBL      = 10000        ;MEDIUM ONLINE
190          PIP      = 20000        ;POSITIONING OPERATION IN PROGRESS
191          ERR      = 40000        ;DRIVE ERROR
192          ATA      = 100000       ;DRIVE ATTENTION
193
194          ;RM ERROR REGISTER #1
195
196          ILF      = 1            ;ILLEGAL FUNCTION
197          ILR      = 2            ;ILLEGAL REGISTER
198          RMM      = 4            ;REGISTER MODIFICATION REFUSE
199          PAR      = 10           ;PARITY ERROR
    
```

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200          FER      = 20          ;FORMAT ERROR
201          WCF      = 40          ;WRITE CLOCK FAIL
202          ECH      = 100         ;HARD ERROR ECC
203          HCE      = 200         ;HEADER COMPARE FAIL
204          HCRC     = 400         ;HEADER CRC ERROR
205          AOE      = 1000        ;ADDRESS OVERLAP ERROR
206          IAE      = 2000        ;INVALID ADDRESS ERROR
207          WLE      = 4000        ;WRITE LOCK ERROR
208          DTE      = 10000       ;DRIVE TIMING ERROR
209          OPI      = 20000       ;OPERATION INCOMPLETE
210          UNS      = 40000       ;DRIVE UNSAFE
211          DCK      = 100000      ;DATA CHECK ERROR
212
213          ;RM ERROR REGISTER #2
214
215          DPE      = 10           ;DATA PARITY DURING WRITE
216          DVC      = 200         ;DEVICE CHECK
217          LBC      = 2000        ;LOST BIT CLOCK
218          LSC      = 4000        ;LOST SYSTEM CLOCK
219          SKI      = 40000       ;SEEK INCOMPLETE
220          BSE      = 100000      ;BAD SECTION ERROR
221
222          ;RM OFFSET REGISTER
223
224          FMT      = 10000        ;16 BIT DATA FORMAT
225
    
```

```

1          .SBTTL  START OF PROGRAM
2
3 001670 012767 000400 176216  START:  MOV    #256,,ADU    ;256 WORDS TO MEM/ITERATION
4 001676 012767 000400 176212  MOV    #256,,ADFK  ;256 WORDS FROM MEM/ITERATION
5 001704 012767 000012 176206  MOV    #10,,INTK  ;10 INTERRUPTS/ITERATION
6 001712 016767 176076 176452  MOV    DV101,DVICE ;GET DRIVES TO TEST
7 001720 016706 176106  MOV    SPOINT,R6  ;INITIATE STACK POINT
8 001724 012767 000003 177622  MOV    #3,,TAB03+FI ;SETUP #403/2 FIRST TRACK AND
9 001732 012767 000004 177616  MOV    #4,,TAB03+LI ;SETUP LAST TRACK, IF THE DUAL PORT AND
10                                     ;PORT B ARE SELECTED.
11 001740 012767 000012 177626  MOV    #10,,TAB05+FI ;SETUP #405 FIRST TRACK AND
12 001746 012767 000022 177622  MOV    #16,,TAB05+LI ;SETUP LAST TRACK, IF THE DUAL PORT AND
13                                     ;PORT B ARE SELECTED.
14 001754 016700 176036  MOV    SR1,R0     ;GET SWITCH REGISTER #1
15 001760 042700 167757  BIC    #*C<BIT12:BIT4>,R0
16 001764 022700 010020  CMP    #BIT12:BIT4,R0 ;DUAL PORT AND PORT B ?
17 001770 001415  BEQ    1S        ;BR IF YES
18 001772 005067 177556  CLR    TAB03+FI   ;NO, SETUP FIRST TRACK TO 0
19 001776 005067 177572  CLR    TAB05+FI
20 002002 032700 010000  BIT    #BIT12,R0  ;DUAL PORT AND PORT A ?
21 002006 001406  BEQ    1S        ;BR IF NO
22 002010 012767 000002 177540  MOV    #2,,TAB03+LI ;SETUP #403/2 LAST TRACK AND
23 002016 012767 000011 177552  MOV    #9,,TAB05+LI ;SETUP #405 LAST TRACK FOR PORT A
24 002024 1S:
25 002024 012767 177777 177534  MOV    #-1,TAB03+SEC ;INITIALIZE THE SECTOR ADDRESS
26 002032 012767 177777 177546  MOV    #-1,TAB05+SEC
27 002040 016767 177510 177522  MOV    TAB03,TAB03+TRK ;INITIALIZE THE TRACK ADDRESS
28 002046 016767 177522 177534  MOV    TAB05,TAB05+TRK
29 002054 005067 177512  CLR    TAB03+CYL  ;INITIALIZE THE CYLINDER ADDRESS
30 002060 005067 177526  CLR    TAB05+CYL
31 002064 005067 176310  CLD    DLTCNT    ;RESET THE DATA RATE ERROR COUNT
32 002070 004767 003556  JSR    PC,SETUP  ;SETUP REGISTER ADDRESSES AND PSEL BIT
33 002074 012767 177777 176272  MOV    #1,UNITNO ;PRE-SET SET UNIT NUMBER
34 002107 2S:
35 002102 004767 001606  JSR    PC,GETDRV ;GO GET A DRIVE
36 002106 000407  BR     3S        ;RETURN HERE WHEN ALL DRIVES ARE DONE
37 002110 004567 003116  JSR    RS,READY  ;SEE IF DRIVE IS AVAILABLE
38 002114 000772  BR     2S        ;BR IF NOT AVAILABLE
39 002116 012777 000023 177350  MOV    #23,,HMCS1 ;GO ISSUE A PACK ACKNOWLEDGE COMMAND TO
40                                     ;SET "VV" BIT IN DRIVE STATUS.
41 002124 000766  BR     2S        ;DO NEXT DRIVE
42 002126 3S:
43 002126 000404  BR     RSTRT1   ;START THE PASS
44
45 002130 005767 175700  RSTRT1: TST    PASCNT ;SUPPORT DT03
46 002134 001001  BRNE  RSTRT1
47 002136 000654  BR     START
48 002140  RSTRT1:
49 002140 104415 000000' 000124' GETPAS,BEGIN, HBUFVA ;GET PHYSICAL ADDRESS FROM 16-BIT HBUFVA
50 002146  LOOP1:
51 002146 012767 177777 176220  MOV    #-1,UNITNO ;PRE-SET UNIT NUMBER
52 002154 004767 001140  JSR    PC,GENADR  ;GENERATE BLOCK ADDRESS
53 002160  LOOP2:
54 002160 004767 001530  JSR    PC,GETDRV  ;GO GET A DRIVE
55 002164 000413  BR     2S        ;RETURNS HERE IF ALL DRIVES DONE
56 002166 004567 003040  JSR    RS,READY  ;SEE IF DRIVE IS READY
57 002172 000772  BR     LOOP2    ;LOOP BACK IF NOT READY
    
```

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58
59 002174 004767 001450  JSR    PC,LOADPR ;LOAD BLOCK ADDRESS
60 002200 004767 000022  JSR    PC,CYCLE  ;COMMAND SEQUENCE ROUTINE
61 002204 000240  NOP                                     ;ERROR EXIT FOR CYCLE ROUTINE
62 002206 1S:
63 002206 104413 000000' ENDTIS,BEGIN ;SIGNAL END OF ITERATION.
64 002212 000762  BR     LOOP2    ;MONITOR SHALL TEST END OF PASS
65 002214 2S:
66 002214 005767 176152  TST    DVICE     ;ARE ALL DEVICES DROPPED ?
67 002220 001352  BRNE  LOOP1     ;BR IF NO
68
69 002222 104410 000000' ENDS,BEGIN ;
70
    
```

```

1                                     ;*** SUBROUTINES ***
2
3                                     ;CALL
4                                     ;
5                                     ;   JSR   PC,CYCLE   ;CALL ROUTINE
6                                     ;   BR    ?           ;ERROR EXIT
7                                     ;   RET                   ;NORMAL RETURN
8
9                                     ;
10                                CYCLE: CLR    THY           ;CLEAR THE RETRY COUNT
11                                GWFBS,  BEGIN           ;GET WRITE BUFFER INFORMATION
12                                JSR    PC,WAIT         ;WAIT FOR DRIVE
13                                BR     ZS             ;EXIT ON ERROR
14                                JSR    HS,SEEK         ;DO AN EXPLICIT SEEK
15                                BR     IS             ;EXIT IF SEEK ERROR
16                                JSR    RS,WRITE        ;WRITE DATA COMMAND
17                                BR     IS             ;FAILURE EXIT
18                                JSR    PC,WAIT         ;WAIT FOR DRIVE
19                                BR     ZS             ;EXIT ON ERROR
20                                JSR    HS,SEEK         ;DO AN EXPLICIT SEEK
21                                BR     IS             ;EXIT IF SEEK ERROR
22                                JSR    RS,WRITCK       ;WRITE CHECK DATA COMMAND
23                                BR     IS             ;FAILURE EXIT
24                                JSR    PC,WAIT         ;WAIT FOR DRIVE
25                                BR     ZS             ;EXIT ON ERROR
26                                JSR    HS,SEEK         ;DO AN EXPLICIT SEEK
27                                BR     IS             ;EXIT IF SEEK ERROR
28                                JSR    RS,READ         ;READ DATA COMMAND
29                                BR     IS             ;FAILURE EXIT
30                                CUATAS,BEGIN,MBUFFA    ;REQUEST FOR MONITOR TO CHECK DATA
31                                JS      3S             ; IF ERROR, RETURN AT TAG 3S
32                                ADD    #2,(SP)         ;ADJUST GOOD RETURN FOR CYCLE ROUTINE
33                                BR     3S             ;EXIT
34
35                                1S:   TST    DPPDHV       ;DROP THE DRIVE ?
36                                BR     ZS             ;BR IF NO
37                                JSR    PC,DROP         ;DROP THE DRIVE
38                                MEGMS,BEGIN,DRM ;ASCII MESSAGE CALL WITH COMMON HEADER
39                                RET    PC             ;EXIT
40
41                                2S:
42                                3S:
43
44
45
46

```

```

1                                     ;SBTTL  COMMAND SUBROUTINES
2
3                                     ;CALL
4                                     ;
5                                     ;   JSR   HS,COMMAND ;CALL COMMAND ROUTINE
6                                     ;   BR    ?           ;ERROR RETURN
7                                     ;   RET                   ;NORMAL RETURN
8
9                                     ;THE ROUTINE COMMAND NAMES:
10                                ;WRITE  = WRITE DATA, USING *WRITE BUFFER,
11                                ;WRITCK = WRITE CHECK DATA, USING *WRITE BUFFER,
12                                ;READ   = READ DATA, USING READ BUFFER,
13                                ;WRCKCK = WRITE CHECK DATA, USING READ BUFFER
14                                ;WRCKCR = WRITE CHECK HEADER AND DATA, USING READ BUFFER
15                                ;READHD  = READ HEADER AND DATA, USING READ BUFFER
16                                ;SEEK   = SEEK COMMAND
17
18                                18 002364 012767 000161 176010 WRITE: MOV    #161,FUNC   ;LOAD WRITE FUNCTION
19                                19 002372 012767 002364 176006 MOV    *WRITE,FPADK   ;SAVE WHERE WE WERE
20                                20 002400 016746 175536 MOV    #BUFSSZ,-(SP) ;GET WRITE SIZE
21                                21 002404 005416 NEG    (SP)           ;NEGATE IT
22                                22 002406 012677 177064 MOV    (SP)+,#HWC   ;LOAD WORD COUNT
23                                23 002412 016777 175516 MOV    #BUFFA,#MBA  ;LOAD BUFFER ADDRESS
24                                24 002420 016746 175512 MOV    #BUFEA,-(SP) ;GET EXTENDED MEMORY BITS
25                                002424 006316 ASL    (SP)           ;SHIFT 4 PLACES TO THE LEFT
26                                002426 006316 ASL    (SP)           ;TO LINE UP WITH HWC51
27                                002430 006316 ASL    (SP)
28                                002432 006316 ASL    (SP)
29                                002434 012667 175714 MOV    (SP)+,XMEM    ;SAVE THE SHIFTED BITS
30                                25 002440 000167 000400 JMP    GO1           ;CONTINUE
31
32                                27 002444 012767 000151 175730 WRITCK: MOV    #151,FUNC   ;LOAD WRITE-CHECK FUNCTION
33                                28 002452 012767 002444 175726 MOV    *WRITCK,FPADK ;SAVE WHERE WE WERE
34                                29 002460 016746 175456 MOV    #BUFSSZ,-(SP) ;GET WRITE SIZE
35                                30 002464 005416 NEG    (SP)           ;NEGATE IT
36                                31 002466 012677 177004 MOV    (SP)+,#HWC   ;LOAD WORD COUNT
37                                32 002472 016777 175436 MOV    #BUFFA,#MBA  ;LOAD BUFFER ADDRESS
38                                33 002500 016746 175432 MOV    #BUFEA,-(SP) ;GET EXTENDED MEMORY BITS
39                                002504 006316 ASL    (SP)           ;SHIFT 4 PLACES TO THE LEFT
40                                002506 006316 ASL    (SP)           ;TO LINE UP WITH HWC51
41                                002510 006316 ASL    (SP)
42                                002512 006316 ASL    (SP)
43                                002514 012667 175634 MOV    (SP)+,XMEM    ;SAVE THE SHIFTED BITS
44                                34 002520 000167 000320 JMP    GO1           ;CONTINUE
45
46                                36 002524 012767 000171 175650 READ:  MOV    #171,FUNC   ;LOAD READ FUNCTION
47                                37 002532 012767 002524 175646 MOV    #READ,FPADR  ;SAVE WHERE WE WERE
48                                38 002540 016746 175366 MOV    #RUFSSZ,-(SP) ;GET READ SIZE
49                                39 002544 005416 NEG    (SP)           ;NEGATE IT
50                                40 002546 012677 176724 MOV    (SP)+,#HWC   ;LOAD WORD COUNT
51                                41 002552 016777 175350 MOV    #RUFFA,#MBA  ;LOAD BUFFER ADDRESS
52                                42 002560 016746 175344 MOV    #RUFEA,-(SP) ;GET EXTENDED MEMORY BITS
53                                002564 006316 ASL    (SP)           ;SHIFT 4 PLACES TO THE LEFT
54                                002566 006316 ASL    (SP)           ;TO LINE UP WITH HWC51
55                                002570 006316 ASL    (SP)
56                                002572 006316 ASL    (SP)
57                                002574 012667 175554 MOV    (SP)+,XMEM    ;SAVE THE SHIFTED BITS

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43 002600 000167 000240          JMP      G01          ;CONTINUE
44
45 002604 012767 000151 175570 WRDCKR: MOV      #151,FUNC      ;LOAD WRITE-CHECK FUNCTION
46 002612 012767 002604' 175566      MOV      #WRDCKR,FEHADR ;SAVE WHERE WE WERE
47 002620 016746 175306      MOV      #RBUF SZ,-(SP) ;GET WRITE SIZE
48 002624 005416      NEG      (SP)          ;NEGATE IT
49 002626 012677 176644      MOV      (SP)+, #RWC   ;LOAD WORD COUNT
50 002632 016777 175270 176640      MOV      #RBUFPA, #RMB ;LOAD BUFFER ADDRESS
51 002640 016746 175264      MOV      #RBUFEA,-(SP) ;GET EXTENDED MEMORY BITS
    002644 006316      ASL      (SP)          ;SHIFT 4 PLACES TO THE LEFT
    002646 006316      ASL      (SP)          ;TO LINE UP WITH RHC1
    002650 006316      ASL      (SP)
    002652 006316      ASL      (SP)
    002654 012667 175474      MOV      (SP)+, XMEM   ;SAVE THE SHIFTED BITS
52 002660 000167 000160      JMP      G01          ;CONTINUE
53
54 002664 012767 000153 175510 WRHCKR: MOV      #153,FUNC      ;LOAD WRITE-CHECK-HEADER-AND-DATA FUNCTION
55 002672 012767 002664' 175506      MOV      #WRHCKR,FEHADR ;SAVE WHERE WE WERE
56 002700 016746 175226      MOV      #RBUF SZ,-(SP) ;GET WRITE SIZE
57 002704 005416      NEG      (SP)          ;NEGATE IT
58 002706 012677 176564      MOV      (SP)+, #RWC   ;LOAD WORD COUNT
59 002712 016777 175210 176560      MOV      #RBUFPA, #RMB ;LOAD BUFFER ADDRESS
60 002720 016746 175204      MOV      #RBUFEA,-(SP) ;GET EXTENDED MEMORY BITS
    002724 006316      ASL      (SP)          ;SHIFT 4 PLACES TO THE LEFT
    002726 006316      ASL      (SP)          ;TO LINE UP WITH RHC1
    002730 006316      ASL      (SP)
    002732 006316      ASL      (SP)
    002734 012667 175414      MOV      (SP)+, XMEM   ;SAVE THE SHIFTED BITS
61 002740 000167 000100      JMP      G01          ;CONTINUE
62
63 002744 012767 000173 175430 READHD: MOV      #173,FUNC      ;READ HEADER AND DATA COMMAND
64 002752 012767 002744' 175426      MOV      #READHD,FEHADR ;SAVE THE CALLING ADDRESS
65 002760 016746 175146      MOV      #RBUF SZ,-(SP) ;GET THE BUFFER SIZE
66 002764 005416      NEG      (SP)          ;THE NEGATIVE WORD COUNT
67 002766 012677 176504      MOV      (SP)+, #RWC   ;LOAD THE WORD COUNT
68 002772 016777 175130 176500      MOV      #RBUFPA, #RMB ;LOAD BUFFER ADDRESS
69 003000 016746 175124      MOV      #RBUFEA,-(SP) ;GET EXTENDED MEMORY BITS
    003004 006316      ASL      (SP)          ;SHIFT 4 PLACES TO THE LEFT
    003006 006316      ASL      (SP)          ;TO LINE UP WITH RHC1
    003010 006316      ASL      (SP)
    003012 006316      ASL      (SP)
    003014 012667 175334      MOV      (SP)+, XMEM   ;SAVE THE SHIFTED BITS
70 003020 000167 000020      JMP      G01          ;EXECUTE THE COMMAND
71
72 003024 012767 000105 175350 SELK:  MOV      #105,FUNC      ;SEEK COMMAND
73 003032 012767 003024' 175346      MOV      #SELK,FEHADR  ;CALLING ADDRESS
74 003040 000167 000104      JMP      G02          ;EXECUTE THE SEEK COMMAND
75
    
```

```

1          .SRITL  COMMAND EXECUTE ROUTINE
2
3 003044          G01:
4 003044 005767 175314          1ST  FLAG70          ;11/70??
5 003050 100034          RPL      1$          ;NO
6 003052 017767 176422 175272      MOV      #RMB,PA18   ;GET 18 BIT ADDR
7 003060 006267 175270          ASH      XMEM         ;SHIFT EA BITS TO POSITION 4,5
8 003064 006267 175264          ASR      XMEM
9 003070 006267 175260          ASH      XMEM
10 003074 006267 175254          ASR      XMEM
11 003100 104416 000000' 000352'  MAP22$, #BEGIN,PA18 ; GET 22-BIT ADDR FROM 18-BIT ADDR
12 003106 016777 175244 176364      MOV      PA22, #RMB   ;LOAD BA REG
13 003114 016777 175240 176422      MOV      EA22, #RMB   ;LOAD BAE REG
14 003122 042767 000074 175230      BIC      #74,EA22     ;CLEAR UNWANTED BITS
15 003130 000367 175224          SWAB     EA22         ;LOAD INTO BITS 8,9
16 003134 016767 175220 175212      MOV      EA22,XMEM   ;LOAD XMEM TO SET INTO FUNCTION CODE
17 003142 056767 175206 175232      BIS      XMEM,FUNC   ;LOAD EXTENDED MEMORY BITS
18 003150
19 003150 012777 010000 176350          MOV      #FMT, #RMOF ;SET 16 BIT FORMAT
20 003156 016777 175204 176344      MOV      CYLIND, #RMC ;LOAD THE CYLINDER ADDRESS
21 003164 016777 175200 176310      MOV      DSKADR, #RMD ;LOAD THE TRACK AND SECTOR ADDRESS
22 003172 016777 175204 176274      MOV      FUNC, #RMC1 ;EXECUTE THE FUNCTION
23 003200 104400 000000'          EXITS, #BEGIN       ;EXIT TO MONITOR. MODULE WAIT FOR INTERRUPT.
24 003204
25          ;-----
    003204 000004 000000' 003212'  PIRGS,BEGIN,1$      ; QUEUE UP TO CONTINUE AT 1$ AND RTI
    ;-----
26
27 003212 004567 000602          1$:  JSR      #5, #RDRKS ;GO CHECK FOR ERRORS
28 003216 000403          BR      2$          ;ERRORS DETECTED
29 003220 062705 000002          ADD     #2,R5       ;NO ERRORS DETECTED
30 003224 000427          BR      3$          ;EXIT
31 003226
32 003226 005767 175130          2$:  TST     #RDRPV     ;DROP DRIVE ?
33 003232 001031          BNE     5$          ;BR IF YES
34 003234 004567 001772          JSR     #5, #READY ;GET THE DRIVE AGAIN
35 003240 000426          BR      5$          ;IF FAILS, EXIT
36 003242 022767 177777 175126      CMP     #-1, #BADSEC ;USH DETECTED BAD SPOT ?
37 003250 001422          BEQ     5$          ;BR IF YES
38 003252 032767 100000 175116      BIT     #BSE, #BADSEC ;HAD SECTOR ERROR ?
39 003260 001016          BNE     5$          ;BR IF YES
40 003262 005267 175124          INC     TRY         ;INCREMENT THE RETRY COUNT
41 003266 026727 175120 000003      CMP     TRY, #3     ;OVER 3 RETRIES ?
42 003274 103406          BLO     4$          ;BR IF NOT
43 003276 104403 000000' 007050'  MSGNS, #BEGIN, EXCD ;ASCII MESSAGE CALL WITH COMMON HEADER
44 003304 005067 175102          CLM     TRY
45 003310 000402          BR      5$          ;EXIT
46 003312 162705 000004          4$:  SUB     #4,R5       ;ADJUST ADDRESS FOR RETRY
47 003316 000205          5$:  RTS     #5          ;EXIT
48
49
50          ;THIS ROUTINE IS USED TO GENERATOR A DISK ADDRESS ON WHICH THE
51 ;DRIVE WILL SEEK, WRITE, WRITE CHECK AND READ ON.
52 ;
53 ;CALL
54          JSR     PC, #GENADR ;CALL GENERATOR ROUTINE
55 ;
    
```

```

56 003320
57 003320 012700 001554' GENADR:
58 003324
59 003324 032767 002000 174464 1S: MOV #TAB03,R0 ;GET RM03 LIMITER AND STORAGE TABLE
60 003332 001476
61 003334 104417 000000' RANDB,BEGIN
62 003340 016760 174510 000016 MOV #ANNUM,CYL(R0) ;GENERATE THE CYLINDER ADDRESS
63 003346 042760 176000 000016 BIC #176000,CYL(R0) ;CHOP OFF HIGH BITS
64 003354 104417 000000' RANDB,BEGIN
65 003360 016760 174470 000014 MOV #ANNUM,TRK(R0) ;GENERATE THE TRACK ADDRESS
66 003366 042760 177400 000014 BIC #177400,TRK(R0) ;CHOP OFF HIGH BYTE
67 003374 104417 000000' RANDB,BEGIN
68 003400 016760 174450 000012 MOV #ANNUM,SEC(R0) ;GENERATE THE SECTOR ADDRESS
69 003406 042760 177400 000012 BIC #177400,SEC(R0) ;CHOP OFF HIGH BYTE
70
71 003414 026060 000006 000016 2S: CMP LC(R0),CYL(R0) ;OVER THE CYLINDER LIMIT ?
72 003422 103003 BHIS 35 ;RBR IF NOT
73 003424 006260 000016 ASK CYL(R0) ;REDUCE TO HALF
74 003430 000771 BR 25 ;CHECK AGAIN
75 003432 026060 000002 000014 3S: CMP LT(R0),TRK(R0) ;OVER THE TRACK LIMIT ?
76 003440 103003 BHIS 45 ;RBR IF NOT
77 003442 006260 000014 ASK TRK(R0) ;REDUCE TO HALF
78 003446 000771 BR 35
79 003450 026060 000004 000012 4S: CMP LS(R0),SEC(R0) ;OVER THE SECTOR LIMIT ?
80 003456 103003 BHIS 55 ;RBR IF NOT
81 003460 006260 000012 ASK SEC(R0) ;REDUCE TO HALF
82 003464 000771 BR 45 ;CHECK AGAIN
83 003466 026060 000002 000014 5S: CMP LT(R0),TRK(R0) ;TO ENSURE TRACK IN THE
84 003474 103005 BHIS 65 ;RANGE BETWEEN FT AND LT
85 003476 166060 000002 000014 SUB LT(R0),TRK(R0) ;ADJUST TO THE UPPER PORTION
86 003504 005360 000014 DEC TRK(R0)
87 003510 026060 000014 6S: CMP TRK(R0),FT(R0) ;HIGHER THAN THE LOW LIMITER ?
88 003516 103035 BHIS 85 ;EXIT IF SU, ELSE
89 003520 066060 000000 000014 ADD FT(R0),TRK(R0) ;ELSE, ADJUST TO THE LOWER PORTION
90 003526 000757 BR 55 ;EXIT
91
92 ;GENERATE DISK ADDRESS BY SEQUENTIAL ADDRESSING
93 003530 7S:
94 003530 005260 000012 INC SEC(R0) ;SEQUENTIAL ACCESS TO ALL SECTORS
95 003534 026060 000004 000012 CMP LS(R0),SEC(R0) ;TIME TO ADJUST THE TRACK ADDRESS
96 003542 103023 BHIS 85 ;RBR IF NOT
97 003544 005060 000012 CLR SEC(R0) ;RESET THE SECTOR ADDRESS
98 003550 005260 000014 INC TRK(R0) ;INCREMENT THE TRACK ADDRESS
99 003554 026060 000002 000014 CMP LT(R0),TRK(R0) ;OVER LIMIT ?
100 003562 103013 BHIS 85 ;RBR IF NOT
101 003564 016060 000000 000014 MOV FT(R0),TRK(R0) ;RESET THE STARTING TRACK ADDRESS
102 003572 005260 000016 INC CYL(R0) ;ADJUST THE CYLINDER ADDRESS
103 003576 026060 000016 000016 CMP LC(R0),CYL(R0) ;OVER LIMIT ?
104 003604 103002 BHIS 85 ;RBR IF NOT
105 003606 005060 000016 CLR CYL(R0) ;ELSE, RESET CYLINDER ADDRESS
106
107 ;CHECK FOR MFG/USR BAD SECTOR FILE IN TRACK ADDRESS
108 003612 8S:
109 003612 026060 000006 000016 CMP LC(R0),CYL(R0) ;IS IT THE LAST CYLINDER ?
110 003620 001004 BNE 98 ;RBR IF NO
111 003622 026060 000010 000014 CAP BST(R0),TRK(R0) ;IS IT THE BAD SECTOR TRACK (DEC 144) ?
112 003630 001635 BEQ 15 ;YES, THIS IS BAD SECTOR FILE
    
```

```

113
114 003632
115 003632 022700 001574' 9S:
116 003636 001403 CMP #TAB05,R0 ;IS RM05 TABLE GENERATED YET ?
117 003640 012700 001574' MOV #TAB05,R0 ;RBR IF YES
118 003644 000627 BR 15 ;LOAD INDEX TO RM05 TABLE
119 003646 000207 RIS PC ;AND GENERATE ADDRESSES
120
121 ;THIS ROUTINE IS USED TO LOAD THE SECTOR, TRACK AND CYLINDER ADDRESSES
122 ;FOR THE DEVICE UNDER TEST.
123 ;
124 ;CALL
125 ;
126 ; JSR PC,LDADR ;CALL LOAD ADDRESS ROUTINE
127 003650 012700 001554' LDADR: MOV #TAB03,R0 ;ASSUME RM03/2 TABLE
128 003654 022767 000007 174532 CMP #,DRV TYP ;IS THIS AN RM05 ?
129 003662 001002 RNE 15 ;RBR IF NO
130 003664 012700 001574' MOV #TAB05,R0 ;GET RM05 TABLE
131 003670 116067 000012 174472 1S: MOVB SEC(R0),DSKADR ;LOAD SECTOR ADDRESS
132 003676 116067 000014 174465 MOVB TRK(R0),DSKADR+1 ;LOAD TRACK ADDRESS
133 003704 016067 000016 174454 MOV CYL(R0),CYLIND ;LOAD CYLINDER ADDRESS
134 003712 000207 RTS PC ;EXIT
135
136 ;-----
137 ; JSR PC,GETDRV ;GO GET A DRIVE
138 ; RET ;EXIT ADDRESS IF NO DRIVE IS AVAIL
139 ; ;NORMAL RET
140
141 003714 GETDRV:
142 003714 005267 174454 1S: INC UNITNO ;POINT TO NEXT DRIVE
143 003720 026727 174450 000010 CMP UNITNO,#8 ;DONE LOOKING?
144 003726 002010 BGE 45 ;EXIT IF ALL DRIVE SELECTED
145 003730 016700 174440 2S: MOV UNITNO,R0 ;USE AS AN INDEX
146 003734 136067 003752' 174430 BITB BITTAB(R0),DVICL ;TEST THIS DEVICE?
147 003742 001764 HEQ 15 ;LOOP BACK, IF NOT SELECTED
148 003744 062716 000002 3S: ADD #2,(SP) ;ADJUST FOR DRIVE AVAILABLE RETURN
149 003750 4S:
150 003750 000207 RIS PC
151
152 003752 001001 BITTAB: 1001
153 003754 004004 4004
154 003756 020020 20020
155 003760 100100 100100
156
157 ;-----
158
159 003762 012701 000001 DROP: MOV #1,R1 ;INITIALIZE DRUP PICKER
160 003766 016700 174402 MOV UNITNO,R0 ;GET THE DRIVE NUMBER
161 003772 001403 BEQ 25 ;IF DRIVE 0 GO DROP IT
162 003774 006301 1S: ASL R1 ;POINT TO NEXT DRIVE
163 003776 005300 DEC R0 ;IS THIS THE ONE ?
164 004000 001375 BNE 15 ;NO, LOOK AGAIN
165 004002 040167 174364 2S: BIC R1,DVICL ;DROP THE DRIVE
166 ;*****
;CONVERT UNITNO TO ASCII AND
;STORE AT ADR1
004006 104420 000000' 000374' OT0AS,BEGIN,UNITNO,ADR1
    
```

167 004016 000207
 168

```

;*****
RIS      PC      ;RETURN
    
```

```

1          .SHITL  ERROR HANDLER ROUTINE
2
3
4          ;CALL:
5          ;      JSP      R5,ERRORS      ;CHECK IF ANY ERROR OR RM/RM
6          ;      PET      ;ERROR RET
7          ;      RET2     ;NORMAL RET
8
9          ERRORS:
10         CLR      DRPDVY      ;CLEAR DRUP DRIVE FLAG
11         CLP      BAUSEC      ;CLEAR THE BAD SECTOR FLAG
12         JSR      PC,ERRSUB1   ;SET UP FOR ERROR REPORT
13         JSR      PC,GET       ;READ AND STORE ALL REGISTERS
14         MOV      S+NCS1,R0    ;GET CONTROL STATUS
15         BIC      #'C<RDY!GO>,R0
16         CMP      #RDY,R0      ;RDY = 1 AND GO = 0 ?
17         BEQ      IS          ;BR IF YES
18         MOV      #1,DRPDVY    ;SET DRUP DRIVE FLAG
19         MSGNS,BEGIN,NUDDY    ;ASCII MESSAGE CALL WITH COMMON HEADER
20         MOV      #3,ERRTYP    ;SAVE THE ERROR TYPE
21         ;*****
22         HDRERS,BEGIN,TABLE    ;CONTROLLER NOT READY
23         ;*****
24         JMP      J3S
25
26         21 004106 000167 001116
27         22
28         23 004112 016700 175510      15:  MOV      S+NDS,R0      ;GET DRIVE STATUS
29         24 004116 005100              CUM      R0
30         25 004120 032700 010700      BIT      #DRY!DPR!VV!MUL,R0      ;DRIVE READY ?
31         26 004124 001416              BEQ      ZS          ;BR IF YES
32         27 004126 012767 000001 174226 MOV      #1,DRPDVY    ;SET DRUP DRIVE FLAG
33         28 004134 104403 000000' 007154' MSGNS,BEGIN,NUDDY    ;ASCII MESSAGE CALL WITH COMMON HEADER
34         29 004142 012767 000006 173736 MOV      #6,ERRTYP    ;SAVE THE ERROR TYPE
35         30
36         004150 104405 000000' 001416' HDRERS,BEGIN,TABLE    ;DRIVE NOT READY
37         ;*****
38         JMP      J3S
39
40         31 004156 000167 001046
41         32
42         33 004162 016700 175426      26:  MOV      S+NCS1,R0      ;CHECK IF SEEK COMMAND IS EXECUTED
43         34 004166 042700 177701      BIC      #177701,R0    ;LEFT ONLY FUNCTION BITS
44         35 004172 022700 000004      CMP      #4,R0        ;A SEEK COMMAND ?
45         36 004176 001023              BNE      JS          ;BR IF NOT
46         37 004200 016700 175422      MOV      S+NDS,R0    ;CHECK THE STATUS
47         38 004204 042700 057777      BIC      #'C<ATA!PIP>,R0
48         39 004210 022700 100000      CMP      #ATA,R0     ;ATA = 1 AND PIP = 0 ?
49         40 004214 001423              BEQ      4S          ;BR IF YES
50         41 004216 012777 000013 175250 MOV      #13,@RMCS1   ;RELEASE THE DRIVE
51         42 004224 104407 000000'      BREAKS,BEGIN        ;TEMPORARY RETURN TO MONITOR...
52         004230 104407 000000'      BREAKS,BEGIN        ;THEN CONTINUE AT NEXT INSTRUCTION.
53         43 004234 104403 000000' 007100' MSGNS,BEGIN,SEEKER    ;ASCII MESSAGE CALL WITH COMMON HEADER
54         44 004242 000167 000746      JMP      32S        ;EXIT
55         45
56         46 004246 012777 000013 175220 3S:  MOV      #13,@RMCS1   ;RELEASE THE DRIVE
57         47 004254 104407 000000'      BREAKS,BEGIN        ;TEMPORARY RETURN TO MONITOR...
58         004260 104407 000000'      BREAKS,BEGIN        ;THEN CONTINUE AT NEXT INSTRUCTION.
59         48
60         49 004264 032767 060000 175322 4S:  BIT      #MCP:IRE,S+NCS1   ;ANY RM ERROR DETECTED ?
61         50 004272 001010              BNE      5S          ;BR IS SO
62         51 004274 032767 040000 175324 BIT      #ERR,S+NDS    ;ANY DRIVE ERROR ?
    
```



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52 004302 001004                               BNE 55          ;BR IS SU
53 004304 062705 000002                       ADD  #2,R5     ;ELSE,ADJUST FOR GOOD RETURN
54 004310 000167 000714                       JMP 335        ;EXIT
55
56 ;CHECK TO SEE IF USER DEFINED ANY BAD SPOTS
57 004314                               ;CHECK IF ON THE BAD SPOT ?
58 004314 012700 000252'                       MOV  #BADSPT,R0 ;TABLE ADDRESS
59 004320 012701 000020                       MOV  #16,,R1   ;TOTAL BAD SECTOR COUNT
60 004324 021067 174036                       65:  CMP  (R0),CYLIND ;ON THE SAME CYLINDER ?
61 004330 001014                               BNE 75        ;BR IF NOT
62 004332 126067 000003 174031                CMPB 3(R0),DSNADR+1 ;ON THE SAME TRACK ?
63 004340 001010                               BNE 78        ;BR IF NOT
64 004342 126067 000002 174020                CMPB 2(R0),DSKADR  ;ON THE SAME SECTOR ?
65 004350 001004                               BNE 75        ;BR IF NOT
66 004352 012767 177777 174016                MOV  #-1,BADSEC ;SET USR BAD SPOT FLAG
67 004360 000423                               BR 95         ;EXIT
68 004362 062700 000004                       75:  ADD  #4,R0   ;ADJUST TABLE ADDRESS
69 004366 005301                               DEC  R1        ;DECREMENT ONE BSE COUNT
70 004370 001355                               BNE 65        ;BR IF NOT END OF TABLE
71
72 ;CHECK FOR HARDWARE DETECTED BAD SPOTS
73 004372                               85:
74 004372 022767 000200 175230                CMP  #HCE,S+NER1 ;IS IT ONLY A HEADER COMPARE ERROR ?
75 004400 001415                               BEQ  105      ;BR IF YES
76 004402 032767 000400 175220                BIT  #HCKC,S+NER1 ;HEADER CRC ERROR ?
77 004410 001011                               RNE  105      ;BR IF YES
78 004412 022767 100000 175236                CMP  #HSE,S+NER2 ;IS IT A HARDWARE DETECTED ERROR ?
79 004420 001005                               BNE  105      ;BR IF NO
80 004422 016767 175230 173746                MOV  S+NER2,BADSEC ;GET RMR2 FOR BSE
81
82 004430 000167 000574                       95:  JMP  335      ;TAKE ERROR EXIT , BUT NOT REPORT THE ERROR
83
84 ;CHECK FOR OTHER ERRORS
85 004434                               105:
86 004434 032767 020000 175152                BIT  #MCPE,S+NCS1 ;RH CONTROL BUS PARITY ERROR ?
87 004442 001405                               BEQ  115      ;BR IF NOT
88 004444 104403 000000' 007040'              MSGNS,BEGIN,MCPERR ;ASCII MESSAGE CALL WITH COMMON HEADER
89 004452 000167 000536                       JMP  325      ;EXIT
90 004456 032767 000400 175140                115:  BIT  #MDPE,S+NCS2 ;MASS BUS DATA PARITY ERROR ?
91 004464 001405                               BEQ  125      ;BR IF NOT
92 004466 104403 000000' 007044'              MSGNS,BEGIN,MDPERR ;ASCII MESSAGE CALL WITH COMMON HEADER
93 004474 000167 000514                       JMP  325      ;EXIT
94 004500 032767 037000 175116                125:  BIT  #MXP!PGE!NEMINED!UPE,S+NCS2 ;RH CONTROLL FAILS ?
95 004506 001411                               BEQ  135      ;BR IF NOT
96 004510 032767 040000 175110                BIT  #ERR,S+NDS  ;ANY DRIVE ERROR ?
97 004516 001005                               BNE  135      ;BR IF SO
98 004520 104403 000000' 007054'              MSGNS,BEGIN,RHFAIL ;ASCII MESSAGE CALL WITH COMMON HEADER
99 004526 000167 000462                       JMP  325      ;EXIT
100 004532 032767 100000 175064                135:  BIT  #DIL,S+NCS2 ;DATA LATE ERROR ?
101 004540 001423                               BEQ  155      ;BR IF NOT
102 004542 005267 173632                       INC  DLICNT   ;INCREMENT DATA LATE ERROR COUNT
103 004546 032767 000004 173242                BIT  #BIT2,SK1  ;ALLOW TO TYPE OUT DATA LATE ERROR ?
104 004554 001402                               BEQ  145      ;BR IF SO
105 004556 000167 000446                       JMP  335      ;EXIT
106 004562                               145:
107 004562 104403 000000' 007150'              MSGNS,BEGIN,DLTERM ;ASCII MESSAGE CALL WITH COMMON HEADER
108 004570 012767 000002 173310                MOV  #2,ERRIYP ;SAVE THE ERROR TYPE

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109 004576 104406 000000' 001416'           ;*****
;SUFEXS,BEGIN,TABLE ;DATA LATE ERROR
;*****
110 004604 000167 000420                               JMP  335      ;EXIT
111 004610 032767 040000 175010                155:  BIT  #ERR,S+NDS  ;ANY DRIVE ERROR ?
112 004616 001027                               BNE  185      ;BR IF ANY
113 004620 032767 040000 174776                BIT  #WCE,S+NCS2 ;RH WHITE CHECK ERROR ?
114 004626 001404                               BEQ  165      ;BR IF NOT
115 004630 104403 000000' 007134'              MSGNS,BEGIN,WHTCA  ;ASCII MESSAGE CALL WITH COMMON HEADER
116 004636 000566                               BR  325      ;EXIT
117 004640 032767 040000 174746                165:  BIT  #TRL,S+NCS1 ;THEN RH TRANSFER ERROR ?
118 004646 001404                               BEQ  175      ;BR IF NOT
119 004650 104403 000000' 007034'              MSGNS,BEGIN,1REKR  ;ASCII MESSAGE CALL WITH COMMON HEADER
120 004656 000556                               BR  325      ;EXIT
121 004660 012767 000000 173220                175:  MOV  #0,ERRIYP ;UNKNOWN ERROR
122 004666 104405 000000' 001416'           ;*****
;HRDEFS,BEGIN,TABLE ;UNKNOWN ERROR
;*****
123 004674 000555                               BR  335      ;EXIT
124 004676 032767 040000 174724                185:  BIT  #UNS,S+NER1 ;RM DEVICE UNSAFE ?
125 004704 001404                               BEQ  195      ;BR IF NOT
126 004706 104403 000000' 007060'              MSGNS,BEGIN,UNSAFE ;ASCII MESSAGE CALL WITH COMMON HEADER
127 004714 000537                               BR  325      ;EXIT
128 004716 032767 000600 174704                195:  BIT  #HCRC!HCE,S+NER1 ;HEADER INFORMATION ERROR ?
129 004724 001404                               BEQ  205      ;BR IF NOT
130 004726 104403 000000' 007064'              MSGNS,BEGIN,HEADER ;ASCII MESSAGE CALL WITH COMMON HEADER
131 004734 000527                               BR  325      ;EXIT
132 004736 032767 000020 174664                205:  BIT  #FER,S+NER1 ;DATA FORMAT ERROR ?
133 004744 001404                               BEQ  215      ;BR IF NOT
134 004746 104403 000000' 007070'              MSGNS,BEGIN,FMKMAT ;ASCII MESSAGE CALL WITH COMMON HEADER
135 004754 000517                               BR  325      ;EXIT
136 004756 032767 003000 174644                215:  BIT  #IAE!AUE,S+NER1 ;ADDRESSING ERROR ?
137 004764 001410                               BEQ  225      ;BR IF NOT
138 004766 032767 002000 174632                BIT  #LBI,S+NDS  ;LAST BLOCK TRANSFER SET ?
139 004774 001115                               BNE  335      ;EXIT, IF SU
140 004776 104403 000000' 007074'              MSGNS,BEGIN,ADKRES ;ASCII MESSAGE CALL WITH COMMON HEADER
141 005004 000503                               BR  325      ;EXIT
142 005006 032767 020000 174614                225:  BIT  #UPI,S+NER1 ;POSITIONER FAILURE ?
143 005014 001404                               BEQ  235      ;BR IF NOT
144 005016 104403 000000' 007144'              MSGNS,BEGIN,POSIT  ;ASCII MESSAGE CALL WITH COMMON HEADER
145 005024 000473                               BR  325      ;EXIT
146 005026 032767 040000 174622                235:  BIT  #SKI,S+NER2 ;SEEN ERROR ?
147 005034 001404                               BEQ  245      ;BR IF NOT
148 005036 104403 000000' 007100'              MSGNS,BEGIN,SEEKER ;ASCII MESSAGE CALL WITH COMMON HEADER
149 005044 000463                               BR  325      ;EXIT
150 005046 032767 004000 174602                245:  BIT  #LSC,S+NER2 ;LOSS OF SYSTEM CLOCK ?
151 005054 001403                               BEQ  255      ;BR IF NOT
152 005056 104403 000000' 007104'              MSGNS,BEGIN,LUSYSC ;ASCII MESSAGE CALL WITH COMMON HEADER
153 005064 032767 004000 174536                255:  BIT  #ALE,S+NER1 ;WRITE LOCK ERROR ?
154 005072 001404                               BEQ  265      ;BR IF NOT
155 005074 104403 000000' 007110'              MSGNS,BEGIN,WTLUCK ;ASCII MESSAGE CALL WITH COMMON HEADER
156 005102 000444                               BR  325      ;EXIT
157 005104 032767 010000 174516                265:  BIT  #DTE,S+NER1 ;DRIVE TIMING ERROR ?
158 005112 001404                               BEQ  275      ;BR IF NOT
159 005114 104403 000000' 007140'              MSGNS,BEGIN,TIMERR ;ASCII MESSAGE CALL WITH COMMON HEADER
160 005122 000434                               BR  325      ;EXIT
161 005124 032767 100100 174476                275:  BIT  #DCK!ECH,S+NER1 ;DATA CHECK ERROR ?

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162 005132 001404          BEG      28S          ;HR IF NOT
163 005134 104403 000000' 007114' MSGNS,BEGIN,DATEHR ;ASCII MESSAGE CALL WITH COMMON HEADER
164 005142 000424          BR       32S          ;EXIT
165 005144 032767 002000 174504 28S:  BIT      #LbC,S+NER2 ;MOST BIT CLOCK ?
166 005152 001404          REQ      29S          ;HR IF NOT
167 005154 104403 000000' 007120' MSGNS,BEGIN,LOSHBIT ;ASCII MESSAGE CALL WITH COMMON HEADER
168 005162 000414          BR       32S          ;EXIT
169 005164 032767 000040 174430 29S:  BIT      #WCF,S+NER1 ;WRITE CLOCK ERROR ?
170 005172 001404          BEG     30S          ;HR IF NOT
171 005174 104403 000000' 007124' MSGNS,BEGIN,WTCLOCK ;ASCII MESSAGE CALL WITH COMMON HEADER
172 005202 000404          BR       32S          ;EXIT
173 005204          30S:
174 005204 104403 000000' 007130' MSGNS,BEGIN,DPVERR ;ASCII MESSAGE CALL WITH COMMON HEADER
175 005212 000400          BR       32S          ;EXIT
176 005214          31S:
177 005214          32S:
178 005222 104406 000000' 001416' MOV      #0,ERRKTY ;DUMMY ERROR NUMBER
;*****
;SUFERS,BEGIN,TABLE ;DUMP RH/RM REGISTERS
;*****
179 005230          33S:
180 005230 000205          RTS      R5          ;EXIT
181
182 ;THIS ROUTINE IS USED TO WAIT FOR "DVA" TO SET, CLEAR THE MASSBUS
183 ;AND SELECT THE DEVICE.
184 ;
185 ;CALL
186 ; JSR     R5,READY ;CALL READY ROUTINE
187 ; BR     ? ;ERROR RETURN
188 ; RETURN ;NORMAL EXIT
189
190 005232 004767 000136 READ1: JSR     PC,WAIT ;WAIT FOR "DVA" BIT
191 005236 000436          BR       1S          ;EXIT ON ERROR
192 005240 017700 174256          MOV     #RMDT,R0 ;READ THE DRIVE TYPE
193 005244 010067 173144          MOV     #0,DRVTYP ;GET DRIVE TYPE AND
194 005250 042767 177770 173136  BIC     #*C7,DRVTYP ;SAVE TYPE BITS
195 005256 022700 024025          CMP     #24025,R0 ;DUAL PORT RM02 ?
196 005262 001442          BEQ     2S          ;BR IS SO
197 005264 022700 020025          CMP     #20025,R0 ;SINGLE PORT RM02 ?
198 005270 001437          BEQ     2S          ;BR IF SO
199 005272 022700 024024          CMP     #24024,R0 ;DUAL PORT RM03 ?
200 005276 001434          BEQ     2S          ;BR IF YES
201 005300 022700 020024          CMP     #20024,R0 ;SINGLE PORT RM03 ?
202 005304 001431          BEQ     2S          ;BR IF YES
203 005306 022700 024027          CMP     #24027,R0 ;DUAL PORT RM05 ?
204 005312 001426          BEQ     2S          ;BR IF YES
205 005314 022700 020027          CMP     #20027,R0 ;SINGLE PORT RM05 ?
206 005320 001423          BEQ     2S          ;BR IF YES
207 005322 004767 000306          JSR     PC,ERSUB1 ;SETUP FOR ERROR REPORT
208 005326 104403 000000' 007160' MSGNS,BEGIN,DEVNO1 ;ASCII MESSAGE CALL WITH COMMON HEADER
209 005334          1S:
210 005334 004767 000142          JSR     PC,GET ;READ AND STORE ALL REGISTERS
211 005340 012767 000006 172540  MOV     #6,ERRKTY ;SAVE THE ERROR TYPE
212 ;*****
;HDRS,BEGIN,TABLE ;DEVICE NOT AVAILABLE OR NOT AN RM05/3/2
;*****
213 005354 004767 176402          JSR     PC,DRUP ;DROP THE DRIVE
    
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214 005360 104403 000000' 007174' MSGNS,BEGIN,DRM ;ASCII MESSAGE CALL WITH COMMON HEADER
215 005366 000401          BR       3S          ;EXIT WITH ERROR
216 005370 005725          2S:  IST     (R5)+ ;DRIVE AVAILABLE, ON-LINE, SAFE AND NO ERRORS
217 005372 000205          3S:  RTS     R5          ;NORMAL EXIT
218
219 ;THIS ROUTINE WAITS FOR "DVA" TO SET IN THE RMCS1 REGISTER. IF THE "DVA"
220 ;BIT SETS, THE MBA IS CLEARED AND THE DRIVE IS SELECTED. IF THE "DVA" BIT
221 ;DOES NOT SET WITHIN A CERTAIN PERIOD OF TIME, AN ERROR IS REPORTED AND
222 ;RETURN IS MADE BACK TO THE MAIN PROGRAM, WHERE THE DRIVE WILL BE DROPPED
223 ;FROM THE TEST.
224 ;
225 ;CALL
226 ; JSR     PC,WAIT ;CALL WAIT ROUTINE
227 ; BR     ??? ;ERROR RETURN
228 ; RETURN ;NORMAL RETURN
229
230 005374 016777 172774 174102 WAIT: MOV     UNITNO, #RMCS2 ;SELECT DRIVE ADDRESS
231 005402 012767 017777 173000  MOV     #17777,CLK ;CLOCK COUNT
232 005410          1S:
233 005410 032777 004000 174056  BIT     #DVA,#RMCS1 ;IS DRIVE AVAILABLE ?
234 005416 001020          BNE     2S          ;BR IF YES
235 005420 104407 000000' 005424 104407 000000'  BREQS,BEGIN ;TEMPORARY RETURN TO MONITOR....
;BREQS,BEGIN ;THEN CONTINUE AT NEXT INSTRUCTION.
236 005430 005367 172754          DEC     CLK ;DECREMENT CLOCK COUNT
237 005434 001365          BNE     1S          ;
238 005436 004767 000172          JSR     PC,ERSUB1 ;SETUP FOR ERROR REPORT
239 005442 012767 000000 172436  MOV     #0,ERRKTY ;UNKNOWN ERROR TYPE
240 005450 104403 000000' 007170' MSGNS,BEGIN,NOOVA ;ASCII MESSAGE CALL WITH COMMON HEADER
241 005456 000410          BR       3S          ;TIMEOUT ON "DVA" BIT
242 005460          2S:
243 005460 012777 000040 174016  MOV     #CLR,#RMCS2 ;ISSUE A CLEAR COMMAND
244 005466 016777 172702 174010  MOV     UNITNO,#RMCS2 ;SELECT THE DRIVE ADDRESS
245 005474 062716 000002          ADD     #2,(SP) ;ADJUST FOR GOOD RETURN
246 005500 000207          3S:  RTS     PC          ;RETURN
247
248 ;-----
249 ;THIS ROUTINE IS USED TO READ AND STORE ALL REGISTERS FROM THE RH/RM
250 ;WHEN AN ERROR HAS BEEN DETECTED.
251 ;
252 ;CALL
253 ; JSR     PC,GET ;CALL GET ROUTINE
254 ;
255 005502 005000          GET:  CLP     R0 ;START WITH RMCS1 REGISTER INDEX 0
256 005504 016701 173764          MOV     #RMCS1,R1 ;RH11/RH70 BUS ADDRESS
257 005510 022701 001516'          1S:  CMP     #RMDR,R1 ;IS IT RMDR REGISTER ?
258 005514 001006          BNE     2S          ;BR IF NO
259 005516 032711 000200          BIT     #OR,(R1) ;IS "OR" BIT SET ?
260 005522 001003          BNE     2S          ;BR IF YES
261 005524 005000 001614'          CLR     S(R0) ;ELSE, CLEAR RMDR REGISTER
262 005530 000402          BR     3S          ;
263 005532 012160 001614'          2S:  MOV     (R1)+,S(R0) ;READ ONE REGISTER
264 005536 062700 000002          3S:  ADD     #2,R0 ;ADJUST TABLE INDEX
265 005542 022700 000050          CMP     #50,R0 ;DONE READING ALL REGISTERS ?
266 005546 001360          BNE     1S          ;BR IF NO
267 005550 012760 177777 001416'  MOV     #-1,TABLE(R0) ;TERMINATE DECX REGISTER TABLE FOR AN RH11
268 005556 005767 172602          TST     FLAG70 ;CHECK FOR ADDITIONAL RH70 REGISTERS
269 005562 100011          BPL     4S          ;BR IF NONE
    
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270 005564 012760 001664' 001416'      MOV      #S+NBAE, TABLE(R0)      ;EXTEND THE DECC REGISTER TABLE
271 005572 012160 001614'              MOV      (R1)+, S(R0)           ;GET RMBAE REGISTER
272 005576 062700 000002              ADD      #2, R0                ;ADJUST INDEX TABLE
273 005602 011160 001614'              MOV      (R1), S(R0)           ;GET RMCS3 REGISTER
274 005606 000207                      4s:    RTS                     ;RETURN
275
276
277
278 005610 014167 172272              ERSUB2: MOV      -(R1), ASB      ;LOAD THE DATA
279 005614 010167 172262              MOV      R1, SBADR            ;LOAD ADDRESS OF DATA WRITTEN
280 005620 014267 172264              MOV      -(R2), A+WAS        ;LOAD THE DATA
281 005624 010267 172254              MOV      R2, WASADR          ;LOAD ADDRESS OF DATA READ
282 005630 005721                      TST      (R1)+                ;RESET REG. 1
283 005632 005722                      TST      (R2)+                ;RESET REG. 2
284
285 005634 016767 173634 172236      ERSUB1: MOV      RMCS1, CSRA     ;LOAD ADDR OF CURRENT CSR
286 005642 017767 173626 172232      MOV      @RMCS1, ACSH        ;LOAD CONTENTS OF CURRENT CSR
287 005650 000207                      RTS                     ;RETURN
288
289
290
291 005652 005067 172506              SETUP: CLR      FLAG70        ;CLEAR 11/70 FLAG
292 005656 016700 172124              MOV      ADDR, R0            ;GET DEVICE ADDRESS (DEFAULT = 176700)
293 005662 012701 001474'              MOV      #RMCS1, R1          ;GET STARTING REGISTER ADDRESS
294 005666 010021                      1s:    MOV      R0, (R1)+        ;GENERATE REGISTER ADDRESS
295 005670 062700 000002              ADD      #2, R0              ;
296 005674 022701 001544'              CNP      #MBAE, R1           ;LAST REGISTER FOR RH11 ?
297 005700 001372                      BNE      1s                  ;BR IF NO
298 005702 052777 002000 173564      BIS      #BIT10, #RMCS1     ;TRY SETTING "PSEL" BIT
299 005710 032777 002000 173556      BIT      #BIT10, #RMCS1     ;IS CPU AN 11/70 ?
300 005716 001015                      BNE      3s                  ;BR IF NO
301 005720 052767 100000 172436      BIS      #BIT15, FLAG70     ;SET 11/70 FLAG
302 005726 032767 100000 172062      BIT      #BIT15, SM1        ;SPECIFY 32 REGISTER ON RH70 ?
303 005734 001402                      BEQ      2s                  ;BR IF NU
304 005736 062700 000024              ADD      #24, R0             ;YES, ADJUST THE RMBAE ADDRESS
305 005742 010021                      2s:    MOV      R0, (R1)+        ;SETUP RMBAE REGISTER
306 005744 062700 000002              ADD      #2, R0              ;ADJUST INDEX
307 005750 010011                      MOV      R0, (R1)            ;SETUP RMCS3 REGISTER
308 005752
309 005752 016700 172032              MOV      VECTOR, R0          ;GET VECTOR ADDRESS
310 005756 012720 003204'              MOV      #NTRUPT, (R0)+      ;SET POINTER JUST IN CASE
311 005762 116710 172024              MOV      #P1, (R0)          ;SET PRIORITY
312 005766 000207                      RTS                     ;RETURN
313
314
315
316 005770 045 040 122 MES1: .ASCIZ '% RH11/RH70 TRANSFER ERROR'
317 006023 045 040 115 MES2: .ASCIZ '% MASSBUS PARITY ERROR MDPE=1'
318 006061 045 040 115 MES3: .ASCIZ '% MASSBUS DATA PARITY ERROR MDPE=1'
319 006124 040 104 122 MES4: .ASCIZ ' DRIVE '
320 006134 040 104 122 MES5: .ASCIZ ' DROPPED'
321 006146 045 040 122 MES6: .ASCIZ '% RETRY EXCEEDED'
322 006167 045 040 122 MES10: .ASCIZ '% RM DEVICE UNSAFE'
323 006212 045 040 110 MES11: .ASCIZ '% HEADER INFORMATION ERROR'
324 006245 045 040 104 MES12: .ASCIZ '% DATA FORMAT BIT ERROR'
325 006275 045 040 104 MES14: .ASCIZ '% DRIVE ERROR'
326 006313 045 040 122 MES15: .ASCIZ '% RH11/RH70 CONTROLLER ERROR'
    
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327 006350 045 040 101 MES16: .ASCIZ '% ADDRESSING ERROR'
328 006373 045 040 123 MES17: .ASCIZ '% SEEK ERROR'
329 006410 045 040 114 MES20: .ASCIZ '% LOST SYSTEM CLOCK'
330 006434 045 040 127 MES21: .ASCIZ '% WRITE LOCK ERROR'
331 006457 045 040 104 MES22: .ASCIZ '% DATA CHECK ERROR'
332 006502 045 040 114 MES23: .ASCIZ '% LOST BIT CLOCK'
333 006523 045 040 127 MES24: .ASCIZ '% WRITE CLOCK ERROR'
334 006547 045 040 122 MES25: .ASCIZ '% RH11/RH70 WRITE CHECK ERROR'
335 006605 045 040 104 MES26: .ASCIZ '% DRIVE TIMING ERROR'
336 006632 045 040 000 MES27: .ASCIZ '% '
337 006635 045 040 120 MES30: .ASCIZ '% POSITIONING ERROR'
338 006661 045 040 104 MES31: .ASCIZ '% DATA LATE ERROR'
339 006703 045 040 104 MES32: .ASCIZ '% DRIVE NOT READY'
340 006725 045 040 104 MES33: .ASCIZ '% DEVICE NOT AN RM05/3/2'
341 006756 045 040 103 MES34: .ASCIZ '% CONTROLLER NOT READY'
342 007005 045 040 104 MES35: .ASCIZ '% DRIVE NOT AVAILABLE'
343
344
345 007034 005770'              TRERR: MES1
346 007036 177777              MES2
347 007040 006023'              MCPERR: MES2
348 007042 177777              MES3
349 007044 006061'              MDPEPR: MES3
350 007046 177777              MES6
351 007050 006146'              EXCED: MES6
352 007052 177777              MES15
353 007054 006313'              MHFAIL: MES15
354 007056 177777              MES10
355 007060 006167'              UNSAFE: MES10
356 007062 177777              MES11
357 007064 006212'              HEADER: MES11
358 007066 177777              MES12
359 007070 006245'              FORMAT: MES12
360 007072 177777              MES16
361 007074 006350'              ADDRES: MES16
362 007076 177777              MES17
363 007100 006373'              SEEKER: MES17
364 007102 177777              MES20
365 007104 006410'              LOSYSY: MES20
366 007106 177777              MES21
367 007110 006434'              WLOCK: MES21
368 007112 177777              MES22
369 007114 006457'              DATERR: MES22
370 007116 177777              MES23
371 007120 006502'              LUSBIT: MES23
372 007122 177777              MES24
373 007124 006523'              WCLCK: MES24
374 007126 177777              MES14
375 007130 006275'              DRVERR: MES14
376 007132 177777              MES25
377 007134 006547'              WPTCK: MES25
378 007136 177777              MES26
379 007140 006605'              TIMERR: MES26
380 007142 177777              MES30
381 007144 006635'              POSIT: MES30
382 007146 177777              MES31
383 007150 006661'              DLTEPR: MES31
    
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384 007152 177777
385 007154 006703' NODHD1: MES32 177777
386 007156 177777
387 007160 006725' DEVNUT: MES33 177777
388 007162 177777
389 007164 006756' NOTRDY: MES34 177777
390 007166 177777
391 007170 007005' NODVA: MES35 177777
392 007172 177777
393 007174 006124' DRM: MES4
394 007176 007211' NUMB
395 007200 006134' MES5
396 007202 177777
397
398 007204 ADR1: .BLKB 5 ;STARTING OF THE ASCII NUMBER STRING
399 007211 000 NUMB: .BYTE 0 ;LEAST SIGNIFICANT ASCII DIGIT
400 007212 000 .EVEN .BYTE 0 ;TERMINATOR OF THE ASCII STRING
401
402
403 000001 .END

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ACSR 000102R DRPDRV 000362P LDADR 003650K NUT = 000026 HEAD 002524K
ADDR 000006R DRVERK 007130R LUOP1 002146R NEC1 = 000044 HEADHD 002744R
ADDRS 007074R DRVTYP 000414R LUOP2 002160K NEC2 = 000046 READY 005232R
ADDR27= 001000 DRY = 000200 LUSB11 007120R NED = 010000 HESTRT 002130R
ADR1 007204H DSKADR 000370K LUSYS 007104R NEM = 004000 MES1 000056K
AOE = 001000 DTE = 010000 LS = 000004 NER1 = 000014 RES2 000060H
ASB 000106R DTL = 000000 LSC = 004000 NER2 = 000042 PHFAIL 007054R
ASTAT 000104R DVA = 004000 LT = 000002 NHP = 000036 RMAS 001512K
ATA = 000000 DVC = 000200 MAP22S= 104416 NLA = 000020 RMBA 001500K
AWAS 000110R DVICE 000372K MCPF = 020000 NMR1 = 000024 RMBAE 001544K
BADSEC 000376H DVID1 000014R MCPERR 007040K NMR2 = 000040 RMCS1 001474K
BADSPC 000252R EA22 000360R MDPE = 000400 NURD1 007154R RMCS2 001504R
BEGIN 000000R ECH = 000100 MDPEK 007044H NODVA 007170R RMCS3 001546R
BITTAB 003752R ENDITS= 104413 MES1 005770K NUF = 000032 RMDA 001502K
BIT0 = 000001 ERR = 040000 MES10 006167H NUTRDY 007164R RMDC 001530K
BIT1 = 000002 ERRORS 004020R MES11 006212K NSN = 000030 RMDS 001506R
BIT10 = 002000 ERRTYP 000106R MES12 006245R NTHUPT 003204R RMDT 001522R
BIT11 = 004000 ERSUB1 005634R MES14 006275R NULL = 000000 RMEC1 001540R
BIT12 = 010000 ERSUB2 005610R MES15 006313R NWC = 000020 RMEC2 001542R
BIT13 = 020000 EXCED 007050R MES16 006350H OM = 000001 RMEK1 001510R
BIT14 = 040000 EXITS = 104400 MES17 006373R OPEN = 000000 RMEK2 001536K
BIT15 = 100000 FER = 000020 MES20 006410R OPI = 020000 RMR 001532R
BIT2 = 000004 FEHADR 000406R MES21 006434K OTAS = 004420 RMLA 001514K
BIT3 = 000010 FLAG70 000364K MES22 006457H OP = 000200 RMR1 001520R
BIT4 = 000020 FMT = 010000 MES23 006502R PAK = 000010 RMR2 001534K
BIT5 = 000040 FORMAT 007070H MES24 006523K PASCNT 000034R RMOF 001526H
BIT6 = 000100 FREE 000150R MES25 006547H PA18 000352R RMR = 000004
BIT7 = 000400 FT = 000000 MES26 006605K PA22 000356R RMSN 001524R
BIT8 = 001000 FUNC 000402R MES27 006632R PGE = 002000 RMWC 001476R
BIT9 = 001000 GENADR 003320R MES3 006661R PGM = 001000 RSTR1 000112K
BREAKS= 104407 GET 005502R MES30 006635R PIP = 020000 PSTRT1 002140K
BR1 000012R GETDRV 003714R MES31 006661R PIPQS = 000004 P6 = 000006
BR2 000013R GETPAS= 104415 MES32 006703K PPS 005726 S 001614K
BSE = 100000 GO = 000001 MES33 006725H PPS2 = 022626 SBAUR 000102H
BST = 000010 G01 003044K MES34 006756H POSIT 007144R SC = 100000
BTODS = 104421 G02 003150R MES35 007059R PRTY = 000000 SEC = 000012
CDATA= 104412 GWBUFS= 104414 MES4 006124H PRTY1 = 000040 SEEK 003024H
CDERCT 000144H HCE = 000200 MES5 006134R PRTY2 = 000100 SEEKH 007100H
CDWUCT 000146R HCR = 000400 MES6 006146H PRTY3 = 000140 SETUP 005652H
CLA 000410R HEADER 007064R HODNAM 000000 PRTY4 = 000200 SKI = 040000
CLR = 000040 HRDCNI 000044K HUL = 010000 PRTY5 = 000240 SUFCNI 000042K
CONFIG 000056R HDRERS= 104405 HSGNS = 104403 PRTY6 = 000300 SOKERS= 104406
CSRA 000100R HRUPAS 000050H HSGS = 104402 PRTY7 = 000340 SORPAS 000046K
CYCLE 002226H IAE = 002000 HSGS 104401 PS = 177776 SPDINI 000032P
CYLND 000366R ICDNI 000036R HSGS 104400 PSh = 177776 SPSIZ = 000040
DATCK= 104411 ICDNI 000040R HSGS 104400 NAS = 000016 SH1 000016R
DATEHR 007114H IDNUM 000122R HSGS 104400 NBA = 000004 SH2 000020H
DATE= 104404 ILF = 000001 HSGS 104400 NBE = 000050 SH3 000022R
DCK = 100000 IMODX, = 000000 HSGS 104400 NCS1 = 000000 SH4 000024K
DEVNUT 007160R INLI 000030R HSGS 104400 NCS2 = 000010 STAR1 001670R
DLTCH 000409H INTR 000120R HSGS 104400 NCS3 = 000052 STAT 000026K
DLTERR 007150H IK = 000100 NDA = 000006 SVRO 000062R
DPE = 000010 LBC = 002000 NDB = 000022 SVP1 000064K
DPK = 000400 LBI = 002000 NDC = 000034 SVP2 000066H
DM 007174H LC = 000006 NDS = 000012 RDU = 000200 SVP3 000070P
DROP 003762R

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DPE	3-215#										
DPH	3-185#	8-25									
DRM	5-35	8-214	8-393#								
DRUP	5-34	7-159#	8-213								
DRPDRV	3-22#	5-32	7-32	8-9*	8-17*	8-27*					
DRVERR	8-173	8-375#									
DRVITP	3-36#	7-12#	8-193*	8-194*							
DRY	3-184#	8-25									
OSKADR	3-25#	7-21	7-131*	7-132*	8-62	8-64					
DTE	3-208#	8-157									
DIL	3-176#	8-100									
DVA	3-162#	8-233									
DVC	3-216#										
DVICE	3-26#	4-6*	4-66	7-146	7-165*						
DVID1	3-5#	4-6									
EA22	3-20#	7-13	7-14*	7-15*	7-16						
ECH	3-202#	8-161									
END#	3-5#	4-69									
ENDIT#	3-5#	4-63									
ERR	3-191#	8-51	8-96	8-111							
ERRHQS	7-27	8-88									
ERRTYP	3-5#	8-19#	8-29*	8-108*	8-121*	8-177*	8-211*	8-239*			
ERSUH1	8-11	8-207	8-238	8-285*							
ERSUB2	8-278#										
EXCLU	7-43	8-351#									
EXITS	3-5#	7-23									
FER	3-200#	8-132									
FERADR	3-33#	3-89	6-19*	6-28*	6-37*	6-46*	6-55*	6-64*	6-73*		
FLAG70	3-23#	7-4	8-26#	8-291*	8-301*						
FMT	3-224#	7-19									
FORMAT	8-134	8-359#									
FREE	3-5#										
FT	3-119#	4-8*	4-11*	4-18*	4-19*	7-87	7-89	7-101			
FUNC	3-31#	6-18*	6-27*	6-36*	6-45*	6-54*	6-63*	6-72*	7-17*	7-22	
GENADR	4-52	7-56#									
GET	8-12	8-210	8-255#								
GETDRV	4-35	4-54	7-141#								
GETPAS	3-5#	4-49									
GO	3-164#	8-14									
GO1	6-25	6-34	6-43	6-52	6-61	6-70	7-3#				
GU2	6-74	7-18#									
GWBUF#	3-5#	5-9									
HCE	3-203#	8-74	8-128								
HCRC	3-204#	8-76	8-128								
HEADER	8-130	8-357#									
HRDCNT	3-5#										
HRDR#	3-5#	8-20	8-30	8-122	8-212						
HRDPAS	3-5#										
IAE	3-206#	8-136									
ICONT	3-5#										
ICOUNT	3-5#										
IDNUM	3-5#										
ILF	3-196#										
ILR	3-197#										
IMODX.	3-5#	5-9									
INIT	3-5#										
INTR	3-5#	4-5*									

NCS3	3-63	3-115#																		
NDA	3-45	3-97#																		
NDB	3-51	3-104#																		
NUC	3-56	3-108#																		
NDS	3-47	3-99#	8-23	8-37	8-51	8-96	8-111	8-138												
NOT	3-53	3-105#																		
NEC1	3-60	3-112#																		
NEC2	3-61	3-113#																		
NED	3-175#	8-94																		
NEM	3-174#	8-94																		
NER1	3-48	3-100#	8-74	8-76	8-124	8-128	8-132	8-136	8-142	8-153	8-157	8-161	8-169							
NER2	3-59	3-111#	8-78	8-80	8-146	8-150	8-165													
NHR	3-57	3-109#																		
NLA	3-50	3-102#																		
NMH1	3-52	3-104#																		
NMR2	3-58	3-110#																		
NODRDX	8-28	8-385#																		
NODVA	8-240	8-391#																		
NOF	3-55	3-107#																		
NOTRDY	8-18	8-389#																		
NSN	3-54	3-106#																		
NTRUPT	7-24#	8-310																		
NULL	3-5#																			
NUMB	8-394	8-399#																		
NWC	3-43	3-95#																		
OM	3-182#																			
OPEN	3-5	3-5	3-5	3-5	3-5	3-5	3-5	3-5	3-5	3-5	3-5	3-5	3-5	3-5	3-5	3-5	3-5	3-5	3-5	3-5
	3-5	3-5	3-5	3-5	3-5	3-5	3-5	3-5	3-5	3-5	3-5	3-5	3-5	3-5	3-5	3-5	3-5	3-5	3-5	3-5
	3-5#																			
OPI	3-209#	8-142																		
OR	3-170#	8-259																		
OTUAS	3-5#	7-166																		
PA18	3-17#	7-6#	7-11																	
PA22	3-19#	7-12																		
PAR	3-199#																			
PASCNT	3-5#	4-45																		
PGE	3-173#	8-94																		
PGM	3-186#																			
PIP	3-190#	8-38																		
PIRUS	3-5#	7-25																		
POPSP	3-5#																			
POPSP2	3-5#																			
POSIT	8-144	8-381#																		
PRTY	3-5#																			
PRTY0	3-5	3-5#																		
PRTY1	3-5#																			
PRTY2	3-5#																			
PRTY3	3-5#																			
PRTY4	3-5#																			
PRTY5	3-5	3-5#																		
PRTY6	3-5#																			
PRTY7	3-5#																			
PS	3-5#																			
PSW	3-5#																			
PUSH	3-5#																			
PUSH2	3-5#																			

R6 3-5# 4-7#

