

DVM32R2T4/32G

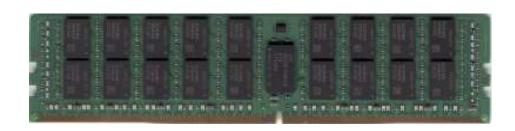
32GB - 288-Pin 2Rx4 Registered ECC DDR4 DIMM

Identification

DVM32R2T4/32G 4Gx72 32GB 2Rx4 PC4-3200AA-R22

Performance Range

 $\frac{\text{Clock / Module Speed / CL-t}_{\text{RCD}} - t_{\text{RP}}}{1600\text{Hz / PC4-3}200 / 22-22-22} \\ 1467\text{Hz / PC4-2}933 / 21-21-21} \\ 1467\text{Hz / PC4-2}933 / 20-20-20} \\ 1333 \text{ Hz / PC4-2}666 / 19-19-19} \\ 1200 \text{ MHz / PC4-2}400 / 18-18-18} \\ 1200 \text{ MHz / PC4-2}400 / 17-17-17} \\ 1067\text{MHz / PC4-2}133 / 16-16-16} \\ 1067\text{MHz / PC4-2}133 / 15-15-15} \\ 933 \text{ Hz / PC4-1}866 / 14-14-14} \\ 933 \text{ Hz / PC4-1}600 / 12-12-12} \\ 667 \text{ MHz / PC4-1}600 / 10-10-10} \\ \\$



Features

288-pin JEDEC-compliant DIMM, 133.35 mm wide by 31.25 mm high

Operating Voltage: VDD/VDDQ = 1.2V (1.14V to 1.26V)

Operating Temperature (Environment, Ambient T_{OPR}) – 0 to 65 C

Operating Temperature (DRAM T_{OPER}) - 0 to 85 C

VPP = 2.5V (2.375V to 2.75V) VDDSPD = 2.25V to 2.75V I/O Type: 1.2 V signaling

On-board I²C temperature sensor with integrated Serial Presence-Detect (SPD) EEPROM

Data Transfer Rate: 25.6 Gigabytes/sec Data Bursts: 8 and burst chop 4 mode

ZQ Calibration for Output Driver and On-Die Termination (ODT)

Programmable ODT / Dynamic ODT during Writes

Programmable CAS Latency: 10, 12, 13, 14, 15, 16, 17, 18, 19, 20, 21 and 22

Bi-directional Differential Data Strobe signals Per DRAM Addressability is supported Write CRC is supported at all speed grades DBI (Data Bus Inversion) is supported (x8 only)

CA parity (Command/Address Parity) mode is supported

Supports ECC error correction and detection

16 internal banks

SDRAM Addressing (Row/Col/BG/BA): 17/10/2/2

Fully RoHS Compliant

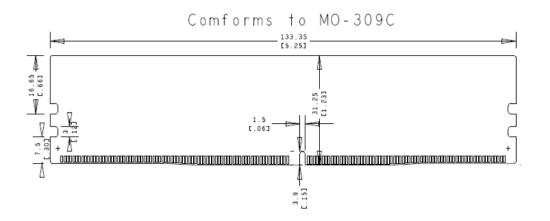
Description

DVM32R2T4/32G is a registered 4Gx72 memory module, which conforms to JEDEC's DDR4-3200AA, PC4-25600 standard. The assembly is Dual-Rank. Each rank is comprised of eighteen 2Gbx4 DDR4-3200 SDRAMs.

One EEPROM is used for Serial Presence Detect and a combination register/PLL, with Address and Command Parity, is also used.

Both output driver strength and input termination impedance are programmable to maintain signal integrity on the I/O signals in a Fly-by topology.

A thermal sensor accurately monitors the DIMM module and can prevent exceeding the maximum operating temperature of 95C.



Notes

Tolerances on all dimensions except where otherwise indicated are $\pm .13$ (.005). All dimensions are expressed in millimeters [inches]

DVM32R2T4/32G 27-Aug-19