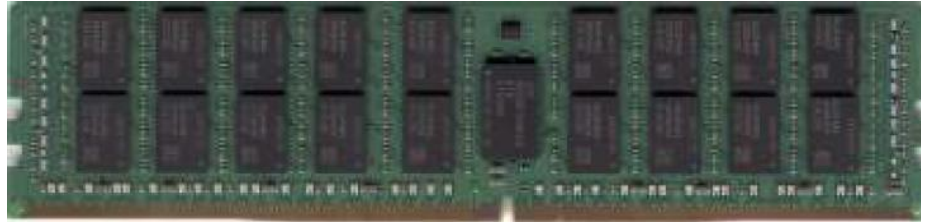


### Identification

**DVM24R4T4/64G** 8Gx72  
64GB 2SRx4 PC4-2400V-R20

### Performance Range

Clock / Module Speed / CL-t<sub>RCD</sub>-t<sub>RP</sub>  
1200 MHz / PC4-2400 / 19-17-17  
1067MHz / PC4-2133 / 17-15-15  
933 Hz / PC4-1866 / 15-14-13  
800 Hz / PC4-1600 / 13-12-11



Features	Description
<p>288-pin JEDEC-compliant DIMM, 133.35 mm wide by 31.25 mm high            Operating Voltage: VDD/VDDQ = 1.2V (1.14V to 1.26V)            Operating Temperature (Environment, Ambient T<sub>OPR</sub>) – 0 to 65 C            Operating Temperature (DRAM T<sub>OPER</sub>) – 0 to 85 C            VPP = 2.5V (2.375V to 2.75V)            VDDSPD = 2.25V to 2.75V            I/O Type: 1.2 V signaling            On-board I<sup>2</sup>C temperature sensor with integrated Serial Presence-Detect (SPD) EEPROM            Data Transfer Rate: 19.2 Gigabytes/sec            Data Bursts: 8 and burst chop 4 mode            ZQ Calibration for Output Driver and On-Die Termination (ODT)            Programmable ODT / Dynamic ODT during Writes            Programmable CAS Latency: 10,11,12,13,14,15,16,17,18,19,20,21 and 22            Bi-directional Differential Data Strobe signals            Per DRAM Addressability is supported            Write CRC is supported at all speed grades            DBI (Data Bus Inversion) is supported (x8 only)            CA parity (Command/Address Parity) mode is supported            Supports ECC error correction and detection            16 internal banks            SDRAM Addressing (Row/Col/BG/BA): 17/10/2/2            Fully RoHS Compliant</p>	<p><b>DVM24R4T4/64G</b> is a registered 8Gx72 memory module, which conforms to JEDEC's DDR4-2400, PC4-2400 standard. The assembly appears as Dual-Rank (2 physical ranks / 2 logical ranks). Each rank is comprised of eighteen 8Gb<sub>x</sub>4 DDR4-2400 SDRAMs.</p> <p>One EEPROM is used for Serial Presence Detect and a combination register/PLL, with Address and Command Parity, is also used.</p> <p>Both output driver strength and input termination impedance are programmable to maintain signal integrity on the I/O signals in a Fly-by topology.</p> <p>A thermal sensor accurately monitors the DIMM module and can prevent exceeding the maximum operating temperature of 95C.</p>

### Notes

Tolerances on all dimensions except where otherwise indicated are ±.13 (.005).  
All dimensions are expressed in millimeters [inches]

