

# **DVM16U2S8/8G**

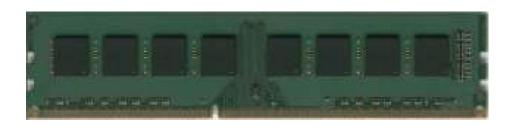
## 8GB - 240-Pin 2Rx8 Unbuffered Non-ECC DDR3 DIMM

#### Identification

**DVM16U2S8/8G** 1Gx64 8GB 2Rx8 PC3-12800U-11

## **Performance Range**

Clock / Module Speed / CL-t<sub>RCD</sub> -t<sub>RP</sub> 800 MHz / PC3-12800 / 11-11-11 667 MHz / PC3-10600 / 10-10-10 667 MHz / PC3-10600 / 9-9-9 533 MHz / PC3-8500 / 8-8-8 533 MHz / PC3-8500 / 7-7-7 400 MHz / PC3-6400 / 6-6-6



#### Features

240-pin JEDEC-compliant DIMM, 133.35 mm wide by 30.00 mm high

Operating Voltage: VDD = VDDQ = +1.5V ±0.0075 V

I/O Type: SSTL\_15

Data Transfer Rate: 12.8 Gigabytes/sec Data Bursts: 8 and burst chop 4 mode

ZQ Calibration for Output Driver and On-Die Termination (ODT)

Programmable ODT / Dynamic ODT during Writes Programmable CAS Latency: 6, 7, 8, 9, 10 and 11 Differential Data Strobe signals

SDRAM Addressing (Row/Col/BG/BA): 16/10/3

Fully RoHS Compliant

## Description

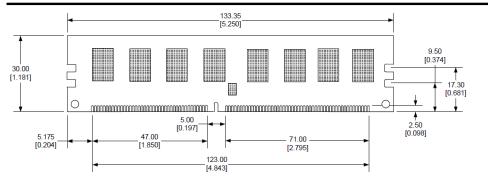
Side view

**DVM16U2S8/8G** is an unbuffered 1Gx64 memory module, which conforms to JEDEC's DDR3, PC3-12800 standard. The assembly is dual Rank. The rank is comprised of sixteen 512Mx8 DDR3 SDRAMs.

One EEPROM is used for Serial Presence Detect.

Both output driver strength and input termination impedance are programmable to maintain signal integrity on the I/O signals in a Fly-by topology.

### Front view



#### Notes

Tolerances on all dimensions except where otherwise indicated are  $\pm .13$  (.005). All dimensions are expressed in millimeters [inches]

#### Back view

