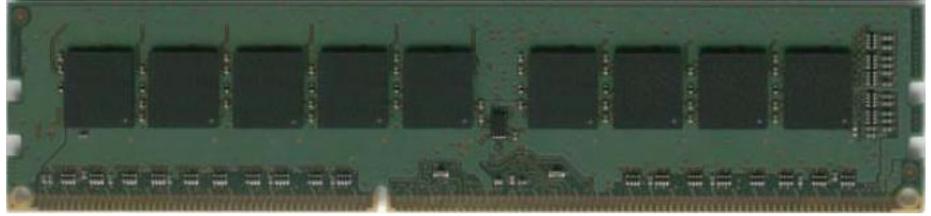


Identification

DVM16E2L8/8G 1Gx72
8GB 2Rx8 PC3L-12800E-11

Performance Range

Clock / Module Speed / CL-t_{RCD}-t_{RP}
800 MHz / PC3-12800 / 11-11-11
667 MHz / PC3-10600 / 10-10-10
667 MHz / PC3-10600 / 9-9-9
533 MHz / PC3-8500 / 8-8-8
533 MHz / PC3-8500 / 7-7-7
400 MHz / PC3-6400 / 6-6-6



Features

240-pin JEDEC-compliant DIMM, 133.35 mm wide by 30.00 mm high
Operating Voltage: VDD = VDDQ = +1.35V (1.283V to 1.45V)
Backward-compatible to VDD = VDDQ = +1.5V ±0.075V
On-board I²C temperature sensor with integrated Serial Presence-Detect (SPD) EEPROM
Data Transfer Rate: 12.8 Gigabytes/sec
Data Bursts: 8 and burst chop 4 mode
ZQ Calibration for Output Driver and On-Die Termination (ODT)
Programmable ODT / Dynamic ODT during Writes
Programmable CAS Latency: 6, 7, 8, 9, 10 and 11
Differential Data Strobe signals
SDRAM Addressing (Row/Col/BG/BA): 16/10/3
Fully RoHS Compliant

Description

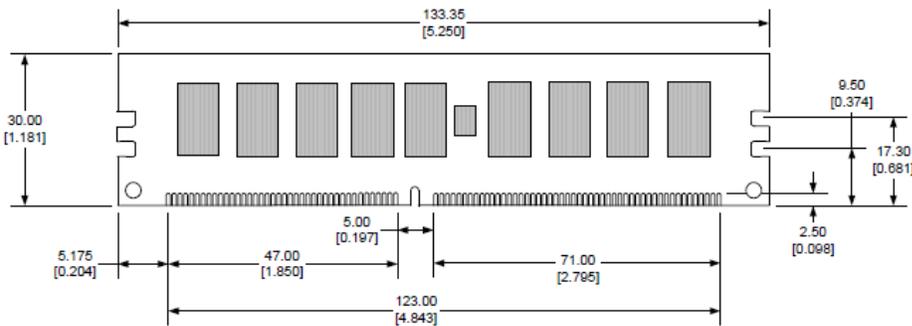
DVM16E2L8/8G is a low voltage, unbuffered 1Gx72 memory module, which conforms to JEDEC's DDR3L, PC3L-12800 standard. The assembly is dual Rank. The rank is comprised of nine 512Mx8 DDR3 SDRAMs.

One EEPROM is used for Serial Presence Detect and a combination register/PLL, with Address and Command Parity, is also used.

Both output driver strength and input termination impedance are programmable to maintain signal integrity on the I/O signals in a Fly-by topology.

A thermal sensor accurately monitors the DIMM module and can prevent exceeding the maximum operating temperature of 95C.

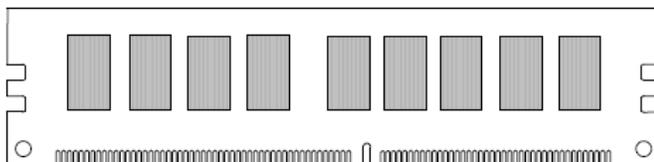
Front view



Notes

Tolerances on all dimensions except where otherwise indicated are ±.13 (.005). All dimensions are expressed in millimeters [inches]

Back view



Side view

