# 8860A Digital Multimeter 

## Service Manual

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8860A Digital Multimeter

# Section 1 <br> Introduction and Specifications 


#### Abstract

WARNING THESE SERVICING INSTRUCTIONS ARE FOR USE BY QUALIFIED PERSONNEL ONLY. TO AVOID ELECTRIC SHOCK, DO NOT PERFORM ANY SERVICING UNLESS YOU ARE QUALIFIED TO DO SO.


1-1. THE 8860A INSTRUCTION MANUAL SET
1-2. The John Fluke Model 8860A Digital Multimeter and its options are documented by a series of seven manuals. These manuals can be separated for use in different locations or joined together in a single three-ring binder.
OPERATOR
MANUAL
CALIBRATION
MANUAL

Describes how to operate and maintain the basic 8860 A ; briefly describes the options and accessories: includes the installation procedures for all options.

## MANUAL

SERVICE
MANUAL
Contains specifications, maintenance information, performance tests, and procedures for access, calibration, and adjustment.

This manual contains the theory of operation, troubleshooting, replace- able parts, and schematics for the basic 8860 A and all of its options.

CALCULATING Describes how to operate and progCONTROLLER
USER
HANDBOOK gram the Calculating Controller Option (-004). Includes applications in the appendix.

CALCULATING CONTROLLER REFERENCE GUIDE

IEEE-488
INTERFACE
OPTION USER HANDBOOK

IEEE-488
INTERFACE
OPTION
REFERENCE GUIDE

Describes how to operate the 8860A with the IEEE-488 option ( -005 ) installed. Includes controller examples.

Handy pocket-sized guide containing operating information for the IEEE488 Interface Option. Condensed from the IEEE-488 Interface Option User Handbook.

## 1-3. THE 8860A DIGITAL MULTIMETER

1-4. The 8860 A is a $51 / 2$ digit, microprocessor-controlled digital multimeter, capable of making measurements in VDC, true rms VAC (AC or DC-coupled), two-terminal ohms, and four-terminal ohms. The range extends from a resolution of 1 uV and $1 \mathrm{~m} \Omega$ to $700 \mathrm{VAC}, 1000 \mathrm{VDC}$, and 20 $\mathrm{M} \Omega$. The instrument also has a GUARD terminal for making guarded measurements.

## 1-5. OPTIONS

1-6. Several options are available for use with the 8860A. as listed in Table 1-I. A theory of operation, troubleshooting information, and a list of replaceable parts are given for each option in Section 6 of this manual.

## 1-7. LIST OF RECOMMENDED TEST EQUIPMENT

1-8. Table 1-2 lists the test equipment required to perform the procedures described in this manual. Substitute equivalent instruments if the recommended models are not available

## 1-9. SPECIFICATIONS

1-10. Table $1-3$ lists the 8860 A specifications.

Table 1-1. 8860A Options

| OPTION <br> NO. | NAME | DESCRIPTION |
| :---: | :--- | :--- |
| -004 | Calculating Controller | Programmable scientific calculator interfaced to control <br> the 8860 A |
| -005 | Interfaces the 8860A as a talker and listener to the IEEE-488 |  |
| bus |  |  |
| -006 | Exear Input | Allows connection of all analog signals from the rear panel <br> through a single connector <br> Enables connection of an external reference for making <br> ratio measurements |

Table 1-2. Recommended Test Equipment

| INSTRUMENT TYPE | MINIMUM SPECIFICATIONS | RECOMMENDED MODEi. |
| :---: | :---: | :---: |
| AC Calibrator | Voltage Range: $0-1000 \mathrm{~V}$ ac Freq. Range: $20 \mathrm{~Hz}-300 \mathrm{kHz}$ Voltage Accuracy: $0-100 \mathrm{~V}$ ac: <br> $20 \mathrm{~Hz}-50 \mathrm{~Hz} .1 \%$ <br> $50 \mathrm{~Hz}-10 \mathrm{kHz} .03 \%$ <br> $10 \mathrm{kHz}-100 \mathrm{kHz} .03 \%$ <br> $100 \mathrm{kHz}-300 \mathrm{kHz} .4 \%$ <br> $100-1000 \mathrm{~V}$ ac: <br> $20 \mathrm{~Hz}-50 \mathrm{~Hz} .15 \%$ <br> $50 \mathrm{~Hz}-10 \mathrm{kHz} .05 \%$ <br> $10 \mathrm{kHz}-100 \mathrm{kHz} .1 \%$ | Fluke 5200A, 5205A |
| DC Calibrator | Voltage Range: $0-1000 \mathrm{~V}$ dc Accuracy: .003\% | Fluke 332B |
| Oscilloscope | General purpose with 10M probe | Tektronix T932A |
| Digital Voltmeter | Voltage Accuracy: <br> .01\% (V dc) <br> $1.0 \%(\mathrm{~V} \mathrm{ac})$ for 1 volt <br> input at 100 kHz <br> Input Impedance: <br> 10 Megohm or greater in V dc <br> 1 Megohm in parallel with <br> $<100 \mathrm{pF}$ in V ac | Fluke 8800A |

Table 1-3. 8860A Specifications

## DC VOLTS

| Ranges | $\pm 200 \mathrm{mV}, 2 \mathrm{~V}, 20 \mathrm{~V}, 200 \mathrm{~V}, 1000 \mathrm{~V}$ |
| :---: | :---: |
| Ranging | Fully automatic or manual |
| Polarity of Input | Automatic polarity selection and display |
| Resolution (Max.) | $0.0005 \%$ of fuil scale ( $1 \mu \mathrm{~V}$ on 200 mV range) with 5-1/2 digit display. |
| Accuracy | Using front panel zero, $\pm$ (\% input + no. of digits) |

5-1/2 DIGIT DISPLAY*

| RANGE | $\begin{gathered} 24 \mathrm{HR} \\ 23^{\circ} \mathrm{C} \pm 1^{\circ} \mathrm{C} \end{gathered}$ | $\begin{gathered} 90 \mathrm{DAY} \\ 18^{\circ} \mathrm{C}-28^{\circ} \mathrm{C} \end{gathered}$ | $\begin{gathered} 1 \mathrm{YR} \\ 18^{\circ} \mathrm{C}-28^{\circ} \mathrm{C} \end{gathered}$ | NORMAL MODE REJECTION |  |
| :---: | :---: | :---: | :---: | :---: | :---: |
|  |  |  |  | NO FILTER | FILTER |
| 200 mV | $(0.004+3)$ | $(0.008+3)$ | $(0.01+3)$ | $\begin{aligned} & >60 \mathrm{~dB} \\ & 50,60 \mathrm{~Hz} \end{aligned}$ | $\begin{aligned} & >100 \mathrm{~dB} \\ & 50,60 \mathrm{~Hz} \end{aligned}$ |
| 2V-200V | $(0.004+2)$ |  |  |  |  |
| 1000 V |  |  |  |  |  |

4-1/2 DIGIT DISPLAY*

| RANGE | 90 DAY |  |  |  |
| :---: | :---: | :---: | :---: | :---: |
|  | $1 \mathrm{Y}^{\circ} \mathrm{YR}$ | NORMAL MODE REJECTION |  |  |
|  |  |  | NO FILTER | FILTER |
| All | $(0.01+2)$ | $(0.015+3)$ | $>60 \mathrm{~dB}$ | $>100 \mathrm{~dB}$ |
|  |  |  | $50,60 \mathrm{~Hz}$ | $50,60 \mathrm{~Hz}$ |

*Settling Time: 30 ms to within $.01 \%$ of input step size, with filter 300 ms .
3-1/2 DIGIT DISPLAY (Available with -004 or -005 options only)

| RANGE | 1 YR | NORMAL MODE REJECTION |  |
| :---: | :---: | :---: | :---: |
|  | $18^{\circ} \mathrm{C}-28^{\circ} \mathrm{C}$ |  |  | NO FILTER | FILTER |
| :---: |
| All |

Settling Time: 5 ms to within $.1 \%$ of input step size, with filter 250 ms .

## Common Mode Rejection

CONDITIONS
Line frequency switch properly set.
Line frequency at 50 or $60 \mathrm{~Hz} \pm 0.1 \%$.
One kilohm in either lead.
4-1/2 AND 5-1/2 DIGIT RATE
Normal Guard . .............. . $>130 \mathrm{~dB}$
External Guard (Driven) . . . . . . $>150 \mathrm{~dB}$
3-1/2 DIGIT RATE Normal Guard
$>70 \mathrm{~dB}$
External Guard (Driven) . . . . . $>90 \mathrm{~dB}$
DC, ALL READING RATES ..... >160 dB
Input Resistance
200 mV , 2V RANGES . . . . . . . . . $>10,000 \mathrm{M} \Omega$
20V, 200V, 1000V RANGES ..... $10 \mathrm{M} \Omega$

Table 1-3. 8860A Specifications (cont)

```
Input Bias Current (@ 23
Zero Stability
    (after 1 hour warmup) . . . . . . . . . . . . . }\pm10\mu\textrm{V}\mathrm{ for 90 days
Maximum Input .................... }\begin{array}{rl}{\pm1000\textrm{V Pk input HI to LO }}\\{}&{\pm500\textrm{V Pk input LO to Earth}}\\{}&{\pm30\textrm{V Pk input LO to Guard}}
```

AC VOLTS (True RMS, AC only or AC + DC)
Ranges .................................... $200 \mathrm{mV}, 2 \mathrm{~V}, 20 \mathrm{~V}, 200 \mathrm{~V}, 700 \mathrm{~V}$
Ranging . . . . . . . . . . . . . . . . . . . . . . . . . Fully automatic or manual
Resolution (Max) . . . . . . . . . . . . . . . . . . . $0.0005 \%$ F.S. ( $1 \mu \mathrm{~V}$ on 200 mV range) with 5-1/2
digit display.
Accuracy .................................. $\pm$ ( $\%$ INPUT + DIGITS), $2 \mathrm{~V}-700 \mathrm{~V}$ ranges:
$0.5 \%$ F.S. to F.S. AC only*;
200 mV range: $1 \%$ F.S. to F.S. $A C^{*}$

| FREQUENCY | RANGE(S) | $\begin{gathered} 90 \mathrm{DAY} \\ 18^{\circ} \mathrm{C}-28^{\circ} \mathrm{C} \end{gathered}$ |  |  | $\begin{gathered} 1 \mathrm{YR} \\ 18^{\circ} \mathrm{C}-28^{\circ} \mathrm{C} \end{gathered}$ |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  |  | \% INPUT | DIGITS |  | \% INPUT | DIGITS |  |
|  |  |  | 5-1/2 | 4-1/2 |  | 5-1/2 | 4-1/2 |
| $20 \mathrm{~Hz}-50 \mathrm{~Hz}$ | All | 0.25 | 70 ** | $10^{* * *}$ | 0.25 | 100 ** | 13*** |
| $50 \mathrm{~Hz}-10 \mathrm{kHz}$ | All | 0.15 | 70 ** | $10^{* *}$ | 0.15 | 100 * | $13^{* *}$ |
| $10 \mathrm{kHz}-50 \mathrm{kHz}$ | 2V-700V | 0.4 | 150 | 18 | 0.4 | 300 | 33 |
|  | 200 mV | 0.7 | 150 | 18 | 0.7 | 300 | 33 |
| $50 \mathrm{kHz}-100 \mathrm{kHz}$ | $2 \mathrm{~V}-700 \mathrm{~V}$ |  |  |  | 1.0 | 350 | 38 |
|  | 200 mV |  |  |  | 2.5 | 350 | 38 |
| $100 \mathrm{kHz}-300 \mathrm{kHz}$ | All |  |  |  | 8.0 | 700 | 73 |

*For $A C+D C$ operation, add $0.1 \%$ of input +50 digits for $5-1 / 2$ digit resolution.
For $A C+D C$ operation, add $0.1 \%$ of input +5 digits for $4-1 / 2$ digit resolution.
**150 digits for 200 mV range
***15 digits for 200 mV range
Bandwidth (typical) ...................... $\leqslant 3 \mathrm{~dB} @ 1 \mathrm{MHz}$
Crest Factor ............................. 3 at full range, increasing down range
Input Impedance ......................... $10 \mathrm{M} \Omega, \leqslant 70 \mathrm{pF}$
Maximum Input
$700 \mathrm{~V} \mathrm{rms}, 1000 \mathrm{~V} \mathrm{Pk}$, or $2 \times 10^{7} \mathrm{volt} / \mathrm{Hz}$, whichever is less

OHMS (2-terminal or 4-terminal)
Ranges ................................. $200 \Omega, 2 \mathrm{k} \Omega, 20 \mathrm{k} \Omega, 200 \mathrm{k} \Omega, 2 \mathrm{M} \Omega, 20 \mathrm{M} \Omega$
Ranging .................................. Fully Automatic or Manual
Resolution (Max) . . . . . . . . . . . . . . . . . . $0.0005 \%$ F.S. ( $1 \mathrm{~m} \Omega$ on $200 \Omega$ range) with $5-1 / 2$ digit display

Accuracy ............................. Using front panel zero, $\pm$ (\% of input + no. of digits)
5-1/2 DIGIT DISPLAY

| RANGE | 24 HR <br> $23^{\circ} \mathrm{C} \pm 1^{\circ} \mathrm{C}$ | 90 DAY <br> $18^{\circ} \mathrm{C}-28^{\circ} \mathrm{C}$ | 1 YR <br> $18^{\circ} \mathrm{C}-28^{\circ} \mathrm{C}$ |
| :--- | :--- | :--- | :--- |
| $200 \Omega$ | $(0.008+4)$ | $(0.012+4)$ | $(0.015+4)$ |
| $2 \mathrm{k}-200 \mathrm{k} \Omega$ | $(0.006+2)$ | $(0.01+2)$ | $(0.013+2)$ |
| $2 \mathrm{M} \Omega$ | $(0.01+3)$ | $(0.014+3)$ | $(0.017+3)$ |
| $20 \mathrm{M} \Omega$ | $(0.07+3)$ | $(0.09+3)$ | $(0.10+3)$ |

4-1/2 DIGIT DISPLAY

| RANGE | 90 DAY <br> $18^{\circ} \mathrm{C}-28^{\circ} \mathrm{C}$ | 1 YR |
| :---: | :---: | :---: |
| $200-2 \mathrm{M} \Omega$ | $(0.01+2)$ | $(0.02+3)$ |
| $20 \mathrm{M} \Omega$ | $(0.1+2)$ | $(0.14+3)$ |

3-1/2 DIGIT DISPLAY

| RANGE | 1 YR <br> $18^{\circ} \mathrm{C}-28^{\circ} \mathrm{C}$ |
| :--- | :---: |
| $200 \Omega-2 \mathrm{M} \Omega$ | $(0.1+1)$ |
| $20 \mathrm{M} \Omega$ | $(0.3+1)$ |

INPUT CHARACTERISTICS

| RANGE | CURRENT THRU RX | OPEN CIRCUIT VOLTAGE |
| :--- | :---: | :---: |
| $200 \Omega$ | 1 mA |  |
| $2 \mathrm{k} \Omega$ | 1 mA |  |
| $20 \mathrm{k} \Omega$ | $100 \mu \mathrm{~A}$ | 6.0 V MAX |
| $200 \mathrm{k} \Omega$ | $10 \mu \mathrm{~A}$ |  |
| $2 \mathrm{M} \Omega$ | $1 \mu \mathrm{~A}$ |  |
| $20 \mathrm{M} \Omega$ | $.1 \mu \mathrm{~A}$ |  |

Maximum Input
300 V DC or Peak AC
Ohms Settling Times

| RANGE | 5-1/2 and 4-1/2 DIGIT <br> (TO .01\% OF STEP) |  | $3-1 / 2$ DIGIT(TO . $1 \%$ OF STEP) |  |
| :---: | :---: | :---: | :---: | :---: |
|  | NO FILTER | FILTER | NO FILTER | FILTER |
| $200-20 \mathrm{k} \Omega$ | 100 ms | $<300 \mathrm{~ms}$ | $<15 \mathrm{~ms}$ | $<300 \mathrm{~ms}$ |
| $200 \mathrm{k} \Omega$ |  | $<1.1 \mathrm{~s}$ |  | $<800 \mathrm{~ms}$ |
| $2 \mathrm{M} \Omega$ |  | $<650 \mathrm{~ms}$ | $<70 \mathrm{~ms}^{*}$ | $<500 \mathrm{~ms}$ |
| $20 \mathrm{M} \Omega$ | $<1.5 \mathrm{~s}^{*}$ | $<6.8 \mathrm{~s}$ | $<600 \cdot \mathrm{~ms}^{*}$ | $<4.5 \mathrm{~s}$ |

*For these ranges the filter is recommended. This will reduce the effects of noise pick-up common to all high impedance measurements.

Table 1-3. 8860A Specifications (cont)

## GENERAL

| DISPLAY | RESOLUTION <br> $(\%$ FS $)$ | MAX <br> READING/SEC | LINE FREQ. <br> $(\mathrm{HZ})$ | A/D INTEGRATE <br> TIME (MS) |
| :---: | :---: | :---: | :---: | :---: |
| $5-1 / 2$ | 0.0005 | 2.5 | 50,60 | 100 |
| $4-1 / 2$ | 0.005 | 15 | 60 | $16-2 / 3$ |
| $3-1 / 2^{\star}$ | 0.05 | 12 | 50 | 20 |

*Accessible through IEEE-488 or Calculating Controller options only.
Temperature ............... $0^{\circ} \mathrm{C}$ to $+50^{\circ} \mathrm{C}$ operating; $-40^{\circ} \mathrm{C}$ to $+75^{\circ} \mathrm{C}$ nonoperating.
Temperature Coefficient ...... $\pm 0.1 \times$ applicable accuracy specification per ${ }^{\circ} \mathrm{C}$
Relative Humidity . . . . . . . . . . . $\leqslant 80 \%$ to $+35^{\circ} \mathrm{C}$; $\leqslant 70 \%$ to $+50^{\circ} \mathrm{C}$
Shock \& Vibration . . . . . . . . . . . MIL-T - 28800B, class 4
Power . . . . . . . . . . . . . . . . . . . . . 100, 120, 220, 240V AC $\pm 10 \%$, 250VAC MAX., 50 Hz or 60 Hz
Size . . . . . . . . . . . . . . . . . . . . . . $13.08 \mathrm{~cm} \times 20.45 \mathrm{~cm} \times 32.69 \mathrm{~cm}(\mathrm{H} \times W \times \mathrm{L})$
( 5.15 in $\times 8.05$ in $\times 12.85 \mathrm{in}$ ) See Figure 1-1.
Weight
3.39 kg ( 7.48 lbs .)

Protection Class 1 . . . . . . . . . . Relates solely to insulating or grounding properties defined in IEC 348


Figure 1-1. Outline Drawing

## Section 2 <br> Shipping and Service Information

## 2-1. SHIPPING INFORMATION

$2-2$. The 8860 A is packaged and shipped in a cardboard container. When you receive the 8860 A , inspect the instrument thoroughly for proper contents and possible shipping damage. Special instructions for inspection and claims are included on the shipping container itself.

NOTE
If an option is installed in the 8860 A , the rear panel cover plate for that option is shipped with the instrument. If the option is ever removed, install the cover plate over the connector hole as a safety precaution.

2-3. If reshipment is necessary, use the original container. If the original container is not available, order a new one
from John Fluke Mfg. Co., Inc.; specify model number 8860A.

## 2-4. SERVICE INFORMATION

2-5. Each 8860A Digital Multimeter is warranted to the original purchaser for a period of one year from date of delivery. The warranty is located at the front of this manual.
2-6. Factory authorized calibration and service for each Fluke product is available at various world wide locations. A complete list of these service centers is given in Section 7. If requested, Fluke will provide the customer with an estimate before any work begins for an out-of-warranty instrument.

## 2-7. QUESTIONS/PROBLEMS

2-8. For additional shipping or service information, contact your nearest John Fluke Sales Representative or Technical Service Center, listed in Section 7.

# Section 3 Theory of Operation 

## 3-1. INTRODUCTION

3-2. This section of the manual contains the theory of operation for the 8860 A . The theory is presented in two parts, an overall block diagram description followed by a detailed block diagram description. The theory of operation for the options is covered in Section 6 in this manual.

## 3-3. OVERALL BLOCK DIAGRAM DESCRIPTION

3-4. The overall block diagram description of the 8860 A is keyed to the simplified block diagram shown in Figure 3-1. The description concentrates on the guard and measurement circuits.

## 3-5. Guard Circuit

3-6. The guard circuit establishes a physical and electrical separation between the analog measurement (in-guard) circuits of the 8860 A and the control, display, and power supply (out-guard) circuits. The separation provides the shielding and isolating qualities required to enable accurate low-level measurements in the presence of common mode voltages. Since the guard forms a natural division of the 8860A circuitry, circuit functions and components are hereafter referred to as being in-guard or out-guard circuitry.

## 3-7. In-Guard and Out-Guard Processors

3-8. The 8860 A uses two 8 -bit microprocessors, one inside the guard (in-guard) and the other outside the guard (outguard). The in-guard microprocessor implements function and range selection (including autoranging), controls the measurement cycle, and communicates with the out-guard microprocessor via optical couplers.
3-9. When the out-guard microprocessor receives the measurement data, it can modify or analyze the data if an offset, limits, or peak to peak function is selected. The resulting data is then sent to the display. In addition, the out-guard microprocessor monitors and responds to frontpanel key selection (function, range, etc.), initiates each A/D conversion cycle, and controls the operation of either of two digital options.

## 3-10. Voltage Measurements

3-11. When the VDC, VAC, or VAC+VDC function is selected, the unknown voltage applied to the HI and LO INPUT terminals is directed through the input protection circuit to the AC/DC scaling and filtering circuit. AC measurements are either capacitively coupled (VAC) or directly coupled (VAC+VDC) into the scaling amplifier. Here the input voltage is either amplified by $10(200 \mathrm{mV}$ range), passed unscaled ( 2 V range), or divided by 100 or 1000 $(20 \mathrm{~V}, 200 \mathrm{~V}, 1000 \mathrm{~V}$ ranges). A full-range input on any range is scaled to $\pm 2 \mathrm{~V}$ dc or 2 V rms (see Table 3-1). Measurements which are strictly dc (VDC, $\Omega 2 \mathrm{~T}$, and $\Omega 4 \mathrm{~T}$ functions) continue directly from the scaling amplifier to the A/D Converter. All ac measurements (VAC and VAC+VDC functions) pass through the RMS-to-DC Converter where they are converted to a dc voltage.

## 3-12. Resistance Measurements

3-13. When the $\Omega 2 \mathrm{~T}$ or $\Omega 4 \mathrm{~T}$ function is selected, two operations occur concurrently at the input terminals:

1. A precision current is applied to the unknown resistor via the HI and LOW INPUT terminals. This current is generated by the Ohms Converter (also known as the Ohms Source). The value of source current for each range (except the 200 ohm range) is established at a level that will generate a two volt full-scale voltage for the 200 ohm range is 200 mV .
2. The voltage generated across the unknown resistor is sensed at the HI and LO INPUT terminals (for $\Omega 2 \mathrm{~T}$ ), or at the $\Omega 4 \mathrm{~T}$ SENSE HI and LO terminals (for $\Omega 4 \mathrm{~T}$ ). This voltage passes unscaled into the A/D Converter (except on the $200 \Omega$ range where it is first amplified by a factor of 10 ).


Figure 3-1. 8860A Block Diagram

Table 3-1. Scalling of Input Signals

| FUNCTION | RANGE <br> (FULL-SCALE INPUT) | OHMS CONVERTER SOURCE CURRENT (OHMS ONLY | AC/DC SSCALING |  | FULL-SCALE OUTPUT OF AC/DC SCALING |
| :---: | :---: | :---: | :---: | :---: | :---: |
|  |  |  | INPUT DIVIDER | SCALING AMPLIFIER |  |
| Volts VDC, VAC, $V A C+V D C$ | $\begin{gathered} 200 \mathrm{mV} \\ 2 \mathrm{~V} \\ 20 \mathrm{~V} \\ 200 \mathrm{~V} \\ 1000 \mathrm{VDC} \\ 700 \mathrm{VAC} \end{gathered}$ | - - - - - | $\begin{gathered} \div 1 \\ \div 1 \\ \div 100 \\ \div 100 \\ \div 1000 \end{gathered}$ | $\begin{gathered} \times 10 \\ \times 1 \\ \times 10 \\ \times 1 \\ \times 1 \end{gathered}$ | $\pm 2 \mathrm{~V} \mathrm{dc}$ $(\mathrm{VDC})$ or 2 Vrms $(V A C, V A C+V D C)$ |
| Ohms $\Omega 2 \mathrm{~T}, \Omega 4 \mathrm{~T}$ | $\begin{gathered} 200 \Omega \\ 2 \mathrm{~K} \Omega \\ 20 \mathrm{~K} \Omega \\ 200 \mathrm{~K} \Omega \\ 2 \mathrm{M} \Omega \\ 20 \mathrm{M} \Omega \end{gathered}$ | $\begin{gathered} 1 \mathrm{~mA} \\ 1 \mathrm{~mA} \\ 100 \mu \mathrm{~A} \\ 10 \mu \mathrm{~A} \\ 1 \mu \mathrm{~A} \\ .1 \mu \mathrm{~A} \end{gathered}$ | NOT CONNECTED | $\begin{aligned} & \times 10 \\ & \times 1 \\ & \times 1 \\ & \times 1 \\ & \times 1 \\ & \times 1 \end{aligned}$ | $+2 \mathrm{Vdc}$ |

## 3-14. AND Converter

3-15. The input to the $A / D$ Converter is a scaled dc voltage ( 2 V max) proportional to the 8860 A input voltage or resistance. In conjunction with the in-guard microprocessor, the $\mathrm{A} / \mathrm{D}$ Converter uses a dual-slope integration technique to convert the analog value to a digital representation.

## 3-16. DETAILED BLOCK DIAGRAM DESCRIPTION

3-17. The following paragraphs describe each of the blocks appearing in the 8860A block diagram, Figure 3-I. The description covers the power supply first, then traces the measurement signal path starting at the input terminals and ending at the display.

3-18. Drawing numbers for the applicable schematic diagrams are shown in parentheses following the description headings. The schematics are located in Section 8 of this manual.

3-19. Circuit descriptions often refer to IC and connector pin-numbers. ICs mentioned in the text are identified by U-numbers, e.g., U6. An IC pin number is identified by a dash and a number following the $U$-number. For example, U6-1 identifies pin 1 of IC U6. Pin 1 of each integrated circuit is identified on the pcb by a square solder pad. To identify a signal path through a series of connectors, refer to the Interconnect Diagram located in the schematic section. When two boards are connected, the pin numbers on both boards match, although the connector identifica-
tion numbers (the J and P numbers) may not match. For example, pin J3-42 (Main board) mates to P1-42 (Controller board).

## 3-20. Power Supply (Schematic 8860-1001)

3-21. The operating voltages for the 8860 are generated on the Al Main PCB. Operating voltages for the in-guard circuitry include $+5,+15$, and -15 volts. A separate +5 volt supply provides the operating voltage for the out-guard circuitry. Elsewhere, $+9,-9$ and -4 volt supplies are derived from the main operating voltages. Table 4-2 lists the circuitry powered by each supply.

3-22. As a troubleshooting aid, the $\pm 15$ volt supplies for the RMS-to-DC Converter and the Ohms Converter can be disconnected by removing jumper wires on the appropriate plug-in board. Refer to Troubleshooting in Section 4 for detailed procedures.

## 3-23. FUSING

3-24. The replaceable fuse located on the rear panel protects against excessive current in the power supply due to a short circuit. An additional non-replaceable thermal fuse, located inside the transformer, protects the 8860 A against fire hazard.

## 3-25. +5 VOLT SUPPLIES

3-26. Functionally, the +5 volt supplies for the in-guard and the out-guard circuitry are nearly identical. Each has a full-wave rectifier (CR10-13), a filter (C1, C2, C7), and a 5 -volt regulator (VR1, VR3).

## 3-27. $+/-15$ VOLT SUPPLIES

$3-28$. The +15 volt supply is regulated by a 15 -volt regulator (VR2). The -15 volt supply uses the output of the +15 volt supply as a reference. That is, as the output of the +15 volt supply becomes more positive, the -15 volt output becomes more negative. The tracking is accomplished by a precision inverter (U1, $\Omega 6, \mathrm{R} 12$, and R13) in which the voltage across R13 is equal to the voltage across R12. Power transistor Q6 is not short-circuit protected. Therefore, care must be taken to avoid shorting the -15 volt output to ground.

3-29. Notice the -15 volt supply requires that the +5 volt in-guard supply be working, since $U 1$ is supplied by the +5 volt supply. The +15 volt supply is unaffected by the +5 volt supply.

## 3-30. CIRCUIT COMMON AND THE GUARD

3-31. The 8860 A is capable of making fully floating measurements since its LO INPUT terminal is not internally connected to earth ground. To isolate the sensitive analog circuitry from the digital circuits, a guard is used. The circuitry outside the guard must interact with the outside world via the IEEE-488 option and external trigger BNC jack. Therefore, its common must sit at or close to earth ground. Thus, there are two electrically separate circuit commons: the in-guard common (also referred to as analog common), and the out-guard common (referred to as digital common). The out-guard common is connected through a $10 \mathrm{M} \Omega$ resistor to the center pin of the ac line cord, and thereby grounded to earth. The in-guard common is connected to the LO INPUT terminal; it is left floating, and can rise up to $\pm 500$ volts peak above the out-guard common (earth).

3-32. The guard is a separate metal shield which encloses the analog circuitry and in-guard microprocessor. By use of the GUARD switch, the guard may be connected to the in-guard common, or to an external common via the front panel GD terminal. Use of the guard switch and terminal is described in the 8860A Operator Manual.

## 3-33. Input Protection (Schematic 8860A-1001)

3-34. The input protection circuit, located on the Al Main PCB Assembly, protects the 8860A against sustained input voltages within its maximum input rating. The circuit also provides protection against voltage transients beyond this range. Sustained voltages beyond the rated range may damage the instrument.

3-35. The input protection description which follows is sectioned according to the various input paths:

1. DC and AC Voltage Sense
2. Ohms Source
3. $\Omega 4 \mathrm{~T}$ Sense
4. Guard

3-36. The relays located on the A1 Main PCB Assembly are not part of the input protection circuitry. Instead, they route the input signal according to the selected range and function. Additional relay details are provided later in this section under Scaling and Filtering.

## 3-37. PROTECTION FOR DC AND AC VOLTAGE SENSE

3-38. For dc or ac input signals the sense path is from the INPUT HI terminal through $\mathrm{R} 7(2 \mathrm{k} \Omega, 7 \mathrm{~W}$ resistor). At the junction of R7 and R10, four metal oxide varistors (MOV) RV1 through RV4 are connected to analog LO. These bipolar MOVs limit high voltage transients to $\pm 2$ kV at point E3. If the MOVs overheat and fail, they short circuit and thereby continue to provide protection for the scaling circuitry.

3-39. Coils LI and L2 suppress arcing when the contacts of KI are switching high voltages. The individual switches on K1, K2, and K4 are wired in series to obtain the 1000 V isolation required for input switching. Resistors R 10 and R11 protect the contacts of relay K3 from current surges when capacitor C6 discharges through K3.

## 3-40. OHMS SOURCE PROTECTION

3-41. The protection path for the ohms source is through R6. Varistors RV5 through RV8 limit high voltage transients to $\pm 2 \mathrm{kV}$, as described previously. The thermistor RTI (nominally $1 \mathrm{k} \Omega$ ) protects against high sustained voltages up to 300 V peak. As the temperature of RT1 rises, its resistance increases and effectively isolates the ohms source circuitry from the HI INPUT terminal. The clamp circuit (Q8, Q9, Q10, CR6, R14, and R15) serves two purposes: first, it clamps the open-circuit voltage of the current source (point E8) to about 5 V ; second it protects the Ohms Converter from voltage spikes at the input by limiting positive spikes to +5 V (via Q 8 and Q10) and negative spikes to $-2 V$ (via CR6 and Q9). Capacitor C 16 helps to shunt transient voltages to ground.

## 3-42. FOUR-TERMINAL OHMS SENSE PROTECTION

3-43. Resistors R8 and R9 provide protection for the 4-terminal ohms sense circuitry. To prevent ac cross talk. FET Q13 grounds the $\Omega 4 \mathrm{~T}$ input line when VAC or $\mathrm{VAC}+\mathrm{VDC}$ is selected. Transistor Q7 keeps the $\Omega 4 \mathrm{~T}$ SENSE LO line within -.7 V to +9 V of the in-guard common. This clamping of the sense inputs protects JFET AI-E on the AC/DC Scaling circuit.

## 3-44. GUARD PROTECTION

3-45. Components R25, C17, and R29 prevent the guard from making fast voltage transitions. As a result, voltage spikes at the GD terminal do not reach the guard itself.

## 3-46. Scaling and Filtering (Schematic 8860A-1004, Sheet 1 of 2)

3-47. The ranging and filtering for the selected function takes place on the AC/DC Scaling PCB (A4). When a range is selected, either manually or automatically, the $\mathrm{AC} / \mathrm{DC}$ scaling circuitry conditions the input signal to produce $\mathrm{a} \pm 2 \mathrm{~V}$ dc or 2 V rms signal for a full-range input.

## 3-48. AC/DC SCALING

3-49. The amount of scaling for each range and function is given in Tables 3-1. Figure 3-2 shows how the scaling takes place. Either JFET switch Al-A, A1-B, or Q13 is ON to divide by 1,100 , or 1000 . FETs Q12 and Q18 configure the scaling amplifier for a gain of either 1 or 10 . For both voltage resistance measurements, a conditioned signal of 2 volts dc at the $\mathbf{A} / \mathrm{D}$ Converter is recognized as a full scale-input for all ranges.

3-50. For all resistance measurements (except on the $200 \Omega$ range) the sense voltage generated across the unknown resistor is scaled to the 2 V range by the current source (Ohms Converter). The $200 \Omega$ range has a full-scale sense voltage of 200 mV . Consequently, the $\mathrm{AC} / \mathrm{DC}$ Scaling amplifier multiplies this voltage by 10 to establish the required 2 V dc at full scale. The JFET state tables are located with the AC/DC Scaling schematic in Section 8.

3-51. The scaling amplifier (Q17 and U14) is the first amplifier an input signal encounters. In VDC, the differential JFET input stage Q17 provides an input resistance greater than $10,000 \mathrm{M} \Omega$ for the 200 mV and 2 V ranges. The input divider presents a $10 \mathrm{M} \Omega$ input resistance for the higher voltage ranges. Capacitors C 2 through C 7 , connected to the resistive divider, are adjusted to maintain a flat frequency response for the divider ranges.

3-52. The voltage clamp (Q2, Q3, Q7, Q8, VR1, VR2) limits the voltage applied to the scaling amplifier to $\pm 10 \mathrm{~V}$ peak on the two lowest voltage ranges (both ac and dc) and all ranges of ohms. The other voltage ranges do not require clamping since the largest voltage that can appear at the scaling amplifier is $10 \mathrm{~V}(1000 \mathrm{~V}$ divided by 100$)$.

## 3-53. JFET BIAS AMPLIFIERS

3-54. The high-impedance, unity-gain, JFET amplifier, Q16 and U5, follows the input voltage to pull up the gate of each conducting JFET in the scaling circuit. Amplifier U6A performs the same bias function for JFET switches Al-G, QI2, and Q18.

## 3-55. FILTERING

3-56. A passive and an active filter are a part of the AC/DC Scaling network. Both are shown in simplified form in Figure 3-2.

3-57. If either the Calculating Controller Option ( -004 ) or the IEEE-488 Interface Option ( -005 ) is installed, a settling
delay (Modifier A4) may be enabled. In this case, each measurement is initiated only after the filter voltages have settled. The a mount of delay is controlled by the in-guard processor.

## 3-58. Passive Filtering

3-59. The passive filter consists of capacitor C9, JFET Q15, and the resistive component (approximately 100 kilohms) of the input divider. The VDC and the ohms functions allow the filter to be selected using the front panel filter switch. If the filter is not selected, its state is conditional as described in the state table (see schematic). Selecting either the VAC or the VAC+VDC functions disables both filters regardless of other operating conditions.

## 3-60. 3-Pole Active Filtering

3-61. The front panel FILTER modifier, for certain functions and ranges, inserts a low-pass 3-pole Butterworth filter (U3) with a corner frequency of approximately 7 Hz . It provides additional noise rejection in VDC, $\Omega 2 \mathrm{~T}$, and $\Omega 4 \mathrm{~T}$.

## 3-62. AUTOZERO

3-63. The scaling amplifier (Q17 \& U14) has an inherent input offset voltage which drifts with time and temperature. In the VDC and ohms functions the autozero circuitry eliminates the effect of this error at the start of every VDC or ohms measurement cycle. (In VAC and VAC+VDC the autozero routine is not performed.) Functionally, the auto zero circuit may be divided into the following three groups:

1. Components to momentarily short the input of Q17 to ground through A1-G and either JFET A1-D (for VDC and $\Omega 2 \mathrm{~T}$ ), or A1-E (for $\Omega 4 \mathrm{~T}$ ). The drive signal for Al-G is INT.
2. Components to store and subtract the offset voltage from the output of U14: C15 and Q10 located on the A/D Converter board.
3. Components to correct for charge injection during the measurement cycle: C1, R5, C44.

3-64. A functional grouping of the autozero components is shown in Figure 3-3. The auto zero sequence is performed under the control of the in-guard microprocessor as follows: FETs Q10 and A1-G close simultaneously. The input of Q17 is grounded causing capacitor C15 to charge to the combined offset voltage of Q17 and U14. Then Q10 and Al-G open causing the corrected input signal to be applied to the input buffer of the $A / D$ Converter, A2-J.

3-65. In the four-terminal ohms function, the DMM autozeros through JFET A1-E to the $\Omega 4$ T SENSE LO terminal. This terminal is the measurement reference, giving true four-terminal sense.


NOTE: THE SWITCHES IN THIS DIAGRAM SYMBOLIZE JFET SWITCHES.
THE SWITCH POSITIONS ARE IN THE VDC, $2 V$ RANGE, FILTER NOT CALLED, AND INT CONFIGURATION

## A. CHARGING C15 TO OFFSET VOLTAGE


B. CONFIGURATION FOR APPLYING Vin TO A/D CONVERTER


3-66. During the measurement cycle, switching signals are capacitively coupled into the input node of Q17. Capacitor Cl is driven with the INT signal to correct for charge injection errors.

## 3-67. AC BOOTSTRAP AMPLIFIER

3-68. Operational amplifier U6B is capacitively coupled to the non-inverting input of Q17. At higher frequencies U6B operates as a bootstrap to compensate for the high frequency rolloff of ac signals in the 200 mV and 2 V rānges. The rolloff is due to the parasitic capacitance of the JFET switches connected to pin 17 of AI. Amplifier U6B has a gain of 1.75 to 2.00 (depending on how R29 is set). JFET Q19 is turned on for VDC and ohms measurements to reduce the gain of U6B. This gain reduction eliminates charge transfer through C 17 during the autozero process, and keeps input bias current to a minimum. The charge transfer is especially evident when making high resistance (greater than 10 megohm) measurements.

## 3-69. RMS-to-DC Converter (Schematic 8860A-1004, Sheet 2 of 2)

3-70. The RMS-to-DC Converter, hereafter referred to as the RMS Converter, is located on the AC/DC Scaling PCB. For the VAC and the VAC+VDC functions the converter generates a positive dc voltage with a magnitude equal to the true rms value of the input (up to crest factor of 3). The RMS Converter, shown in Figure 3-4, computes the rms voltage using a log-antilog circuit.

3-71. The following description of the RMS Converter is divided into four separate sections:

1. Absolute Value Converter
2. 2 X Log Amplifier
3. Log Feedback Amplifier
4. Antilog Amplifier

3-72. The absolute value converter, composed of U8 and its associated components, forms a full-wave rectifier which converts a bipolar voltage to a positive collector current at UI7A. A positive input voltage (Vin) causes a collector current of Vin/40k ( $I_{1}$ in Figure 3-4). When Vin is positive, $\mathrm{I}_{2}$ is zero since CR6 is off; diode CR7 is turned on.

3-73. A negative input voltage (Vin) produces the same U17A collector current, but in a different manner. Diode CR6 is turned on, and CR7 is turned off. The negative input voltage appears at the cathode of CR6, inverted (with unity gain). Half of current $I_{2}$ flows through the 40 kilohm resistor and the other half (Vin/40k) flows into the collector of U17A.

3-74. The offset compensation amplifier U15 corrects for the dc offset of U 8 . The correction improves the dc stability of U8 over the operating temperature range of the 8860 A

3-75. The 2X Log Amplifier takes the logarithm of the U17A collector current and multiplies the logarithm by 2. Transistors U17A and U20A are the logarithmic elements in the amplifier. The logarithmic function is derived from the relationship of base-emitter voltage to collector current of a bipolar transistor.

3-76. A few components in the 2 X Log Amplifier help to improve stability and high frequency response. For example, Q14, a transconductance amplifier, assures loop stability; RC network R75 and C41 provide ac compensation; and R61 adjusts the loop gain of the circuit to improve high frequency response. Low voltage power supplies are used with U16 to ensure low power dissipation and improved stability.
3-77. The amplifier consisting of U19A and U20B performs the antilog function of the RMS Converter. The collector current of $\mathrm{U} 20 \mathrm{~B}(\mathrm{~V} 3 / 400 \mathrm{k} \Omega)$ is logarithmically related to the difference between its base and emitter voltages (V2 and V1). Capacitor C34 operates as a filter and U19B operates as the log feedback amplifier.

3-78. In operation the output of U19A is a dc voltage equal to five times the rms value of the input to the RMS Converter. At full scale, its output is 10 V . Resistive divider network U18 divides the output of U19A by five to obtain a full scale output of 2 volts. Jumper wires W5 through W8 are removed as necessary during factory calibration to bring the divider output within the adjustment range of R67. The output is filtered by R59 and C32 before being applied to the A/D Converter.

3-79. Jumpers W5 through W8 are selectively cut at the factory during pre-calibration, and should not be altered unless the U17 or U20 transistor arrays are replaced. See Table 4-5 for the jumper selection guide.

## 3-80. Ohms Converter (Schematic 8860A-1005, Sheet 1 of 2)

3-81. The Ohms Converter is physically located at the forward end of the $\mathbf{A / D}$ and Ohms Converter PCB. The Ohms Converter is enabled when the $\Omega 2 \mathrm{~T}$ or $\Omega 4 \mathrm{~T}$ function is selected. Circuit operation is the same for both functions. The Ohms Converter supplies a source current through the unknown resistance ( Rx ), generating a dc voltage proportional to Rx . This voltage is sensed and measured in the same way as a dc input voltage, but is displayed in ohms.

## 3-82. SOURCE CURRENT

3-83. Figure 3-5 shows a simplified schematic of the Ohms Converter. Source current for Rx flows through relay K4, to the front panel terminal labeled INPUT H1, through $\operatorname{Rx}$ (the resistor being measured), and returns to the source through the INPUT LO terminal. This current is scaled according to the selected resistance range. The scaled values for each range are shown in Table 3-I. The $200 \Omega$ range has a 1 mA source current and produces a full-range voltage of 200 mV . All other ranges produce a 2 volt output at full-range.


TYPICAI WAVEFORMS FOR LOW FREQUENCY SINE WAVE INPUT


$$
\begin{aligned}
& v_{1} \alpha-\log \frac{\left|v_{i n}\right|^{2}}{(40 K)^{2}} \\
& v_{2} \alpha-\log \left(\frac{v_{3}}{100 \mathrm{~K}}\right) \\
& v_{3}=400 \mathrm{~K} \text { antilog }\left(v_{2}-v_{1}\right)=5 \sqrt{v_{\mathrm{vin}^{2}}} \\
& v_{0}=\sqrt{\overline{v_{i n}{ }^{2}}}
\end{aligned}
$$



## 3-84. RANGING VIA JFET SWITCHING

3-85. The ranging resistors are switched into the circuit by a series of JFETs located on the AI hybrid assembly. ICs U6 and U7 are quad comparators with open-collector outputs. They translate digital control signals to voltage levels suitable for driving JFET switches. The JFET gate voltage requirements are -15 volts for turn off and a value equal to the channel voltage for turn on. The 2 to 4 decoder, U21, controls (through U6 and U7) the selection of four precision range resistors. The U21 truth table is given in Section 8, Ohms Converter.

3-86. On the lowest five resistance ranges, the 0.1 mA reference current flows through $70 \mathrm{k} \Omega$ ( $\mathrm{R} 9+63 \mathrm{~K}+7 \mathrm{~K}$ ) to produce a constant +7 volt drop across the enabled range resistor. Holding the voltage across the selected range resistors produces the constant source current for Rx. For example, on the $200 \Omega$ range, +7 volts across 7 kilohms produces a 1 mA source current. On the $20 \mathrm{M} \Omega$ range, JFETs Al-A and A1-B switch the 0.1 mA through the 7 kilohm reference resistor, producing a +0.7 volts drop across the 7 megohm reference resistor. The 0.7 volt drop maintains the $0.1 \mu \mathrm{~A}$ source current for Rx .

3-87. Amplifier U4, configured as a unity-gain amplifier, tracks the channel voltage of the AI switching FETs. The output of U 4 is used to supply the on-state gate bias voltage for all of the Al switching JFETs. By tracking the voltage at pin 6 of $\mathrm{A} 1, \mathrm{U} 4$ maintains a constant, low junction voltage for all input voltages, thus keeping leakage effects constant. U4 also bootstraps the protection circuit on the main board to minimize leakage errors.

## 3-88. A/D Converter (Schematic 8860A-1005, Sheet 2 of 2)

3-89. The A/D Converter is located on the A/D and Ohms Converter PCB. Its purpose is to convert a measured quantity from analog to digital form for the purpose of display. Figure 3-6 is a simplified circuit diagram of the A/D Converter. The entire A/D conversion process, including timing, is under the control of the in-guard microprocessor. The A/D Converter indicates the polarity of the input (for selection of the reference) and signals the processor when the correct count has been reached.

3-90. The A/D Converter uses a dual-slope conversion technique and operates in both polarities. The dc voltage input to the $A / D$ Converter represents the unknown resistance or voltage at the 8860 A input terminals. This dc voltage is integrated (charges C7) for a fixed amount of time, called the integration period; see Figure 3-7. At the end of this period the input of the $\mathrm{A} / \mathrm{D}$ converter switches to either an internal or an external reference voltage with a polarity that is opposite that of the input voltage. This discharges capacitor C7 at a controlled rate. A comparator interrupts the microprocessor and ends the discharge period when the charge remaining on C7 is equal to the charge that was present just prior to integration.

3-91. Figure 3-7 illustrates and describes the various periods within a measurement cycle. Figures 4-4 and 4-5 in Section 4 of this manual give the associated JFET timing diagrams and signal waveforms.

3-92. The in-guard microprocessor derives the digital readout by counting at a 1 MHz rate during the discharge cycle. If the counter reached 199,999 counts without being interrupted (in the 5-1/2 digit mode), the display will indicate overrange.

## 3-93. PRECISION VOLTAGE REFERENCE

3-94. The Precision Voltage Reference, Figure 3-8, provides the voltage standard for all 8860A measurements by establishing a precise discharge rate for C 7 . Reference amplifier U 22 is a temperature compensated 6.5 volt zener reference. Op amp U23A is connected in a bootstrap configuration to supply a very stable +11 volt output to R40 and R4I, assuring highly stable currents for U22. Resistor R40 sets the zener current. Resistors R41 and R42 are selected to set the correct temperature compensation current for the reference amplifier.

3-95. Amplifier U23B fixes the collector of U22 at zero volts and buffers the output of U 22 for use by the reference divider network, U10. Jumper wires W4-W8 are removed as necessary during factory calibration to bring the reference divider output voltage within the adjustment range of R17. Diode CR11 and R44 assure that the reference circuit always powers up to the correct polarity.

## 3-96. PRECISION CURRENT REFERENCE

3-97. Amplifier U5 taps 5.480 V dc from U10 and applies it to R11 and R12 to generate a precise 0.1 mA dc reference current for the Ohms Converter. JFET Q3 assures a constant output current over the entire compliance voltage range of the Ohms Converter.

## 3-98. A/D SWITCHING NETWORK

3-99. Hybrid A2 on the A/D Converter PCB contains a series of JFET switches. These switches are used to perform the following functions:

1. Select the VDC, Ohms (via A2-C), or VAC (via A2-D) functions for processing during the integrate period.
2. Enables the internal reference (via A2-B) or the external reference (via A2-A) for use during the counting period. (This selection is made from the front panel.)
3. Switches the polarity of the IV reference (via A2-F, G, H, and C14) for the A/D Converter.

3-100. Items 2 and 3 are described further under Internal/External Reference.


## AUTOZERO PERIOD (AZ)

The initial small voltages on C 7 and C 13 are established during this period with Q 6 switched on and the A/D buffer input grounded through A2-H. AZ2 assures fast recovery from overloads.

TIME-OUT PERIODS (Delta-2)
Each of these .5 ms periods allows the $A / D$ buffer to respond to the switched-in voltage and settle, before the voltage is applied to the integrator.

## INTEGRATE PERIOD (INT)

C 7 charges to a voltage proportional to the applied input. The length of the integrate period depends on the sample rate chosen, as follows:

| RESOLUTION | AC LINE FREQUENCY | INTEGRATION <br> PERIOD (INT) | MEASUREMENT CYCLE <br> (approximate) |
| :---: | :--- | :--- | :--- |
| $51 / 2$ digit | 50 Hz or 60 Hz | 100 ms | 400 ms |
| $41 / 2$ digit | 50 Hz | 20 ms |  |
| $31 / 2$ digit | 60 Hz | $16.2 / 3 \mathrm{~ms}$ | 66.7 ms |
|  | 50 Hz or 60 Hz | 2 ms | 20 to 50 ms |

## DISCHARGE PERIOD (DE)

C7 discharges for a length of time proportional to the applied input, during which digital counts accumulate. This count represents the value of the input resistance or voltage being measured. The rate of discharge is the same for all $A / D$ conversion speeds when the internal reference is chosen.

Figure 3-7. AD Converter Measurement Cycle


Figure 3-8. Precision Voltage and Current References-Simplified Schematic

3-10I. The JFET switches of A2 are controlled by comparators U15 through U18, which in turn are controlled by the in-guard microprocessor. The timing for the JFET switches is shown in Figure 4-4. IC U2I decodes two lines from the microprocessor into a 1 -of- 4 output.

3-102. Amplifiers U13A and U13B supply gate bias to JFET switches which must conduct non-zero voltages. This bias arrangement assures a constant switch resistance for all voltage levels.

## 3-103. AND BUFFER

3-104. The $A / D$ buffer, as shown in Figure 3-6, consists of dual JFET A2-J and amplifier UllA. The buffer receives a scaled dc input from the $\mathrm{AC} / \mathrm{DC}$ scaling circuits, amplifies the input by a factor of 3 , and provides the integrator with the amplified signal.

## 3-105. INTEGRATOR AMPLIFIER

3-106. The integrator consists of Q11, U11B, R47 and C7. JFET Q5 is on during the integrate and discharge periods to allow C7 to charge and discharge. JFET Q5 is switched off for 0.5 ms (Delta-2) before the charge and discharge periods. Clamp transistor Q12 ensures that Q5 does not conduct current during these off times. The Delta-2 periods serve to isolate the integrator from transient voltages due to switching of the $A / D$ buffer input. In addition, input polarity is sensed during the second Delta-2 so that the appropriate reference can be applied to the $A / D$ buffer.

3-107. JFET Q4 is normally off and Q7 is normally on. However, they change state simultaneously for a short time (called AZ2) at the beginning of the autozero period. Q4 switches on during $\mathrm{A} Z 2$ to rapidly remove any residual charge on C7. Q7 switches off to minimize disturbance of the charge stored on Cl3 during the previous autozero. The AZ2 period is the key to high-speed operation of the A/D Converter (4-1/2 and 3-1/2 digit modes). AZ2 also assures rapid overload recovery. Resistors R22 and R23 provide a small amount of linearity correction.

## 3-108. INTERNAL/EXTERNAL REFERENCE

3-109. The selected reference, internal or external, is applied to the $\mathbf{A} / \mathbf{D}$ Buffer during the discharge period. The internal reference is a precise +1 or -1 volt level. It is applied with a polarity opposite the scaled dc input voltage in order to discharge C7. The precision - 1 volt internal reference is available via JFET A2-B.

3-110. The +1 volt reference is derived by storing the precision-1 volt level on capacitor C 14 and then reversing the capacitor's connections. JFETs A2-F and A2-H are switched on for the duration of the autozero period to charge C14. When the positive reference is required, A2-G is switched on durng the tischarge period.

3-111. An external reference voltage may be of either polarity since the $A$ C Converter incorporates a precision
inversion circuit. The inversion is accomplished by connecting C14 to the reference voltage during autozero and reversing the capacitor's connections during the discharge period.

## 3-112. SLOPE AMPLIFIER AND COMPARATOR

3-113. Op amp UI2 is configured as an inverting amplifier with a gain of 60 . Its output is used to improve the accuracy of zero-crossing detection (via U14) at the end of the discharge period, and to assure accurate and repeatable autozeroing of the integrator during the autozero period (via (6). JFET Q6 conducts during the autozero period to close the loop which initializes the voltages on C7 and C13.

3-114. The comparator is composed primarily of U14, and includes Q11 on the Main PCB. The output of the comparator indicates polarity during the second Delta-2, and interrupts the counter at the end of the discharge period.

3-115. Diodes CR5, 6, 8, and 9 limit the slope amplifier output to ensure pinchoff of Q6 during the integrate and discharge periods. A dc voltage ( 70 mV to 120 mV ) determined by R29 and R30 is applied to U14-4 during the discharge period. When the output of the slope amplifier reaches the same voltage as U14-4, the comparator changes state and interrupts the in-guard microprocessor. Q9 is enabled for positive inputs, and Q8 for negative inputs.

## 3-116. In-Guard Microprocessor (Schematic 8860A-1001)

3-I17. The in-guard controller is an 8-bit microprocessor, complete with RAM and ROM. It plugs into a socket on the Main PCB Assembly and controls the entire measurement cycle. Measurement cycle control includes:

1. Implementing front panel selections: function, range, autoranging, zero, filter, sample rate, external reference, and trigger arm.
2. Timing the JFET switching associated with the A/D Converter.
3. Transmitting the measured value to the outguard microprocessor at the end of every measurement cycle.

3-118. The in-guard microprocessor controls autoranging. When autoranging is selected, the 8860 A begins in the highest range and downranges. If the input signal represents less than 18000 counts (in the $5-1 / 2$ digit mode), the 8860 A switches to the next lower range. If at any time the input signal represents more than 199999 counts, the 8860 A upranges.

3-119. The front panel ZERO function allows the inguard microprocessor to store an offset value for the

VDC and resistance measurement functions (2- or 4-terminal). The value is stored in three separate and independent RAM locations, and is subtracted from measured value before sending it to the out-guard microprocessor.

3-120. The in-guard microprocessor is powered by the +5 V in-guard supply. A reset circuit at U6-39 momentarily holds the microprocessor in the reset state during power-up to initialize internal conditions.

## 3-121. Guard-Crossing Circuitry (Schematic 8860A-1001)

3-122. The guard-crossing, located on the Main PCB Assembly, is an optically coupled data transmission path for communication between the in-guard and out-guard microprocessors. The use of opto-isolators allows a differential of up to $\pm 500$ volts between out-guard common and in-guard common.

3-123. Communication between the microprocessors employs detection and correction, and is fully selfrestarting when data is lost or incorrectly transmitted. Inadvertent loss of data is usually indicated by an error message on the display

3-124. In each direction there are two transmission paths, clock and data, which carry parallel signals. Transmissions in either direction, out-guard to in-guard (through U9 and U10) or in-guard to out-guard (through U7 and U8), are fully symmetrical. The following description of one of the guard-crossing data paths applies to all four.

3-125. A digital signal from J3-15 (Controller PCB connector) drives the inverting input of a comparator in U2. The output of the comparator drives the input of optoisolator U10. A low input to U10 produces an isolated high output level ( +0.42 to +0.6 V dc ). This signal drives the inverting input of another comparator (contained in U5) that has a switching threshold of +0.2 volts to 0.35 volts. The output of this comparator (pin 14) drives U6-14, the Receive Clock input to the in-guard microprocessor. The signal is inverted three times in crossing the guard, resulting in a net signal inversion.

## 3-126. Out-Guard Microprocessor (Schematlc 8860A-1003)

3-127. The out-guard controller U 2 is an 8 -bit microprocessor which plugs into a socket on the A3 Controller PCB Assembly. It is supported with external ROM and expanded I/O capability.

## 3-128. OUT-GUARD MICROPROCESSOR SOFTWARE

3-129. The out-guard microprocessor (U2) has an external program ROM (U9). This ROM contains the program which operates the 8860 A in the local mode;
another ROM takes over in the remote mode. From local ROM, the out-guard microprocessor:

1. Reads the front panel keys and internal switches.
2. Communicates front panel selections to the inguard microprocessor.
3. Passes all triggers to the in-guard microprocessor, including continuous triggers and those from manual, external, and bus sources.
4. Receives measurements from the in-guard microprocéssor.
5. Processes numerical data entered from the front panel.
6. Performs limits and peak to peak comparisons.
7. Performs offset subtraction.
8. Controls the display and front panel LEDs.
9. Performs self-diagnostic error checks.
10. Interfaces with the two digital options: the Calculating Controller (-004) and the IEEE-488 Interface ( -005 ).

3-130. Table 3-2 shows how the various ROMs are sectioned into four address spaces, and how each section is accessed using ports P23, P26, and P50. The table also shows the state of the control lines for each ROM device. The RAM internal to the out-guard microprocessor holds the three stored values for offset, high limit, and low limit.

## 3-131. OUT-GUARD MICROPROCESSOR HARDWARE

3-132. The four major components which suport the operations listed previously are located on the Controller PCB. They are:

1. U2, Out-Guard Microprocessor
2. U9, Local Program Memory (ROM)
3. Ul0, 8-Bit Latch
4. U3, I/O Expander

3-133. Operating power for the Controller PCB Assembly comes from the +5 volt out-guard supply. At power-up, capacitor Cl charges slowly through an internal resistor in U 2 to release the reset line (pin 4) after a delay. This initial delay sets the logic on the Controller PCB Assembly to a known state on power-up.

Table 3-2. Out-Guard ROM Selection

|  | ROM DEVICE |  | ROMADDRESS | PORT NO. |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  |  |  | $\begin{gathered} \text { P23 } \\ \text { U2-24 } \end{gathered}$ | $\begin{gathered} \text { P26 } \\ \text { U2-37 } \end{gathered}$ | $\begin{aligned} & \text { P50 } \\ & \text { U3-1 } \end{aligned}$ |
| BASIC instrument (LOCAL ROM) | U9 |  |  | 0-2047 | 0 | x | 0 |
|  |  |  | 2048-4095 | 0 | x | 1 |
| OPTION (OPTION ROM) | IEEE | CALC. | 0-2047 | 1 | 0 | X |
|  |  | U10 |  |  |  |  |
|  |  | U19 | 2048-4095 | 1 | 1 | x |
| $\mathrm{X}=\text { don }$ <br> Device/p example | bers device | schema 24. | -1003, Cont | uit bc | $2-24$ |  |

3-134. The out-guard microprocessor communicates with the other ICs (U9, U10, and the two digital options) by way of the data bus, lines D80 through D87. This bus is multiplexed; the data and the eight lower-order address bits appear at different times on these lines. The eight-bit latch (U10) holds the address at its output for the local program memory (U9). The address is latched from the data bus by a signal called ALE (Address Latch Enable). ALE is generated by the out-guard microprocessor.

3-135. The local ROM U9 actually requires a total of 12 address bits. The upper four bits of U9 are static during program memory read operations; the processor outputs them directly to U9 on lines P20 to P23.

3-136. The I/O Expander U3 expands lines P20 through P23 to 16 bits. Table 3-3 shows the functions that are assigned to each pin of U3. Notice that most of the pin assignments are bidirectional (input and output data). This expanded I/O operates the multiplexed display, reads the option identification, and reads the three slide switches SI, S2, and S3. The pin labeled PROG controls the timing of U3.

3-137. The display receives its control from the output ports of U2 and U3. Non-inverting drivers U4, U5 and U7 buffer the port outputs. Resistor network U6, and resistors R4, R5, and R6 are series resistors to limit the drive current to the display LEDs.

3-138. The two D-type flip-flops of U1 operate as signal conditioners for the out-guard microprocessor. The first flip-flop (pins 1-5) is part of the external trigger circuitry. The second (pins 9-13) conditions signals arriving from the installed digital option. The IEEE-488 option uses this line
to interrupt the out-guard microprocessor. The Calculating Controller option, however, uses this line as simply another input to the out-guard microprocessor.

## 3-139. EXTERNAL TRIGGER CIRCUITRY

3-140. The external trigger circuit is designed to trigger from either a switch opening or a rising TTL signal. The signal passes through two stages of conditioning. One-shot UII, when triggered, eliminates switch bounce by producing a positive output pulse of approximately 40 ms . This pulse sets D-type flip-flop U1 to signal the microprocessor that a trigger has been received. The microprocessor clears the flip-flop after it detects the set condition.

## 3-141. Front Panel Push Buttons (Schematic 8860A-1002)

3-142. The front panel push buttons are scanned by the out-guard microprocessor at the rate of two keys every 2.5 ms (regardless of the A/D sample rate). The out-guard microprocessor interrupts whatever it is doing to perform this function. (The IEEE-488 option causes the scan rate to slow when certain bus interrupts occur. This is because data communication between the GPIA and the out-guard microprocessor has priority over the 2.5 ms scan interrupts.)

3-143. A binary sequence at the input of UI (pins 13, 14 and 15) sets each of the eight output lines of $U 1$ low, one at a time. In this way the sixteen keys are strobed a column at a time through diodes CRI through CR8. The two strobed keys are read simultaneously via pins 16 and 17 of J . A line is low (at zero volts) only if the corresponding key is depressed. Thus the entire keyboard is read over a 20 ms interval.

## 3-144. Display (Schematic 8860A-1002)

3-145. The same U/ strobe lines that scan the front panel push-buttons also strobe the eight display digits. ondecimal points. 2 unisannunciators and 15 indicator lights. Wher pin 1 of 11 goes low, Q1 turns on activating the tiry seven segment readout and three indicator lights. Signals applied to the cathodes of the segments determine which segments
will light. As this first column of lights in lit all other columns transistor: $\mathrm{Q}^{2}$ through $\mathrm{Q}^{8}$ and their diplay fights) are turned ofl. the eight columns are strobed one at a time at a ratc high en wigh w make all digis appear to be on at the vame time. a time interrupt icture every? 5 m wxeept uith lt H - -xx Intelherthadance column. Th.
 ade once chery 20 m

Table 3-3. I/O Expander (U3) Pin Assignments

| PORT | U3 PIN NO. | OUTPUT FUNCTION | INPUT FUNCTION |
| :---: | :---: | :---: | :---: |
| P40 | 2 | Send data ${ }^{\text {a }}$ ( | Test Mode a switch (S3) |
| P41 | 3 | Send clock Guara crossing bit | Test Mode ? switer (S1) |
| P42 | - | ( not used) | (not used) |
| P43 | -- | 'not used) | ( not used) |
| P50 | 1 | ROM hank switch ment... | (pulled to sogic up |
| P51 | 23 | LSt | 50/60 Hz switch (S2: |
| P52 | 22 | midalf the ammenater data | ( $n$ ot used) |
| P53 | 21 | MSR | ( not used) |
| P60 | 20 | LSE | :D0 |
| P61 | 19 | middle bit front pariel scan data | ID1 Option Identification |
| P62 | 18 | MSB | ID2 3 Option Identification |
| P63 | 17 | (not used) | ID3 |
| P70 | 13 | (not used) | Receive data Guard cissing bit |
| F71 | 14 | (not used) | Receive clock Guard (1)ssing bit |
| P72 | 15 | (not used) | Bottom row froni parlei |
| P73 | 16 | (not used) | Top row kevboard |

## Section 4 Troubleshooting

## 4-1. INTRODUCTION

4-2 This section of the manual contains troubleshooting information for the 8860 A . The information is divided into live inajor parts. They are:

1. General Maintenance
2. Troubleshooting Approach
3. Analog Troubleshooting
4. Digital Troubleshooting
5. Troubleshooting Aids

## 4-3. GENERAL MAINTENANCE

4-4. Disassembly Procedure
WARNING
TO AVOID ELECTRICAL SHOCK HAZARD, DISCONNECT LINE POWER AND ANY INPUT CONNECTIONS FROM THE 8860A BEFORE STARTING THE DISASSEMBLY PROCEDURE.

4-5. Disassemble the 8860 A as follows:

1. Disconnect the 8860 A from line power; remove all front (and rear) panel inputs.
2. Remove the four screws located on the bottom of the chassis, and pull the top cover straight up and off.
3. For access to the analog circuitry, remove the guard cover by unscrewing its four top screws (the guard cover is the large metal cover with adjustment holes). Both analog circuit boards can be removed by pulling them straight up.
4. Remove the Display PCB by pulling the bottom off the chassis, disconnecting the five INPUT terminal wires, and pulling the entire front panel assembly forward. The front panel and the circuit board are held together by the connector to the Controller PCB.
5. Refer to Section 8 for identification of the circuit board assemblies. Each assembly unplugs from its connector.

## CAUTION

Do not contaminate the area around the INPUT terminal connections on the main PCB or the front end of the AC/DC Scaling PCB. Low level leakage can result in calibration errors.

## 4-6. Cleaning

4-7. To clean the front panel and exterior surfaces of the 8860 A, use a soft cloth dampened with either a mild solution of detergent and water or anhydrous ethyl alcohol.

## CAUTION

Do not get water on the transformer. The transformer will absorb the water and eventually fail. Use special care when cleaning the fragile hybrid assemblies; they are easily damaged.

CAUTION
If fluorocarbons or other solvents are used to clean the pcbs, keep it off switches and potentiometers. Solvents will remove the lubricants from these components and shorten service life.

4-8. To clean the interior of the unit, use clean, dry air at low pressure ( $<20 \mathrm{psi}$ ). If contaminants remain, clean the individual pcbs using warm water. The AC/DC Scaling and the A/D and Ohms PCBs may be safely washed with all components intact; the Main PCB requires special handling.

4-9. The Main PCB may also be cleaned using warm water. However, in doing so do not get the armature relays or the transformer wet. The recommended approach is to cover the transformer and remove the armature relays
during the washing process. Removereids K1. K3. and K4 by unplugging them from the peb; do not remove the reed relay.

4-10. After washing the pebs, remove excess water using clean dry air at low pressure. Dry the pebs in an oven at a temperature of $50^{\circ} \mathrm{C}$ or less.

## 4-11. Fuse Replacement

## WARNING

TO AVOID ELECTRICAL SHOCK HAZARD, DISCONNECT THE POWER CORD BEFORE SERVICING THE FUSE. ACLINE VOLTAGE IS PRESENT WHEN THE POWER CORD IS CONNECTED.

4-12. The power fuse (FI) is accessible from the rear panel. Replace the fuse, if necessary, with an MDL (slowblow) $1 / 4$-ampere fuse with a voltage rating ( 125 V or 250 V ac) exceeding the line voltage.

## 4-13. Static Awareness

4-14. Whenever troubleshooting, follon procedures coutlined on the yellow Static Awareness sheet located in this manual. These procedures are intended to prevent darnage to MOS devices due to static charge.

## 4-15. Pin Numbering

4-16. Note that pin I of each integrated circuit is identified by a square solder pad on the circuit hoard. Connector pinare numbered as shown in Section 8, in the figure latheled Interconnection of Assemblies.

## 4-17. Extender Cards

4-18. The following extender cards are available for troubleshooting the 8860 A plug-in pcb assemblies. The extenders may be used during troubleshooting and functional testing. However, all extenders must be removed during the performance test and the calibration procedure. Order by model number.

EXTENDER BOARD
MIODEL IUUMBER

$\sin (0) . \lambda-40 \%$

A( ) ( Sating PCB


Calculating Controller (-004)
$\sin 60 \lambda-4009$
and IELE-488 Interlace ( -005 )

## 4-19. TROUBLESHOOTING APPROACH

4-20. Figure 4-I shows the recommended approach for troubleshooting the 8860 A . When the instrument fails to perform as expected, use Table 4-1 to identify the fault as analog, digital, or power supply related. Then proceed to the analog or digital troubleshooting procedures. If additional circuit details are required after the fault area is located, refer to the theory of operation in Section 3 and the schematic diagrams in Section 8.

## 4-21. POWER SUPPLY CHECK

4.2. Table 4-2 lists the basic power supply wolages. their tev points and tolerances, and the circuits the sonp: Iest point locations are shown in Fipure 4-2. Check each al the power supply oltages aning thon bowing procedures:

1. In-Cinard Supply

Connect the eommon kead of a DMM to InGuard Common. Measure each of the three inguard coltages $(+5 \mathrm{~V} .+15 \mathrm{~V},-15 \mathrm{~V})$. Fach suppl? voltage should be within the tolerance indicated in Table 4-2.
2. Out-Guard Supply

Connect the common lead of the DMM to OutGuard Common. Measure the ontguated +5 V supply. It should measure within the tolerance indicated in Table 4-2.

> VOTE
\}. clipping jumper wires, wn can remone the $\therefore \therefore$ voll supply the R W.S-to-I)C Converter (wires H 3 and H 4 ) and Ohms Comerter (wires Wlo and W'll. This should onll be done to help locate a fault which is onerloading the $\pm 15$ woll supplies.

## 4-23. ANALOG TROUBLESHOOTING

1-24. A list of test points for troubleshooting the anatog vection of the 8860 A is shown in Table 4-3. Verify the werall operation of the analog section by conlirming the presence of these voltages. If a woltage is incorrect. matio a detailed check of the indicated circuit location or section. Procedures for troubleshooting the individual analog sections are given in the following paragraphs. The sections are covered in the following order:

- AC DC Scaling
- RMS-to-DC Converter
- Ohms Converter
- Precision Voltage Reference
- A/D Converter

NOTE
The A/D \& Ohms board can be operated with the $A C / D C$ Scaling hourd removed; however. the reverse is not true. DO .VOT TRY TO OPERATE THE AC/ DC SCALINGBOARD HITH THE A/D \& OHMS BOARD REMOVED. (The AC/ DC Scaling ground connections are made on the $A / D \&$ Ohms board.)

## 4-25. AC/DC Scaling

4-26. The following procedures assume that the signal path from the front panel INPUT terminals to the AC DC Scaling PCB has been checked and is operating properls. The AC/DC Scaling Extender Card is necessary for the following procedures.
4-27. The AC/DC Scaling circuitry is functionally dilided into two parts, the Front End and the Amplilier Section.


Table 4-1. Distingulshing Analog and Digital Faults at Front Panel

| An analog fault exists if a measurement reading is incorrect, but the following functions operate correctly: |
| :--- |
| - Front panel indicator lights respond properly when a measurement function is selected (e.g., switch from |
| VDC to VAC to $\Omega 2 \mathrm{~T}$ ). |
| - Decimal point is positioned correctly in response to a range change. |
| - Annunciators (mV, $\mathrm{V}, \Omega, \mathrm{k} \Omega, \mathrm{M} \Omega$ ) light up properly for each function and range. |
| - A number can be stored and recalled from the High, Low, or Offset registers. |
| Analog faults are located inside the guard on one of three pcbs: |
| - Main PCB Assembly |
| - AC/DC Scaling PCB Assembly |
| - ADD and Ohms Converter PCB Assembly |
| A digital fault usually exhibits at least one of the following svmptoms: |
| - Display appears faulty; reading does not change or display segments do not light. |
| - One digit is bright, others are off. |
| - All display and indicator lights are off. |
| - Instrument fails to respond to a front panel push button. |
| Digital faults are located on one of four PCB Assemblies: |
| - Controller PCB Assembly |
| - Display PCB Assembly |
| - Main PCB Assembly |
| - Option -004 or -005 PCB Assemblies |

1. The Front End includes:
a. Input Divider Lil and associated capacitors
b. Voltage clamp circuit
c. JFEI switches, including AI
d. Active Filter U3
2. The amplifier section includes:
a. Dual JFET QI7 and amplifice U14
b. Bootstrap Amplifiers Q16 (with I'5), U6A. and lob

4-28. Proper waveforms for the AC/DC Scaling board are shown in Figure 4-3, for a +1 V dc input, VDC. These signals are referred to in Table 4-4, which lists typical fault symptoms for the AC/DC Scaling PCB. When troubleshooting frequency response problems, voltage test measurements can load the front end circuitry. To avoid circuit loading, measure front end voltages only at the specified test points. Voltages below 2 V rms may be injected at various points in the front end (e.g., Al-17, Al-6, Al-9) and measured at appropriate test points.

4-29. Excessive leakage current in the front end JFETs can be pinpointed using the following guidelines:

1. Leakage in a JFET adversely affects a circuit only when the JFET is off (not conducting).
2. The leakage path may be from drain to source, preventing a fully off condition, or from gate to source.
3. Identify and inspect those JFETs that are off when leakage symptom is present. For example. if a dc offset disappears when the filter is enabled (Q11 on), then Q11 is probably defective.

## 4-30. RMS-to-DC Converter

4-31. Table 4-5 lists some general fault symptoms and corrections for the RMS Converter. Detailed procedures which may be used to check various functional aspects of the R MS Converter are given in the following paragraphs. The first procedure checks the VAC+VDC function. The second checks the VAC function. If a fault is identified. investigate the component; that precede the test point location.


Table 4-2. Power Supply Assignments
(Troubleshooting Section, Power Supply)

| POWER SUPPLY | TEST POINTS | TOLERANCE | SUPPLIES ONLY THE FOLLOWING CIRCUITRY |
| :---: | :---: | :---: | :---: |
| In-guard +15 V $-15 \mathrm{~V}$ <br> (relative to inguard common, TP2) | $\begin{aligned} & \text { TP3 } \\ & \text { TP4 } \end{aligned}$ | $\begin{aligned} & 14.25 \mathrm{~V} \text { to } 15.75 \mathrm{~V} \\ & -14.25 \mathrm{~V} \text { to }-15.75 \mathrm{~V} \end{aligned}$ | On the AC/DC Scaling PCB (A4): all circuitry except comparator reference level (R40, R41) <br> On the A/D \& Ohms PCB (A5): all circuitry except U21 and comparator reference level (U20) |
| In-guard +5 V <br> (relative to inguard common, TP2) | TP1 | 4.7 V to 5.3 V | On the Main PCB (A1): <br> -15 V supply (U1) <br> in-guard processor (U6) <br> opto-isolator circuitry (U5) <br> relay coils (K1-K4) <br> On the AC/DC Scaling PCB (A4): comparator reference level (R40, R41) <br> On the A/D \& Ohms PCB (A5): binary to 1-of-4 decoder (U21) comparator reference levels (U20) |
| Out-guard +5 V <br> (relative to outguard common, TP6) | TP5 | 4.7V to 5.3 V | On the Main PCB (A1): <br> opto-isolator circuitry (U2) <br> The entire Display PCB (A2) <br> The entire Controller PCB (A3), which includes: outguard processor <br> local ROM <br> external-trigger one-shot associated latches, flip-flops, and drivers <br> The entire Calculating Controller Option (-004) <br> The entire IEEE-488 Interface Option (-005) |
| Note: The test points are labeled on the schematic, but not on the circuit board itself. |  |  |  |

4-32. This procedure functionally checks the RMS Converter by tracing a dc signal through the converter while the dc-coupled VAC+VDC function is enabled. Set the 8860 A to the $\mathrm{VAC}+\mathrm{VDC}$ function and the 2 V range.
I. Apply +1.000 V dc between the HI and LO INPUT terminals of the 8860 A .
2. Using the test DMM, measure TP5 on the AC/DC Scaling PCB. The measurement should be within 10 mV of the input value.
3. Move the DMM input to test point E2, the input to the RMS Converter. The voltage measured should be the same as that at TP5.
4. Measure the voltage at TP3, the output of U8. It should measure approximately -1.6 V .
5. Reverse the polarity of the input signal and measure the voltage at TP3 again. It should
measure approximately +1.6 V . If tests 4 and 5 fail, U8, U15, CR6, or CR7 may be at fault.
6. Measure the voltage at TP2. It should be OV $\pm 20 \mathrm{mV}$.
7. Measure the voltage at TPI. It should be $-1.2 \mathrm{~V} \pm 0 . \mathrm{IV}$.
8. Measure the voltage at U19A-1. It should be $+5.0 \mathrm{~V} \pm 25 \mathrm{mV}$.
9. Measure the voltage at E3. It should be +1.0 V $\pm 5 \mathrm{mV}$. An offset may be present since auto-zero is not functional for VAC+VDC measurements.

4-33. This procedure functionally checks the RMS Converter by tracing an ac signal through the converter while the VAC function is enabled. Set the 8860A to the VAC function and the 2 V range.

Table 4-3. Quick Check to Locate Faulty Analog Circult

## TEST POINTS ON THE MAIN PCB

| TEST POINTS ON THE MAIN PCB |  |  |  |
| :---: | :---: | :---: | :---: |
| Use these test points to check the signal path from the front panel Input terminals, through the input relays, to the AC/DC Scaling PCB: |  |  |  |
| TEST POINT | LOCATION | TEST POINT VOLTAGE UNDER THESE CONDITIONS: |  |
|  |  | 1V DC INPUT, VAC+VDC, 2V RANGE | 1V rms @ 300 Hz INPUT, VAC, 2V RANGE |
| E2 | Junction of W6 and R7 | 1 V dc | - |
| E19 | Junction of W11 and L2 (checks K1) | 1 V dc | - |
| E29 | Junction of K3 and W12 (checks K3) | 1 V de | - |
| E19 | Checks K2 | - | 1 V rms |

TEST POINTS ON THE AC/DC SCALING PCB

| TEST POINT | LOCATION | TEST POINT VOLTAGE UNDER THESE CONDITIONS: |  |
| :---: | :---: | :---: | :---: |
|  |  | 10V DC INPUT, <br> VDC, 20V RANGE <br> TRIG ARM ENABLED | 10V DC INPUT, VAC+VDC, 20 V RANGE, TRIG ARM DISABLED |
| TP8 | AC/DC Scaling (output of JFET bias amplifier) | $100 \mathrm{mV} \mathrm{dc}+/-25 \mathrm{mV}$ * | $100 \mathrm{mV} \mathrm{dc}+/-25 \mathrm{mV}$ * |
| TP5 | AC/DC Scaling (Output of scaling amplifier) | OV dc $+/-10 \mathrm{mV}^{*}$ | $1 \mathrm{Vdc}+/-10 \mathrm{mV}^{*}$ |
| TP2 | RMS Converter (U16 inverting input) | OV dc $+/-20 \mathrm{mV}^{*}$ | OV dc $+/-20 \mathrm{mV}^{*}$ |
| TP3 | RMS Converter (Output of absolute value converter) | OV dc $+/-500 \mathrm{mV}^{*}$ (Will be very noisy) | Approx. -1.6 V dc |
| TP1 | RMS Converter (Output of 2X $\log$ amplifier) |  | Approx. -1.2 V dc |
| E3 | RMS Converter (Output of RMS Converter) | OV dc $+/-5 \mathrm{mV}$ | $1 \mathrm{Vdc}+/-5 \mathrm{mV}^{*}$ |

[^0]Table 4-3. Quick Check to Locate Faulty Analog Circuit (cont.)

| TEST POINTS ON THE A/D \& OHMS CONVERTER PCB |  |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: |
| TEST POINT | LOCATION | TEST POINT VOLTAGE ACCORDING TO RANGE WITH THE INPUT TERMINALS SHORTED |  |  |  |
|  |  | $200 \Omega / 2 \mathrm{k} \Omega$ | $20 \mathrm{k} \Omega$ | $200 \mathrm{k} \Omega / 2 \mathrm{M} \Omega$ | $20 \mathrm{M} \Omega$ |
| U1-10 | Ohms Converter | $\begin{aligned} & 8.6 \mathrm{~V} \\ & \text { to } \\ & 9.7 \mathrm{~V} \end{aligned}$ | $\begin{aligned} & 7.1 \mathrm{~V} \\ & \text { to } \\ & 7.3 \mathrm{~V} \end{aligned}$ | $\begin{aligned} & 6.95 \mathrm{~V} \\ & \text { to } \\ & 7.05 \mathrm{~V} \end{aligned}$ | $\begin{aligned} & 0.69 \mathrm{~V} \\ & \text { to } \\ & 0.71 \mathrm{~V} \end{aligned}$ |
| TP9 <br> U10-2 <br> U10-3 | Ohms Converter <br> Precision Reference <br> Precision Reference | 7.00 V below the reading at $\mathrm{U} 1-10$ <br> -0.99980 V to -1.00000 V dc <br> -6.478 V to -6.482 V dc |  |  |  |
| Enable the TRIG ARM function before measuring the following test points: |  |  |  |  |  |
| TP11 <br> TP12 <br> TP13 | A/D Converter <br> AVD Converter <br> AV Converter | OVdc +1 <br> OV dc <br> OV dc |  |  |  |
| Turn the 8860A power off, and remove the AC/DC Scaling PCB. Turn the power back on, and select the VAC function, 2 V range. Temporarily connect A2-7 ( $\mathrm{A} / \mathrm{D}$ input) to $\mathrm{U} 10-2$ ( -1 volt reference) with a clip-lead wire. The display reading should be a value from .99960 to 1.00020 . Reinstall the AC/DC Scaling PCB after this test. |  |  |  |  |  |

I. Apply a $1 \mathrm{~V}, 100 \mathrm{~Hz}$ sine wave to the 8860 A HI and LO INPUT terminals. Using a scope, monitor TP5 on the AC/DC Scaling PCB. The ac input should appear as a clean, undistorted sine wave.
2. Move the scope probe to TP3 of the absolute value converter. The signal should appear as in Figure 3-4 (TP3).
3. Move the scope probe to TPI of the $2 \times \log$ Amplifier. The signal should appear as in Figure 3-4 (TPl). The waveform should be free of oscillations and noise. Waveform symmetry is not critical. If the waveform is not correct the problem is in the $2 \mathrm{X} \log$ amplifier, the $\log$ feedback amplifier, or the antilog amplifier.
4. Using the DMM, measure the dc output voltage of the RMS-to-DC converter at E3. It should measure +1 V dc with an applied input of 1 V rms ac.

4-34. The following tests should be performed if the RMS-to-DC Converter is functional but will not calibrate properly:

1. Short the 8860 A input terminals. Select VAC function, 20 V range.
2. Measure the voltage at TP5. It should be $0 \pm 0.0$ I mV dc.
3. Measure the voltage at TP2. It should be $0 \pm 0.01$ mV dc.
4. Using a scope, check TP3 to see that R46 can provide adjustment on either side of zero. If the adjustment is not possible, U15 or the $2 \mathrm{X} \log$ amplifier may be at fault.

## 4-35. Ohms Converter

$4-36$. If the voltage at point U1-10 is outside the values given in Figure 4-3 the Ohms Converter is at fault. To isolate the fault, temporarily disable the feedback loop by connecting a short across R4 with a clip lead. Then check the operational amplifier by placing a short across the 8860 A INPUT terminals, selecting $\Omega 2 \mathrm{~T}$ function and 2 $\mathrm{M} \Omega$ range, and shorting TP9 on the Ohms Converter to (E5). In this configuration, pins 26 and 29 of hybrid Al should measure within 10 mV of each other (at approximately +12.75 V dc ). Also, the voltage at $\mathrm{U} 4-6$ should be within 7 mV of TP9.

## NOTE

Disconnect the jumper from TP9 and E5 before continuing.

4-37. The reference current can be tested by checking the voltage between TP9 and the cathode of CRI while the 8860 A is on the $2 \mathrm{~K} \Omega$ range. The voltage should be 7.00 V dc. (The short across R4 may be left in place.) JFETs Q8


Figure 4-3. AC/DC Scaling Waveforms
and Q9 on the Main PCB are important for leakage control as well as protection. If either JFET leaks excessively, readings on the high-resistance ranges will drift during warm-up.
4-38. If the Ohms Converter malfunctions only on certain ranges, then the output voltages from U6 and U7 should be checked. Use the switch state table shown with the Ohms Converter schematic in Section 8.
4-39. The voltages across the U1 resistors 7.017, 70.71, and 778.9 kilohms should be 7.00 V dc when the associated range is selected, and 0.00 V dc otherwise. Each resistor can be checked in-circuit for the correct resistance value with an ohmmeter when either the $2 \mathrm{M} \Omega$ or $20 \mathrm{M} \Omega$ range is selected. Isolation between pins 9,12 , and 16 on A1 can also be measured with either the $2 \mathrm{M} \Omega$ or $20 \mathrm{M} \Omega$ range selected. For example, the resistance between pins 12 and 16 of Al should be approximately 77.8 kilohms, which is the series value of R3, 70.71 kilohms and 7.017 kilohms.

## 4-40. Precision Voltage Reference

4-41. Voltage readings at pins 1,2 and 3 of resistor network U10 should be within the following limits. Refer to the theory of operation (Precision Voltage Reference) in Section 3 for help in troubleshooting the voltage reference.

1. U10-I:
0.0 V
2. UI0-2: $-1.0 \mathrm{~V} \pm 100 \mathrm{uV}$
3. U10-3:

$$
-6.48 \mathrm{~V} \pm 1 \mathrm{mV}
$$

4-42. The reference amplifier U 22 , and resistors R 41 and R42 must be replaced as a set if $U 22$ is faulty. After $U 22$ is replaced, perform the jumper selection procedure given at the end of this section under Post Repair Procedures.

## 4-43. A/D Converter

4-44. Troubleshooting information for the A/D Converter is presented in four parts. First, a list of possible problems and symptoms is given in Table 4-6. This is followed by a functional check of the A/D Converter with

Table 4-4. Typical Symptoms of AC/DC Scaling Faults

| SYMPTOMS | INSTRUCTIONS OR COMMENTS |
| :---: | :---: |
| DC PROBLEMS <br> 1. Input bias current at front panel terminals exceeds $100 \mathrm{pA}^{*}$ <br> 2. Downscale performance in VDC, 200 mV and 20 V ranges is out of specification <br> 3. Downscale, low frequency signals read too high on 200 mV range of VAC but not in VAC+VDC (see following note) | Symptom may indicate excessive leakage current in a JFET (dual JFETs Q16 and Q17 are usually not at fault). If the faulty JFET is localized to hybrid A1, replace the entire hybrid assembly. Otherwise, replace discrete JFETs one at a time until the fault clear. Use the guidelines mentioned in the preceding paragraph to identify leaking FETs. |
| In VAC and VAC+VDC, the dis 200 mV range) even when the over the specified input range | NOTE <br> $y$ will indicate a reading (typically less than 400 counts in the ut is shorted. This reading will not affect the rated accuracy does not indicate a fault condition. |
| 4. VDC function inoperative, VAC operative <br> AC PROBLEMS <br> 1. Excessive peaking of frequency response on the 20,200 , or 700 VAC ranges <br> 2. Poor frequency response on the 200 mV or 2 V range, VAC | Check for the presence of the waveforms shown in Figure 4-3. Check operation of the INT, $\overline{\mathrm{INT}}$, or A1-D JFETs. <br> Check the voltage at TP8. If it exhibits peaking, then the fault is ahead of the scaling amplifier in the front end. Check both Q6 on the AC/DC Scaling PCB and Q13 on the Main PCB. <br> Check R10, R11, C8, and the JFET switches in the front end. Check U6B and C17, and the voltage at TP7; it should be approximately 2 X Vin. Check the ON resistance of Q12 and Q18. It should be less than 30 ohms. |
| *To measure input bias current, select VDC and the 200 mV range, short the input terminals and note the dispiav reading. Remove the short and replace it with a 1 megohm resistor in parallel w.th 0.1 uF capacitor. Note the new reading. A large difference between readings indicates a large input bias current. Calculate the bias current by dividing the difference between voltage readings by 1 megohm. For example, a 100 uV difference corresponds to a 100 pA input bias current |  |

autozero enabled. Next, timing diagrams and waveforms are given for a properly operating A/D Converter. Finally, a few useful troubleshooting tips are given.

## 4-45. INITIAL AND CHECK IN AUTOZERO

4-46. Enable the autozero mode by pressing FCN. then TRIG ARM on the front panel, or by changing the setting of switch S3, as shown in Figure 4-6. Measure the voltages at TP11, 10, and 12. If they are within the following limits. autozero is working.

1. TPll should read $\mathrm{OV} \pm 25 \mathrm{mV}$ dc.
2. TPIO should read OV $\pm 10 \mathrm{mV}$ dc.
3. TP 12 should read $O V \pm 10 \mathrm{mV}$ dc; its ac-coupled rms voltage should be less than 1 mV ac.

## 4-47. A/D TIMING DIAGRAM

4-48. A timing diagram for the switching JFETs in the A D Converter is shown in Figure 4-4.

## 4-49. A/D WAVEFORMS

4-50. The waveforms for a functional A I) Conterter ate shown in Figure 4-5. These waveforms occur when the 8860 A is operating in the continuous mode rather than locked into the autozero mode.

4-5I. With + IV dc applied to the 8860A INPUT terminals. the waveform at TPll should appear as shown in Figure 4-5. There should be no droop or rise in voltage during the INT (integrate) or DE (dincharge) periods. Droop can be caused by either a leaky or shorted IFET or

Table 4-5. Typlcal Symptoms of RMS Converter Faults

| SYMPTOMS | INSTRUCTIONS OR COMMENTS |
| :--- | :--- |
| 1. RMS Converter does not respond | Check voltages at TP3 and TP1 as described earlier in this section under <br> RMS to-DC Converter. If the voltages at TP3 are incorrect, the problem is <br> usually in the absolute value circuitry. If TP1 incorrect, the problem is <br> probably in the 2X log amplifier, the log feedback amplifier or the anti-log <br> amplifier. If U17 or U20 require changing, jumpers W5 through W8 need <br> to be reconfigured. Refer to the Post Repair Procedures at the end of this <br> section for the jumper replacement procedures. |
| 2. RMS Converter is functional, but <br> the reading is noisy. | U15 may be defective. Also check U16, U8 and the logging arrays (U17 <br> and U20). <br> 3. Poor downscale performance on <br> all ranges. |
| Check calibration adjustments for TP5 (R27), RMS Zero (R46), RMS <br> offset (R54), or R73. Also check U15 and U19. |  |

Table 4-6. Typical Symptoms of A/D Converter Faults

| SYMPTOM | POSSIBLE CAUSE |
| :---: | :---: |
| 1. Incorrect Scale Factor | - Precision reference malfunction. <br> - Q10 faulty or has drive signal missing. |
| 2. Nonlinear Response | - One or more JFETs on the A2 hybrid are faulty. <br> - AZ2 or Delta-2 operation is faulty. |
| 3. Persistent Overrange Indication | - Precision reference malfunction. <br> - Integrator, slope amplifier, or A/D comparator malfunction. |
| 4. Unstable (Noisy) Reading | - Faulty op amps or JFETs within the autozero loop. C7 may also be defective. |
| 5. Excessive Offset | - Faulty JFETs in the autozero loop, or drive signals missing. <br> - Q8 or Q9 faulty, or their drive signals are absent. <br> - Offset is not properly adjusted. |
| 6. Full Scale Reading Not Possible | - Integrator malfunction or faulty operation of Q4. |

by a defective JFET driver (U15-U17). The figure also shows the correct response to $\mathrm{a}+1 \mathrm{mV}$ dc and a +1.9 V dc input. Notice that the DE width varies in proportion to the magnitude of the input signal.
4-52. The waveform shown in Figure 4-5 for the junction of C7 and Q5 is the signal that should appear at the integrator summing junction with inrange and overrange inputs. Improper response to overrange inputs suggests a malfunction during AZ 2 , particularly of Q 4 or its driver.
4-53. The waveforms shown for the junction of R47 and Q5 give a quick check of JFET Q5 and transistor Q12. The pulses occur during the two Delta-2 periods.
$4-54$. The two TP10 waveforms of Figure $4-5$ show the normal signal at the integrator output for inputs of +1 V dc
and overrange. Note during overrange that the voltage returns very rapidly to zero during the AZ2 period.

4-55. The two TPI2 waveforms of Figure 4-5 show the signal that should be present at TP12 for +1 V dc and 0.0 V dc (shorted) inputs. Voltage limiting is caused by diodes CR5,6,8, and 9. When the input voltage is zero, one of two waveforms is present at TP12, depending on the sign of the display $(+0.0$ or -0.0$)$. The voltage at TP12 should not change more than 3 mV during the integrate period.

## 4-56. A/D TROUBLESHOOTING TIPS

4-57. Signal paths ahead of the $A / D$ Converter can be bypassed by removing the AC/DC Scaling board and applying de test voltages to A2-3 for VDC and A2-7 for VAC. When VAC is selected, no polarity sign appears.

[^1]

Figure 4-4. Timing Diagram for A/D Converter JFETs (cont)


Figure 4-5. Signal Waveforms in A/D Converter


Figure 4-5. Signal Waveforms in A/D Converter (cont)

4-58. Operation in the $41 / 2$ or $31 / 2$ digit mode makes the A D cycle easier to observe, due to the higher sample rate. To select the $31 / 2$ digit mode, set switch SI to the TMI position. This switch, shown in Figure 4-6, is located on the top edge of the Controller PCB.

> NOTE

Be sure to return both SI and S3 slide switches to NORM after trouble shooting. Otherwise the instrument will remain in autozero or in the $31 / 2$ digit mode.

## 4-59. DIGITAL TROUBLESHOOTING OF BASIC INSTRUMENT

4-60. General troubleshooting information for the digital section of the 8860)A is given in Table 4-7. The table prosides : !ist of solutions for general symptoms. The bymptomsare separated into two categories: error message displayed or now error message displayed. Error code descriptions follow the table.

## 4-61. Error Messages

4-62. Basic instrument error mesages fall into two categories: user errors and internal DMM errors. User errors can generally be corrected at the front panel. They are:

Err 10 -. External reference has been selected but the -007 option circuit board is not installed. To correct, install the option or cancel the selection.

Err 11 Front panel ZERO function has been attempted. but the input is greater than the allowed range of $\pm 99 \mathrm{uV}$ or $\pm 99 \mathrm{~ms}$. To correct. Verify that the input terminals are shorted.

Err 13 - Exponent magnitude is too large. This occurs when attempting to enter a number which exceeds $\pm 1.99999 \times 10^{94}$ into the High. Low. or Offset register (e.g. NUM 2 EFX 99 FCN STORE HI(iH).


Figure 4-6. Slide Switches Used in Troubleshooting

Table 4-7. Digital Troubleshooting of Basic Insirument
This table is divided into two sequences: choose the first if an error message is displayed, or the second if an error message is not displayed. Both sequences assume that the fault is digital and not analog. Perform the steps in sequence; stop when the fault disappears. Remove the 8860A from line power before unplugging printed circuit boards or removing components.
IF AN ERROR MESSAGE IS DISPLAYED (Err 12, 14, 15, 16, or 17), the fault is confined to the guard-crossing circuitry, one of the microprocessors, or the interconnections:

| SUSPECT AREA | INSTRUCTION |
| :---: | :---: |
| 1. Loose Connector | Remove and reseat the Controller PCB (in case it was jarred loose from its connector). Check to see if this clears the fault. |
| 2. Power Supply (Main PCB) | Measure the +5 V out-guard supply voltage. It should be +4.7 to +5.3 V dc. |
| 3. Out-guard Microprocessor (U2 on Controller PCB) | Replace U2, observing static precautions. |
| 4. In-guard Microprocessor (U1 on Main PCB) | Replace U1. |
| 5. Guard-Crossing Circuitry (on Main PCB) | With any of these error messages transmissions between microprocessors will stop. Test each opto-isolator individually, as in Table 4-8, and observe the waveform at the noted test point. A good optoisolator will produce an inverted 5 V square wave at the test point. |
| 6. I/O Expander (U3 on Controller PCB) | If the fault has still not cleared, check the PROG control line (pin7) and data lines (pins 8, 9, 10, 11). Replace this device ( $(3)$ ) if any lines are stuck high or low. (Access these pins from the non-component side of the board.) |

IF NO ERROR MESSAGE IS DISPLAYED, then the in-guard microprocessor and guard-crossing circuits are probably good. The fault is instead on either the Controller or Display PCB. The following sequence of steps checks all integrated circuits, U1 through U11, on the Controller PCB. Perform these steps in sequence:

| SUSPECT AREA | INSTRUCTION |
| :---: | :---: |
| 1. Connector or Slide Switches (on Controller PCB) | Remove and reseat the Controller PCB (in case it was jarred loose from its connectors). Also make sure that slide switches S1 (TM1/NORM) and S3 (TMO/NORM) at the top edge of the board are in their normal |
| 2. Digital Option (-004 or -005 ) | If present, remove the option PCB (Calculating Controller or IEEE488). If the fault clears, troubleshoot the option assembly using the procedures given in Section 6 of this manual. |
| 3. Power Supply (on Main PCB) | Check the output of the +5 V out-guard supply. It should be 4.7 V to 5.3 V . |
| 4. Out-guard Microprocessor (U2 on Controller PCB) | Replace U2 observing static precautions. Check pin 4, the reset line. It should be at +5 V after power up; if stuck low, C1 may be defective. |
| 5. Crystal | Check line ALE (pin 11 of U 2 ) for a 400 kHz square wave. If this signal is not present, crystai Y1 or capacitors C2 or C3 may be defective. Check either pin of the crystal for a 1 V pk-pk sinusoid, 6 MHz waveform. |

Table 4-7. Digital Troubleshooting of Basic Instrument (cont)

| SUSPECT AREA | INSTRUCTION |
| :---: | :---: |
| 6. Display PCB | If one or more of the 7 -segment display digits never light up, check pins 2,4 , and 6 of U7 on the Controller PCB for activity (these lines scan the display and keyboard). If all lines are switching, the Controller PCB is probably good; check the Display PCB, devices U1 and Q1 through Q8. All U1 outputs should be switching. Also make sure the Controller and display PCBs are firmly seated in their connectors. If at least one of pins 2,3 , or 6 of $\cup 7$ (on the Controller PCB) is stuck high or low, suspect the Controller PCB, especially devices $\cup 3$ or $\cup 7$. Check the corresponding input pins of U7 for activity. |
| 7. Bad LED Display Segment | Replace the 7 -segment digit. |
| 8. Segment Drivers | If the same segment on all digits is out, suspect segment driver U4 or U5 on the Controller PCB. Also check the series resistors U6, R4, R5, R6, and the connector (P2). |
| 9. Local Program Memory (ROM) (U9 on Controller PCB) | Replace if a spare is available; check to see if fault has cleared. |
| 10.Control Lines (on Controller PCB) | With a known good out-guard microprocessor in place, look at the control signals PSEN, ALE, and PROG generated by the processor; all should be switching. If one is stuck high or low, remove the ICs connected to that line until the line is freed. |
| 11. Data Bus (on Controller PCB) | Check the data bus for a stuck line; all lines should be switching. If a line is stuck high or low, suspect U9 or U10. Check U10 as described in step 12. |
| 12.Address Latch (U10 on Controller PCB) | If you suspect that address latch U10 is faulty, use a dual-trace scope to check its operation. Trigger the scope on ALE and look at the input and output of each bit. If ALE and the latch are working properly, the output follows the input value when ALE is high and latches when ALE goes low. |
| 13.Resistor Network (U8 on Controller PCB) | Check U8 for a bad resistor, using a low-voltage ohmmeter (to prevent diode turn-on). With $U 8$ in the circuit, all resistors should measure somewhere between $5 \mathrm{k} \Omega$ and $40 \mathrm{k} \Omega$. |
| 14.External Trigger | U11 and half of U1 is used to condition the external trigger signal (the other half of $U 1$ is used to condition a signal from a digital option). If devices U1 or U11 are faulty, they will not hang up the instrument unless U1-13 is low. This pin should be high when a digital option is not present in the instrument. |

Err 18 - An input or offset value exceeds 1999.99 V or $19.9999 \mathrm{M} \Omega$. To correct, reduce the value to an acceptable level.
4-63. Error numbers 12, 14. 15. 16, and 17 represent internal DMM errors, and when they persist, generally indicate a hardware failure in the guard-crossing. Hardware faults associated with these error codes are confined to the opto-isolator circuitry, the in-guard microprocessor, the I O Expander U3, the out-guard microprocessor, or
the paths connecting these devices. The troubleshooting procedure is basically the same for each of these errors. and is given in Table 4-7. (A high input voltage transient may cause an Err 14, 15. 16. or 17 to be displayed for up to 4 seconds. This is not considered a fault condition.)

4-64. When the in-guard and out-guard microprocessors communicate, they check the aceuracy of the transmission in each direction: Err 12, 14. and 15 indicate criors in communication from in-guard to out-guard circuits: Err 16

Table 4-8. Testing Guard-Crossing Circuitry

1. For out-guard to in-guard circuit paths:
a. Remove the Controller PCB from connector J3.
b. Check the clock path by applying a square wave ( 0 to +5 V ) to $J 3-15$, and, using a scope, observe the resulting waveform at U6-14. Record the propagation time.
c. To check the data path, repeat step b using J3-13 as the input and U6-15 as the output.
2. For in-guard to out-guard circuit paths:
a. Remove U6 (the in-guard microprocessor) and the Controller PCB from their sockets.
b. Check the clock path by applying a square wave ( 0 to +5 V ) to U6-12, and, using a scope observe the resulting waveform at U2-1. Record the propagation time.
c. To check the data path. repeat step b using U6-13 as the input and U2-2 as the output.
3. The measured propagation times of the two paths should differ by less than 7 us. A greater difference will cause occasional transmission errors. A difference greater than 15 us will cause a continuous error message to be displayed.
4. Measure the voltage at pin 4 of each opto-isolator with the square wave applied as in steps 1 and 2 . The high level should be at least 0.42 V .
5. If either the propagation delay or the voltage level requirements are not met, replace the opto-isolator.
and 17 indicate errors in communication from out-guard to in-guard circuits.

Err 12 - Measurement data received by the outguard microprocessor from in-guard circuitry is not BCD. The out-guard microprocessor receives measurement data bit-by-bit. Every four bits is verified as a $B C D$ character (0-9). If a hexadecimal character ( $A$. B, C. D, E, or $\mathbf{F}$ ) occurs, for whatever reason (e.g.. bad data or lost synchronization), Err 12 is declared.

Err 14 - The out-guard microprocessor cannot start receiving data from in-guard circuitry. After transmitting command data to the in-guard circuits, the out-guard microprocessor waits up to 3.5 seconds in remote or 4.2 seconds in local for the in-guard microprocessor to respond. This is enough time for any complete measurement cycle. If the out-guard microprocessor does not receive a message or receives a wrong message, it declares Err 14.

Err 15 - The out-guard microprocessor has received either invalid data or no data. If, after the in-guard microprocessor starts transmitting, the out-guard microprocessor receives the incorrect clock bit. or has to wait longer than $518 \mu$, for data, Err 15 is declared.

Err 16 -.. The out-guard microprocessor cannot start transmitting to the in-guard microprocessor. When the out-guard microprocessor is ready to transmit to the in-guard circuit, it sends a ready message. If the in-guard microprocessor does not echo the message within 3.4 seconds, Err 16 is declared.

Err 17 A transmission crror from the out-guard microprocessor to the in-guard microprocessor has occurred. When data is sent to the in-guard mieroprocessor. cach bit is echoed back to the out-guard microprocessor. The in-guard microprocessor must correctly echo cach bit within $495 \mu$. or Err 17 is declared.

4-65. Messages are transmitted across the guard using parallel clock and data lines. The clock bit toggles with each transmitted data bit. As a data message is sent. the receiving microprocessor returns (echos) the data and clock bits to the sender for comparison. For instance, if the out-guard microprocesor transmits data bit 1 . the inguard microprocessor sends back data bit I. This echo assures the out-guard mieroprocessor that the message was correctly received. The data echo occurs for each bit transmitted in either direction. Error 15 or 17 is declared when an echo bit differs from the bit sent.

4-66. Error codes 14 and 16 usually oceur when the microprocessors have lost synchronization, and a transmission cannot get started. Errors 15 and 17 mean that the microprocessors started in syne. but then lost a bit. The out-guard mieroprocessor is the master. and the in-guard microprocessor is the slave. Whenever the echo time period elapses, the in-guard microprocessor detatus to receiving. while the out-guard microprocessor delaults to transmitting.

4-67. Error messages are buffered one deep. If, for example, two crrors occur and clear within milliscconds of each other, both errors will be displayed, one after the other, for approximately 1.1 seconds cach.

## 4-68. TROUBLESHOOTING AIDS

## 4-69. Visual Inspection

4-70. Visual inspection can sometimes quickly locate instrument faults, saving troubleshooting time. Use the Disassembly procedure presented earlier in this section to remove the top cover. Carefully inspect each circuit board for:

- loose or broken wivss and component leads
- improperly seated plug-in assemblies
- physically damaged components
- discoloration due to arcing or overheating
- discolored or burnt capacitors or resistors
- cracked or bulging resistors, diodes, thermistors


## 4-71. Short Circuit in Power Supply

4-72. Current Tracer probes, such as the HP 547A, are usually the best way to locate a short that loads the power supply. To locate such a short, start at the output of the power supply and move the Current Tracer along the supply output path until the short is found. The Current Tracer will glow brightest at the terminal of the shorted component. Shorted logic elements are more difficult to locate because of the small currents involved.

## 4-73. Intermittent Faults

4-74. To locate intermittent and temperature induced faults, alternately warm and cool the suspect circuits. A heat gun and a can of aerosol circuit cooler are recommended as the heating and cooling agents.

## 4-75. Connectors with Poor Contacts

4-76. If connectors are suspected of making poor contact, clean the circuit board fingers by rubbing them with a cotton swab moistened with isopropyl alcohol. Do not use abrasives to clean the gold-plated contacts.

## 4-77. POST REPAIR PROCEDURES

$4-78$. The 8860 A contains a series of factory selected jumpers in the RMS Converter and the Precision Voltage Reference circuits. After either of these circuits have been repaired by parts replacement, it may bc necessary to change their jumper settings. The parts that affect the jumper settings are as follows:

- RMS Converter U17 or U20
- Precision Voltage Reference U22

4-79. Instructions for verifying and or relocating the jumper settings are given in Tables 4-9 and 4-10 Iable 4-9 contains the procedure for the RMS Converter. The procedure for the Precision Voltage Reference is given in Table 4-10.

Table 4-9. Jumper Selection, RMS Converter
After replacing $\cup 17$ or U20 on the RMS Converter, use the following procedure to verify and/or select the jumper locations:

1. Locate the row of sleeved jumpers adjacent to U18, the RMS resistor network.
2. Solder short lengths of solid wire in place of any jumpers that have been previously cut.
3. Install all pcb assemblies, and turn-on power to the 8860A.
4. Connect a short between the 8860A INPUT terminals, and select the VAC function, 2 V range.
5. Connect a DMM between the INPUT LO terminal of the 8860A and each of the following test points on the AC/DC Scaling PCB Assembly. At each test point measure the dc voltage. If necessary, bring the voltage within limits by making the indicated adjustment.

| Test Point | Adjustment | DC Voltage Reading |
| :--- | :--- | :--- |
| TP5 | R27 Buffer Offset | $0.0+/-0.2 \mathrm{mV}$ |
| TP2 | R54 RMS Offset | $0.0+/-0.2 \mathrm{mV}$ |
| TP3 | R46 RMS Zero | $0.0+/-100 \mathrm{mV}$ |

*Reading will be unsteady.
6. Disconnect both the DMM and the short across the INPUT terminals.
7. Connect an AC Calibrator with a $1 \mathrm{~V}, 200 \mathrm{~Hz}$ output to the 8860 A input terminals.
8. Center the 1V, $200 \mathrm{Hzadjustment} \mathrm{(R67)} \mathrm{and} \mathrm{the} 10 \mathrm{mV}, 200 \mathrm{~Hz}$ adjustment (R73). Record the 8860A display reading.
9. Use the recorded reading and the list at the end of this procedure to determine which jumpers need to be cut. 10.Turn off power to the 8860A, remove the AC/DC Scaling PCB, and cut the appropriate jumpers.
11.Install the PCB in the 8860A, and perform the calibration procedure (see the Calibration Manual).


Table 4－10．Jumper Selection，Precision Voltage Reference
After replacing U22，R41，and R42 in the Precision Voltage Reference Circuit（A／D and Ohms Converter PCB ）．use the following procedure to verify and／or select the jumper locations：

1．Connect a precision 1.0 V dc source to the INPUT terminals of the 8860 A ；select the VDC function． 2 V range
2．Adjust $R 17$（ $+1 \vee \mathrm{CAL}$ ）for a display reading of +1.00000 ．If this adjustment is achieved，the existing jumper locations are correct；perform the calibration procedure（see Calibration Manual）．If the adjustment cannot be made，continue with this procedure．
3．Locate the row of sleeved jumpers adjacent to U 10 in the Precision Voltage Reference circuit．
4．Solder short lengths of solid wire in place of jumpers which have been previously cut
5．Install all pcb assemblies，and turn－on power to the 8860A．
6．With the precision 1.0 V dc source still connected to the INPUT terminais，turn R7 counterclockwise until the reading no longer decreases．Record the reading
7．Use the recorded reading and the list at the end of this procedure to determine which jumpers need to be cut
8．Turn off the 8860 A ，remove the $\mathrm{A} D$ and Ohms Converter PCB，and cut the appropriate jumpers．
9．Install the pcb in the 8860A，and perform the calibration procedure（see the Calibration Manual）．

| RECORDED DISPLAY READING | JUMPERS |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: |
|  | W4 | W5 | W6 | W7 | W8 |
| 0.99923 to 0.99372 | －－－ | －－－ | －－－ | －－－ | －－－ |
| 0.99371 to 0.98827 | － | － | －－－ | －－－ | cut |
| 0.98826 to 0.98287 | －－－ | －－－ | －－－ | cut | －．．． |
| 0.98286 to 0.97753 | －－－ | －－－ | －－－ | cut | cut |
| 0.97752 to 0.97225 | －－－ | －ー－ | cut | － | －－－－ |
| 0.97224 to 0.96703 | －－－ | －－－ | cut | － | cut |
| 0.96702 to 0.96186 | －－－ | －－－ | cut | cut | － |
| 0.96185 to 0.95675 | －－－ | －－－ | cut | cut | cut |
| 0.95674 to 0.95169 | －－－ | cut | －－－ | － | －－－ |
| 0.95168 to 0.94669 | － | cut | － | － | cut |
| 0.94668 to 0.94173 | － | cut | － | cut | －－－ |
| 0.94712 to 0.93683 | －－－ | cut | －－－ | cut | cut |
| 0.93682 to 0.93198 | － | cut | cut | －－－ | －－－ |
| 0.93197 to 0.92718 | －－－ | cut | cut | － | cut |
| 0.92717 to 0.92243 | －－－ | cut | cut | cut | －－－ |
| 0.92242 to 0.91773 | －－－ | cut | cut | cut | cut |
| 0.91772 to 0.91307 | cut | －－－ | －－－ | － | －－－ |
| 0.91306 to 0.90846 | cut | －－ | －－ | － | cut |
| 0.90845 to 0.90390 | cut | －－－ | －－－ | cut | －－－ |
| 0.90389 to 0.89939 | cut | － | －－－ | cut | cut |
| 0.89938 to 0.89491 | cut | － | cut | －－－ | －－－ |
| 0.89490 to 0.89049 | cut | －ー－ | cut | －－－ | cut |
| 0.89048 to 0.88610 | cut | －－－ | cut | cut | －－－ |
| 0.88609 to 0.88176 | cut | －－－ | cut | cut | cut |
| 0.88175 to 0.87746 | cut | cut | －－－ | －－－ | －－－ |
| 0.87745 to 0.87321 | cut | cut | －－－ | －－－ | cut |
| 0.87320 to 0.86899 | cut | cut | －－－ | cut | －－－ |
| 0.86898 to 0.86482 | cut | cut | －－－ | cut | cut |
| 0.86481 to 0.86068 | cut | cut | cut | － | －－－ |
| 0.86067 to 0.85659 | cut | cut | cut | －ーー | cut |
| 0.85658 to 0.85253 | cut | cut | cut | cut | －－－ |
| 0.85252 to 0.84851 | cut | cut | cut | cut | cut |

## Section 5 <br> List of Replaceable Parts

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## 5-1. INTRODUCTION

5-2. This section contains an illustrated parts breakdown of the instrument. A similar parts listing for each of the Options will be found in Section 6. Components are listed alphanumerically by assembly. Both electrical and mechanical components are listed by reference designation. Each listed part is shown in an accompanying illustration.

5-3. Parts lists include the following information:
1 Reference Designation.
2. Description of each part.
3. FLUKE Stock Number.
4. Federal Supply Code for Manufacturers. (See Section 7 for Code-to-Name list).
5. Manufacturer's Part Number.
6. Total Quantity of components per assembly.
7. Recommended Quantity: This entry indicates the recommended number of spare parts necessary to support one to five instruments for a period of 2 years. This list presumes an availability of common electronic parts at the maintenance site. For maintenance for 1 year or more at an isolated site, it is recommended that at least one of each assembly in the instrument be stocked (see paragraph 5-7). In the case of optional subassemblies, plug-ins, etc., that are not always part of the instrument, or are deviations from the basic instrument model, the REC QTY column lists the recommended spares quantity for the items in that particular assembly.

## 5-4. HOW TO OBTAIN PARTS

$5-5$. Components may be ordered directly from the manufacturer by using the manufacturer's part number, or from the John Fluke Mfg. Co., Inc. or its authorized representatives by using the FLUKE STOCK NUMBER. In the event the part you order has been replaced by a new or improved part, the replacement will accompanied by an explanatory note and installation instructions, if necessary.

5-6. To ensure promer and efficient handling of your order, include the following intormaitin:
I. Quantity.
2. FLUKE Stock Number.
3. Description.
4. Reference Designation
5. Printed Circuit Board Part Number and Revision Letter.
6. Instrument Model and Serial Number.

5-7. A Recommended Spare Parts Kit for your basic instrument is available from the factory. This kit contains those items listed in the REC QTY column of the parts list in the quantities recommended.
$5-8$. Parts price information is available from the John Fluke Mfg. Co., Inc. or its representatives. Prices are also available in a Fluke Replacement Parts Catalog. which is available on request.

## CAUTION (1)

Indicated devices are subject to damage by static discharge.

Table 5-1. 8860A Final Assembly

| $\begin{aligned} & \text { REF } \\ & \text { DES } \end{aligned}$ | OESCAIPTION | $\begin{aligned} & \text { FLUKE } \\ & \text { STACK } \\ & \text { NO. } \\ & \hline \end{aligned}$ | MFG SPLY CODE | MFG PART NO. | $\begin{aligned} & \text { TOT } \\ & \text { QTY } \end{aligned}$ | REC QTY | $N$ <br> $N$ <br> 0 <br> 1 <br> $E$ |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | FINAL ASSEMBLY <br> FIGURE 5-1 (8860A-5001/TB) | 8860A | 89535 |  |  |  |  |
| A1 | MAIN PCB ASSEMBLY | 531640 | 89536 | 531640 | 1 |  |  |
| A2 | DISPLAY PCB ASSEMBLY | 502708 | 89536 | 502708 | 1 |  |  |
| A3 | © CONTROLLER PCB ASSEMBLY | 502716 | 89536 | 502716 | 1 |  |  |
| A4 | AC/DC SCALING PCB ASSEMBLY | 526665 | 89536 | 526665 | 1 |  |  |
| A5 | $\otimes$ A/D AND OHMS CONVERTER PCB ASSEMBLY | 526673 | 89536 | 526673 | 1 |  |  |
| F1 | FUSE, SLO-BLO, $1 / 4 \mathrm{AMP}$ | 166306 | 71400 | MDL1-4 | 1 | 5 |  |
| H1 | SCREW, FHP/SS, 4-40 X 3/16 | 149567 | 89536 | 149567 | 9 |  |  |
| H2 | SCREW, PHP/SS, 6-32 X 1/4 | 385401 | 89536 | 385401 | 4 |  |  |
| H3 | SCREW, 6-32 X $1 / 4$ | 543447 | 89536 | 543447 | 4 |  |  |
| H4 | SCREW, PHP THD/FORM, 2-28 X 3/8 | 493965 | 89536 | 493965 | 2 |  |  |
| H5 | SCREW, PHP, 6-32 x 3/8, S/S | 334458 | 89536 | 334458 | 1 |  |  |
| H6 | SCREW; PHP, 4-40 X 1/4 | 256156 | 89536 | 256156 | 1 |  |  |
| H7 | SCREW, FHP, U/CUT, 6-32 X 1/4 | 320093 | 89536 | 320093 | 4 |  |  |
| H8 | SCREW, PHP, 4-40 X 3/8 | 256164 | 89536 | 256164 | 1 |  |  |
| H9 | WASHER, FLAT, S/STEEL | 260471 | 89536 | 260471 | 3 |  |  |
| H10 | WASHER, SPLIT/LOCK, S/STEEL | 147603 | 89536 | 147603 | 5 |  |  |
| H11 | WASHER, SHOULDER | 436386 | 89536 | 436386 | 1 |  |  |
| H12 | NUT, HEX, S/STEEL, 4-40 | 147611 | 89536 | 147611 | 3 |  |  |
| MP1 | COVER, GUARD | 502575 | 89536 | 502575 | 1 |  |  |
| MP2 | PANEL, BLANK SUB- | 531004 | 89536 | 531004 | 2 |  |  |
| MP3 | CUSHION | 541896 | 89536 | 541896 | 2 |  |  |
| MP4 | COVER, D-SI2E (WITHOUT SHIELD) | 516682 | 89536 | 516682 | 1 |  |  |
| MP5 | CUSHION | 541870 | 89536 | 541870 | 2 |  |  |
| MP6 | CUSHION | 545871 | 89536 | 545871 | 1 |  |  |
| MP7 | RETAINER STRAP, RELAY | 381624 | 77342 | 27 E 348 | 3 |  |  |
| MP8 | PANEL, FRONT | 502534 | 89536 | 502834 | 1 |  |  |
| MP9 | BUTTON, GRAY (FRONT PANEL) | 509232 | 89536 | 509232 | 14 |  |  |
| MP 10 | BUTTON, ORANGE (FRONT PANEL) | 509265 | 89536 | 509265 | 1 |  |  |
| MP11 | BUTTON (FRCNT PANEL) | 509356 | 89536 | 509356 | 1 |  |  |
| MP 12 | DECAL, FRONT PANEL | 507574 | 89536 | 507574 | 1 |  |  |
| MP13 | DECAL, BASE SIDES | 473652 | 89536 | 473652 | 2 |  |  |
| MP14 | PANEL, REAR | 502559 | 89536 | 502559 | 1 |  |  |
| MP15 | GUARD, MAIN BOARD | 509273 | 89536 | 509273 | 1 |  |  |
| MP16 | PLUG, REAR PANEL | 530998 | 89536 | 530998 | 1 |  |  |
| MP17 | GUARD, BASE | 502567 | 89536 | 502567 | 1 |  |  |
| MP18 | INSULATOR, XSTR | 508630 | 55285 | 7403-09FR-51 | 1 |  |  |
| MP19 | SPRING CONTACT, SHIFLD | 525261 | 89536 | 525261 | 3 |  |  |
| MP20 | BASE (STANDARD) | 454702 | 89536 | 454702 | 1 |  |  |
| MP2 1 | BAIL STAND | 467555 | 89536 | 467555 | 1 |  |  |
| MP22 | LATCH | 467548 | 89536 | 467548 | 2 |  |  |
| MP23 | FOOT, NON-SKID | 467571 | 89536 | 467571 | 4 |  |  |
| MP2 4 | CONN, BNC FE PANEL MOUNT | 414201 | 02660 | 31-010 | 1 |  |  |

Table 5-1. 8860A Final Assembly (cont)

| REF DES | DESCRIPTION | FLUKE STOCK NO . | MFG SPLY CDDE | MFG PART NO. | $\begin{aligned} & \text { TOT } \\ & \text { QTY } \end{aligned}$ | $\begin{aligned} & \text { REC } \\ & \text { QTY } \end{aligned}$ | N <br> 0 <br> $T$ |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| TM1 | 8860A INSTRUCTION MANUAL SET (NOT SHOWN) | 545004 | 89536 | 545004 | 1 | 1 |  |
| VR3 | VOL TAGE REGULATOR, 3-TERMINAL | 538108 | 89536 | 538108 | 1 |  |  |
| W1 | LINE CORD WITH INTRNL CONN, (NOT SHOWN) | 343723 | 89536 | 343723 | 1 |  |  |
| W2 | WIRE ASSEMBLY (GRN/YEL) | 509348 | 89536 | 509349 | 1 |  |  |
| W18 | WIRE ASSY, (BLK) | 538165 | 89536 | 538165 | 1 |  |  |
| W19 | WIRE ASSY, (BRN) | 538173 | 89536 | 538173 | 1 |  |  |
| W20 | WIRE ASSY, (BLU) | 538181 | 89536 | 538181 | 1 |  |  |
| W21 | WIRE ASSY, (WHT) | 538199 | 89536 | 538199 | 1 |  |  |
| XF 1 | FUSEHOLDER (BODY/NUT ONLY) | $375188$ | 89536 | $375188$ | $1$ |  | $1$ |
|  | FUSEHOLDEK CAP (CAP ONLY) | $460238$ | 89536 | $46 \mathrm{c} 238$ | 1 |  | 1 |
|  | LEAD \& PROBE ASSEMBLY (NOT SHOWN) | 516666 | 89536 | Y8132 | 1 |  |  |
|  | RECOMMENDED SPARE PARTS LIST/KIT | 583500 | 89536 | 583500 |  |  |  |  |  |

1 MUST EE ORDERED AS SEPAKATE ITEMS.


$\otimes$
CAUTION SUBJECT TO DAMAGE by STATIC ELECTRICITY


Figure 5-1. Final Assembly (cont)


Figure 5-1. Final Assembly (cont)

Table 5-2. Main PCB Assembly

| $\begin{aligned} & \text { HEF } \\ & \text { DES } \end{aligned}$ | DESCRIPTIDN | FLUKE STOCK NO. | MFG SPLY CDDE | MFG PART NO. | $\begin{aligned} & \text { TOT } \\ & \text { QTY } \end{aligned}$ | REC <br> QTY | N 0 T E |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| A1 | MAIN PCB ASSEMBLY <br> FIGURE 5-2 (8860A-1001T) | 531640 | 89536 | 531640 | REF |  |  |
| C 1 | CAP, ELECCT, 4700 UF $-10 /+100 \%$, 15 V | 379370 | 80031 | 30501134720015 | 2 |  |  |
| C2 | CAP, FLECT, $4700 \mathrm{UF}-10 /+100 \%$, 15 V | 379370 | 80031 | 305017J472.U015 | FEF |  |  |
| C3 | CAP, ELECT, 470 UF $-10 /+25 \%$, 35V | 478752 | 89536 | 478792 | 2 |  |  |
| C4 | CAF, ELECT, 470 UF $-10 /+25 \%, 35 \mathrm{~V}$ | 478752 | 89536 | 478792 | HEF |  |  |
| C6 | CAP, MYLAR, FXD, $0.047 \mathrm{LF}+/ \ldots 10 \%, 1000 \mathrm{~V}$ | 529446 | 03797 | 1.600.047/10/1000 | 1 |  |  |
| C7 | CAP, ELECT, 4700 UF $-10 /+100 \%, 100 \mathrm{~V}$ | 460261 | 5447j | ECE-T16F4700S | 1 |  |  |
| C8 | CAP, FLECT, 1200 UF $-10 /+100 \%, 2000 \mathrm{VDC}$ | 50032 ? | 56289 | 672D12816 P3DS2C | , |  |  |
| C9 | CAF, CER, $0.22 \mathrm{UF},+/-20 \%, 50 \mathrm{~V}$ | 519157 | 51406 | RPE11125U224M50V | z' |  |  |
| C 10 | CAP, TA, 4.7 UF +/-20\%, 25 V | 161943 | 56289 | 1965475X0025KA 1 | $\hat{2}$ |  |  |
| C12 | CAP, TA, $4.7 \mathrm{UF}+1.20 \%, 25 \mathrm{~V}$ | 161943 | 56289 | 1565475 X0025KA 1 | F EF |  |  |
| C. 14 | CAF, TA, $2.2 \mathrm{UF}+/-20 \%, 20 \mathrm{~V}$ | 161927 | 56289 | 156D2225x0020HA1 | 1 |  |  |
| C16 | CAP, MYLAR, $0.0047 \mathrm{UF}+/ .20 \%, 200 \mathrm{~V}$ | 106054 | 56289 | 152P47\%O2 | 1 |  |  |
| C17 | CAF, CEFAN, 0.05 UF-20/ $+80 \%$, 500 V | 1C5676 | 56289 | 33 C 58 H | 1 |  |  |
| C18 | CAP, CER, $0.22 \mathrm{UF},+/-20 \%, 50 \mathrm{~V}$ | 519157 | 5.1406 | PrE11125U2く4M50V | PEF |  |  |
| CR1 | IIODE, SI, HI-SPEED SWITCHING | 203303 | 07910 | 1N4448 | 7 | $\ddot{c}$ |  |
| CR2 | UIGDE, SI, HI-SPEED SWITCHING | 203323 | 07910 | 114448 | IEF |  |  |
| CR3 | LIGDE, SI, HI-SPEED SWITCHING | 203323 | 07910 | 1N4448 | FEF |  |  |
| CR4 | LIODE, SI, HI-SPEED SWITCHING | 203323 | 07910 | 11.4448 | REF |  |  |
| CR6 | EIODE, SI | 343491 | C.4713 | 1N4002 | $\underline{\square}$ | 1 |  |
| CR7 | IIODE, SI, HI-SPEED SWITCHING | 203323 | 07910 | $1 N 4448$ | FEF |  |  |
| CR10 | DIODE, SI | 343491 | 04713 | 1 N 4002 | REF |  |  |
| CR11 | DIODE, SI | 343491 | 04713 | 1 N400\% | H:EF |  |  |
| CR12' | DIODE, SI | 343491 | 04713 | 1N4002 | REF |  |  |
| CR13 | DIODE, SI | 343491 | 04713 | 1 N 4002 | REF |  |  |
| CR14 | RECTIFIER BRIDGE | 206509 | 21845 | F903C-22 | 1 | 1 |  |
| CR15 | DIODE, SI, HI-SPEED SWITCHING | 203323 | 07910 | 1 N 4448 | REF |  |  |
| CR16 | DIODE, SI, HI-SPEED SWITCHING | 203323 | 07910 | 1 N 4448 | REF |  |  |
| E: | WIRE TERMINATIONS |  |  |  |  |  |  |
| H1 | NUT, 6-32 (NOT SHOWN) | 110551 | 89536 | 110551 | 1 |  |  |
| H2 | WASHER, EXT/LK \#4 (NOT SHOWN) | 169235 | 73734 | 1322 | 1 |  |  |
| J1 | CONN, 44 CONTACT | 542258 | 00779 | 1-530843-5 | 3 |  |  |
| J2 | CONN, 30 CONTACT | 520163 | 00779 | 1-530843-3 | 1 |  |  |
| J3 | CONN, 44 CONTACT | 542258 | 00779 | 1-530843-5 | REF |  |  |
| J4 | CONN, 44 CONTACT | 542258 | 00779 | 1-530843-5 | REF |  |  |
| J5 | CONN, CARD-EDGE | 291708 | 91662 | 6308-006-313-001 | 3 |  |  |
| J6 | CONN, CARD-EDGE | 291708 | 91662 | 6308-006-313-001 | PEF |  |  |
| J7 | CONN, CARD-EDGE | 291708 | 91662 | 6308-006-313-001 | FEF |  |  |
| J9 | CONNECTOR, AC | 461806 | 89536 | 461806 | 1 |  |  |
| K1 | RELAY, DPDT, 4.5V | 514240 | 89536 | 514240 | 3 |  |  |
| K2 | FELAY, DPDT, 4.5 V | 514240 | 89536 | 514240 | AEF |  |  |
| K3 | REED RELAY, HV, 1000VDC | 520247 | 71707 | UF-40115 | 1 |  |  |
| K4 | RELAY, DPDT, 4.5V | 514240 | 89536 | 514240 | REF |  |  |
| L1 | INDUCTOR $10 \mathrm{UH}+/-10 \%$ | 249078 | 24759 | MR-10 | 2 |  |  |
| L2 | INDUCTOF $10 \mathrm{UH}+/-10 \%$ | 249078 | 24759 | MR-10 | REF |  |  |
| MP1 | CONNECTOR (FASTON TAP) | 512889 | 02660 | 62395-1834 | 6 |  |  |
| MP2 | HEATSINK (TO VR1, VR2 AND Q6) | 428805 | 13103 | 6046 P 8 | 3 |  |  |
| MP3 | TERMINAL (TEFLON) (NOT SHOWN) | 529297 | 98291 | 011-6812-00-0-206 | 12 |  |  |

Table 5-2. Main PCB Assembly (cont)

| REF <br> DES | OESCRIPTION | $\begin{aligned} & \text { FLUKE } \\ & \text { STOCK } \\ & \text { NO. } \end{aligned}$ | MFG SPLY COOE | MFG PART NO. | $\begin{aligned} & \text { TOT } \\ & \text { OTY } \end{aligned}$ | $\begin{aligned} & \text { REC } \\ & \text { QTY } \end{aligned}$ | N <br> $\mathbf{O}$ <br> $\mathbf{T}$ <br> $\mathbf{E}$ |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| MP4 | TERMINAL (TEFLON) (NOT SHOWN) | 529305 | 98291 | 011-6811-00-0-202 | 4 |  |  |
| MP5 | BUTTON, SWITCH (TO S3) GREEN | 445197 | 89536 | 445197 | 1 |  |  |
| MP6 | BUTTON, SWITCH (TO S1) GREY | 425900 | 89536 | 425900 | 1 |  |  |
| MP8 | PUSH ROD | 509380 | 89536 | 509380 | 1 |  |  |
| MP9 | COVER, AC SWITCH (W/S3) | 475681 | 89536 | 475681 | 1 |  |  |
| Q1 | XSTR, SI, PNP | 195974 | 64713 | 2N3906 | 4 | 1 |  |
| Q2 | XSTR, SI, PNP | 195974 | 64713 | 2N3906 | REF |  |  |
| Q3 | XSTR, SI, PNP | 195974 | 64713 | 2N3906 | REF |  |  |
| Q4 | XSTR, SI, PNP | 195974 | 64713 | 2N3906 | REF |  |  |
| Q6 | XSTR, PWR, PNP, SI | 325753 | 09214 | D45C5 | 1 | 1 |  |
| Q7 | XSTR, SI, NPN | 218396 | 89536 | 218396 | 4 | 1 |  |
| Q8 | XSTR, SI, NPN | 218396 | 89536 | 218396 | REF |  |  |
| Q9 | XSTR, SI, NPN | 218396 | 89536 | 218396 | REF |  |  |
| Q10 | XSTR, SI, PNP | 340026 | 07263 | MPS6563 | 1 | 1 |  |
| Q1 1 | XSTR, SI, NPN | 218396 | 89536 | 218396 | REF |  |  |
| Q13 | XSTR, J-FET, N-CHANNEL | 343830 | 89536 | 343830 | 2 | 1 |  |
| Q15 | XSTR, SI, NPN | 218396 | 89536 | 218396 | REF |  |  |
| R1 | RES, DEP. CAR, $2.2 \mathrm{~K}+/-5 \%$, 1/4W | 343400 | 80031 | CR251-4-5P2K2T | 4 |  |  |
| R2 | RES, DEP. CAR, $2.2 \mathrm{~K}+/-5 \%, 1 / 4 \mathrm{~W}$ | 343400 | 80031 | CR251-4-5P2K2T | REF |  |  |
| R3 | RES, DEP. CAR, $2.2 \mathrm{~K}+/-5 \%, 1 / 4 \mathrm{~W}$ | 343400 | 80031 | CR251-4-5P2K2T | REF |  |  |
| R4 | RES, DEP. CAR, $2.2 \mathrm{~K}+/-5 \%$, $1 / 4 \mathrm{~W}$ | 343400 | 80031 | CR251-4-5P2K2T | REF |  |  |
| R6 | RES, FXD WW, $1000+/-10 \%$, 2 W | 474080 | 89536 | 474080 | 1 |  |  |
| R7 | RES, MTL. FILM, $2 \mathrm{~K}+/-1 \%$, 7 W | 500033 | 89536 | 500033 | 1 |  |  |
| R8 | RES, COMP $100 \mathrm{~K}+/-5 \%$, 2 W | 285056 | 89536 | 285056 | 2 |  |  |
| R9 | RES, COMP $100 \mathrm{~K}+/-5 \%, 2 \mathrm{~W}$ | 285056 | 89536 | 285056 | REF |  |  |
| R10 | RES, MTL. FILM, $2 \mathrm{~K}+/-1 \%, 1 / 2 \mathrm{~W}$ | 151266 | 91637 | CMF552001F | 2 |  |  |
| F11 | RES, MTL. FILM, $2 \mathrm{~K}+/-1 \%, 1 / 2 \mathrm{~W}$ | 235226 | 91637 | CMF552001F' | REF |  |  |
| R12 | RES, MTL. FILM, $10 \mathrm{~K}+/-1 \%, 1 / 8 \mathrm{~W}$ | 168260 | 91637 | CMF551002F | $?$ |  |  |
| R13 | RES, MTL. FILM, $10 \mathrm{~K}+/-1 \%, 1 / 8 \mathrm{~W}$ | 168260 | 91637 | MFF 1-81002F | KEF |  |  |
| R14 | RES, DEP. CAR, 1.3K +/-5\%, 1/4W | 441394 | 80031 | CR251-4-5P1K3 | 1 |  |  |
| R15 | RES, DEP. CAR, $3.6 \mathrm{~K}+/-5 \%, 1 / 4 \mathrm{~W}$ | 442343 | 80031 | CR251-4-5P3K6 | 2 |  |  |
| R16 | RES, DEP. CAR, 3.6K +/-5\%, 1/4W | 442343 | 80031 | CR251-4-5P3K6 | REF |  |  |
| R18 | RES, DEP. CAR, 6.2K +/-5\%, 1/4W | 442368 | 80031 | CR251-4-5P6K2 | 5 |  |  |
| R19 | RES, DEP. CAR, $6.2 \mathrm{~K}+/-5 \%$, $1 / 4 \mathrm{~W}$ | 442368 | 80031 | CR251-4-5P6K2 | REF |  |  |
| R20 | RES, DEP. CAR, 6.2K +/-5\%, 1/4W | 442368 | 80031 | CR251-4-5P6K2 | KEF |  |  |
| R21 | HES, DEP. CAR, 6.2K +/-5\%, 1/4W | 442368 | 80031 | CR251-4-5P6K2 | REF |  |  |
| R22 | RES, DEP. CAR, $2.7 \mathrm{~K}+/-5 \%, 1 / 4 \mathrm{~W}$ | 386490 | 80031 | CR251-4-5P2K7T | 3 |  |  |
| R23 | RES, DEP. CAR, $2.7 \mathrm{~K}+/-5 \%$, $1 / 4 \mathrm{~W}$ | 386490 | 80031 | CR251-4-5P2K7T | REF |  |  |
| R24 | RES, DEP. CAR, $2.7 \mathrm{~K}+/-5 \%, 1 / 4 \mathrm{~W}$ | 386490 | 80031 | CR251-4-5P2K7] | REF |  |  |
| R25 | RES, DEP. CAR, $15 \mathrm{~K}+/-5 \%, 1 / 4 \mathrm{~W}$ | 348854 | 80031 | CR251-4-5P15K | 1 |  |  |
| H26 | RES, DEP. CAR, 6.2K +/-5\%, 1/4W | 442368 | 80031 | CR251-4-5P6K2 | REF |  |  |
| R27 | RES, DEP. CAR, $2 \mathrm{~K}+/-5 \%$, $1 / 4 \mathrm{~W}$ | 441469 | 80031 | CR251-4-5P2K | 1 |  |  |
| H28 | RES, DEP. CAR $220+/-5 \%, 1 / 4 \mathrm{~W}$ | 342626 | 80031 | CR251-4-5P220ET | 1 |  |  |
| R29 | RES, COMP, $10 \mathrm{M}+/-5 \%$, $1 / 4 \mathrm{~W}$ | 194944 | 01121 | CB1065 | 2 |  |  |
| R30 | RES, COMP, $10 \mathrm{M}+/-5 \%, 1 / 4 \mathrm{~W}$ | 194944 | 01121 | CB1065 | REF |  |  |
| R31 | RES, DEP. CAR, $100 \mathrm{~K}+/-5 \%, 1 / 4 \mathrm{~W}$ | 348920 | 80031 | CR251-4-5P100K | 1 |  |  |
| R32 | RES, DEP. CAR, $150 \mathrm{~K}+/-5 \%, 1 / 4 \mathrm{~W}$ | 348938 | 80031 | CR251-4-5P150K | 4 |  |  |
| R33 | RES, DEP. CAR, $150 \mathrm{~K}+/-5 \%$, 1/4W | 348938 | 80031 | CR251-4-5P150K | REF |  |  |
| R34 | RES, DEP. CAR, $150 \mathrm{~K}+/-5 \%, 1 / 4 \mathrm{~W}$ | 348938 | 80031 | CR251-4-5P150K | REF |  |  |
| R35 | RES, DEP. CAR, $150 \mathrm{~K}+/-5 \%, 1 / 4 \mathrm{~W}$ | 348938 | 80031 | CR251-4-5P150K | REF |  |  |

Table 5-2. Main PCB Assembly (cont)

| $\begin{aligned} & \text { REF } \\ & \text { DES } \end{aligned}$ | DESCRIPTION | FLUKE STOCK NO. | MFG <br> SPLY <br> CODE | MFG PART NO. | $\begin{aligned} & \text { TTT } \\ & \text { QTY } \end{aligned}$ | $\begin{aligned} & \text { REC } \\ & \text { QTY } \end{aligned}$ | N O T E |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| RT1 | THERMISTER, 1K, +/-40\% | 494740 | 50157 | 180 Q10215 | 1 |  |  |
| RV1-8 | VARISTOR, 390V | 423475 | 09214 | V390MAX781 | 8 |  |  |
| S1 | SWITCH, DPDT | 520437 | 89536 | 520437 | 1 |  |  |
| S3 | SWITCH, POWER, ON-OFF | 453605 | 89536 | 453605 | 1 |  |  |
| S4 | SWITCH, SLIDE, DPDT | 504738 | 82389 | 11A-1437 | 2 |  |  |
| S5 | SWITCH, SLIDE, DPDT | 504738 | 82389 | 11A-1437 | REF |  |  |
| T1 | TRANSFORMER, POWER | 531558 | 89536 | 531558 | 1 |  |  |
| U1 | IC, LIN, OP-AMP | 413740 | 12040 | LM307N | 1 | 1 |  |
| U2 | IC, LIN, QUAD, COMPARATOR | 387233 | 12040 | LM339N | 2 | 1 |  |
| U3 | NETWORK, RESISTOR | 520353 | 89536 | 520353 | 2 | 1 |  |
| U4 | NETWORK, RESISTOR | 520353 | 89536 | 520353 | REF |  |  |
| U5 | IC, LIN, QUAD, COMPARATOR | 387233 | 12040 | LM339N | REF |  |  |
| U6 | IC, MICROCOMPUTER | 504563 | 89536 | 504563 | 1 | 1 |  |
| U7 | IC, PHOTOTRANSISTOR, OPTICALLY COUPLED | 504977 | 29083 | MCT2E | 4 | 1 |  |
| U8 | IC, PHOTOTRANSISTOR, OPTICALLY COUPLED | 504977 | 29083 | MCT2E | REF |  |  |
| U9 | IC, PHOTOTRANSISTOR, OPTICALLY COUPLED | 504977 | 29083 | MCT2E | REF |  |  |
| U10 | IC, PHOTOTRANSISTOR, OPTICALLY COUPLED | 504977 | 29083 | MCT2E | REF |  |  |
| VR1 | VOLTAGE REGULATOR, LIN, FXD | 428847 | 04713 | MC805TP | 1 | 1 |  |
| VR2 | VOLTAGE REGULATOR, LIN, RCD | 413187 | 04713 | MC7815CT | 1 | 1 |  |
| VR3 | VOL TAGE REGULATOR, 3-TERMINAL | 538108 | 89536 | 538108 | 1 | 1 |  |
| W1-W24 | JUMPER WIRE (NOT SHOWN) |  |  |  |  |  |  |
| XK 1 | SOCKET RELAY | 376665 | 77342 | 27 E501 | 3 |  |  |
| XK2 | SOCKET RELAY | 376665 | 77342 | 27 E501 | REF |  |  |
| XK 4 | SOCKET RELAY | 376665 | 77342 | 27 E501 | REF |  |  |
| XU6 | SOCKET, IC, 40-PIN | 429282 | 09922 | DILB40P-108 | 1 |  |  |
| Y 1 | CRYSTAL 4 MHZ , QUARTZ | 474072 | 89536 | 474072 | 1 | 1 |  |



Figure 5-2. A1 Main PCB Assembly

Table 5-3. Display PCB Assembly

| $\begin{aligned} & \text { REF } \\ & \text { DES } \end{aligned}$ | DESCRIPTION | FLUKE STOCK NO. | MFG SPLY CODE | MFG PART NO. | $\begin{aligned} & \text { TOT } \\ & \text { QIY } \end{aligned}$ | $\begin{aligned} & \text { REC } \\ & \text { OTY } \end{aligned}$ | N O T E |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| A2 | DISPLAY PCB ASSEMBLY <br> FIGURE 5-3 (8860A-4002T) | 502708 | 89536 | 502708 | REF |  |  |
| C1 | CAP, TA, 1UF, +/-20\%, 35V | 161919 | 56289 | 196D105×0020JA1 | 3 |  |  |
| C2 | CAP, TA, 1UF, +/-20\%, 35V | 161915 | 56289 | 196D 105x0020021 | REF |  |  |
| C3 | CAP, TA, $1 \mathrm{UF},+/-20 \%, 35 \mathrm{~V}$ | 161919 | 56289 | 196D105X0020JA1 | REF |  |  |
| CR1 | DIODE, HI-SPEED SWITCHING | 203323 | 0791C | 1N4448 | 8 | 2 |  |
| CR2 | DIODE, HI-SPEED SWJTCHING | 203323 | 0791C | $1{ }^{1} 44488$ | fef |  |  |
| CR3 | DIODE, HI-SPEED SWITCHING | 203323 | 07910 | 1N4448 | REF |  |  |
| CR4 | DIODE, HI-SPEED SWITCHING | 203323 | 07910 | 1N4月48 | REF |  |  |
| CR5 | DIODE, HI-SPEED SWITCHING | 203323 | 0791C | $1 \times 4448$ | REF |  |  |
| CR6 | DIODE, HI-SPEED SWITCHING | 203323 | 07910 | 1 N4448 | REF |  |  |
| CR7 | DIODE, HI-SPEED SWITCHING | 203323 | 07910 | 1 N 4448 | REF |  |  |
| CR8 | DIODE, HI-SPEED SWITCHING | 203323 | 07910 | $1 \times 4448$ | REF |  |  |
| DS1 | DISPLAY, LED | 504787 | 89536 | 504787 | 1 | 1 |  |
| DS2 | DISPLAY, LED, 7-SEGMENT | 418012 | 28480 | 5082-7651 | 5 | 1 |  |
| DS3 | DISPLAY, LED, 7-SEGMENT | 418012 | 28480 | 5082-7651 | hEF |  |  |
| DS4 | DISPLAY, LED, 7-SEGMENT | 418012 | 28480 | 5082-7651 | REF |  |  |
| DS5 | DISPLAY, LED, 7-SEGMENT | 418012 | 28480 | 5082-7651 | REF |  |  |
| DS6 | DISPLAY, LED, 7-SEGMENT | 418012 | 28480 | 5082-7651 | REF |  |  |
| DS7 | DISPLAY, LED | 495457 | 28480 | QDSP3507 | 1 |  |  |
| DS8 | DISPLAY, LED | 504779 | 89536 | 504779 | 1 |  |  |
| DS9 | DISPLAY, LED | 504779 | 89536 | 504779 | REF' |  |  |
| DS10-22 | DISPLAY, LED | 504753 | 28480 | HLMP-1301 | 16 | 4 |  |
| DS23 | LIGHT EMITTING DIODE | 504761 | 14936 | MV57124 | 5 | 1 |  |
| DS24 | LIGHT Emitting diode | 504761 | 14936 | MV57124 | REF |  |  |
| DS25 | LIGht Emitting diode | 504761 | 14936 | MV57124 | REF |  |  |
| DS26 | DISPLAY, LED | 504753 | 28480 | HLMP-1301 | REF |  |  |
| DS27 | LIGHT EMITTING DIODE | 504761 | 14936 | MV57124 | REF |  |  |
| DS28 | LIGHT Emitting djode | 504761 | 14936 | MV57124 | REF |  |  |
| DS29 | DISPLAY, LED | 504753 | 28480 | HLMP-1301 | HEF |  |  |
| DS30 | DISPLAY, LED | 504753 | 28480 | HL.MP-1301 | REF |  |  |
| J1 | RECEPTACLE | 520189 | 01295 | H421121-18 | 1 |  |  |
| MP1 | KEYBOARD, FRONT PANEL (NOT SHOWN) | 504886 | 89536 | 504886 | 1 |  |  |
| MP2 | SOCKET, COMPONENT LEAD (NOT SHOWN) | 376418 | 22526 | 75060-007 | 42 |  |  |
| Q1 | XSTR, SI, PNP, SM. SIG | 418707 | 04713 | MPS56562 | 8 | 2 |  |
| Q2 | XSTR, SI, PNP, SM. SIG | 418707 | 04713 | MPS56562 | REF |  |  |
| Q3 | XSTR, SI, PNP, SM. SIG | 418707 | 04713 | MPS56562 | REF |  |  |
| Q4 | XSTR, SI, PNP, SM. SIG | 418707 | 04713 | MPS56562 | REF |  |  |
| Q5 | XSTR, SI, PNP, SM. SIG | 418707 | 04713 | MPS5656? | REF |  |  |
| Q6 | XSTR, SI, PNP, SM. SIG | 418707 | 04713 | MPS56562 | REF |  |  |
| Q7 | XSTR, SI, PNP, SM. SIG | 418707 | 04713 | MPS56562 | REF |  |  |
| Q8 | XSTR, SI, PNP, SM. SIG | 418707 | 04713 | MPS56562 | REF |  |  |
| U1 | IC, 4-LINE TO 10-LINE DECODI? | 408716 | 01295 | SN74LS42N | 1 | 1 |  |
| U2 | RESISTOR NETWORK, 270 OHMS | 501239 | 89536 | 501239 | 1 | 1 |  |



Figure 5-3. A2 Display PCB Assembly

Table 5-4. A3 Controller PCB Assembly

| $\begin{aligned} & \text { REF } \\ & \text { DES } \end{aligned}$ | DESCRIPTION | FLUKE STOCK NO. | MFG SPLY CODE | MFG PART NO. | $\begin{aligned} & \text { TOT } \\ & \text { QTY } \end{aligned}$ | REC QTY | N O T E |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| A3 | ( CONTROLLER PCB ASSEMBLY <br> FIGURE 5-4 (8860A-4003T) | 502716 | 89536 | 502716 | REF |  |  |
| C1 | CAP, TA, 1UF, +/-20\%, 35V | 161919 | 56289 | 196D105X0020JA1 | 4 |  |  |
| C2 | CAP, CERAM, $20 \mathrm{PF}+/-10 \%$ | 106369 | 56289 | 561 CT (HBA102AE200K | 2 |  |  |
| C3 | CAP, CERAM, $20 \mathrm{PF}+/-10 \%$ | 106369 | 56289 | $561 \mathrm{CT} 2 \mathrm{HBA1} 02 \mathrm{AE} 200 \mathrm{~K}$ | REF |  |  |
| C4 | CAP, CERAM, 0.22 UF +/-20\%, 50V | 519157 | 51406 | RPE11125U224M50V | 5 |  |  |
| C5 | CAP, CERAM, $0.22 \mathrm{UF}+/-20 \%, 50 \mathrm{~V}$ | 519157 | 51406 | RPE11125U224M50V | REF |  |  |
| C6 | CAP, CERAM, $0.22 \mathrm{UF}+/-20 \%$, 50 V | 519157 | 51406 | RPE11125U224M50V | REF |  |  |
| C7 | CAP, CERAM, $20 \mathrm{PF}+/-10 \%$, 500V | 357806 | 71590 | CF-102 | 1 |  |  |
| C8 | CAP, CERAM, . 05 UF +/-20\%, 50V | 149161 | 56289 | 55C23A1 | 1 |  |  |
| C 10 | CAP, CERAM, $0.22 \mathrm{UF}+/-20 \%$, 50 V | 519157 | 51406 | RPE11125U224M50V | REF |  |  |
| C11 | CAP, CERAM, $0.22 \mathrm{UF}+/-20 \%$, 50 V | 519157 | 51406 | RPE11125U224M50V | REF |  |  |
| CR1 | DIODE, SI, HI-SPEED SWITCHING | 203323 | 07910 | 1N4448 | 1 | 1 |  |
| P1 | BOARD CONNECTION CIRCUIT |  |  |  |  |  |  |
| P2 | BOARD CONNECTION CIRCUIT |  |  |  |  |  |  |
| Q1 | XSTR, SI, NPN | 218396 | 07263 | 2N3904 | 1 | 1 |  |
| R1 | RES, DEP. CAR, 10K +/-5\%, 1/4W | 348839 | 80031 | CR251-4-5P10K | 2 |  |  |
| R2 | RES, DEP. CAR, $10 \mathrm{~K}+/-5 \%, 1 / 4 \mathrm{~W}$ | 348839 | 80031 | CR251-4-5P10K | REF |  |  |
| R3 | RES, DEP. CAR, $33 \mathrm{~K}+/-5 \%$, 1/4W | 348888 | 80031 | CR251-4-5P33K | 2 |  |  |
| R4 | RES, DEP. CAR, $51+/-5 \%, 1 / 4 \mathrm{~W}$ | 414540 | 80031 | CR251-4-5P51E | 3 |  |  |
| R5 | RES, DEP. CAR, $51+/-5 \%, 1 / 4 \mathrm{~W}$ | 414540 | 80031 | CR251-4-5P51E | REF |  |  |
| R6 | RES, DEP. CAR, 51 +/-5\%, 1/4W | 414540 | 80031 | CR251-4-5P51E | REF |  |  |
| R7 | RES, DEP. CAR, $6.2 \mathrm{~K}+/-5 \%, 1 / 4 \mathrm{~W}$ | 442368 | 80031 | CR251-4-5P6K2 | 4 |  |  |
| R8 | RES, DEP. CAR, 6.2K +/-5\%, 1/4W | 442368 | 80031 | CR251-4-5P6K2 | REF |  |  |
| R9 | RES, DEP. CAR, $6.2 \mathrm{~K}+/-5 \%, 1 / 4 \mathrm{~W}$ | 442368 | 80031 | CR251-4-5P6K2 | REF |  |  |
| R10 | RES, DEP. CAR, 6.2K +/-5\%, $1 / 4 \mathrm{~W}$ | 442368 | 80031 | CR251-4-5P6K2 | REF |  |  |
| R11 | HES, DEP. CAR, $33 \mathrm{~K}+/-5 \%, 1 / 4 \mathrm{~W}$ | 348888 | 80031 | CR251-4-5P33K | REF |  |  |
| R12 | RES, DEP. CAR, 300K +/-5\%, 1/4W | 441535 | 80031 | CR251-4-5P300K | 1 |  |  |
| R13 | RES, DEP. CAR, FXD, $2 \mathrm{~K}+/-5 \%, 1 / 4 \mathrm{~W}$ | 441469 | 80031 | CR251-4-5P2K | 1 |  |  |
| R14 | RES, DEP. CAR, 220 +/-5\%, 1/4W | 342626 | 80031 | CR251-4-5P220E | 1 |  |  |
| S1 | SWITCH, SLIDE | 477984 | 79727 | GS-115 | 3 | 1 |  |
| S2 | SWITCH, SLIDE | 477984 | 79727 | GS-115 | REF |  |  |
| S3 | SWITCH, SLIDE | 477984 | 79727 | GS-115 | REF |  |  |
| U1 | IC, C-MOS, DUAL D F/F | 340117 | 02735 | CD4013AE | 1 | 1 |  |
| U2 | IC, MICRO PROCESSOR | 524827 | 89536 | 524827 | 1 | 1 |  |
| U3 | IC, N-MOS, INPUT OUTPUT EXPANDER | 507293 | 34649 | P82 43 | 1 | 1 |  |
| U4 | IC, TTL, DIGITAL, COLLECTOR | 328021 | 01295 | SN7417N | 2 | 1 |  |
| U5 | IC, TTL, DIGITAL, COLLECTOR | 328021 | 01295 | SN7 417 N | REF |  |  |
| U6 | RESISTIOR NETWORK, 82 OHMS | 478859 | 8953E | 478859 | 1 | 1 |  |
| U7 | IC, C-MOS, HEX BUFF/INVERTER | 381830 | 02735 | CD650AE | 1 | , |  |
| U8 | RESISTOR NETWORK | 501494 | 89536 | 501494 | 1 | 1 |  |
| 09 | IC, 4K X 8 BIT | 525048 | 89536 | 525048 | 1 | 1 |  |
| U10 | IC, TTL, dIGITAL | 504514 | 01295 | SN7 4 LS 373 | 1 | 1 |  |
| U11 | Q IC, C-MOS, MONO/ASTABLE MLTVERTR | 535575 | 12040 | CD4047E | 1 | 1 |  |
| XU2 | SOCKET, IC, 40-PIN | 429282 | 09922 | DILB4CP-108 | 1 |  |  |
| XU9 | SOCKET, IC, 24-PIN | 418970 | 91506 | 324-AC39D | 1 |  |  |
| Y 1 | CRYSTAL, $6 \mathrm{MHZ}+/-0.015 \%$ | 461665 | 89536 | 461665 | 1 | 1 |  |



CAUTION
STATIC ELECTRICITY

Figure 5-4. A3 Controller PCB Assembly

Table 5-5. A4 AC/DC Scaling PCB Assembly

| $\begin{aligned} & \text { REF } \\ & \text { OES } \end{aligned}$ | DESCRIPTION | FLUKE STOCK NO. | MFG <br> SPLY <br> CODE | MFG PART NO. | $\begin{aligned} & \text { TT } \\ & \text { OTY } \end{aligned}$ | REC QTY | N O T E |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| A4 | AC/DC SCALING PCB ASSEMBLY FIGURE 5-5 (8860A-4004T) | 526665 | 89536 | 526665 | REF |  |  |
| A4A1 | CIRCUIT, HYBRID, AC/DC | 496349 | 89536 | 496349 | 1 | 1 |  |
| C1 | CAP, VAR, . 25 - $1.5 \mathrm{PF}, 2000 \mathrm{VDC}$ | 435016 | 72082 | 530-006 | 3 |  |  |
| C2 | CAP, VAR, . 25 - $1.5 \mathrm{PF}, 2000 \mathrm{VDC}$ | 435016 | 72082 | 530-006 | REF |  |  |
| C3 | CAP, VAR, . 25 - $1.5 \mathrm{PF}, 2000 \mathrm{VDC}$ | 435016 | 72082 | 530-006 | REF |  |  |
| C4 | CAP, MICA, $270 \mathrm{PF},+/-5 \%, 500 \mathrm{~V}$ | 148452 | 72136 | DM15F271J | 1 |  |  |
| C5 | CAP, VAR, $1.7-10 \mathrm{PF}, 250 \mathrm{~V}$ | 375238 | 56289 | GKC 10000 | 2 |  |  |
| C6 | CAP, MICA, $27 \mathrm{PF},+/-5 \%$, 500V | 177998 | 72136 | DM15E270J | 1 |  |  |
| C7 | CAP, MICA, $330 \mathrm{PF},+/-5 \%, 500 \mathrm{~V}$ | 148445 | 72136 | DM15E331J | 2 |  |  |
| C8 | CAP, CERAM, 68 PF | 519181 | 71590 | DD-3R3 | 1 |  |  |
| C9 | CAP, POLYPROP, . 033 UF | 519850 | 52763 | MKP-1840/1841 | 1 |  |  |
| C10 | CAP, POLYPROP, . $22 \mathrm{UF}+/-10 \%$, 50V | 423210 | 89536 | 423210 | 2 |  |  |
| C11 | CAP, POLYPROP, . $22 \mathrm{UF}+/-10 \%$, 50 V | 423210 | 89536 | 423210 | REF |  |  |
| C12 | CAP, MYLAR, . $22 \mathrm{UF}+/-10 \%$, 100 V | 436113 | 73445 | C280MAH/A220K | 2 |  |  |
| C13 | CAP, MYLAR, . $047 \mathrm{UF}+/-10 \%, 250 \mathrm{~V}$ | 162008 | 73445 | C280MAE/A47K | 1 |  |  |
| C14 | CAP, TA, 4.7 UF +/-20\%, 25 V | 161943 | 56289 | 196D685X9035KA 1 | 5 |  |  |
| C15 | CAP, MICA, $100 \mathrm{PF}+/-5 \%$, 500V | 148494 | 72136 | DM15F101J | 1 |  |  |
| C17 | CAP, CERAM, 33PF +/-5\%, 100V | 354852 | 80031 | 2222-638-10339 | 1 |  |  |
| C18 | CAP, TA, 4.7 UF +/-20\%, 25 V | 161943 | 56289 | 196D685X9035KA 1 | REF |  |  |
| C19 | CAP, TA, $4.7 \mathrm{UF}+/-20 \%, 25 \mathrm{~V}$ | 161943 | 56289 | 196D685X9035KA 1 | FEF |  |  |
| C20 | CAP, MICA, 680 PF | 148403 | 02799 | DM15F101J | 1 |  |  |
| C21 | CAP, CERAM, 4.7 UF +/-. $25 \%$, 100V | 362731 | 89536 | 362731 | 3 |  |  |
| C22 | CAP, VAR, $1.7-10 \mathrm{PF}, 250 \mathrm{~V}$ | 375238 | 56289 | GKC10000 | REF |  |  |
| C23 | CAP, CERAM, 2.2 PF +/-. $2.5 \%$, 100V | 362731 | 89536 | 362731 | REF |  |  |
| C25 | CAP, MICA, $150 \mathrm{PF}+/-5 \%$, 500V | 148478 | 72136 | DM15F151J | 1 |  |  |
| C26 | CAP, CERAM, 2.2 PF +/-.25\%, 100V | 362731 | 89536 | 362731 | REF |  |  |
| C27 | CAP, CERAM, . $22 \mathrm{UF}+/-20 \%$, 50V | 309849 | 71590 | CW30C224K | 1 |  |  |
| C28 | CAP, CERAM, . $01 \mathrm{UF}+/-20 \%$, 100V | 149153 | 56289 | C0238101F103M | 2 |  |  |
| C29 | CAP, MICA, $330 \mathrm{PF},+/-5 \%, 500 \mathrm{~V}$ | 148445 | 72136 | DM15E331J | REF |  |  |
| C30 | CAP, TA, 4.7 UF +/-20\%, 25V | 161943 | 56289 | 196D685X9035KA1 | REF |  |  |
| C31 | CAP, TA, 4.7 UF +/-20\%, 25V | 161943 | 56289 | 196D685X9035KA1 | REF |  |  |
| C32 | CAP, POLY, . $47 \mathrm{UF}+/-10 \%, 100 \mathrm{~V}$ | 446807 | 89536 | 446807 | 1 |  |  |
| C34 | CAP, POLY, . $22 \mathrm{UF}+/-10 \%$, 100V | 614172 | 73445 | C280MCH/ A220K | 1 |  |  |
| C35 | CAP, CERAM, $22 \mathrm{PF}+/-5 \%$, 100 V | 448449 | 80031 | 2222-638-10229 | 2 |  |  |
| C36 | CAP, CERAM, $22 \mathrm{PF}+/-5 \%$, 100V | 448449 | 80031 | 2222-638-10229 | REF |  |  |
| C38 | CAP, CERAM, . $01 \mathrm{UF}+/-20 \%$, 100V | 149153 | 56289 | C0238101F 103M | REF |  |  |
| C40 | CAP, CERAN, 1.0 PF | 436477 | 80031 | 2222-638-03108 | 1 |  |  |
| C41 | CAP, MYLAR, . $22 \mathrm{UF}+/-10 \%$, 100V | 436113 | 73445 | C280MAH/ A220K | REF |  |  |
| C42 | CAP, CERAM, 4700 PF | 362871 | 72982 | 8121-A100-W5R-472M | 1 |  |  |
| C43 | CAP, CERAM, $22 \mathrm{PF}+1-5 \%$, 100V | 448449 | 80031 | 2222-638-10229 | REF |  |  |
| C44 | CAP, CERAM, . $68 \mathrm{PF}+/-1 \%$, 100V | 485011 | 89536 | 485011 | 1 |  |  |
| CL1 | DIODE, FED, CURRENT REG. | 393454 | 07910 | TCR5290 | 1 | 1 |  |
| CR6 | DIODE, LOW-LEAK, LO-CAP | 375907 | 07263 | FD7222 | 4 | 1 |  |
| CR7 | DIODE, LOW-LEAK, LO-CAP | 375907 | 07263 | FD7222 | REF |  |  |
| CR8 | DIODE, LOW-LEAK, LO-CAP | 375907 | 07263 | FD7222 | REF |  |  |
| CR9 E1-E49 | DIODE, LOW-LEAK, LO-CAP <br> (SEE INSERT A, WIRE LIST) | 375907 | 07263 | FD7222 | REF |  |  |
| H1 | SCREW, FHP, S/S, 6-32 X 1/4 (ON SHIELD) | 385401 | 89536 | 385401 | 3 |  |  |

Table 5-5. A4 AC/DC Scaling PCB Assembly (cont)

| $\begin{aligned} & \text { REF } \\ & \text { DES } \end{aligned}$ | DESCRIPTION | FLUKE STOCK NO. | MFG SPLY COOE | MFG PART NO. | $\begin{aligned} & \text { TOT } \\ & \text { QTY } \end{aligned}$ | $\left.\begin{array}{\|l\|} \text { REC } \\ \text { QTY } \end{array} \right\rvert\,$ | N <br> 0 <br>  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| MP1 | SHIELD, AC/DC (NOT SHOWN) | 502591 | 89536 | 502591 | 1 |  |  |
| MP2 | SUPPORT, RES. NETWORK (ON SHIELD) | 531046 | 89536 | 531046 | 1 |  |  |
| MP3 | TERMINAL, FEED-THRU/TEFLON | 529305 | 98291 | 011-6811-00-0-202 | 14 |  |  |
| MP4 | TERMINAL, FEED-THRU/TEFLON | 529297 | 98291 | 011-6812-00-0-206 | 9 |  |  |
| MP5 | HEATSINK (WITH U17, U20) | 354993 | 98978 | TXC20CB | 2 |  |  |
| Q2 | XSTR, J-FET, N-CHAN | 343830 | 89536 | 343830 | 5 | 2 |  |
| Q3 | XSTR, J-FET, N-CHAN | 535039 | 89536 | 535039 |  | , |  |
| Q6 | XSTR, J-FET, N-CHAN | 343830 | 89536 | 343830 | REF |  |  |
| Q7 | XSTR, J-FET, N-CHAN | 343830 | 89536 | 343830 | REF |  |  |
| Q8 | XSTR, J-FET, N-CHAN | 508697 | 21845 | FS933 | 1 | 1 |  |
| Q11 | XSTR, J-FET, N-CHAN | 429977 | 21845 | F2811 | 1 | 1 |  |
| Q12 | XSTR, FET, N-CHAN | 261578 | 89536 | 261578 | 3 | 1 |  |
| Q13 | XSTR, J-FET, N-CHAN | 343830 | 89536 | 343830 | REF |  |  |
| Q14 | XSTR, SI, PNP | 229898 | 04713 | MPS6522 | 1 | 1 |  |
| Q15 | XSTR, J-FET, N-CHAN | 343830 | 89536 | 343830 | REF |  |  |
| Q16 | XSTR, dUAL FET, N-CHAN | 419283 | 89536 | 419283 | 1 | 1 |  |
| Q17 | XSTR, DUAL FET, N-CHAN | 578799 | 89536 | 578799 | 1 | 1 |  |
| Q18 | XSTR, FET, N-CHAN | 261578 | 89536 | 261578 | REF |  |  |
| Q19 | XSTR, FET, N-CHAN | 386730 | 12040 | SF-1102 | 1 | 1 |  |
| R2 | RES, DEP. CAR, $47 \mathrm{~K}+/-5 \%, 1 / 4 \mathrm{~W}$ | 348896 | 80031 | CR251-4-5P47K | 6 |  |  |
| R3 | RES, DEP. CAR, $47 \mathrm{~K}+/-5 \%, 1 / 4 \mathrm{~W}$ | 348896 | 80031 | CR251-4-5P47K | REF |  |  |
| R4 | RES, DEP. CAR, $47 \mathrm{~K}+/-5 \%, 1 / 4 \mathrm{~W}$ | 348896 | 80031 | CR251-4-5P47K | REF |  |  |
| R5 | RES, DEP. CAR, $22 \mathrm{~K}+/-5 \%, 1 / 4 \mathrm{~W}$ | 348870 | 80031 | CR251-4-5P22K | 1 |  |  |
| R8 | RES, VAR. CERMET, $200+/-10 \%, 1 / 2 \mathrm{~W}$ | 285148 | 89536 | 285148 | 3 |  |  |
| R9 | RES, VAR. CERMET, $200+/-10 \%, 1 / 2 \mathrm{~W}$ | 285148 | 89536 | 285148 | REF |  |  |
| R10 | RES, COMP, $47 \mathrm{~K}+/-5 \%$, 1/4W | 150219 | 01121 | CB4735 | 2 |  |  |
| R11 | RES, COMP, $47 \mathrm{~K}+/-5 \%, 1 / 4 \mathrm{~W}$ | 150219 | 01121 | CB4735 | REF |  |  |
| R12 | RES, DEP. CAR, $100 \mathrm{~K}+/-5 \%, 1 / 4 \mathrm{~W}$ | 348920 | 80031 | CR251-4-5P100K | 3 |  |  |
| R13 | RES, DEP. CAR, $10 \mathrm{~K}+/-5 \%, 1 / 4 \mathrm{~W}$ | 348839 | 80031 | CR251-4-5P10K | 3 |  |  |
| R14 | RES, DEP. CAR, 10K +/-5\%, 1/4W | 348839 | 80031 | CR251-4-5P10K | REF |  |  |
| R15 | RES, MTL FILM, 100K $+/-1 \%, 1 / 8 \mathrm{~W}$ | 248807 | 91637 | MFF 1-81003F | 2 |  |  |
| R17 | RES, MTL FILM, $100 \mathrm{~K}+/-1 \%, 1 / 8 \mathrm{~W}$ | 248807 | 91637 | MFF1-81003F | REF |  |  |
| R20 | RES, DEP. CAR, $3.3 \mathrm{~K}+/-5 \%, 1 / 4 \mathrm{~W}$ | 348813 | 80031 | CR251-4-5P3K3 | 2 |  |  |
| R21 | RES, DEP. CAR, 3.3K +/-5\%, 1/4W | 348813 | 80031 | CR251-4-5P3K3 | REF |  |  |
| R23 | RES, MTL FILM, $10 \mathrm{~K}+/-1 \%, 1 / 8 \mathrm{~W}$ | 168260 | 91637 | MFF1-81002F | 2 |  |  |
| R24 | RES, DEP. CAR, 4.3K +/-5\%, 1/4W | 441576 | 80031 | CR251-4-5P4K3 | 1 |  |  |
| R25 | RES, MTL FILM, $10 \mathrm{~K}+/-1 \%, 1 / 8 \mathrm{~W}$ | 168260 | 91637 | MFF 1-81002F | REF |  |  |
| R27 | RES, VAR. $50+/-10 \%, 1 / 2 \mathrm{~W}$ | 285122 | 89536 | 285122 | 1 |  |  |
| R28 | RES, MTL FILM, 3.83K +/-1\%, 1/8W | 260323 | 91637 | CMF553831F | 1 |  |  |
| R29 | RES, VAR. $2 \mathrm{~K}+/-10 \%$, 1/2W | 285163 | 89536 | 285163 | 1 |  |  |
| R30 | RES, MTL FILM, $3.65 \mathrm{~K}+/-1 \%, 1 / 8 \mathrm{~W}$ | 168252 | 91637 | CMF553651F | 1 |  |  |
| R31 | RES, MTL FILM, $392+/-1 \%, 1 / 8 \mathrm{~W}$ | 260299 | 91637 | MFF1-83920F | 1 |  |  |
| R32 | RES, DEP. CAR, $7.5 \mathrm{~K}+/-5 \%, 1 / 4 \mathrm{~W}$ | 441667 | 80031 | CR251-4-5P7K5 | 1 |  |  |
| R33 | RES, DEP. CAR, $47 \mathrm{~K}+/-5 \%, 1 / 4 \mathrm{~W}$ | 348896 | 80031 | CR251-4-5P47K | REF |  |  |
| R34 | RES, MTL FILM, 3.57K +/-1\%, 1/8W | 226217 | 91637 | MFF 1-83571F | 1 |  |  |
| R35 | RES, DEP. CAR, $47 \mathrm{~K}+/-5 \%, 1 / 4 \mathrm{~W}$ | 348896 | 80031 | CR251-4-5P47K | REF |  |  |
| R36 | RES, DEP. CAR, $47 \mathrm{~K}+/-5 \%, 1 / 4 \mathrm{~W}$ | 348896 | 80031 | CR251-4~5P47K | REF |  |  |
| R37 | RES, DEP. CAR, $100+/-5 \%, 1 / 4 \mathrm{~W}$ | 348771 | 80031 | CR251-4-5P100E | 1 |  |  |
| R38 | RES, MTL FILM, $143 \mathrm{~K}+/-1 \%, 1 / 8 \mathrm{~W}$ | 291336 | 91637 | MFF 1 -81433F | 1 |  |  |
| R39 | RES, VAR. CERMET, $50 \mathrm{~K}+/-10 \%, 1 / 2 \mathrm{~W}$ | 288290 | 89536 | 288290 | 1 |  |  |

Table 5-5. A4 AC/DC Scaling PCB Assembly (cont)

| $\begin{aligned} & \text { REF } \\ & \text { DES } \end{aligned}$ | DESCRIPTION | FLUKE STOCK NO. | MFG <br> SPLY <br> CODE | MFG PART NO. | $\begin{aligned} & \text { TOT } \\ & \text { QTY } \end{aligned}$ | $\begin{aligned} & \text { REC } \\ & \text { QTY } \end{aligned}$ | N <br> 0 <br>  <br>  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| R40 | RES, DEP. CAR, 4.7K +/-5\%, 1/4W | 348821 | 80031 | CR251-4-5P4K7 | 3 |  |  |
| R4 1 | RES, DEP. CAR, 4.7K +/-5\%, 1/4W | 348821 | 80031 | CR251-4-5P4K7 | REF |  |  |
| R43 | RES, VAR. CERMET, $100+/-10 \%, 1 / 2 \mathrm{~W}$ | 285130 | 89536 | 285130 | 1 |  |  |
| R45 | RES, COMP, $22 \mathrm{M}+/-5 \%, 1 / 4 \mathrm{~W}$ | 221986 | 01121 | CB2265 | 1 |  |  |
| R46 | RES, VAR. CERMET, $100 \mathrm{~K}+/-10 \%, 1 / 4 \mathrm{~W}$ | 288308 | 89536 | 288308 | 2 |  |  |
| R49 | RES, DEP. CAR, $43 \mathrm{~K}+/-5 \%, 1 / 4 \mathrm{~W}$ | 442418 | 80031 | CR251-4-5P43K | 2 |  |  |
| R50 | RES, DEP. CAR, $43 \mathrm{~K}+/-5 \%, 1 / 4 \mathrm{~W}$ | 442418 | 80031 | CR251-4-5P43K | REF |  |  |
| R54 | RES, VAR. CERMET, $25 \mathrm{~K}+/-10 \%, 1 / 2 \mathrm{~W}$ | 289678 | 89536 | 289678 | 1 |  |  |
| R57 | RES, DEP. CAR, $100 \mathrm{~K}+/-5 \%, 1 / 4 \mathrm{~W}$ | 348920 | 80031 | CR251-4-5P100K | REF |  |  |
| R59 | RES, DEP. CAR, $100 \mathrm{~K}+/-5 \%, 1 / 4 \mathrm{~W}$ | 348920 | 80031 | CR251-4-5P100K | REF |  |  |
| R61 | RES, VAR. CERMET, $10 \mathrm{~K}+/-10 \%$, $1 / 2 \mathrm{~W}$ | 285171 | 89536 | 285171 | 1 |  |  |
| R63 | RES, DEP. CAR, $6.2 \mathrm{~K}+/-5 \%, 1 / 4 \mathrm{~W}$ | 442368 | 80031 | CR251-4-5P6K2 | 1 |  |  |
| R64 | RES, DEP. CAR, $1+/-5 \%, 1 / 4 \mathrm{~W}$ | 357665 | 80031 | CR251-4-5P1E | 1 |  |  |
| R65 | RES, DEP. CAR, $10 \mathrm{~K}+/-5 \%, 1 / 4 \mathrm{~W}$ | 348839 | 80031 | CR251-4-5P1CK | REF |  |  |
| R66 | RES, MTL FILM, 1K +/-1\%, 1/8W | 320333 | 91637 | CMF551001F | 1 |  |  |
| R67 | RES, VAR. CERMET, $200+/-10 \%$, $1 / 2 \mathrm{~W}$ | 285148 | 89536 | 285148 | REF |  |  |
| R68 | RES, DEP. CAR, 120K +/-5\%, 1/4W | 441386 | 80031 | CR251-4-5P120K | 1 |  |  |
| R70 | RES, MTL FILM, $402 \mathrm{~K}+/-1 \%, 1 / 8 \mathrm{~W}$ | 217984 | 91637 | MFF 1-84023F | 1 |  |  |
| R71 | RES, COMP, $2.7 \mathrm{M}+/-5 \%, 1 / 4 \mathrm{~W}$ | 153490 | 01121 | CB2755 | 1 |  |  |
| R72 | RES, DEP. CAR, 1.8K +/-5\%, 1/4W | 4414.44 | 80031 | CR251-4-5P1K8 | 1 |  |  |
| R73 | RES, VAF. CERMET, $100 \mathrm{~K}+/-10 \%, 1 / 4 \mathrm{~W}$ | 288308 | 89536 | 288308 | REF |  |  |
| R75 | RES, MTL FILM, 715 +/-1\%, 1/8W | 313080 | 91637 | CMF557150F | 1 |  |  |
| R76 | RES, DEP. CAR, 4.7K +/-5\%, 1/4W | 348821 | 80031 | CR251-4-5P4K7 | PEE |  |  |
| U1 | RES. NETWORK, INPUT DIVIDER | 510636 | 89536 | 510636 | 1 | $i$ |  |
| U2 | IC, LIN, QUAD, COMPARATOK | 387233 | 12040 | LM339N | 4 | 1 |  |
| U3 | IC, LIN, OP-AMP | 478107 | 12040 | LM308A | , | 1 |  |
| U4 | IC, LIN, QUAD, COMPARATOR | 387233 | 12040 | LM339N | REF |  |  |
| U5 | IC, OP-AMP, J-FET INPUT | 418780 | 12040 | LF'351 | $!$ | 1 |  |
| U6 | IC, LIN, J-FET INPUT, DUAL OP-AMP | 495192 | 12040 | LF353EN | 1 | 1 |  |
| ${ }^{4} 7$ | IC, LIN, QUAD, COMPARATOR | 387233 | 12040 | LM339N | REF |  |  |
| U8 | IC, LIN, OP-AMP, J-FET INPUT | 535856 | 01295 | TL081ACL | 1 | 1 |  |
| U9 | IC, LIN, QUAD, COMPARATOR | 387233 | 12040 | LM339N | REF |  |  |
| U10 | RES. NETWORK, OUTPUT DIVIDER | 511196 | 89536 | 511196 | 1 | 1 |  |
| U13 | RESISTOR NETWORK | 520387 | 89536 | 520387 | 1 | 1 |  |
| 014 | IC, LIN, OP- AMP | 495051 | 18324 | NE5534N | 1 | 1 |  |
| 015 | IC, LIN, OP-AMP, PROGRAMMABLE, 8 PIN DIP | 418913 | 12040 | LM4250CN | 1 | 1 |  |
| 016 | IC, OP-AMP, MONO, J-FET INPUT | 524033 | 12040 | LF 356 H |  | , |  |
| U17 | IC, XSTR ARRAY | 504191 | 89536 | 504191 | 2 | , |  |
| U18 | RES. NETWORK, RMS | 511147 | 89536 | 511147 | 1 | 1 |  |
| U19 | IC, LIN, SELECTED | 473777 | 89536 | 473777 | 1 | 1 |  |
| U20 | IC, XSTR ARRAY | 504191 | ع9536 | 504791 | FEF |  |  |
| VR1 | DIODE, ZENER, 6.2V +/-5\% | 325811 | 04713 | 1N753A | 2 | 1 |  |
| VR2 | DIODE, ZENER, 6.2V +/-5\% | 325811 | 04713 | 1N753A | REF |  |  |
| VR3 | DIODE, ZENER, 9.1v +/-5\% | 386557 | 04713 | 1 N 960 B | 2 | 1 |  |
| VR4 | DIODE, ZENER, 9.1V +/-5\% | 386557 | 04713 | 1N960B | REF |  |  |
| W1-W25 | WIRE, JUMPER AND HOOK-UP <br> (SEE INSERT A, WIRE TERMINATIONS Figure |  |  |  |  |  |  |


WIRE TERMINATIONS

| WIRE LIST |  |  |
| :---: | :---: | :---: |
| NO. | FROM | TO |
| W1 | E1 | E2 |
| W2 | E3 | E4 |
| W3 | E5 | E6 |
| W4 | E7 | E8 |
| W5 | E10 | E11 |
| W6 | E11 | E12 |
| W7 | E12 | E13 |


| WIRE LIST |  |  |
| :---: | :---: | :---: |
| NO. | FROM | TO |
| W8 | E13 | E14 |
| W9 | E15 | E16 |
| W10 | E17 | E18 |
| W11 | E16 | E18 |
| W12 | E18 | E19 |
| W13 | E19 | E20 |
| W14 | E21 | E22 |


| WIRE LIST |  |  |
| :---: | :---: | :---: |
| NO . | FROM | TO |
| W15 | E22 | E23 |
| W16 | E24 | E25 |
| W17 | E25 | E26 |
| W18 | E27 | E28 |
| W19 | E29 | E30 |
| W20 | E31 | E32 |
| W21 | E32 | E33 |


| WIRE LIST |  |  |
| :---: | :---: | :---: |
| NO. | FROM | TO |
| W22 | E33 | E34 |
| W23 | E35 | E36 |
| W24 | E37 | E38 |
| W25 | E49 | E50 |

Table 5-6. A/D And Ohms Converter PCB Assembly

| $\begin{aligned} & \text { REF } \\ & \text { DES } \end{aligned}$ | DESCRIPTION | FLUKE STOCK NO. | MFG <br> SPLY <br> CODE | MFG PART NO. | $\begin{aligned} & \text { TOT } \\ & \text { QTY } \end{aligned}$ | $\begin{aligned} & \text { REC } \\ & \text { QTY } \end{aligned}$ | N <br> O <br> T <br> E |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| A5 | (8) A/D AND OHMS CONVERTER PCB ASSEMBLY FIGURE 5-6 (8860A-4005T) | 526673 | 89536 | 526673 | REF |  |  |
| A5A1 | IC, OHMS Range hybrid | 496356 | 89536 | 496356 | 1 | 1 |  |
| A5A2 | IC, A-D SWITCHING HYBRID | 496364 | 89536 | 496364 | 1 | 1 |  |
| C1 | CAP, CERAM, . 05 UF +/-20\%, 50V | 175232 | 56289 | C023B101H253M | 3 |  |  |
| C2 | CAP, FXD, . $01 \mathrm{UF}+/-20 \%, 400 \mathrm{~V}$ | 402818 | 72445 | C280MAF/A10K | 1 |  |  |
| C3 | CAP, CERAM, $33 \mathrm{PF}+/-2 \%, 100 \mathrm{~V}$ | 354852 | 80031 | 2222-638-10339 | 4 |  |  |
| C5 | CAP, CERAM, $33 \mathrm{PF}+/-2 \%, 100 \mathrm{~V}$ | 354852 | 80031 | 2222-638-10339 | REF |  |  |
| C6 | CAP, CERAM, . $005 \mathrm{UF}+/-20 \%$, 50V | 175232 | 56289 | C023B101E502M | REF |  |  |
| C7 | CAP, POLYPRO, $0.47 \mathrm{UF}+/-5 \%$, 50V | 364042 | 84411 | JF78B | 1 |  |  |
| C8 | CAP, MICA, $150 \mathrm{PF}+/-5 \%, 500 \mathrm{~V}$ | 148478 | 02799 | DM150F101 J | 2 |  |  |
| C9 | CAP, MICA, $150 \mathrm{PF}+/-5 \%, 500 \mathrm{~V}$ | 148478 | 02799 | DM150F101 J | REF |  |  |
| C10 | CAP, CERAM, $33 \mathrm{PF}+/-2 \%, 100 \mathrm{~V}$ | 354852 | 80031 | 2222-638-10339 | REF |  |  |
| C11 | CAP, CERAM, . $005 \mathrm{UF}+/-20 \%$, 50 V | 175232 | 56289 | C023B101E502M | REF |  |  |
| C12 | CAP, MICA, $430 \mathrm{PF}+/-5 \%$, 500V | 177980 | 02799 | DM430F101J | 1 |  |  |
| C13 | CAP, POL. CAR, 2.2 UF $+-10 \%, 100 \mathrm{~V}$ | 306522 | 80031 | C280MC | 1 |  |  |
| C14 | CAP, FXD, $1 \mathrm{MF}+/-10 \%$, 100V | 447847 | 73445 | С280МАН/a 1 M | 2 |  |  |
| C15 | CAP, FXD, $1 \mathrm{MF}+/-10 \%$, 100 V | 447847 | 73445 | C280MAH/A1M | REF |  |  |
| C16 | CAP, CERAM, $100 \mathrm{PF}+/-2 \%, 100 \mathrm{~V}$ | 369173 | 80031 | 2222-638-1010 | 1 |  |  |
| C17 | CAP, MICA, $2 \mathrm{PF}+/-10 \%$, 500 V | 175208 | 02799 | DM02C101D | 1 |  |  |
| C18 | CAP, MICA, $8 \mathrm{PF}+/-10 \%$, 500V | 216986 | 02799 | DM08C101K | 1 |  |  |
| C19 | CAP, CERAM, $33 \mathrm{PF}+/-2 \%, 100 \mathrm{~V}$ | 354852 | 80031 | 2222-638-10339 | REF |  |  |
| C20 | CAP, MICA, $270 \mathrm{PF}+/-5 \%, 500 \mathrm{~V}$ | 148452 | 02799 | DM270F101 J | 1 |  |  |
| CL1 | DIO, (FED) 0.47 NOM., 400 MW | 393454 | 07910 | TCR5290 | 2 | 1 |  |
| CL2 | DIO, (FED) $0.47 \mathrm{NOM.}$, | 393454 | 07910 | TCR5290 | REF |  |  |
| CR1 | DIO, SI, LO-CAP/LO-LEAK | 375907 | 07263 | FD7223 | 7 | 2 |  |
| CR5 | DIO, SI, LO-CAP/LO-LEAK | 375907 | 07263 | FD7223 | REF |  |  |
| CR6 | DIO, SI, LO-CAP/LO-LEAK | 375907 | 07263 | FD7223 | REF |  |  |
| CR8 | DIO, SI, LO-CAP/LO-LEAK | 375907 | 07263 | FD7223 | REF |  |  |
| CR9 | DIO, SI, LO-CAP/LO-LEAK | 375907 | 07263 | FD7223 | REF |  |  |
| CR10 | DIO, SI, LO-CAP/LO-LEAK | 375907 | 07263 | FD7223 | REF |  |  |
| CR11 | DIO, SI, LO-CAP/LO-LEAK | 375907 | 07263 | FD7223 | REF |  |  |
| E | JUMPER WIRE CONNECTIONS |  |  |  |  |  |  |
| J1 | CONN, HEADER | 519751 | 89536 | 519751 | 1 |  |  |
| MP1 | SOCKET, COMPONENT LEAD (NOT SHOWN) | 376418 | 22526 | 75060-007 | 4 |  |  |
| MP2 | TRANSISTOR PAD, SPACER (NOT SHOWN) | 152207 | 07047 | 10123-DAP | 1 |  |  |
| Q3 | XSTR, J-FET, N-CHAN | 343830 | 89536 | 343830 | 6 | 2 |  |
| Q4 | XSTR, FET | 429977 | 89536 | 429977 | 2 | , |  |
| Q5 | XSTR, FET | 429977 | 89536 | 429977 | REF |  |  |
| Q6 | XSTR, J-FET, N-CHAN | 343830 | 89536 | 343830 | REF |  |  |
| Q7 | XSTR, J-FET, N-CHAN | 343830 | 89536 | 343830 | REF |  |  |
| Q8 | XSTR, J-FET, N-CHAN | 343830 | 89536 | 343830 | REF |  |  |
| Q9 | XSTR, J-FET, N-CHAN | 343830 | 89536 | 343830 | REF |  |  |
| Q10 | XSTR, J-FET, N-CHAN | 343830 | 89536 | 343830 | REF |  |  |
| Q11 | XSTR, J-FET, N-CHAN | 419283 | 32293 | ITS3079 | 1 | 1 |  |
| Q12 | XSTR, NPN | 218396 | 89536 | 218396 | 1 | 1 |  |
| R1 | RES, VAR, SIDE-ADJUST, 20K | 291609 | 75378 | 360S-203AZ | 1 |  |  |
| R2 | RES, VAR, $2 \mathrm{~K}+/-10 \%$ | 285163 | 75378 | 360T-202AZ | 1 |  |  |
| R3 | RES, VAR, $200+/-10 \%$ | 285148 | 75378 | 360T-200AZ | 3 |  |  |

Table 5-6. A/D And Ohms Converter PCB Assembly (cont)

| $\begin{aligned} & \text { REF } \\ & \text { DES } \end{aligned}$ | DESCRIPTION | FLUKE STOCK NO. | MFG SPLY CODE | MFG PART NO. | $\begin{aligned} & \text { TOT } \\ & \text { QTY } \end{aligned}$ | $\begin{aligned} & \text { REC } \\ & \text { QTY } \end{aligned}$ | N <br> 0 <br>  <br> E |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| R4 | RES, COMP, $4.7 \mathrm{M}+/-5 \%, 1 / 4 \mathrm{~W}$ | 220046 | 01121 | CB4755 | 1 |  |  |
| R5 | RES, DEP. CAR, $10 \mathrm{~K}+/-5 \%, 1 / 4 \mathrm{~W}$ | 348839 | 80031 | CR251-4-5P10K | 1 |  |  |
| R6 | RES, MTL. FILM, $10 \mathrm{~K}+/-1 \%, 1 / 8 \mathrm{~W}$ | 168260 | 91637 | CMF55103 | 2 |  |  |
| R7 | RES, MTL. FILM, $10 \mathrm{~K}+/-1 \%, 1 / 8 \mathrm{~W}$ | 168260 | 91637 | CMF55103 | REF |  |  |
| R8 | RES, DEP. CAR, $2.0 \mathrm{~K}+/-5 \%, 1 / 4 \mathrm{~W}$ | 441469 | 80031 | CR251-4-5P2K | 2 |  |  |
| R9 | RES, VAR, 20 +/-10\% | 285114 | 75378 | 360T-020A2 | 1 |  |  |
| R10 | RES, VAR, $50+/-10 \%$ | 285122 | 75378 | 360T-050A2 | 1 |  |  |
| R11 | RES, VAR, $200+/-10 \%$ | 285148 | 75378 | 360T-200AZ | REF |  |  |
| R12 | RES, $54.7 \mathrm{~K}+/-.05 \%$, $1 / 4 \mathrm{~W}$ | 492223 | 89536 | 492223 | 1 |  |  |
| R17 | RES, VAR, 50, RECT. | 267815 | 11236 | 190 PC500B | 1 |  |  |
| R18 | RES, VAR, $100 \mathrm{~K}+/-10 \%$ | 288308 | 75378 | 360T-102A2 | 1 |  |  |
| R19 | RES, DEP. CAR, $1 \mathrm{M}+/-5 \%, 1 / 4 \mathrm{~W}$ | 348987 | 80031 | CR251-4-5P1M | 1 |  |  |
| R20 | RES, DEP. CAR, $10+/-5 \%, 1 / 4 \mathrm{~W}$ | 340075 | 80031 | CR251-4-5P10E | 1 |  |  |
| R21 | RES, DEP. CAR, 2.0K +/-5\%, 1/4W | 441469 | 80031 | CR251-4-5P2K | REF |  |  |
| R22 | RES, $\operatorname{COMP,~1.5M~+/-5\% ,~1/4W~}$ | 182857 | 01121 | CB1555 | 1 |  |  |
| R23 | RES, COMP, $10=/-5 \%, 1 / 4 \mathrm{~W}$ | 147868 | 01121 | CB1005 | 1 |  |  |
| R24 | RES, DEP. CAR, $1 \mathrm{~K}+/-5 \%, 1 / 4 \mathrm{~W}$ | 343426 | 80031 | CR251-4-5P1K | 2 |  |  |
| R25 | RES, DEP. CAR, $33 \mathrm{~K}+/-5 \%, 1 / 4 \mathrm{~V}$ | 348888 | 80031 | CR251-4-5P33K | 2 |  |  |
| R26 | RES, DEP. CAR, 8.2K $+/-5 \%, 1 / 4 \mathrm{~W}$ | 441675 | 80031 | CR251-4-5P8K2 | 1 |  |  |
| R27 | RES, DEP. CAR, $1 \mathrm{~K}+/-5 \%, 1 / 4 \mathrm{~W}$ | 343426 | 80031 | CR251-4-5P1K | REF |  |  |
| R28 | RES, VAR, 200 +/-10\% | 285148 | 75378 | 360T-200AZ | REF |  |  |
| R29 | RES, VAR, $5 \mathrm{~K}+/-10 \%$ | 288282 | 75378 | 360T-052A2 | 2 |  |  |
| R30 | RES, VAR, $5 \mathrm{~K}+/-10 \%$ | 288282 | 75378 | 360T-052A2 | REF |  |  |
| R31 | RES, DEP. CAR, $82 \mathrm{~K}+/-5 \%, 1 / 4 \mathrm{~W}$ | 348912 | 80031 | CR251-4-5P82K | 1 |  |  |
| R32 | RES, DEP. CAR, 200K +/-5\%, 1/4W | 441485 | 80031 | CR251-4-5P200K | 1 |  |  |
| R36 | RES, DEP. CAR, $68 \mathrm{~K}+/-5 \%, 1 / 4 \mathrm{~W}$ | 376632 | 80031 | CR251-4-5P68K | 1 |  |  |
| R37 | RES, MTL. FILM, 6.81K +/-1\%, 1/8W | 268417 | 91637 | CIFF556813F | 1 |  |  |
| R38 | RES, MTL. FILM, $402 \mathrm{~K}+/-1 \%, 1 / 8 \mathrm{~W}$ | 217984 | 91637 | CMF554023 | 1 |  |  |
| R39 | RES, DEP. CAR, 33K $+/-5 \%, 1 / 4 \mathrm{~W}$ | 348888 | 80031 | CR251-4-5P33K | REF |  |  |
| R40 | RES, MTL. FILM, 3.74K +/-1\%, 1/8W | 272096 | 91637 | CMF553743F | 2 |  |  |
| R41 | PART OF U22 REF AMP SET |  |  |  |  |  |  |
| R42 | PART OF U22 REF AMP SET |  |  |  |  |  |  |
| R43 | RES, MTL. FILM, $3.74 \mathrm{~K}+/-1 \%, 1 / 8 \mathrm{~W}$ | 272096 | 91637 | CMF553743F | REF |  |  |
| R44 | RES, COMP, 3.3K +/-5\%, 1/4W | 148056 | 01121 | CB3325 | 1 |  |  |
| R45 | RES, MTL. FILM, $64.9 \mathrm{~K}+/-1 \%, 1 / 8 \mathrm{~W}$ | 288530 | 91637 | CMF556493F | 1 |  |  |
| R46 | RES, MTL. FILM, $110 \mathrm{~K}+/-1 \%, 1 / 8 \mathrm{~W}$ | 234708 | 91637 | CMF551103F | 1 |  |  |
| R47 | RES, MTL. FILM, 113K $+/-1 \%, 1 / 8 \mathrm{~W}$ | 291302 | 91637 | CMF551133F | 1 |  |  |
| TP | TEST POINTS |  |  |  |  |  |  |
| U1 | RESISTOR NETWORK | 511097 | 89536 | 511097 | 1 | 1 |  |
| U2 | IC, OP AMP | 413732 | 12040 | LM308N | 2 | 1 |  |
| U4 | IC, OP AMP | 413732 | 12040 | LM308N | REF |  |  |
| U5 | IC, LINEAR | 478107 | 12040 | LM308A | 1 | 1 |  |
| U6 | IC, LIN, QUAD COMPARATOR | 387233 | 12040 | LM339N | 6 | 2 |  |
| U7 | IC, LIN, QUAD COMPARATOR | 387233 | 12040 | LM339N | REF |  |  |
| U10 | RES NETWORK, 5.6K | 511048 | 89536 | 511048 | 1 | 1 |  |
| U11 | IC, LIN, J-FET | 495192 | 12040 | LF353BN | 3 | 1 |  |
| U12 | IC, LIN, OP AMP | 495051 | 18324 | NE5534N | 1 | 1 |  |
| U13 | IC, LIN, J-FET | 495192 | 12040 | LF353BN | REF |  |  |
| U14 | IC, LIN, NPN, 5-XSTR, SIL. ARRAY | 248906 | 02735 | CA3046 |  | 1 |  |
| U15 | IC, LIN, QUAD COMPARATOR | 387233 | 12040 | LM339N | REF |  |  |

Table 5-6. A/D And Ohms Converter PCB Assembly (cont)

| $\begin{aligned} & \text { REF } \\ & \text { DES } \end{aligned}$ | DESCRIPTION | $\begin{aligned} & \text { FLUKE } \\ & \text { STOCK } \\ & \text { NO. } \\ & \hline \end{aligned}$ | MFG SPLY COOE | MFG PART NO. | $\begin{aligned} & \text { TOT } \\ & \text { QTY } \end{aligned}$ | $\begin{aligned} & \text { REC } \\ & \text { QTY } \end{aligned}$ | N 0 T E |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| U16 | IC, LIN, QUAD COMPARATOR | 387233 | 12040 | LM339N | REF |  |  |
| U17 | IC, LIN, QUAD COMPARATOR | 387233 | 12040 | LM339N | REF |  |  |
| U18 | IC, LIN, QUAD COMPARATOR | 387233 | 12040 | LM339N | REF |  |  |
| U19 | RES NETWORK, MIXED VALUE +/-2\%, 1/8W | 520379 | 89536 | 520379 | 1 | 1 |  |
| U20 | RES NETWORK, MIXED VALUE +/-2\%, 1/8W | 520361 | 89536 | 520361 | 1 | 1 |  |
| U21 | (8) IC, C-MOS, DUAL MULTIPLEXER | 408369 | 95303 | CD4556BE | 1 | 1 |  |
| 022 | REF AMP SET (WITH R41 \& R42) | 523407 | 89536 | 532407 | 1 | 1 |  |
| U23 | IC, LIN, J-FET | 495192 | 12040 | LF353BN | REF |  |  |
| W4-W8 | JUMPER WIRE CONNECTIONS |  |  |  |  |  |  |


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# Section 6 <br> Option Information 

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-004 Calculating Controller ..... 004-1
-005 IEEE-488 Interface ..... 005-1
$-006$ Rear Input ..... 006-1
$-007$ External Reference ..... 007-1

## 6-1. INTRODUCTION

6-2. I his section of the manual contains service information for the 8860 A options. Each option has its own subsection which includes: a theory of operation, trouble-
shooting information. and a list of replaceable parts. The schematics for the options are located in Section 8. The option number is used in the page and paragraph numbers of each option. For instance, option -004 starts on page 004-1.

## 004-1. THEORY OF OPERATION

$004-2$. The Calculating Controller, Option (-004) is composed of the following four circuit boards. The schematic diagram for each circuit board is located in Section 8. A simplified block diagram is shown in Figure 004-1.

- Calculator/Printer PCB Assembly
- Rear Interface PCB Assembly
- Memory Cartridge PCB Assembly
- Control Keyboard PCB Assembly

004-3. The first two boards listed are connected with a ribbon cable and are installed inside the 8860 A chassis. The latter two boards are external to the chassis and plug into the connectors on the Rear Interface board. The Calculating Controller main board is described first.

## 004-4. Local/Remote Switching

$004-5$. Selecting the local or remote control function switches the program memory which directs the out-guard microprocessor. In local, the local program memory is in control. When remote is selected, the option program memory is in control.
004-6. The local program memory directs the operations mentioned under Out-guard Processor Software in the Theory of Operation for the basic instrument. The additional operations required by the Calculating Controller option are directed by the option program memory when the remote control function is selected.
004-7. In remote, the option program memory calls parts of the local program memory as subroutines. For example, the option program memory calls on the local program memory routine to scan the keyboard and strobe the display. When the 8860 A is switched back to local, control returns to the local program memory.
004-8. Option Program Memory
004-9. The program memory is split between two ROMs
(U19 and U10) on the Calculating Controller main board. The active ROM is determined by a group of gates (U17). The out-guard microprocessor controls these gates via P26. The ROMs are custom devices, mask-programmed with the Calculating Controller software. Table 3-2 shows how the two ROMs are accessed using ports P23, P26, and P50.

## 004-10. Calculator

004-11. The number-oriented processor (U5) executes all the math functions and contains the XYZT stack. A divide-by-5 circuit (U16) provides a $400-\mathrm{kHz}$ clock for U5. Processor U5 interfaces to the out-guard microprocessor through U2, an 1/O Expander with RAM. For example, when the square root function is executed, U5 performs the calculation and $U 2$ reports the result back to the out-guard microprocessor for display. U2 also receives and responds to switch closures from the handheld Control Keyboard. A 256-byte RAM in U2 holds the contents of the addressable registers R10-R49 and the print buffer.
$004-12$. The two ports of U10 communicate with the rear panel Data Port. The Data Port is the interface for the optional printer or the user 1/O functions, R50-R57. Tristate buffers U7, U11, U12, and U13 provide bi-directional data buffering to the Data Port. U10 also contains a 2 kbyte ROM.

## 004-13. Data Bus and Address Bus

004-14. The out-guard microprocessor communicates over the data bus DB0-DB7 with the ROM and $1 / 0$ expanders (U2, U10, and U19), the Memory Cartridge, the optional printer, and the User 1/O. Control lines which identify and route each byte on the data bus are $A L E$ (address latch enable), $\overline{\mathrm{PSEN}}$ (program store enable), $\overline{\mathrm{RD}}$, and $\overline{W R}$.

- ALE (address latch enable) is a steady 400 kHz .
- PSEN (program select enable) is active whenever


Figure 004-1. Calculating Controller Option-004 Block Diagram
the processor is reading its program ROM, which it does regularly.

- RD (read) and WR (write) are used only when Option -004 or -005 is installed. They are active when the processor is using the data bus for communication other than reading the program memory. For instance, they are active when the processor is reading the Control Keyboard.
004-15. The address and data for ROM U10 are multiplexed over the data bus. By contrast, the address and data for ROM UI9 and the Memory Cartridge RAM are carried on separate lines. The 8 -bit latch U18 stores the address for these latter two devices. The upper four bits of address, A8-A11, travel on their own lines, P20-P23, to Ul9 and Ul0.


## 004-16. Power Supply

004-17. All circuits operate off the +5 V out-guard supply. IC U5, the only P-channel MOS device, requires an additional -4 V supply derived through CR1, CR2, CR3, Q1, and the power transformer secondary.
004 -18. Three level shifters in U4 convert a TTL level ( 0 V to 5 V ) to a PMOS level $(-4 \mathrm{~V}$ to $+5 \mathrm{~V})$ for pins 7,9 , and 11 of U5.

004-19. Memory Cartridge (Schematic 8860A-1013) $004-20$. The Memory Cartridge contains two CMOS RAM devices to hold addressable registers R00 through R09 and all of programmable memory, steps 00 through 99. All data and address lines are pulled to ground through $100 \mathrm{k} \Omega$ resistors to keep the current drain at a minimum.

004-2 I. Two silver-oxide watch batteries (TB1, TB2) supply power to the RAMs when the cartridge is not receiving power from the 8860 A . Three diodes (CRI on the memory Cartridge board: CR4 and CR5 on the Calculating Controller main board) prevent the +5 V supply from
attempting to charge the batteries. The RAM devices draw a current of 50 nA to 1 uA from the batteries at approximately 2.5 V .
004-22. Jumper W1 at pin 22 of U1 allows power to be removed from UI during troubleshooting. If it is discovered that the Memory Cartridge is drawing an excessive amount of current from the batteries, remove this jumper to identify the faulty RAM.

## 004-23. TROUBLESHOOTING THE CALCULATING CONTROLLER

$004-24$. Table 004-1 contains troubleshooting information for the Calculating Controller. Before using the table, remove the Option -004 PCB from its slot in the 8860 A , and check the operation of the basic DMM. If the DMM is operating properly, reinstall the PCB, and refer to the table.
$004-25$. The troubleshooting table is a series of symptoms and solutions. Check the unit for the symptoms in sequence. When a symptom is identified, clear the fault using the solutions listed for that fault. All devices mentioned in the table are located on the Calculating Controller PCB.

## CAUTION

To avoid instrument damage, remove power from the 8860A before unplugging the circuit board or removing plug-in devices.

## 004-26. LIST OF REPLACEABLE PARTS

004-27. A list of replaceable parts for the Calculating Controller is given in Table 004-2. Refer to Section 5 of this manual for ordering information.

CAUTION (3)
Indicated devices are subject to damage by static discharge.

Table 004-1. Calculating Controlier Troubleshooting

| SYMPTOM | INSTRUCTIONS |
| :---: | :---: |
| 1. The 8860A does not operate in local when the -004 Option PCB is installed, but works when the board is removed. <br> 2. With the option installed, the 8860A operates in local but not in remote. <br> 3. Cannot store or recall Memory Cartridge data <br> 4. User I/O and/or Print functions do not work. <br> 5. Control Keyboard cannot be read <br> 6. Math Functions and XYZT Stack are inoperative. <br> 7. Faulty Address Latch (U18) <br> 8. Faulty Control Line | - Suspect the Memory Cartridge, U19, U2 or U10. <br> - Remove these devices one at a time, until the basic instrument operates normally (in local). These devices are all in sockets and all sit on the internal bus. Replace the device which clears the fault. <br> - Replace U10, U19, U2, and U5. <br> - Check U17 (pin 6 is high when pins 4 and 10 are both high). <br> - Check U18 as described in step 7. <br> - Check U12 for high state at pin 9. <br> - Check U16 for 2 MHz at pin 1, and 400 kHz at pin 8 . <br> - Check U4 for 400 kHz at pin $2,+4.5 \mathrm{~V}$ to -3.5 V swing. <br> - Check U5, pin 21, for a dc voltage between -3.5 V and -4.5 V (negative supply). <br> - Check U5, pin 11, for a dc voltage between -3.5 V and -4.5 V (release of initial reset). <br> - Check U2, pin 28, for a low state (drives U5, pin 11). <br> - Check Q2, Q3, U14 (on Option -004 mainboard); pin 11 of U14 should be high after initial turn-on delay. <br> - Check U15, pin 11, for continuous switching. <br> - Check control lines as described in step 8. <br> - Replace U10. Check U7, U11, and U13 as follows (with nothing connected to the data port): <br> -RCL 50 causes pin 1 of U7 and U11 to go low. STO 50 causes pin 1 of U13 and U7 to go low. <br> - Check U12. pins 13 and 14, and U10 pin 31 for a low state when nothing is connected to the data port. <br> - With the printer connected (make sure the printer is a 2020A with Option -001 installed; Option -004 or a Model 2030A Printer will also work): <br> U12, pins 14 and 13 , and $U 10$ pin 31 should all be high when the printer is on. Pins 1,6 , and 7 of $U 7$ and pin 1 of $U 11$ should remain low for the duration of a print function (Print $X$, for example). During this time, 18 pulses should occur on pins $4,5,9$, and 10 of U7 and on pins 37 and 39 of U10. <br> - Check U3; outputs should sequentially pulse low. <br> - Replace U2 if pins 33 through 36 switch, but are not affected when a key is pressed. <br> - Check the following points for switching when a key is pressed (x-exchange-y key, for instance): <br> - Pin 10 of U2, WR (normally high), for one negative pulse. <br> - Pin 6 of U15 (normally low) for one positive pulse. <br> - Pin 8 of U15 (normally high) for approximately 12 pulses. <br> -Pins 1 and 3 of U14 (normally high) for approximately 12 pulses. <br> - Pin 4 of U14 (normally high) for one negative pulse. <br> - Pin 5 of U12 (normally high) for approximately 12 pulses. <br> - Check pin 9 of U12 (normally low) to go high on Err 99. <br> - Check U18 with a dual-trace scope. Trigger the scope on ALE and look at the input and output of each latch. If ALE and the latch are working properly, then the latch output follows the latch input when ALE is high. The latch input is stored when ALE goes low. <br> - Check $\overline{\text { PSEN, }}$, pin 20 of U19, for continuous switching. <br> - Check ALE, pin 11 of U2, for continuous switching. <br> - Check $\overline{R D}$, pin 9 of U2, for continuous switching. |

Table 004-2. Calculating Controller Assembly

| $\begin{aligned} & \text { REF } \\ & \text { DES } \end{aligned}$ | OESCRIPTION | fLUKE <br> STOCK <br> NO. | MFG SPLY CODE | MFG PART NO. | $\begin{aligned} & \text { TOT } \\ & \text { QTY } \end{aligned}$ | REC QTY | N 0 T E |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | CALCULATING CONTROLLER ASSEMBLY FIGURE 004-2 (8860A-004) | ORDER | BY | OPTION -004 | 1 |  |  |
|  | CONTROL KEYBOARD | 533588 | 89536 | 533588 |  |  |  |
|  | MEMORY CARTRIDGE | Y8833 | 89536 | Y8833 | 1 |  |  |
|  | CALCULATOR/PRINTER PCB ASSEMBLY | 516328 | 89536 | 516328 | 1 |  |  |
| H1 | HARDWARE KIT | 512400 | 89536 | 512400 | 2 |  |  |
| MP1 | PANEL, (SUB) CAL PRINTER | 531038 | 89536 | 531038 | 1 |  |  |
| MP2 | INSULATOR | 541862 | 89536 | 541862 | 1 |  |  |
| MP3 | CUP | 541888 | 89536 | 541888 | 1 |  |  |



Figure 004-2. Calculating Controller Assembly

Table 004-3. Control Keyboard Assembly

| $\begin{aligned} & \text { REF } \\ & \text { DES } \end{aligned}$ | DESCRIPTION | $\begin{aligned} & \text { FLUKE } \\ & \text { STOCK } \\ & \text { NO. } \\ & \hline \end{aligned}$ | MFG SPLY <br> CODE | MFG PART NO. | $\begin{aligned} & \text { TOT } \\ & \text { QTY } \end{aligned}$ | $\begin{aligned} & \text { REC } \\ & \text { QTY } \end{aligned}$ | N 0 T E |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | CONTROL KEYBOARD ASSEMBLY <br> FIGURE 004-3 (8860A-4026) | 533588 | 89536 | 533588 | REF |  |  |
| H1 | SCREW, FHP, 4-40 X 7/16 | 542225 | 89536 | 542225 | 2 |  |  |
| MP1 | CASE, FRONT | 509406 | 89536 | 509406 | 1 |  |  |
| MP2 | CASE, REAR | 509281 | 89536 | 509281 | 1 |  |  |
| MP3 | BUTTON, SLIDE SWITCH (W/S10) | 509331 | 89536 | 509331 | 1 |  |  |
| MP4 | FOOT, CASE | 507624 | 89536 | 507624 | 4 |  |  |
| MP5 | BUTTON, GREY | 509398 | 89536 | 509398 | 14 |  |  |
| MP6 | BUTTON, ORANGE | 509364 | 89536 | 509364 | 1 |  |  |
| MP7 | BUTTON, WHITE | 509372 | 89536 | 509372 | 12 |  |  |
| MP8 | BUTTON, DARK GREY | 509257 | 89536 | 509257 | 1 |  |  |
| MP9 | DECAL | 507616 | 89536 | 507616 | 1 |  |  |
| MP10 | SPRING (ALL SWITCHES) | 414516 | 00779 | 62353-3 | 28 |  |  |
| MP11 | CONTACT, FIXED (ALL SWITCHES) | 416875 | 00779 | 62380-4 | 28 |  |  |
| P1 | HEADER, 14-PIN | 519652 | 22526 | 65521-114 | 1 |  |  |
| S10 | SWITCH, SLIDE (W/MP3) | 477984 | 79727 | GS-115 | 1 |  |  |
| W1 | CALCULATOR CABLE | 534099 | 89536 | 534099 | 1 |  |  |
| X | CALCULATOR KEYBOARD PCB | ORDER | NEXT | HIGHER ASSY. |  |  |  |



8860A-1626

Figure 004-3. Control Keyboard PCB Assembly


Figure 004-3. Control Keyboard PCB Assembly (cont)

Table 004-4. Memory Cartridge


A WARNING, DO NOT RECHAFGE!
BATTERIES MAY EXPLODE OR LEAK.
B ITEMS ON MEMORY PCB ASSEMBLY.


Figure 004-4. Memory Cartridge

Table 004-5. Calculator/Printer PCB Assembly

| $\begin{aligned} & \text { REF } \\ & \text { DES } \end{aligned}$ | DESCRIPTION | FLUKE STOCK NO. | MFG SPLY COOE | MFG PART NO. | $\begin{aligned} & \text { TOT } \\ & \text { QTY } \end{aligned}$ | $\begin{aligned} & \text { REC } \\ & \text { QTY } \end{aligned}$ | N <br> 0 <br> T <br> E |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | (8) CALCULATOR/PRINTER PCB ASSEMBLY FIGURE 004-5 (8860A-4014T) | 516328 | 89536 | 516328 | REF |  |  |
|  | REAR INTERFACE PCB ASSEMBLY | ORDER | NEXT | HIGHER ASSEMBLY |  |  |  |
| C1 | CAP, TA, $47 \mathrm{UF}+/-20 \%, 20 \mathrm{~V}$ | 348516 | 56289 | A96D476x0020KE4 | 1 |  |  |
| C2 | CAP, TA/DISC, $10 \mathrm{UF}+/-20 \%, 10 \mathrm{~V}$ | 176214 | 56289 | 196D $106 \times 0010 \mathrm{KA} 1$ | 1 |  |  |
| C3 | CAP, TA, $15 \mathrm{UF}, 20 \mathrm{~V}$ | 519686 | 56289 | 196D $156 \times 0020$ KE4 | 1 |  |  |
| C4 | CAP, TA, 68 UF, $6 \mathrm{~V} / 8 \mathrm{~V}$ | 519702 | 56289 | 196D686X0008KE4 | 1 |  |  |
| C5 | CAP, TA, $39 \mathrm{MF}+/-20 \%, 6 \mathrm{~V}$ | 163915 | 56289 | 196D394X0020KA1 | 1 |  |  |
| C6 | CAP, TA/DISC, 4.7 UF +/-20\%, 20V | 161943 | 56289 | 196D476X0020KA1 | 1 |  |  |
| C7 | CAP, CERAM, $0.22 \mathrm{UF}+/-20 \%$, 50 V | 309849 | 72982 | 8131-050-651-022 | 6 |  |  |
| C8 | CAP, CERAM, 0.22 UF +/-20\%, 50 V | 309849 | 72982 | 8131-050-651-022 | REF |  |  |
| C9 | CAP, CERAM, $0.22 \mathrm{UF}+/-20 \%$, 50 V | 309849 | 72982 | 8131-050-651-022 | REF |  |  |
| C10 | CAP, CERAM, $0.22 \mathrm{UF}+/-20 \%, 50 \mathrm{~V}$ | 309849 | 72982 | 8131-050-651-022 | REF |  |  |
| C11 | CAP, CERAM, 0.22 UF +/-20\%, 50V | 309849 | 72982 | 8131-050-651-022 | REF |  |  |
| C12 | CAP, CERAM, $0.22 \mathrm{UF}+/-20 \%$, 50 V | 309849 | 72982 | 8131-050-651-022 | REF |  |  |
| CR1 | DIODE, SIL RECTIFIER, $1 \mathrm{~A}, 100 \mathrm{~V}$ | 343491 | 03877 | 1 N4002 | 2 | 1 |  |
| CR2 | DIODE, SIL RECTIFIER, 1A, 100V | 343491 | 03877 | 1N4002 | REF |  |  |
| CR3 | DIODE, ZENER, $400 \mathrm{MW}, 4.7 \mathrm{~V}$ | 524058 | 14552 | 1N751 | 1 | 1 |  |
| CR4 | DIODE, SI, HI-SPEED SWITCHING | 203323 | 06001 | 1N4448 | 2 | 1 |  |
| CR5 | DIODE, SI, HI-SPEED SWITCHING | 203323 | 06001 | $1 \times 4448$ | REF |  |  |
| J1 | CONN, 50-PIN | 519918 | 52152 | 3426-0000 T | 1 |  |  |
| MP1 | COVER, CONN (TO J1) | 519934 | 89536 | 519934 | 2 |  |  |
| P1 | BOARD CONNECTION |  |  |  |  |  |  |
| P26 | BOARD CONNECTION |  |  |  |  |  |  |
| Q1 | XSTR, SI, PNP | 195974 | 64713 | 2N3906 | 3 | 1 |  |
| Q2 | XSTR, SI, PNP | 195974 | 64713 | 2N3906 | REF |  |  |
| Q3 | XSTR, SI, PNP | 195974 | 64713 | 2N3906 | REF |  |  |
| R1 | RES, DEF. CAR, $27 \mathrm{~K}+/-5 \%, 1 / 4 \mathrm{~W}$ | 441501 | 89536 | 441501 | 1 |  |  |
| R2 | RES, DEP. CAR, $470+/-5 \%, 1 / 4 \mathrm{~W}$ | 343434 | 89536 | 343434 | 1 |  |  |
| R3 | RES, DEP. CAR, $10 \mathrm{~K}+/-5 \%$, 1/4W | 348839 | 89536 | 348839 | 2 |  |  |
| R4 | RES, DEP. CAR, $82+/-5 \%, 1 / 4 \mathrm{~W}$ | 442277 | 89536 | 442277 | 1 |  |  |
| R5 | RES, DEP. CAR, $33 \mathrm{~K}+/-5 \%, 1 / 4 \mathrm{~W}$ | 348888 | 89536 | 348888 | 1 |  |  |
| R6 | RES, DEP. CAR, $2 \mathrm{~K}+/-5 \%, 1 / 4 \mathrm{~W}$ | 441469 | 89536 | 441469 | 1 |  |  |
| R7 | RES, DEP. CAR, $39 \mathrm{~K}+/-5 \%, 1 / 4 \mathrm{~W}$ | 442400 | 89536 | 442400 | 1 |  |  |
| R8 | RES, DEP. CAR, $10 \mathrm{~K}+/-5 \%, 1 / 4 \mathrm{~W}$ | 348839 | 89536 | 348839 | REF |  |  |
| R9 | RES, DEP. CAR, $1.1 \mathrm{~K}+/-5 \%, 1 / 4 \mathrm{~W}$ | 348797 | 89536 | 348797 | 1 |  |  |
| R10 | RES, DEP. CAR, $270+/-5 \%, 1 / 4 \mathrm{~W}$ | 348789 | 89536 | 348789 | 1 |  |  |
| R11 | RES, DEP. CAR, $2.7 \mathrm{~K}+/-5 \%, 1 / 4 \mathrm{~W}$ | 386490 | 89536 | 386490 | 1 |  |  |
| R12 | RES, DEP. CAR, $5.6 \mathrm{~K}+/-5 \%, 1 / 4 \mathrm{~W}$ | 442350 | 89536 | 442350 | 1 |  |  |
| R13 | RES, DEP. CAR, 100K +/-5\%, 1/4W | 348920 | 89536 | 348920 | 1 |  |  |
| U1 | RESISTOR NETWORK, SIP, 3.6K $+/-2 \%, 1 / 8 \mathrm{~W}$ | 478818 | 89536 | 478818 | 1 | 1 |  |
| U2 | IC, $2 \mathrm{~K} \times 8$ bit ram, Programmable timer | 524884 | 34649 | P8155 | 1 | 1 |  |
| U3 | IC, DEMULTIPLEXER | 508473 | 01295 | SN7 4LS156N | 1 | 1 |  |
| U4 | IC, LIN, QUAD COMPARATOR | 387233 | 12040 | LM339N | 1 | 1 |  |
| U5 | MICROCOMPUTER, PROCESSOR, MOS/LSI | 524066 | 12040 | MM57109 | 1 | 1 |  |
| U6 | RESISTOR NETWORK, 10K | 412924 | 89536 | 412924 | 1 | 1 |  |
| U7 | © IC, C-MOS, HEX NON-INVERT BUFFER | 407759 | 12040 | MM80C97N | 3 | 1 |  |
| U8 | RESISTOR NETWORK, 5.1 X 1 K | 519694 | 89536 | 519694 | 1 | 1 |  |

Table 004-5. Calculator/Printer PCB Assembly (cont)

| $\begin{aligned} & \text { REF } \\ & \text { DES } \end{aligned}$ | DESCRIPTION | FLUKE STOCK NO. | $\begin{aligned} & \hline \text { MFG } \\ & \text { SPLY } \\ & \text { CODE } \end{aligned}$ | MFG PART NO. | $\begin{aligned} & \text { TOT } \\ & \text { QTY } \end{aligned}$ | $\begin{aligned} & \text { REC } \\ & \text { QTY } \end{aligned}$ | N O T E |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| U9 | RESISTOR NETWORK, 10K | 500876 | 89536 | 500876 | 1 | 1 |  |
| U10 | IC, DIGITAL 2KX8 BIT ROM | 524876 | 34649 | P8355 | 1 | 1 |  |
| U11 | IC, 3-STATE BUFFER | 454819 | 07263 | 4009PC | 1 | 1 |  |
| U12 | (1C, C-MOS, HEX NON-INVERT BUFFER | 407759 | 12040 | MM80C97N | REF |  |  |
| U13 | © IC, C-MOS, HEX NON-INVERT BUFFER | 407759 | 12040 | MM80C97N | REF |  |  |
| U14 | OIC, C-MOS, QUAD 2-IN \& GATE | 408401 | 02735 | CD4081EE | 1 | 1 |  |
| U15 | IC, QUAD 2-IN POS-OR GATE | 393108 | 01295 | SN74LS32N | 1 | 1 |  |
| U16 | IC, TTL MSI, DECADE COUNTER | 402545 | 01295 | SN74LS90N | 1 | 1 |  |
| U17 | IC, TTL MSI, QUAD 2-IN POS-NAND GATE | 393033 | 07263 | 74 LSOOPC | 1 | 1 |  |
| U18 | IC, TTL, OCTAL "D ${ }^{\text {TTYPE }} \mathrm{F} / \mathrm{F}$ | 504514 | 01295 | SN7 4LS373 | 1 | 1 |  |
| W1 | CABLE, 50-Strand flat | 404822 | 89536 | 404822 | 1 |  |  |
| XU2 | SOCKET, 40-PIN | 429282 | 09922 | DILB40P-108 | 2 |  |  |
| XU5 | SOCKET, 7-PIN | 520809 | 30035 | SS-109-1-07 | 4 |  |  |
| XU10 | SOCKET, 40-PIN | 429282 | 09922 | DILB40P-108 | REF |  |  |
| XU18 | SOCKET, 12-PIN | 417733 | 30035 | SS-109-1-12 | 2 |  |  |
| XU19 | SOCKET, 12-PIN | 417733 | 30035 | SS-109-1-12 | REF |  |  |



8860A-1614


Table 004-6. Rear Interface PCB Assembly

| $\begin{aligned} & \text { REF } \\ & \text { DES } \end{aligned}$ | DESCRIPTION | $\begin{gathered} \text { FLUKE } \\ \text { STOCK } \\ \text { NO. } \end{gathered}$ | MFG SPLY CODE | MFG PART NO. | $\begin{aligned} & \text { TOT } \\ & \text { QTY } \end{aligned}$ | $\begin{aligned} & \text { REC } \\ & \text { QTY } \end{aligned}$ | N <br> 0 <br> $T$ <br> E |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | REAR INTERFACE PCB ASSEMBLY FIGURE 004-6 (8860A-4024) | ORDER | NEXT | HIGHER ASSEMBLY | REF |  |  |
| J1 | CONNECTOR, 50-POSITION | 519918 | 52152 | $3426-0000 \mathrm{~T}$ | 1 |  |  |
| J2 | CONNECTOR, 24-POSITION | 519397 | 01295 | H421121-18 | 1 |  |  |
| J3 | CONNECTOR, 36-POSITION | 479261 | 00779 | 552235-1 | 1 |  |  |
| J4 | CONNECTOR, 14-POSITION | 512392 | 00779 | 552212-1 | 1 |  |  |
| U1 | IC, RES. NETWORK, 56K +/-2\%, 1/8W | 529131 | 89536 | 529131 | 1 | 1 |  |



Figure 004-6. Rear Interface PCB Assembly

# Option -005 <br> IEEE-488 Interface 

## 005-1. THEORY OF OPERATION

005-2. The IEEE-488 Interface, Option -005, consists of two circuit boards: the IEEE-488 Interface PCB (Schematic 8860A-1015) and the Rear Interconnect PCB (Schematic 8860A-1025). These boards are connected with a ribbon cable. The IEEE connector and the six IEEE address switches are located on the Rear Interconnect PCB. The schematic diagram for each of the two circuit boards is located in Section 8.

005-3. A simplified schematic of the lEEE-488 Interface is shown in Figure 005-1. The IEEE-488 Bus is located at the left, the 8860 A basic instrument is at the right.

## 005-4. Local/Remote Switching

005-5. When the IEEE-488 Interface is installed, the option program memory (U4) is in control for both local and remote operation. Control can be passed to the local program memory ( U 9 on the Controller PCB ), but is always returned to the option program memory. For example, the option program memory calls on the local program memory to perform the measurement routine. When the measurement cycle is finished and the result is obtained, the option program memory again becomes active.

## 005-6. General Purpose Interface Adapter

005-7. The main device on the IEEE-488 Interface PCB is U1, the general purpose interface adapter (GPIA). This device is designed specifically to interface 8 -bit microprocessor data and address buses to the lEEE- 488 bus. The GPIA handles the bus protocol functions, including the bus handshake. The GPIA communicates with the bus through two bidirectional bus transceivers ( U 2 and U 5 ).
005-8. The GPIA contains the serial poll register where the present 8860 A measurement status is stored. When a serial poll occurs, the contents of this register are loaded directly onto the IEEE-488 bus.

## 005-9. Data Bus and Address Bus

005-10. The internal 8-bit data bus, DB0 through DB7,
carries information between the devices (GPIA, ROM, RAM) and the out-guard microprocessor. The 8-bit address used by each of these devices is latched by UIO. Gates U6 and U8 are used to enable devices (UI, U3 and/or U4) to read or write on the internal bus.

005-11. The rear panel IEEE address switches and the Talk-Only switches connect to the data bus through a hex inverter (U11). The tri-state outputs are enabled by a line from UI. The switch output is read at regular intervals.

## 005-12. Option Program Memory

$005-13$. The program memory is contained in U4. Figure 3-2 in Section 3 of this manual shows how the ROM is partitioned and how it is accessed from ports P23, P26, and P50. This ROM (U4) is a custom device that is maskprogrammed with the IEEE-488 Interface software.

## 005-14. DATA STORAGE RAM

005-15. A 128 -byte RAM (U3) is used for storing l/O data that appears on the data bus. It contains the input buffer for handling input commands, the output buffer for handling output data, and locations for other data storage.

## 005-16. TROUBLESHOOTING THE IEEE-488 OPTION

$005-17$. The following troubleshooting procedure requires that the basic 8860A is working properly. Before starting the procedure, remove the IEEE-488 Interface from its slot in the 8860 A , and check the operation of the basic DMM. If the 8860 A is operating properly, reinstall the option pcb and proceed with the troubleshooting information given in Table 005-1.

005-18. The troubleshooting table is a series of symptoms and solutions. Check the unit for the symptoms in sequence. When a symptom is identified, clear the fault using the solutions listed for that fault. All devices mentioned in the table are located on the IEEE-488 Interface PCB.


Table 005-1. IEEE-488 Interface Troubleshooting

| SYMPTOM | INSTRUCTIONS |
| :---: | :---: |
| 1. Any fault-(initial check) | - Check ALE at U10-11 for 400 kHz . <br> - Check for a high state $(+5 \mathrm{~V})$ at U1-19 to ensure that reset is released. <br> - Check for a high state ( +5 V ) at U1-4 ( $\overline{\mathrm{ASE}}$, address switch enable). |
| 2. The 8860A does not respond to front panel local controls (or IEEE-488 bus commands) when the -005 Option is installed. | - Suspect, U1, U3, U4, U6, U8, or U11. Remove these devices one at a time, until the 8860A returns to proper operation. These devices are socketed (except U11) and all sit on the internal bus. |
| 3. The 8860A operates properly from the front panel (with the -005 Option installed), but will not respond to IEEE-488 but commands. | - Suspect U1, U3, U2, U5, U6, U8 (in that order). |
| 4. The displayed IEEE address (using PROG SEL) is different than that selected at the rear panel IEEE switches. | - Suspect faulty IEEE address switches or U11. |
| 5. Faulty Address Latch (U10) | - Check U10 with a dual-trace scope. Trigger the scope on ALE and look at the input and output of each bit. If ALE and the latch are working properly, then the output follows the input value when ALE is high and latches when ALE goes low. |

## CAUTION

To avoid instrument damage, remove power from the 8860A before unplugging the circuit board or removing plug-in devices.

## 005-19. LIST OF REPLACEABLE PARTS

005-20. A list of replaceable parts for the IEEE-488

Interface is given in Table 005-2. Refer to Section 5 of this manual for ordering information.

## CAUTION 0

Indicated devices are subject to damage by static discharge.

Table 005-2. IEEE-488 Interface

| $\begin{aligned} & \text { REF } \\ & \text { DES } \end{aligned}$ | DESCRIPTION | $\begin{aligned} & \text { FLUKE } \\ & \text { STOCK } \\ & \text { NO. } \end{aligned}$ | MFG <br> SPLY <br> CODE | MFG PART NO. | $\begin{aligned} & \text { TOT } \\ & \text { QTY } \end{aligned}$ | $\begin{array}{\|l\|} \hline \text { REC } \\ \text { QTY } \end{array}$ | N O T E |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| $-005$ | IEEE-488 INTERFACE <br> FIGURE 005-2 ( $8860 \mathrm{~A}-005$ ) | ORDER | BY | OPTION -005 |  |  |  |
|  | IEEE-488 INTERFACE PCB ASSEMBLY | 516310 | 89536 | 516310 | 1 |  |  |
|  | REAR INTERCONNECT PCB ASSEMBLY | 521294 | 89536 | 521294 | 1 |  |  |
| H1 | HARDWARE KIT | 543736 | 89536 | 543736 | 1 |  |  |
| MP1 | PANEL, (SUB) IEEE INTERFACE | 531020 | 89536 | 531020 | 1 |  |  |



Table 005-3. IEEE-488 Interface PCB Assembly

| $\begin{aligned} & \text { REF } \\ & \text { DES } \end{aligned}$ | DESCRIPTION | FLUKE STOCK No. | MFG SPLY CODE | MFG PART NO. | $\begin{aligned} & \text { TOT } \\ & \text { QTY } \end{aligned}$ | REC QTY | N O T E |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | (8) IEEE-488 INTERFACE PCB ASSEMBLY FIGURE 005-3 ( $8860 \mathrm{~A}-4015 \mathrm{~T}$ ) | 516310 | 89536 | 516310 | REF |  |  |
| C1 | CAP, TA, $1 \mathrm{UF}+/-20 \%, 35 \mathrm{~V}$ | 161919 | 56289 | 196D105X0020JA1 | 6 |  |  |
| C2 | CAP, TA, $1 \mathrm{UF}+/-20 \%$, 35V | 161919 | 56289 | 196D 105X0020JA1 | REF |  |  |
| C3 | CAP, TA, $1 \mathrm{UF}+/-20 \%, 35 \mathrm{~V}$ | 161919 | 56289 | 196D 105×0020JA1 | REF |  |  |
| C4 | CAP, TA, $1 \mathrm{UF}+/-20 \%, 35 \mathrm{~V}$ | 161919 | 56289 | 196D105X0020JA1 | REF |  |  |
| C5 | CAP, TA, $1 \mathrm{UF}+/-20 \%, 35 \mathrm{~V}$ | 161919 | 56289 | 196D 105X0020JA1 | REF |  |  |
| C6 | CAP, TA, 1 UF +/-20\%, 35V | 161919 | 56289 | 196D105X0020JA1 | REF |  |  |
| J1 | CONNECTOR BODY | 295337 | 52152 | 3402-0000 T | 1 |  |  |
| MP 1 | COVER, CONNECTOR (TO J1) | 295329 | 52152 | 3402-0001T | 2 |  |  |
| MP2 | MYLAR INSULATOR (NOT SHOWN) | 443903 | 89536 | 443903 | 1 |  |  |
| R1 | RES, DEP. CAR, $2.7 \mathrm{~K}+/-5 \%, 1 / 4 \mathrm{~W}$ | 386490 | 80031 | CR251-4-5P2K7T | 1 |  |  |
| U1 | (8) IC, MOS, N-CHANNEL, SI | 477794 | 04713 | MC68488P | 1 | 1 |  |
| U2 | IC, BUS TRANSCIEVER, DIGITAL | 524835 | 04713 | MC3447P | 2 | 1 |  |
| U3 | (8IC, MOS RAM, $128 \times 8$ BIT | 524843 | 07263 | F6810PC | 1 | 1 |  |
| U4 | IC, DIGITAL, 4K X 8 BIT, MOS ROM | 535070 | 55576 | SYP233 | 1 | 1 |  |
| U5 | IC, BUS TRANSCIEVER, DIGITAL | 524835 | 04713 | MC3447P | REF |  |  |
| U6 | IC, POS NOR, TOTEM POLE OUTPUTS | 393041 | 01295 | SN74LSO2N | 1 | 1 |  |
| U8 | IC, TTL, QUAD, 2-INPUT, POS, NAND GATE | 393033 | 01295 | SN74SLOON | 1 | 1 |  |
| U9 | RES. NETWORK, SIP, $33 \mathrm{~K}+/-2 \%, 1 / 8 \mathrm{~W}$ | 484741 | 89536 | 484741 | 1 | 1 |  |
| 010 | IC, TTL, OCTAL MD" TYPE F/F | 504514 | 01295 | SN74LS373 | 1 | 1 |  |
| U11 | $\otimes$ IC, C-MOS, 3-STATE, INVERTER BUFFER | 454819 | 07263 | 40098PC | 1 | 1 |  |
| U12 | RES. NETWORK, SIP, 4.7K +/-2\%, 1/8W | 412916 | 89536 | 412916 | 1 | 1 |  |
| W1 | CABLE, 34 STRAND | 519926 | 89536 | 519926 |  |  |  |
| XU1 | SOCKET, IC, 40 PIN | 429282 | 09922 | DILB40P-108 | 1 |  |  |
| XU3 | SOCKET, IC, 24 PIN | 376236 | 91506 | 324-AG39D | 2 |  |  |
| XU4 | SOCKET, IC, 24 PIN | 376236 | 91506 | 324-AG39D | REF |  |  |



Table 005-4. Rear Interconnect PCB Assembly

| REF <br> DES | DESCRIPTION | FLUKE <br> STOCK <br> NO. | MFG <br> SPLY <br> CODE | MFG PART NO. | TOT | REC <br> QTY | N <br> QTY |
| :--- | :---: | :---: | :---: | :---: | :---: | :---: | :---: |



## Option -006 Rear Input

## 006-1. THEORY OF OPERATION

006-2. The Rear Input, Option -006, consists of a circuit board and a 20 -pin connector. The circuit board mounts on the A/D and Ohms PCB. A schematic diagram for the option is shown in Figure 006-1.
006-3. The Rear Input option electrically relocates the five INPUT terminal connections from the front panel banana jacks to a 20 -pin connector mounted to the rear panel. This enables all voltage and resistance measurement connections (both two- and four-terminal) to be made at the rear panel.

## 006-4. TROUBLESHOOTING

006-5. Any fault which occurs in the Rear Input connector will usually consist of either poorly soldered connections or broken wires, which can be traced visually or with an ohmmeter. The two ceramic capacitors ensure stable readings by suppressing high voltage ac crosstalk to the A/D Converter.
006-6. LIST OF REPLACEABLE PARTS
006-7. A list of replaceable parts for the Rear Input Assembly is given in Table 006-1. Refer to Section 5 of this manual for ordering information.


Figure 006-1. Rear Input Option Schematic

Table 006-1. Rear Input

| $\begin{aligned} & \text { REF } \\ & \text { DES } \end{aligned}$ | DESCRIPTION | FLUKE STOCK NO. | MFG <br> SPLY <br> CODE | MFG PART NO. | $\begin{aligned} & \text { TOT } \\ & \text { QTY } \end{aligned}$ | $\begin{aligned} & \text { REC } \\ & \text { QTY } \end{aligned}$ | $N$ 0 $T$ E |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| -006 | REAR INPUT <br> FIGURE 006-2 (8860A-006) | ORDER | BY | OPTION -006 |  |  |  |
|  | REAR INPUT PCB ASSEMBLY | 538264 | 89536 | 538264 | 1 |  |  |
| H1 | NUT, HEX 4-40 | 147611 | 89536 | 147611 | 3 |  |  |
| H2 | SCREW, 4-40 X 1/4 PHP | 256156 | 89536 | 256156 | 2 |  |  |
| H3 | SCREW, 4-40 X 3/16 PHP | 149567 | 89536 | 149567 | 2 |  |  |
| H4 | SCREW, 6-32 X 1/4 FH UC | 320093 | 89536 | 320093 | 2 |  |  |
| KIT | HARDWARE CONNECTOR KIT | 541797 | 89536 | 541797 | 1 |  |  |
| MP1 | BRACKET, ANGLE 4-40 | 474239 | 89536 | 474239 | 2 |  |  |



Figure 006-2. Rear Input

Table 006-2. Rear Input PCB Assembly

| $\begin{aligned} & \text { REF } \\ & \text { DES } \end{aligned}$ | DESCRIPTION | FLUKE stock NO. | $\begin{aligned} & \hline \text { MFG } \\ & \text { SPLY } \\ & \text { CODE } \end{aligned}$ | MFG PART NO. | $\begin{aligned} & \text { TOT } \\ & \text { QTY } \end{aligned}$ | $\begin{aligned} & \text { REC } \\ & \text { QTY } \end{aligned}$ | N <br> 0 <br> $\mathbf{O}$ <br>  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | REAR INPUT PCB ASSEMBLY FIGURE 006-3 (8860A-4027) | 538264 | 89536 | 538264 | REF |  |  |
| C1 | CAP, CER, $68 \mathrm{PF}+/-2 \%, 100 \mathrm{~V}$ | 519181 | 71590 | DD-3R3 | 2 |  |  |
| C2 | CAP, CER, $68 \mathrm{PF}+/-2 \%, 100 \mathrm{~V}$ | 519181 | 71590 | DD-3R3 | REF |  |  |
| H1 | NUT, HEX, 2-56 | 355453 | 73734 | 67023 | 2 |  |  |
| H2 | SCREW, 2-56 x 3/4 | 530246 | 89536 | 530246 | 2 |  |  |
| J1 | CONNECTOR 20-PIN RECEPT. | 369249 | 91662 | 00-8016-020-000-707 | 1 |  |  |
| MP1 | Cable tie | 172080 | 06383 | SST-1M | 2 |  |  |
| MP2 | RECEPTACLE PIN | 529263 | 00779 | 350491-1 | 7 |  |  |
| MP3 | MOUNTING BLOCK | 516765 | 89536 | 516765 | 1 |  |  |
| W1 | CHASSIS GROUND WIRE ASSY. | 537795 | 89536 | 537795 | 1 |  |  |
| W2 | WIRE ASSEMBLY - SINGLE COND. | 537738 | 89536 | 537738 | 1 |  |  |
| W3 | CABLE ASSY. 4-COND | 537712 | 89536 | 537712 | 1 |  |  |
| W4 | GRAY WIRE ASSY. | 537753 | 89536 | 537753 | 1 |  |  |
| W5 | VIOLET WIRE ASSY. | 537704 | 89536 | 537704 | 1 |  |  |
| W6 | ORANGE WIRE ASSEMBLY | 537720 | 89536 | 537720 | 1 |  |  |
| W7 | BLUE WIRE ASSEMBLY | 537746 | 89536 | 537746 | 1 |  |  |



## Option -007 External Reference

## 007-1. THEORY OF OPERATION

007-2. The External Reference, Option -007, consists of a single circuit board and a dual banana connector. The circuit board mounts on the A/D and Ohms PCB. The schematic (8860A-1016) is located in Section 8.
007-3. The External Reference is a conditioning circuit which divides an externally applied de voltage by 10 and changes the polarity of the result. If, for example, $a+10 \mathrm{~V}$ dc signal is applied at the input, a-1V dc signal appears at the output, Pl-2. The circuit contains a two-pole active Butterworth low-pass filter to give 40 dB of noise rejection at 50 Hz .
007-4. The input buffer amplifier U 2 is connected with a gain of one-half in a differential-input configuration. The floating input allows the option to receive a voltage which is not ground-referenced. The output of U2 is filtered by U3, which in turn is divided by five. This is the reference voltage sent on to the A/D Converter. Precision resistor network Ul contains all of the required voltage divider networks.
007-5. Protection devices Q1 and Q2 protect against overvoltages appearing at the external reference input terminals. Variable resistor R 1 helps correct for the dc offset voltages of U2 and U3. Variable resistors R4 and R5 are calibration adjustments.

007-6. When selected, the output of the external reference replaces the internal reference used to discharge the A/Dintegrator. The external reference polarity is detected at pin P1-5 by the in-guard microprocessor which reverses the polarity (at the $\mathrm{A} / \mathrm{D}$ Converter) if necessary, in order to discharge the capacitor. Thus, the polarity is selected to be
opposite that of the applied input. Such a reversal is necessary, for instance, when the 8860 A is measuring an ac voltage with a negative external reference.

007-7. Pins Pl-6 and P1-7 form a shorting link to tell the in-guard microprocessor that the external reference is installed. If the option is not installed, an error message is displayed when external reference (EXT REF) is selected at the front panel.

## 007-8. TROUBLESHOOTING

007-9. Troubleshooting the External Reference for a failed IC is a matter of tracing the signal path. Use the A/D and Ohms Extender Card for easy circuit access.
007-10. Connect the External Reference input LO to the front panel INPUT LO. Apply $\mathrm{a}+10 \mathrm{v}$ de signal at the external reference input HI. The following signals should be present on the External Reference PCB.

1. -5 V dc at U2-6 and U3-6
2. -1V dc at the output, Pl-2.
$007-11$. When a step input is applied to the External Reference, the settling time of the External Reference circuitry should not exceed 5 seconds. If either C3 or C4 is defective, the response of the external reference may be very slow.

## 007-12. LIST OF REPLACEABLE PARTS

007-13. A list of replaceable parts for the External Reference is given in Table 007-1. Refer to Section 5 of this manual for ordering information.

[^2]Table 007-1. External Reference

| $\begin{aligned} & \text { REF } \\ & \text { DES } \end{aligned}$ | DESCRIPTION | $\begin{aligned} & \text { FLUKE } \\ & \text { STOCK } \\ & \text { NO. } \end{aligned}$ | MFG SPLY CODE | MFG PART NO. | $\begin{aligned} & \text { TOT } \\ & \text { QTY } \end{aligned}$ | REC QTY |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| -007 | EXTERNAL REFERENCE FIGURE $007-1(8860 \mathrm{~A}-4016 \mathrm{~T})$ | ORDER | BY | OPTION -007 |  |  |  |
| C1 | CAP, CERAM, $33 \mathrm{PF}+/-2 \%, 100 \mathrm{~V}$ | 354852 | 80031 | 2222-638-10339 | 2 |  |  |
| C2 | CAP, CERAM, $33 \mathrm{PF}+/-2 \%$, 100V | 354852 | 80031 | 2222-638-10339 | REF |  |  |
| C3 | CAP, MYLAR, . $22 \mathrm{UF}+/-10 \%, 100 \mathrm{~V}$ | 436113 | 73445 | C280MAH/A220K | 2 |  |  |
| C4 | CAP, MYLAR, $22 \mathrm{UF}+/-10 \%$, 100 V | 436113 | 73445 | C280MAH/A220K | REF |  |  |
| H1 | SCREW, FH, UC, 6-321/4 | 320093 | 89536 | 320093 | 2 |  |  |
| H2 | SCREW, FHP/SS, 6-32 X 3/4 | 114504 | 89536 | 114504 | 1 |  |  |
| MP1 | SPACER, CENTER | 352021 | 89536 | 352021 | 1 |  |  |
| MP2 | MOUNTING BLOCK | 530980 | 89536 | 530980 | 1 |  |  |
| P1 | CONNECTOR, 9-POSITION | 519744 | 89536 | 519744 | 1 |  |  |
| Q1 | XSTR, J-FET | 343830 | 12040 | NSSF50024 | 2 | 1 |  |
| Q2 | XSTR, J-FET | 343830 | 12040 | NSSF50024 | REF |  |  |
| R1 | RES, VAR, $50 \mathrm{~K}+/-10 \%, 1 / 2 \mathrm{~W}$ | 288290 | 75378 | 360S-502AZ | 1 |  |  |
| R2 | RES, MTL. FILM, $150 \mathrm{~K}+/-1 \%, 1 / 8 \mathrm{~W}$ | 241083 | 91637 | CMF551503F | 2 |  |  |
| R3 | RES, MTL. FILM, $150 \mathrm{~K}+/-1 \%, 1 / 8 \mathrm{~W}$ | 241083 | 91637 | CMF551503F | REF |  |  |
| R4 | RES, VAR. CERMET, $1 \mathrm{~K}+/-10 \%, 1 / 2 \mathrm{~W}$ | 285155 | 71450 | 360S102A | 2 |  |  |
| R5 | RES, VAR, CER, $1 \mathrm{~K}+/-10 \%, 1 / 2 \mathrm{~W}$ | 285155 | 71420 | 360S102A | REF |  |  |
| R6 | RES, MTL. FILM, $37.4 \mathrm{~K}+/-1 \%, 1 / 8 \mathrm{~W}$ | 226241 | 91637 | CMF553742F | 1 |  |  |
| R7 | RES, DEP. CAR, $1+/-5 \%, 1 / 4 \mathrm{~W}$ | 357665 | 80031 | CR251-4-5P1E | 1 |  |  |
| R8 | RES, MTL. FILM, $301 \mathrm{~K}+/-1 \%, 1 / 8 \mathrm{~W}$ | 289488 | 91637 | CMF55301C2F | 1 |  |  |
| U1 | RESISTOR NETWORK | 510990 | 89536 | 510990 | 1 | 1 |  |
| U2 | IC, LIN, OP-AMP, MTL. CAN | 478107 | 12040 | 308A | 2 | 1 |  |
| U3 | IC, LIN, OP-AMP, MTL. CaN | 478107 | 12040 | 308A | REF |  |  |
| W1 | WIRE ASSEMBLY, VIOLET | 538215 | 89536 | 538215 | 1 |  |  |
| W2 | WIRE ASSEMBLY, GRAY | 538207 | 89536 | 538207 | 1 |  |  |



8860A-007


## Section 7 <br> General Information

7-1. This section of the manual contains generalized user information as well as supplemental information to the List of Replaceable Parts contained in Section 5.

List of Abbreviations and Symbols

| A or amp | ampere | hf | high frequency | $(+)$ or pos | positive |
| :---: | :---: | :---: | :---: | :---: | :---: |
| ac | alternating current | Hz | hertz | pot | potentiometer |
| af | audio frequency | IC | integrated circuit | p-p | peak-to-peak |
| a/d | analog-to-digital | If | intermediate frequency | ppm | parts per million |
| assy | assembly | In | inch(es) | PROM | programmablle read-only |
| AWG | american wire gauge | Intl | internal |  | memory |
| B | bel | 1/0 | input/output | psi | pound-force per square inch |
| bcd | binary coded decimal | k | kilo (103) | RAM | random-access memory |
| ${ }^{\circ} \mathrm{C}$ | Celsius | kHz | kilohertz | t | radio frequency |
| cap | capacitor | k $\Omega$ | kilohm(s) | rms | root mean square |
| ccw | counterclockwise | kV | kilovolt(s) | ROM | read-only memory |
| cer | ceramic | If | low frequency | $s$ or sec | second (time) |
| cermet | ceramic to metal(seal) | LED | light-emitting diode | scope | oscilloscope |
| ckt | circuit | LSB | least significant bit | SH | shield |
| cm | centimeter | LSD | least significant digit | Si | silicon |
| cmrr | common mode rejection ratio | M | mega (10 ${ }^{6}$ ) | serno | serial number |
| comp | composition | m | milli ( $10^{-3}$ ) | sr | shift register |
| cont | continue | mA | milliampere(s) | Ta | tantalum |
| crt | cathode-ray tube | max | maximum | tb | terminal board |
| cw | clockwise | mf | metal film | tc | temperature coefficient or |
| d/a | digital-to-analog | MHz | megahertz |  | temperature compensating |
| dac | digital-to-analog converter | min | minimum | texo | temperature compensated |
| dB | decibel | mm | millimeter |  | crystal oscillator |
| dc | direct current | ms | millisecond | tp | test point |
| dmm | digital multimeter | MSB | most significant bit | u or $\mu$ | micro ( $10^{-6}$ ) |
| dvm | digital voltmeter, | MSD | most significant digit | uhf | ultra high frequency |
| elect | electrolytic | MTBF | mean time between failures | us or $\mu \mathrm{s}$ | microsecond(s) ( $10^{-6}$ ) |
| ext | external | MTTR | mean time to repair | uut | unit under test |
| F | farad | mV | millivolt(s) | V | volt |
| ${ }^{\circ} \mathrm{F}$ | Fahrenheit | mv | multivibrator | $v$ | voltage |
| FET | Field-effect transistor | $\mathrm{M} \Omega$ | megohm(s) | var | variable |
| $f f$ | flip-flop | n | nano (10-9) | vco | voltage controlled oscillator |
| freq | frequency | na | not applicable | vhf | very high frequency |
| FSN | federal stock number | NC | normally closed | vif | very low frequency |
| g | gram | (-) or neg | negative | W | watt(s) |
| G | giga ( $10^{9}$ ) | NO | normally open | ww | wire wound |
| gd | guard | ns | nanosecond | xfmr | transformer |
| Ge | germanium | opnl ampl | operational amplifier | xstr | transistor |
| GHz | gigahertz | p | pico (10 $0^{-12}$ ) | xtal | crystal |
| gmv | guaranteed minimum value | para | paragraph | xtlo | crystal oscillator |
| gnd | ground | pcb | printed circuit board | $\Omega$ | ohm(s) |
| H | henry | pF | picofarad | $\mu$ | micro ( $10^{-6}$ ) |
| hd | heavy duty | pn | part number |  |  |

## Federal Supply Codes for Manufacturers

| 00213 | 02660 | 04946 | 06751 |
| :---: | :---: | :---: | :---: |
| Nytronics Comp. Group Inc. | Bunker Ramo Corp., Conn Div. | Standard Wire \& Cable | Components, Inc. Semcor Div. |
| Subsidiary of Nytronics Inc. | Formerly Amphenol-Borg | Los Angeles, California | Phoenix, Arizona |
| Formerly Sage Electronics | Electric Corp. |  |  |
| Rochester, New York | Broadview, Illinois | 05082 | 06860 |
|  |  | Replaced by 94988 | Gould Automotive Div. |
| 00327 | 02799 |  | City of Industry, California |
| Welwy International, Inc. | Areo Capacitors, Inc. | 05236 |  |
| Westlake, Ohio | Chatsworth, California | Jonathan Mfg. Co. | 06961 |
|  |  | Fullerton, California | Vernitron Corp., Piezo |
| 00656 | 03508 |  | Electric Div. |
| Aerovox Corp. | General Electric Co. | 05245 | Formerly Clevite Corp., Piezo |
| New Bedford, Massachusetts | Semiconductor Products Syracuse, New York | Components Corp. now Corcom, Inc. | Electric Div. Bedford, Ohio |
| 00686 |  | Chicago, Illinois |  |
| Film Capacitors, Inc. | 03614 |  | 06980 |
| Passaic, New Jersey | Replaced by 71400 | 05277 | Eimac Div. |
|  |  | Westinghouse Electric Corp. | Varian Associates |
| 00779 | 03651 | Semiconductor Div. | San Carlos, California |
| AMP Inc. | Replaced by 44655 | Youngwood, Pennsylvania |  |
| Harrisburg, Pennsylvania |  |  | 07047 |
|  | 03797 | 05278 | The Ross Milton Co. |
| 01121 | Eldema Div. | Replaced by 43543 | South Hampton, PennsyIvania |
| Allen-Bradley Co. | Genisco Technology Corp. |  |  |
| Milwaukee, Wisconsin | Compton, California | 05279 | 07115 |
|  |  | Southwest Machine \& | Replaced by 14674 |
| 01281 | 03877 | Plastic Co. |  |
| TRW Electronic Comp. | Transistron Electronic Corp. | Glendora, California | 07138 |
| Semiconductor Operations | Wakefield, Massachusetts |  | Westinghouse Electric Corp., |
| Lawndale, California |  | 05397 | Electronic Tube Div. |
|  | 03888 | Union Carbide Corp. | Horsehead, New York |
| 01295 | KDI Pyrofilm Corp. | Materials Systems Div. |  |
| Texas Instruments, Inc. | Whippany, New Jersey | New York, New York | 07233 |
| Semiconductor Group |  |  | TRW Electronic Components |
| Dallas. Texas | 03911 | 05571 | Cinch Graphic |
|  | Clairex Electronics Div. | Use 56289 | City of Industry, California |
| 01537 | Clairex Corp. | Sprague Electric Co. |  |
| Motorola Communications \& | Mt. Vernon, New York | Pacific Div. | 07256 |
| Electronics Inc. |  | Los Angeles, California | Silicon Transistor Corp. |
| Franklin Park, Illinois | 03980 |  | Div. of BBF Group Inc. |
|  | Muirhead Inc. | 05574 | Chelmsford, Massachusetts |
| 01686 | Mountainside, New Jersey | Viking Industries |  |
| RCL Electronics Inc. |  | Chatsworth, California | 07261 |
| Manchester, New Hampshire | 04009 |  | Aumet Corp. |
|  | Arrow Hart Inc. | 05704 | Culver City, California |
| 01730 | Hartford, Connecticut | Replaced by 16258 |  |
| Replaced by 73586 |  |  | 07263 |
|  | 04062 | 05820 | Fairchild Semiconductor |
| 01884 | Replaced by 72136 | Wakefield Engineering Inc. | Div. of Fairchild Camera |
| Use 56289 |  | Wakefield, Massachusetts | \& Instrument Corp. |
| Sprague Electric Co. | 04202 |  | Mountain View, California |
| Dearborn Electronic Div. | Replaced by 81312 | 06001 |  |
| Lockwood, Florida |  | General Electric Co. | 07344 |
|  | 04217 | Electronic Capacitor \& | Bircher Co., Inc. |
| 02114 | Essex International Inc. | Battery Products Dept. | Rochester, New York |
| Ferroxcube Corp. | Wire \& Cable Div. | Columbia, South Carolina |  |
| Saugerties, New York | Anaheim, California |  | 07597 |
|  |  | 06136 | Burndy Corp. |
| 02131 | 04221 | Replaced by 63743 | Tape/Cable Div. |
| General Instrument Corp. | Aemco, Div. of |  | Rochester, New York |
| Harris ASW Div. | Midtex inc. | 06383 |  |
| Westwood, Maine | Mankato, Minnesota | Panduit Corp. | 07792 |
|  |  | Tinley Park, Illinois | Lerma Engineering Corp. |
| 02395 | 04222 |  | Northampton, Massachusetts |
| Rason Mfg. Co. | AVX Ceramics Div. | 06473 |  |
| Brooklyn, New York | AVX Corp. | Bunker Ramo Corp. | 07910 |
|  | Myrtle Beach, Florida | Amphenol SAMS Div. | Teledyne Semiconductor |
| 02533 |  | Chatsworth, California | Formerly Continental Device |
| Snelgrove, C.R. Co., Ltd. | 04423 |  | Hawthorne, California |
| Don Mills, Ontario, Canada | Telonic Industries | 06555 |  |
| M3B 1M2 | Laguna Beach, California | Beede Electrical Instrument Co. Penacook, New Hampshire | 07933 <br> Use 49956 |
| 02606 | 04645 |  | Raytheon Co. |
| Fenwal Labs | Replaced by 75376 | 06739 | Semiconductor Div. HQ |
| Div. of Travenal Labs. |  | Electron Corp. | Mountain View, California |
| Morton Grove, Illinois | 04713 | Littleton, Colorado |  |
|  | Motorola Inc. Semiconductor |  | 08225 |
|  | Products | 06743 | Industro Transistor Corp. |
|  | Phoenix, Arizona | Clevite Corp. Cleveland, Ohio | Long Island City, New York |

## Federal Supply Codes for Manufacturers (cont)

| 08261 | 11726 | 13606 | 16299 |
| :---: | :---: | :---: | :---: |
| Spectra Strip Corp. | Qualidyne Corp. | Use 56289 | Corning Glass |
| Garden Grove, California | Santa Clara, California | Sprague Electric Co. Transistor Div. | Electronic Components Div. Raleigh, North Carolina |
| 08530 | 12014 | Concord, New Hampshire |  |
| Reliance Mica Corp. | Chicago Rivet \& Machine Co. |  | 16332 |
| Brooklyn, New York | Bellwood, Illinois | $\begin{aligned} & 13839 \\ & \text { Replaced by } 23732 \end{aligned}$ | Replaced by 28478 |
| 08806 | 12040 |  | 16473 |
| General Electric Co. | National Semiconductor Corp. | 14099 | Cambridge Scientific Ind. |
| Miniature Lamp Products Dept Cleveland, Ohio | Danburry, Connecticut | Semtech Corp. <br> Newbury Park, California | Div. of Chemed Corporation Cambridge, Maryland |
|  | 12060 |  |  |
| 08863 | Diodes, Inc. | 14140 | 16742 |
| Nylomatic Corp. | Chatsworth, California | Edison Electronic Div. | Paramount Plastics |
| Norrisville, Pennsylvania |  | Mc Gray-Edison Co. | Fabricators, Inc. Downey, Caiifornia |
|  | 12136 | Manchester, New Hampshire | Downey, Caiifornia |
| 08988 | Philadelphia Handle Co. |  |  |
| Use 53085 | Camden, New Jersey | 14193 | 16758 |
| Skottie Electronics Inc. |  | Cal-R-Inc. formerly | Delco Electronics |
| Archbald, Pennsylvania | 12300 | California Resistor, Corp. | Div. of General Motors Corp. |
|  | Potter-Brumfield Div. | Santa Monica, California | Kokomo, Indiana |
| 09214 | AMF Canada LTD. |  |  |
| G.E. Co. Semi-Conductor | Guelph, Ontario, Canada | 14298 | 17001 |
| Products Dept. |  | American Components, Inc. | Replaced by 71468 |
| Power Semi-Conductor | 12323 | an Insilco Co. |  |
| Products OPN Sec. | Presin Co., Inc. | Conshohocken, Pennsylvania | 17069 |
| Auburn, New York | Shelton, Connecticut |  | Circuit Structures Lab. |
|  |  | 14655 | Burbank, California |
| 09353 | 12327 | Cornell-Dublier Electronics |  |
| C and K Components | Freeway Corp. formerly | Division of Federal Pacific | 17338 |
| Watertown, Massachusetts | Freeway Washer \& Stamping Co. Cleveland, Ohio | Electric Co. Govt. Control Dept. Newark, New Jersey | High Pressure Eng. Co., Inc. Oklahoma City, Oklahoma |
| 09423 |  |  |  |
| Scientific Components, Inc. | 12443 | 14752 | 17545 |
| Santa Barbara, California | The Budd Co. Polychem Products Plastic Products Div. | Electro Cube Inc. <br> San Gabriel, California | Atlantic Semiconductors, Inc. Asbury Park, New Jersey |
| 09922 | Bridgeport, Pennsylvania |  |  |
| Burndy Corp. |  | 14869 | 17856 |
| Norwalk, Connecticut | 12615 | Replaced by 96853 | Siliconix, Inc. |
|  | U.S. Terminals Inc. |  | Santa Clara, California |
| 09969 | Cincinnati, Ohio | 14936 |  |
| Dale Electronics Inc. |  | General Instrument Corp. | 17870 |
| Yankton, S. Dakota | 12617 | Semi Conductor Products Group | Replaced by 14140 |
|  | Hamlin Inc. | Hicksville, New York |  |
| 10059 | Lake Mills, Wisconsin |  | 18178 |
| Barker Engineering Corp. |  | 15636 | Vactec Inc. |
| Formerly Amerace, Amerace | 12697 | Elec-Trol Inc. | Maryland Heights, Missouri |
| ESNA Corp. | Clarostat Mfg. Co. | Saugus, California |  |
| Kenilworth, New Jersey | Dover, New Hampshire |  | 18324 |
|  |  | 15801 | Signetics Corp. |
| 11236 | 12749 | Fenwal Electronics Inc. | Sunnyvale, California |
| CTS of Berne | James Electronics | Div. of Kidde Walter and Co., Inc. |  |
| Berne, Indiana | Chicago, Illinois | Framingham, Massachusetts | 18612 |
| 11237 | 12856 | 15818 | Vishay Resistor Products Div. |
| CTS Keene Inc. | Micrometals | Teledyne Semiconductors, | Malvern, Pennsylvania |
| Paso Robles, California | Sierra Madre, California | formerly Amelco Semiconductor |  |
|  |  | Mountain View, California | 18736 |
| 11358 | 12954 |  | Voltronics Corp. |
| CBS Electronic Div. | Dickson Electronics Corp. | 15849 | Hanover, New Jersey |
| Columbia Broadcasting System | Scottsdale, Arizona | Litton Systems Inc. Useco Div. |  |
| Newburyport, Minnesota |  | formerly Useco Inc. | 18927 |
|  | 12969 | Van Nuys, California | GTE Sylvania Inc. |
| 11403 | Unitrode Corp. |  | Precision Material Group |
| Best Products Co. Chicago, Illinois | Watertown, Massachusetts | 15898 <br> International Business | Parts Division Titusville, Pennsylvania |
| Chicago, Illinois | 13103 | International Business Machines Corp. | Titusville, Pennsylvania |
| 11503 | Thermalloy Co., Inc. | Essex Junction, Vermont | 19451 |
| Keystone Columbia Inc. | Dallas, Texas |  | Perine Machinery \& Supply Co. |
| Warren, Michigan |  | 15909 | Seattle, Washington |
|  | 13327 | Replaced by 14140 |  |
| 11532 | Solitron Devices Inc. |  | 19701 |
| Teledyne Relays | Tappan, New York | 16258 | Electro-Midland Corp. |
| Hawthorne, California |  | Space-Lok Inc. | Mepco-Electra Inc. |
|  | 13511 | Burbank, California | Mineral Wells, Texas |
| 11711 | Amphenol Cadre Div. |  |  |
| General Instrument Corp. | Bunker-Ramo Corp. |  | 20584 |
| Rectifier Division | Los Gatos, California |  | Enochs Mfg. Inc. |
| Hicksville, New York |  |  | Indianapolis, Indiana |

Federal Supply Codes for Manufacturers (cont)

| 20891 | 28480 | 43543 | 70903 |
| :---: | :---: | :---: | :---: |
| Self-Organizing Systems, Inc. | Hewlett Packard Co. | Nytronics Inc. | Belden Corp. |
| Dallas, Texas | Corporate HQ Palo Alto, California | Transformer Co. Div. Geneva, New York | Geneva, Illinois |
| 21604 |  |  | 71002 |
| Bucheye Stamping Co. | 28520 | 44655 | Birnback Radio Co., Inc. |
| Columbus, Ohio | Heyman Mfg. Co. Kenilworth, New Jersey | Ohmite Mfg. Co. Skokie, Illinois | Creeport, New York |
| 21845 |  |  | 71400 |
| Solitron Devices Inc. | 29083 | 49671 | Bussmann Mfg. |
| Transistor Division | Monsanto, Co., Inc. | RCA Corp. | Div. of McGraw-Edison Co. |
| Riveria Beach, Florida | Santa Clara, California | New York, New York | Saint Louis, Missouri |
| 22767 | 29604 | 49956 | 71450 |
| ITT Semiconductors | Stackpole Components Co. | Raytheon Company | CTS Corp. |
| Palo Alto, California | Raleigh, North Carolina | Lexington, Massachusetts | Elkhart, Indiana |
| 23050 | 30148 | 50088 | 71468 |
| Product Comp. Corp. | AB Enterprise Inc. | Mostek Corp. | ITT Cannon Electric Inc. |
| Mount Vernon, New York | Ahoskie, North Carolina | Carrollton, Texas | Santa Ana, California |
| 23732 | 30323 | 50579 | 71482 |
| Tracor Inc. | Illinois Tool Works, Inc. | Litronix Inc. | Clare, C.P. \& Co. |
| Rockville, Maryland | Chicago, Illinois | Cupertino, California | Chicago, Illinois |
| 23880 | 31091 | 51605 | 71590 |
| Stanford Applied Engrng. | Optimax Inc. | Scientific Components Inc. | Centrelab Electronics |
| Santa Clara, California | Colmar, Pennsylvania | Linden, New Jersey | Div. of Globe Union Inc. Milwaukee, Wisconsin |
| 23936 | 32539 | 53021 |  |
| Pamotor Div., Wm. J. Purdy Co. | Mura Corp. | Sangamo Electric Co. | 71707 |
| Burlingame, California | Great Neck, New York | Springfield, Illinois | Coto Coil Co., Inc. Providence, Rhode Island |
| 24248 | 32767 | 54294 |  |
| Replaced by 94222 | Griffith Plastic Corp. | Cutler-Hammer Inc. formerly | 71744 |
|  | Burlingame, California | Shallcross, A Cutter-Hammer Co. | Chicago Miniature Lamp Works |
| 24355 |  | Selma, North Carolina | Chicago, Illinois |
| Analog Devices Inc. | 32879 |  |  |
| Norwood, Massachusetts | Advanced Mechanical | 55026 | 71785 |
|  | Components | Simpson Electric Co. | TRW Electronics Components |
| 24655 | Northridge, California | Div. of Am. Gage and Mach. Co. | Cinch Connector Operations Div. |
| General Radio |  | Elgin, Illinois | Elk Grove Village |
| Concord, Massachusetts | 32897 ( |  | Chicago, Illinois |
|  | Erie Technological Products, Inc. | 56289 |  |
| 24759 | Frequency Control Div. | Sprague Electric Co. | 72005 |
| Lenox-Fugle Electronics Inc. South Plainfield, New Jersey | Carlisle, Pennsylvania | North Adams, Massachusetts | Wilber B. Driver Co. Newark, New Jersey |
|  | 32997 | 58474 |  |
| 25088 | Bourns Inc. | Superior Electric Co. | 72092 |
| Siemen Corp. <br> Isilen, New Jersey | Trimpot Products Division | Bristol, Connecticut | Replaced by 06980 |
|  | Riverside, California |  |  |
|  |  | 60399 | 72136 |
| 25403 | 33173 | Torin Corp. formerly | Electro Motive Mfg. Co. |
| Amperex Electronic Corp. | General Electric Co. | Torrington Mfg. Co. | Williamantic, Connecticut |
| Semiconductor \& | Products Dept. | Torrington, Connecticut |  |
| Micro-Circuits Div. | Owensboro, Kentucky |  | 72259 |
| Slatersville, Rhode Island |  | $63743$ | Nytronics Inc. |
|  | 34333 | Ward Leonard Electric Co., Inc. | Pelham Manor, New Jersey |
| 27014 | Silicon General | Mount Vernon, New York |  |
| National Semiconductor Corp. | Westminister, California |  | 72619 |
| Santa Clara, California |  | 64834 | Dialight Div. |
|  | 34335 | West Mfg. Co. | Amperex Electronic Corp. |
| 27264 | Advanced Micro Devices | San Francisco, California | Brooklyn, New York |
| Molex Products | Sunnyvale, California |  |  |
| Downers Grove, Illinois |  | 65092 | 72653 |
|  | 34802 | Weston Instruments Inc. | G.C. Electronics |
| 28213 | Electromotive Inc. | Newark, New Jersey | Div. of Hydrometals, Inc. |
| Minnesota Mining \& Mfg. Co. | Kenilworth, New Jersey |  | Brooklyn, New York |
| Consumer Products Div. |  | 66150 |  |
| St. Paul, Minnesota | 37942 | Winslow Tele-Tronics Inc. | 72665 |
|  | P.R. Mallory \& Co., Inc. | Eaton Town, New Jersey | Replaced by 90303 |
| 28425 | Indianapolis, Indiana | 70485 | 72794 |
| Serv-/-Link formerly |  | Atlantic India Rubber Works | Dzus Fastener Co., Inc. |
| Bohannan Industries | 42498 | Chicago, Illinois | West Islip, New York |
| Fort Worth, Texas | National Radio <br> Melrose, Massachusetts | 70563 | 72928 |
| 28478 |  | Amperite Company | Gulton Ind. Inc. |
| Deltrol Controls Div. |  | Union City, New Jersey | Gudeman Div. |
| Deltrol Corporation |  |  | Chicago, llinois |

Federal Supply Codes for Manufacturers (cont)


## Federal Supply Codes for Manufacturers (cont)



## Appendix 7A <br> Manual Change Information

## INTRODUCTION

This appendix contains information necessary to backdate the manual to conform with earlier pcb configurations. To identify the configuration of the pcb's used in your instrument, refer to the revision letter (marked in ink) on the component side of each pcb assembly. Table 7A-1 defines the assembly revision levels documented in this manual.

## NEWER INSTRUMENTS

As changes and improvements are made to the instrument, they are identified by incrementing the revision letter marked on the affected pcb assembly. These changes are documented on a supplemental change/errata sheet which, when applicable, is inserted at the front of the manual.

## OLDER INSTRUMENTS

To backdate this manual to conform with earlier assembly revision levels, perform the changes indicated in Table 7A-1.

## CHANGES

The following design changes, unless otherwise noted, affect only Section 5 and Section 8 of this manual:

- Section 5, parts list and component location drawings
- Section 8 , schematics and component location drawings

The material affected within these sections is easily determined by the type of change. See Table 7A-2.

Table 7A-1. Manual Status and Backdating Information


[^3]Table 7A-2. Material Affected By a Change

| TYPE OF CHANGE | MATERIAL AFFECTED $=\bullet$ |  |  |
| :---: | :---: | :---: | :---: |
|  | Parts List | Schematic | Component <br> Location |
|  | $\bullet$ | $\bullet$ |  |
| Part Number | $\bullet$ |  |  |
| Hardware | $\bullet$ |  | $\bullet$ |
| Size/Location <br> (physical) |  |  |  |
| Addition/Deletion <br> (electrical) | $\bullet$ |  |  |

Change \#1 13321
A/D and Ohms Converters PCB Assembly
Change R5
FROM: Res, dep car, 10k $\pm 5 \%$, $1 / 4 \mathrm{~W} / 348839 / 89536 / 348839$
TO: $\quad$ Res, dep car, $100 \mathrm{k} \pm 5 \%, 1 / 4 \mathrm{~W} / 348920 / 89536 / 348920$
Change \#2 13322
AC/DC Scaling PCB Assembly
Change C35 and C36
FROM: Cap, cer, $15 \mathrm{pF} \pm 2 \%$, $100 \mathrm{~V} / 369074 / 89536 / 369074$
TO: Cap, cer, $12 \mathrm{pF} \pm 2 \%, 100 \mathrm{~V} / 376871 / 89536 / 376871$
Change R37
FROM: Res, dep car, $200 \pm 5 \%, 1 / 4 \mathrm{~W} / 441451 / 80031 / 441451$
TO: $\quad$ Res, dep car, $2 \mathrm{k} \pm 5 \%$, $1 / 4 \mathrm{~W} / 441469 / 80031 / 441469$
Change \#3 13636
AC/DC Scaling PCB Assembly
Change R30
FROM: Res, mf, $511 \mathrm{k} \pm 1 \%, 1 / 8 \mathrm{~W} / 292868 / 89536 / 292868$
TO: Res, mf, $2 \mathrm{k} \pm 1 \%, 1 / 8 \mathrm{~W} / 235226 / 89536 / 235226$
Change R28
FROM: Res, mf, $3.83 \mathrm{k} \pm 1 \%$, $1 / 8 \mathrm{~W}, 235143,89536 / 235143$
TO: Res. mf, 1.19k $\pm 1 \%, 1 / 8 \mathrm{~W}, 349126 / 89536 / 349126$
Change R29
FROM: Res, var, $1 \mathrm{k} \pm 10 \%, 1 / 2 \mathrm{~W} / 285155 / 89536 / 285155$
TO: Res, var, $500 \pm 10 \%, 1 / 2 \mathrm{~W} / 291120 / 89536 / 291120$
Change C17
FROM: Cap, cer, $33 \mathrm{pF} \pm 2 \%$. 100V, 354852/89536/354852
TO: Cap, cer, $22 \mathrm{pF} \pm 5 \%, 100 \mathrm{~V}, 448449 / 89536 / 448449$
Change the part number of Q19
FROM: 386730/89536/386730
TO: 261578/89536/261578
Change R37
FROM: Res, dep car, $100 \pm 5 \%$, 1/4W/348771/89536/348771
TO: Res, dep car, $200 \pm 5 \%$, 1/4W/441451/89536/441451
Change R5
FROM: Res, dep car. 22k $\pm 5 \%$, 1/4W, $348870 / 89536 / 348870$ TO: Res, dep car, 10k $\pm 5 \%, 1 / 4 \mathrm{~W}, 348839 / 89536 / 348839$
Change R2, R3, R4, R33, R35, and R36
FROM: Res, dep car, $47 \mathrm{k} \pm 5 \%, 1 / 4 \mathrm{~W} / 348896 / 89536 / 348896$
TO: $\quad$ Res, dep car, $22 \mathrm{k} \pm 5 \%, 1 / 4 \mathrm{~W} / 348870 / 89536 / 348870$
Change R68
FROM: Res, dep car, $100 \mathrm{k} \pm 5 \%, 1 / 4 \mathrm{~W} / 348920 / 89536 / 348920$
TO: Res, dep car, $91 \mathrm{k} \pm 5 \%$, 1/4W/441709/89536/441709
Delete C43
Cap, cer, $22 \mathrm{pF} \pm 5 \%, 100 \mathrm{~V} / 448449 / 89536 / 448449$
Delete C44
Cap, cer, $0.68 \mathrm{pF}, 458011 / 89536 / 458011$
Delete CR9
Diode, Si, low cap, 375907/89536/375907
Change \#4 13643
Main PCB Assembly
Add QI4
Transistor, JFET/343830/89536/343830

Delete C18
Cap, Ta, 22uF $\pm 20 \%$, 15V/ 423012/89536/423012
Delete CR7
Diode, Si/ 203323/89536/203323
Change schematic to:


Change \#5 13834
A/D and Ohms Converters PCB Assembly
Change Cl
FROM: Cap, cer, . 005 uF $\pm 20 \%, 50 \mathrm{~V} / 175232 / 89536 / 175232$
TO: Cap, cer, $05 \mathrm{uF} \pm 20 \%, 50 \mathrm{~V} / 149161 / 89536 / 149161$
Change R26
FROM: Res, dep car, $8.2 \mathrm{k} \pm 5 \%$, $1 / 4 \mathrm{~W} / 441675 / 89536 / 441675$
TO: $\quad$ Res, dep car, $6.8 \mathrm{k} \pm 5 \%, 1 / 8 \mathrm{~W} / 368761 / 89536 / 368761$
Change \#6 13835
Main PCB Assembly
Change R32, R33, R34, and R35
FROM: Res, dep car, 150k $\pm 5 \%, 1 / 4 \mathrm{~W} / 348938 / 89536 / 348938$
TO: $\quad$ Res, dep car, $390 \mathrm{k} \pm 5 \%, 1 / 4 \mathrm{~W} / 442475 / 89536 / 442475$
Change \#7 13899
AC/DC Scaling PCB Assembly
Change U17
FROM: IC, Xstr array, dual/ 504191/ 89536/504191
TO: IC, Xstr array, quad/ 445213/ 89536/445213
Change R68
FROM: Res, dep car, 120k $\pm 5 \%, 1 / 4 \mathrm{~W} / 441386 / 89536 / 441386$
TO: Res, dep car, 100k $\pm 5 \%, 1 / 4 \mathrm{~W} / 348920 / 89536 / 348920$
Delete U20
IC, Xstr array, dual/504191/89536/504191
Delete R66
Res, $\mathrm{mf}, \mathrm{lk} \pm 1 \%, 1 / 8 \mathrm{~W} / 320309 / 89536 / 320309$
Delete
Heatsink, xstr, U17 and U20/354993/89536/354993
Add R60
Res, var, $3 \pm 25 \%$, $1 / 2$ W/347963/89536/347963
Connect between U17-7 and U17-4/5.
Locate between R54 and R67.

Add R64
Res, dep car, $1 \pm 5 \%, 1 / 4 \mathrm{~W} / 357665 / 89536 / 357665$
Connect between U17-10 and junction of R68/ U17-2.
Locate between R50 and R68.
Change \#8 13925
A/D and Ohms Converter PCB Assembly
Change R6 and R7
FROM: Res, mf, $10 \mathrm{k} \pm 1 \%$. $1 / \mathrm{kW} / 168260 / 89536 / 168260$
TO: Res. mf, 20k $\pm 1 \%$, 1/8W/ 291872/89536/ 291872
Change \#9 13936
Controller PCB Assembly
Change U6
FROM: Res, network, 82/478859/89536/478859
TO: Res, network, 51/501502/89536/501502
Change \#10 13965
AC/DC Scaling PCB Assembly
Change R24
FROM: Res, dep car, $4.3 \mathrm{k} \pm 5 \%, 1 / 4 \mathrm{~W} / 441576 / 89536 / 44$ I576
TO: Res, dep car, $6.8 \mathrm{k} \pm 5 \%$, $1 / 8 \mathrm{~W} / 368761 / 89536 / 368761$
Change U19
FROM: IC, op amp, linear / 473777/89536/473777
TO: IC, op amp, linear / 507947/89536/507947
Change \#11 13970
AC/DC Scaling PCB Assembly
Change C21
FROM: Cap, cer, $2.2 \mathrm{pF} \pm 2 \%, 100 \mathrm{~V} / 362731 / 89536 / 362731$
TO: Cap, cer, $4.7 \mathrm{pF} \pm 2 \%, 100 \mathrm{~V} / 362772 / 89536 / 362772$
Change \#12 14385
AC/DC Scaling PCB Assembly
Change R75
FROM: Res, mf, $715 \pm 1 \%, 1 / 8 \mathrm{~W} / 313080 / 89536 / 313080$
TO: Res, mf, $806 \pm 1 \%, 1 / 8 \mathrm{~W} / 223552 / 89536 / 223552$
Change \#13 14397
AC/DC Scaling PCB Assembly
Add Qi0
Xstr, JFET/ 343830/89536/343830
Connect in parallel with Q11.
Locate between UI and R1I.
Change Q3
FROM: Xstr, JFET/ 535039/89536/535039
TO: Xstr, JFET/ 343830/89536/343830
Change Q8
FROM: Xstr, JFET/ 508697/89536/508697
TO: Xstr, JFET/ 343830/89536/343830
Change Q1I
FROM: Xstr, JFET/ 429977/ 89536/429977
TO: Xstr, JFET/ 343830/89536/343830
Change R30
FROM: Res, mf, $4.99 \mathrm{k} \pm 1 \%$, $1 / \mathrm{kW} / 168252 / 89536 / 168252$ TO: Res, mf, $5.11 \mathrm{k} \pm 1 \%, 1 / \mathrm{W} / 294868 / 89536 / 294868$

Change \#14 14528
Controller PCB Assembly
Delete
C10/ Cap, cer, $.22 \mathrm{uF} \pm 2 \%, 50 \mathrm{~V} / 519157 / 89536 / 519157$
Q1 / Xstr, NPN/ 218396/89536/218396
R13/Res, dep car, $2 \mathrm{k} \pm 5 \%, 1 / 4 \mathrm{~W} / 441469 / 89536 / 441469$
R14/Res, dep car, $220 \pm 5 \%, 1 / 4 \mathrm{~W} / 342626 / 89536 / 342626$
Change schematic to:


Change \#15 14529
Main PCB Assembly
Change C8
FROM: Cap, elect, $1200 \mathrm{uF}-10 /+100 \%, 200 \mathrm{~V} / 500322 / 89536 /$ 500322
TO: Cap, Ta, $150 \mathrm{uF} \pm 20 \% .20 \mathrm{~V} / \quad 422576$ 89536/ 422576
Change C9
FROM: Cap, cer, $.22 \mathrm{uF} \pm 20 \%, 50 \mathrm{~V} / 519157 / 89536 / 519157$
TO: Cap, Ta $150 \mathrm{uF} \pm 20 \%, 20 \mathrm{~V} / 422576 / 89536 / 422576$
Change C18
FROM: Cap, cer, $.22 \mathrm{uF} \pm 20 \%, 50 \mathrm{~V} / 519157 / 89536 / 519157$
TO: Cap, Ta 22 uF $\pm 20 \%, 15 \mathrm{~V} / 4230$ I2/ 89536/4230I2
Delete
Cl4/ Cap, Ta, $2.2 \mathrm{uF} \pm 20 \%$, 20V/161927/89536/161927
Q15/ Xstr, Si, NPN/ 218396/89536/218396
R27/Res, dep car, $2 \mathrm{k} \pm 5 \%$, 1/4W/441469/89536/441469
R28/Res, dep car, $220 \pm 5 \%, 1 / 4$ W/342626/89536/342626
Change schematic to:


Change \#16 14624
AC/DC Scaling PCB Assemebly
Change C35 and C36
FROM: Cap, cer, $22 \mathrm{pF}+5 \%, 100 \mathrm{~V} / 448449 / 89536 / 448449$

TO: Cap, cer, $15 \mathrm{pF} \pm 2 \%, 100 \mathrm{~V} / 369074 / 89536 / 369074$

## Change \#17 14663

AC/DC Scaling PCB Assembly

## Change R28

FROM: Res, mf, $3.4 \mathrm{k} \pm 1 \%, 1 / 8 \mathrm{~W} / \quad 260323 / 89536 / 260323$
TO: Res, mf, 3.83k $\pm 1 \%, 1 / 8 \mathrm{~W} / \quad 235143 / 89536 / 235 \mathrm{l} 43$

| Change R29 |  |  |
| :--- | :--- | :--- |
| FROM: | Res, var, $2 \mathrm{k} \pm 10 \%, 1 / 2 \mathrm{~W} /$ | $285163 / 89536 / 285163$ |
| TO: | Res, var, $1 \mathrm{k} \pm 10 \%, 1 / 2 \mathrm{~W} /$ | $285155 / 89536 / 285155$ |

Change \#18 14872
AC/DC Scaling PCB Assembly
Change C32
FROM: Cap, mylar, $.47 \mathrm{uF} \pm 10 \%$, $100 \mathrm{~V} / \quad 369124 / 89536 /$
369124
TO: Cap, mylar, $47 \mathrm{uF} \pm 10 \%, 100 \mathrm{~V} / \quad 446807 / 89536 /$
446807

Change C34
FROM: Cap.poly, $22 \mathrm{uF} \pm 10 \%$, $100 \mathrm{~V} / 614172 / 89536 / 614172$
TO: Cap, mylar, $22 \mathrm{uF} \pm 10 \%, 100 \mathrm{~V} / \quad 436113 / 89536 /$ 436113
Change \#19 14887
AC/DC Scaling PCB Assembly
Add C24
Cap, cer. $.22 \mathrm{uF} \pm 20 \%, 50 \mathrm{~V} / 309849 / 89536 / 309849$
Connect between Pins 2 and 3 of U13.
Locate between C25 and C26.
Change \#20 15061
Controller PCB Assembly
Change C4, C5, and C6
FROM: Cap, cer, $.22 \mathrm{uF} \pm 20 \%, 50 \mathrm{~V} / 519157 / 89536 / 519157$
TO: Cap, Ta I uF $\pm 20 \%$, 35V/ 161919/89536/161919
Delete CII
Cap, cer, $.22 \mathrm{uF} \pm 20 \%, 50 \mathrm{~V} / \quad 519157 / 89536 / 519157$

```
CHANE F1 - 15061
```

    Rev.-D, A3 Controller PCB Assembly (8860A-4003)
    On page 5-14, Table 5-4, change the TOT QTY of C1,
        FROM: 4
        TO: \(\quad 1\)
    
## ERRATA 1

On page 5-10, Table 5-2:
CHANGE: U6|IC, MICROCOMPUTER $\quad|504563| 895361504563|1| 1$
TO: U6|IC, MICROCOMPUTER, MOS 8-BIT|536334;89536;536334|1|1

## * nOTE $^{\text {n }}$

When replacing 06 ( $\mathrm{P} / \mathrm{N} 536334$ ), check to see if it is a dual piggy back assembly with a 6.2 k , $1 / 4 \mathrm{H}$ resistor on the A1 Main PCB across pins 39 and 40 of 06 . If so, this resistor should be removed before installing the replacement unit.

## ERRATA ${ }^{\text {E2 }}$

On page 5-16, Table 5-5, change the TOT QTY of C35,
FROM: 2
TO: 3
CHANGE $2-18685$
Rev.-R, A4 AC/DC Scaling PCB Assembly (8860A-4004)
On page 5-16, Table 5-5:
CHANGE: C13|CAP, MYLAR,. 047 UF +/-10\%,250V|162008173445|C280MAE/A47Ki1
T0: $\quad$ C13|CAP, POLY, . 47 UF $+/-10 \%$, $50 \mathrm{~V} \mid 7147251609351168$
On page 8-12, Figure 8-6, change the value of C13,
FROM: $\quad .047$
TO: $\quad .47$
CHAGE 3-19282
Rev.-A, Calculator/Printer PCB Assembly ( $8860 \mathrm{~A}-4014$ )
On page 004-12, Table 004-5:
ADD: U19|IC, NMOS, 2K X 8-BIT ROM|536359|89536|536359|1
CBATGE 鲑 - 19323
Rev.-G, A1 Main PCB Assembly (8860A-4001)
On page 5-10, Table 5-2:
CHANGE: U7-U10|IC, PHOTOTRANSISTOR,OPTICALLY COUPLED|504977129083|MCT2E
TO: U7-U10|IC, OPTO-ISOLATOR |312298|29083|Q1813

## CHAER/EPRATA ITPORTATIO

ISSUE E0: 2 5/84

This change/errata contains information necessary to ensure the accuracy of the following manual. Enter the corrections in the manual if either one of the following conditions exist:

1. The revision letter stamped on the indicated PCB is equal to or higher than that given with each change.
2. No revision letter is indicated at the beginning of the change/errata.

## MnMA

Title: 8860A Service
Print Date: June 1981
Rev.- Date: ---

C/R PAGE EFFECIIVITY
Page No. Print Date

## Section 8

## Schematic Diagrams

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Figure 8-1. 8860A PCB Locations








Figure 8-5. A3 Controller PCB Assembly (cont)


> JFET STATE TABLES
> OFF $=$ FET is not conducting.
> ON $=$ FET is conducting.

|  | VDC | VAC | $\begin{aligned} & \text { VAC } \\ & +V D C \end{aligned}$ | $\Omega 2 \mathrm{~T}$ | $\Omega 4 \mathrm{~T}$ |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| Q6 |  |  |  |  |  |  |
| $\begin{gathered} 200 \mathrm{mV}, 2 \mathrm{~V} \\ 20 \mathrm{~V}-1000 \mathrm{~V} \end{gathered}$ | OFF\% | OFEL | OFFE | OfF | OFFF | $200 \Omega, 2 \mathrm{~K} \Omega$ |
|  | ON | ON | ON | orf | orf. | $20 \mathrm{k} \Omega$ - $20 \mathrm{M} \Omega$ |
| A1-A (gate pin 4) |  |  |  |  |  |  |
| $\begin{gathered} 200 \mathrm{mV}, 2 \mathrm{~V} \\ 20 \mathrm{~V}-1000 \mathrm{~V} \end{gathered}$ | ON | ON | ON | ON | Off: | $200 \Omega, 2 \mathrm{~K} \Omega$ |
|  | OFFF | CFFF | OfF | ON | OfF | $20 \mathrm{~K} \cdot 20 \mathrm{M} \Omega$ |
| A1-C (gate pin 22), A1 - E (gate pin 25) |  |  |  |  |  |  |
| All ranges | Brf | OFF | Off | OFF | ON |  |
| A1-D (gate pin 26) |  |  |  |  |  |  |
| All ranges | ON | ON | ON | ON | OFF |  |
| A1-B (gate pin 3) |  |  |  |  |  |  |
| $\begin{aligned} & 200 \mathrm{mV}, 2 \mathrm{~V} \\ & 20 \mathrm{~V}, 200 \mathrm{~V} \\ & 1000 \mathrm{~V} \\ & \hline \end{aligned}$ | OTEF | Offy | OFFI | OFFF | OFF. | $200 \Omega, 2 \mathrm{~K} \Omega$ |
|  | ON | ON | ON | Off | Ofr | $20 \mathrm{~K} \Omega, 200 \mathrm{~K} \Omega$ |
|  | OFFI | CFFF | Offf | Off | Off: | $2 \mathrm{M} \Omega, 20 \mathrm{~m} \Omega$ |
| 013 |  |  |  |  |  |  |
| $\begin{aligned} & 200 \mathrm{mV}, 2 \mathrm{~V} \\ & 20 \mathrm{~V}, 200 \mathrm{~V} \\ & 1000 \mathrm{~V} \end{aligned}$ | OfF: | OFFF | Off: | Off | Off | $200 \Omega, 2 \mathrm{~K} \Omega$ |
|  | OFF | Off | Off | Of\% | OFF. | $20 \mathrm{k} \Omega, 200 \mathrm{k} \Omega$ |
|  | ON | ON | ON | off | orfe | $2 \mathrm{M} \Omega, 20 \mathrm{~m} \Omega$ |
| A1-F (gate pin 29) |  |  |  |  |  |  |
| All ranges | INT | ON | ON | INT | INT |  |
| A1-G (gate pin 28) |  |  |  |  |  |  |
| All ranges | $\overline{\mathrm{INT}}$ | Off | orf | $\overline{\mathrm{INT}}$ | $\overline{\text { INT }}$ |  |


|  | VDC | VAC | $\begin{aligned} & \text { VDC } \\ & +V A C \end{aligned}$ | $\Omega 2 \mathrm{~T}$ | $\Omega A T$ |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| 019 |  |  |  |  |  |  |
| All ranges | ON | Off | orf | ON | ON |  |
| 012 |  |  |  |  |  |  |
| 200 mV <br> 2 V <br> 20 V <br> $200 \mathrm{~V}, 1000 \mathrm{~V}$ | OLF | Off | Coff | Off | Off | $\begin{aligned} & 200 \Omega \\ & 2 \mathrm{k} \Omega \\ & 20 \mathrm{k} \Omega \\ & 200 \mathrm{k} \Omega \cdot 20 \mathrm{~m} \Omega \\ & \hline \end{aligned}$ |
|  | ON | ON | ON | ON | ON |  |
|  | Off | CrF | OFF | ON | ON |  |
|  | ON | ON | ON | ON | ON |  |
| Q18 |  |  |  |  |  |  |
| 200 mV | ON | ON | ON | ON | ON | $200 \Omega$ |
| 2 V | orf | Off | off | Off | OfF | $2 \mathrm{~K} \Omega$ |
| 20 V | ON | ON | ON | ofr | OFF: | $20 \mathrm{~K} \Omega$ |
| $200 \mathrm{~V}, 1000 \mathrm{~V}$ | OFF | OFF | OFF | OFFF | Off | $200 \mathrm{~K} \Omega \cdot 20 \mathrm{M} \Omega$ |


|  | VDC | $\begin{aligned} & \text { VDC } \\ & + \text { FIL } \end{aligned}$ | VAC | $\begin{aligned} & \text { VAC } \\ & +V D C \end{aligned}$ | $\Omega 2 \mathrm{~T}$ | $\begin{aligned} & \Omega 2 \mathrm{~T} \\ & +\mathrm{FIL} \end{aligned}$ | $\Omega 4 \mathrm{~T}$ | $\Omega 4 \mathrm{~T}$ + FIL |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| 011 (3-Pole Active Filter) |  |  |  |  |  |  |  |  |  |
| 200 mV -1000 V | off: | ON | OFFF | Off | OFFF | ON | $\begin{aligned} & \text { OFf } \\ & \text { OFF } \end{aligned}$ | ON | $\begin{aligned} & 200 \Omega-200 \mathrm{~K} \Omega \\ & 2 \mathrm{M} \Omega, 20 \mathrm{~m} \Omega \\ & \hline \end{aligned}$ |
|  | orf | ON | Orf | orr | OrF | OFFl |  | Off |  |
| Q15 (Passive Filter) |  |  |  |  |  |  |  |  |  |
| 200 mV -1000 V | * | ON | Off | Off | * | $\begin{aligned} & \mathrm{ON} \\ & \mathrm{ON} \end{aligned}$ | * | $\begin{aligned} & \hline \mathrm{ON} \\ & \mathrm{ON} \\ & \hline \end{aligned}$ | $\begin{aligned} & 200 \Omega-200 \mathrm{~K} \Omega \\ & 2 \mathrm{M} \Omega, 20 \mathrm{M} \Omega \end{aligned}$ |
|  | * | ON | orr | OFf | off |  | orf |  |  |

[^4]




JFET STATE TABLE
$\mathrm{ON}=\mathrm{JFET}$ is conducting

| OHMS CONVERTER <br> For $\Omega 2 \mathrm{~T}$ and $\Omega 4 \mathrm{~T}$ functions, the JFETs on the A1 hybrid circuit are switched |  |  |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| $\begin{aligned} & \text { (gate-pin 12) } \\ & \mathrm{A} 1-\mathrm{E}, \mathrm{~F} \end{aligned}$ | $\begin{aligned} & (\operatorname{pin} 13) \\ & \text { A1 }-\mathrm{G}, \mathrm{H} \end{aligned}$ | $\begin{aligned} & (\text { pin } 11) \\ & \text { A1-I, J } \end{aligned}$ | $\begin{aligned} & \text { (pin 18) } \\ & \text { A1-D } \end{aligned}$ | $\begin{aligned} & (\operatorname{pin} 21) \\ & \text { A1-A } \end{aligned}$ | $\begin{aligned} & (\text { pin } 24) \\ & \text { A1- } \end{aligned}$ |  |
| ON | off | Off | Off | Orf | ON | $200 \Omega$ |
| ON | Off | orr | orf | Orf | ON | $2 \mathrm{k} \Omega$ |
| orf | ON | Orf | off | orf | ON | $20 \mathrm{k} \Omega$ |
| orf | orf | ON | Off | orf | ON | $200 \mathrm{k} \Omega$ |
| orf | orf | orf | ON | Off | ON | $2 \mathrm{M} \Omega$ |
| Ofr | off | orf | ON | ON | Orf | $20 \mathrm{M} \Omega$ |
| (When a function other than $\Omega 2 \mathrm{~T}$ or $\Omega 4 \mathrm{~T}$ is selected, these FETs default to the $2 \mathrm{M} \Omega$ position.) |  |  |  |  |  |  |


|  | U21 BINARY TO 1 -OF - 4 DECODER TRUTH TABLE |  |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | INPUTS |  | OUTPUTS |  |  |  |
| PIN \# | 13 | 14 | 9 | 10 | 11 | 12 |
|  | 0 | 0 | 1 | 1 |  | 0 |
|  | 0 | 1 | 1 | 1 | 0 | 1 |
|  | 1 | 0 | 1 | 0 | 1 | 1 |
|  | 1 | 1 | 0 | 1 | 1 | 1 |
| $\begin{aligned} & 0=0 \mathrm{~V} \\ & 1=+5 \mathrm{~V} \end{aligned}$ |  |  |  |  |  |  |








##  900900. 9090909 919904017






[^5]
[^0]:    *These are dc offset voltages; the tolerances are approximate. Steady, noise free readings are more important than accuracy.

[^1]:    NOTE:

    1. Each JFET timing diagram represents the gate voltage. In the high state the gate is pulled up to the same voltage as the JFET channel.
    2. The transitions with dashed lines are conditional as indicated.
    3. Hybrid JFET A2-A is $O N$ and stays $O N$ as long as EXT. REF. is selected.

    Hybrid JFET A2-B is ON and stays ON as long as EXT. REF. is not selected
    4. The lengths of the $\triangle 2$ periods are exaggerated for clarity.

[^2]:    CAUTION (1)
    Indicated devices are subject to damage by static discharge.

[^3]:    * $X=$ The PCB revision levels documented in this manual.
    - = These revision letters were never used in the instrument.
    $-=$ No revision letter on the PCB.
    $+=$ Change did not affect manual.

[^4]:    \{ ON when in $41 / 2$ or $51 / 2$ digit mode, or if autoranging in $31 / 2$ digit mode
    OFF when in $31 / 2$ digit mode, and not autoranging

[^5]:    LAST NO. USED
    C4 PI Q2 U3

    | E2 R8 |
    | :--- | :--- |

