## NICROM

## The Magazine of the APPLE, KIM, PET

## and Other S50 S Systems



1F08
$\begin{aligned} & \text { 1FOB A2 } \\ & \text { IFOA } \\ & \text { IFO }\end{aligned}$
1FO
$\begin{array}{lll}1 F O A & A O & 02 \\ 1 F O C & \end{array}$
IFOE $10 \quad 10$
1F10 C8
1 F 11 Cg FF
1F13 F0 05
IF15 A9 80
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1F1A B1 7C NEGI

IF1C 10 F7
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1 F22 C8

| 1F23 B1 7C |
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| 1F25 |

18251003
IF27 A9 7 F
IF27 A9 78
IF29 C8
IF29 C8
IF2A 95
1F2C E8
1F2D 98 $\begin{array}{lll}\text { 1F2E } & 18 \\ \text { 1F2F } & 69 & 06\end{array}$ 1F2F 6906
IF32 C9 20
1 F34 D0 D6
1F36 A5 23
1 F38 18


Plotting a Revolution

## Dynamite

## from RAINBOW



Machine Language, cursor-based text editor for 16 K Apple.

- Features format capabilities of most text editors.
- All commands are control characters.
- Enables you to define your own function commands.
Order PIE on Cassette: . . . . . . . . \$19.95
On Diskette . . . . . . . . . . . . . \$24.95


A high quality, challenging game for you and the computer.
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On Diskette
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3.B6y for 24 K Apple

- Flip back and forth between board and text with ESC.
- Correct wrong moves
- Analyze your position

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$\$ 19.95$

Call or write today for your FREE Apple Software Catalog. We welcome B/A-VISA and Mastercharge. Sorry, no CODs. Please add $\$ 1.25$ shipping and handling. California residents add $6 \%$ Sales Tax.

We ship promptly on receipt of your prepaid order. Order direct from:


- 16 K Apple Machine language program
- Define your own character set and graphic shapes.
- Complete English upper/lower case character set.
- Complete Greek Alphabet with upper/lower character set.
- Scroll, vary window size, invert characters, switch back and forth between two character sets.

Order Hi-Res Char. Gen. on Diskette $\mathbf{\$ 1 9 . 9 5}$


- Define a 3-D lo-res shape.
- Animate with full perspective.
- Includes 3 demo shapes.

Order 3-D ANIMATION on diskette . $\mathbf{\$ 2 4 . 9 5}$


## EXCERT INCORPORATED

| P/N |  | Qty $1-9$ |
| :--- | :--- | ---: |
| A65-1 | AIM-65 w/1K RAM | $\$ 375$ |
| A65-4 | AlM-65 w/4K RAM | $\$ 450$ |
| A65-A | Assembler ROM | $\$ 85$ |
| A65-B | BASIC ROMS | $\$ 100$ |

EXCERT has concentrated on the AIM-65 to guarantee YOU that the products we offer are fully compatible. We know how these products work since they are used in our systems. EXCERT can help you get the system YOU want!

## ACCESSORIES

PIN
PRS1 +5 V at $5 \mathrm{~A},+24 \mathrm{~V}$ at 2.5 A , $\pm 12 \mathrm{~V}$ at 1 A (does not fit in-
side ENC1)
PRS2 +5 V at $5 \mathrm{~A},+24 \mathrm{~V}$ at 1 A (mounts inside ENC1)
ENC1 AIM-65 case with space for PRS2 and MEB1 or MEB2 or VIB1
ENC2 ENC1 with PRS2 inside $\$ 100$
TPT1 Approved Thermal Paper Tape, 6/165' rolls
MCP1 Dual 44 pin Mother Card takes MEB1, VIB1, PTC1
MEB1 8K RAM, 8K Prom sockets, 6522 and programmer for 5 V Eproms (2716)
PTC1 Prototype card same size as KIM-1, MEB1, VIB1
VIBI Video bd with 128 char, 128 user char, up to 4K RAM, light pen and ASCII keyboard interfaces
MCP2 Single 44 pin (KIM-4 style) Mother Card takes MEB2, PGR2 and offers 4K RAM sockets
with 4K RAM
MEB2 16K RAM bd takes 2114's with 8K RAM with 16K RAM
PGR2 Programmer for 5V Eproms with ROM firmware, up to 8 Eproms simultaneously with 4 textool skts

Wh $\$ 125$

## SYSTEMS

"ASSEMBLED \& TESTED"
All AIM-65 systems are self contained and have the power supply (PRS2) mounted inside ENC1.

## "STARTER" SYSTEMS

P/N
SB65-1 A65-1 in ENC2 \$475
SB65-4 A65-4 in ENC2 $\$ 540$
SB65-4B Same Plus BASIC $\$ 640$
"EXPANDED" SYSTEMS

| P/N |  | MEB1 | MEB2 | VIB1 |
| :---: | :---: | :---: | :---: | :---: |
| E_65-4 | A65-4, ENC2, w/one MEB1, MEB2, or VIB1 | \$775 | \$855 | \$775 |
| E_65-4B | Same Plus BASIC | \$875 | \$955 | \$875 |

Higher quantities and systems with other options quoted upon request!

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## EXCERT, INCORPORATED Attn: Laurie

 4434 Thomas Avenue South Minneapolis, Minnesota 55410 (612) $920-7792$Add $\$ 5.00$ for shipping, insurance, and handling.

Minnesota residents add 4\% sales tax.

# Faster than a speeding mini Able to leap tall micros in a single 8K ROM <br> Superboard, by Ohio Scientific, - the computer on a board - even includes a keyboard and interface for video display and cassette recorder. <br>  <br> VIS.A AVD M.4STER CH. 1 RGE ORDERS A RE BOTH.ACCEPIED. <br>  <br> <br> \$279 

 <br> <br> \$279}

## NJERO

Scpienber 979


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## Plotting a Revolution

John Sherburne<br>206 Goddard<br>White Sands Missile Range, NM 88002


#### Abstract

An assembly language plotting routine that is callable from BASIC will simplify and speed up the high resolution plotting process.


What does fomenting rebellion have to do with microcomputing? Plotting a revolution refers to the creation of three dimensional figures, called solids of revolution, that are formed by rotating a two dimensional figure about an axis to form a solid.

Solids of revolution can be generated and displayed, under BASIC, by using a fast, general purpose assembly language plotting routine and a technique that allows the assembly language routine to access BASIC variables. The plotting routine and the BASIC language interface are building blocks used to construct a generalized program to display solids of revolution.

## Plotting Routine

The purpose of the assembly language plotting routine is to simplify and speed up the high resolution plotting process. It also allows the operator to choose any point as the center and plot coordinates relative to that center, and it allows the option of plotting with a 45 degree perspective. To accomplish all this, six parameters must be passed from BASIC: $\mathrm{P} \%, \mathrm{Q} \%, \mathrm{R} \%, \mathrm{X} \%, \mathrm{Y} \%$ and Z\%.
$\mathrm{P} \%$ and $\mathrm{Q} \%$ are the screen location for the center of the plot. The screen contains $80 \times 50$ plot positions so $\mathrm{P} \%=40$ and $\mathrm{Q} \%=25$ would plot relative to the center of the screen.

R\% specifies the type of plot. If the zero bit of $R \%$ is set ( $\mathrm{R} \%$ is odd), the plot is displayed as though viewed straight on. If $\mathrm{R} \%$ is even, the plot is at a 45 degree perspective to the viewer's right.

If the one bit of $\mathrm{R} \%$ is not set, the plotting routine will plot over any non-plot


Figure 1: Two sine waves of different frequencies.
characters on the screen and erase them. If the one bit is set, non-plot characters on the screen will not be erased. The other bits of $\mathrm{R} \%$ are ignored.
$X \%, Y \%$ and $Z \%$ are the coordinates of the point to be plotted. The $X$ axis is horizontal, the $Y$ axis is vertical and the $Z$ axis is either vertical or at a 45 degree angle, depending on R\%.

The most complex problem in making three dimensional plots is to draw only lines which are visible and to eliminate lines hidden to the viewer. The plotting routine can perform hidden line elimination automatically for one type of figure; a figure which can be imagined as an object covered by a very large, tight fitting sheet. More precisely, the figure must have a single $Y$ value for each $(X, Z)$ value pair, and the bottom of the figure will be hidden from view.

If such a figure is plotted, starting with the lowest value of $Z$ and progressing in order to the highest value of $Z$, the


Figure 2: Sine wave rotated about the $Y$ axis.

MICRO-WARE ASSEMBLER $65 \mathrm{XX}-1.0$

hidden line problem will be simplified greatly. In fact, it becomes only a matter of printing the value of $Y$ for each $(X, Z)$ and eliminating all previously plotted lower values of $Y$. The plotting routine accomplishes this process by simply erasing all points below the currently plotted point.

Besides having to plot all points in increasing order of $Z$, the procedure requires that, for a given value of $Z$, an ( $X, Y$ ) be computed for each feasible value of $X$. Otherwise gaps in the plot might leave non-visible points unerased. This process can be imagined as cutting the figure into slices parallel to the $X Y$ plane and then stacking the slices up in $Z$ value order to reconstitute the figure.

If the hidden line function is not wanted, it can be turned off with the statement "POKE 8181,96". This will cause the routine to plot a point at $\mathrm{X} \%$, $Y \%, Z \%$ without erasing lower points. Of course if $Z \%$ is held constant at zero, the routine is equivalent to a two dimensional plotting function.


Figure 3: The problem has been corrected by drawing a line between plot points and eliminating the gaps.




Figure 4: The solid formed by rotating $Y=15$ exp ( $\mathrm{X} / 3$ ) about the $Y$ axis.


Figure 5: The solid formed by rotating $Y=1 / 2 \cos$ $(3 X)+\cos (X)$ about the $Y$ axis.

## BASIC Interface

Since the assembly language routine requires that the six parameters be passed from BASIC, the USR function with its single parameter argument cannot be used. POKE will not work, either, because it will not accept negative values. The method I used to overcome this problem was to have the assembly language routine access the BASIC working area to obtain the required parameters.

After the run command is given, PET BASIC takes each variable in the order it is encountered and creates a working storage area for it following the last BASIC statement. For non-subscripted variables the working storage is seven bytes long. The first two bytes are the variable name and the next five are the current value.

Floating point variables are stored in normalized form, while integer variables are stored as two-byte signed numbers. The address of the starting byte of the variable storage area is stored in location \$7C. For simplicity, the plotting routine assumes that the six required parameters-P\%, Q\%, R\%, X\%, Y\%, and $Z \%$-are the first six variables in the program and are in that order.

To insure that all the required variables are in the proper place and in the proper sequence, the assembly language program includes a verification routine starting at location $\$ 1 \mathrm{EC} 3$. This routine is called with the statement "SYS (7875)" and checks for the presence and correct sequence of each parameter. The results of the checks are stored in the PET status word at location $\$ 020 \mathrm{C}$.

If the value of the status word, $S T$, is zero, all variables were located. If one of the variables is not located, the corresponding bit of ST will be set. For example, ST $=6$ would mean bits 1 and 2 are set and thus that $\mathrm{P} \%$ and $\mathrm{Q} \%$ were not found. Bit zero is not used. A typical sequence to establish and verify the BASIC routine would be:

The important point is that the six plotting variables must be the first six variables mentioned in the program, and they must be in the required sequence. Normally, the verification routine will only be used for diagnostic purposes. The plotting routine itself is entered with the statement "SYS (7944)".

## BASIC Programs

The plotting routine described above can be used for any three dimensional plot that satisfies the requirements of being single valued in $Y$ and having only the upper surface visible. For example, Figure 1 is a graph showing the effects of combining two sine waves of different frquencies. The difference in frequency is a function of $Z ; Y$ is amplitude and $X$ is time.

Figure 2 is a solid of revolution formed by rotating a sine wave about the $Y$ axis. The program is written a generalized format, and any function can be used in line 300 as the function generating the solid.

The scale and perspective of the figure are determined in lines 30 thru 50. $X R$ is the actual maximum value that $X$ can take, while -XR is the minimum. XP is the number of plot points that the distance XR will cover. For Figure 2, the $X$ value runs from -1.5 pi to 1.5 pi and is plotted from -35 to 35 . Changing XP changes the width of the plotted figure.

Similarly, the actual range of $Y$ is $Y R$, and $Y P$ is the plotted range of $Y$. Changing YP changes the heighth of the plotted figure. The $X Z$ cross section of the figure is circular so the actual range of $Z$ is the same as $X$ - XR. However, the plotted range of $Z-Z P$ depends upon perspective.

The larger ZP, the greater the apparent depth of the figure and the higher the apparent position of the viewer. The value $Y S$ in line 150 represents the lowest plotted value of $Y$ or the base of the figure.

A potential problem with the program is that while each point in the $X$ direction is plotted, not every point in the $Y$ direction is. Thus for $Z=0$, two consecutive plot points might be ( $X_{1}=3, Y_{1}=12$ ) and $\left(X_{2}=4, Y_{2}=9\right)$. While $X_{1}$ and $X_{2}$ are adjacent, $Y_{1}$ and $Y_{2}$ are not. The problem of such gaps is esthetically more severe with some figures than others.

In Figure 3, the problem has been corected by drawing a line between plot points and eliminating the gaps. The program for Figure 3 is the same as for Figure 2 except that the section between lines 300 and 400 has been modified.

Figure 4 is a plot of the solid formed by revolving $Y=15 e^{-x / 3}$ about the $Y$ axis. The program is the same as for Figure 3 except that line 300 contains the new function, line 150 is changed, and lines $10-50$ contain the new center and scaling factors. Figure 5 is the solid of revolution of $Y=1 / 2 \cos (3 x)+\cos (x)$ about the $Y$ axis. The program is the same as that of Figures 3 and 4 except for lines $10-50,150$, and 300.

The process of rotating the plane figure to obtain a solid of revolution is illustrated in Figures 5 and 6. As described before, the plot for a given value of $Z$ is equivalent to a vertical slice through the solid parallel to the $X$ axis. Figures 5 and 6 represent views of a solid form above and the dotted line is the path of a vertical slice.

The maximum radius of the solid is XT. The apparent distance of a point on the circle from the circle's center (view-



Figure 6: View of a solid from-above. The dotted line is the path of a vertical slice.
ed straight on) is $\mathrm{XL} . \mathrm{XL}$ is computed in line 200 of the programs for Figures 2 through 5.

The next step is to find the $Y$ value for each point on the dotted line between $-X L$ and XL. The FOR loop in line 210 insures that each possible $X$ value is used. The process of computing the $Y$ value for each $X$ value is largely the reverse of the process described above and is illustrated in Figure 6.

Viewed from the top, the contours of the solid form concentric circles. That is, the $Y$ value of every point at a given distance from the center is the same. For a point along the dotted line at an apparent distance XI from the center, the $Y$ value will be the same as for a point where the inner circle crosses the $Z=0$ line.

The distance of either point from the center is the square root of $\left(\mathrm{XI}^{2}+\mathrm{Z} \mathrm{T}^{2}\right)$. The calculation is performed in line 220 and the resultant distance is used to compute the $Y$ value in line 300. The plotting function is called in line 380 and uses whatever values of $X \%, Y \%$ and $Z \%$ are then current.

## A few short assembly language routines implement a notepad and provide the basis for versatile output to the AIM-65 display. These techniques overcome a variety of common output difficulties.

Do you want to learn how to use the 20 -character AIM 65 display? This short article describes several assembly language subroutines that may be used to display input/output information. The entire program functions as a novel "notepad" that may be used to leave a message for someone else or for yourself. However, its primary utility will lie in the applications that you design which use the AIM 65 display. The program listing is given, and its description follows.

We will begin by describing some of the features of the notepad program, and then return to a description of some of the subroutines that you might want to duplicate in your assembly language programs. The notepad program allows the operator to enter a message containing from one to 256 ASCll characters (including spaces) into locations $\$ 0200$ to $\$ 02 F F$ of the AIM 65's memory space.

While entering the message, the characters typed on the keyboard are displayed on the 20 -character display. The message enters the right-hand side of the display, and it is scrolled to the left. If an error is made, the DEL key allows the entire message to be backspaced, and a new character or set of characters may be entered.

Once the desired message is entered, the RETURN key starts the message circulating from right to left on the display. It circulates at a rate that makes it easy to read. If more than one space (ASCII value $=\$ 20$ ) is encountered, the space is not displayed. Thus, a message that contains less than 256 characters does not take a noticeable amount of time to display "empty" locations.

You can leave a message to yourself such as "CALL SAM TONIGHT", or you can remind your wife to "BE SURE TO LET ROVER OUT WHEN YOU GET HOME." Of course there are much less expensive ways to do this than by purchasing AIM 65, and it is doubtful whether this notepad program will provide sufficient justification to convince your spouse that you ought to have a computer. The program is more of a novelty that might be useful as an adver-
tising gimmick, if you are selling AIM 65 s, or to impress your friends.

On the other hand, the subroutines could be useful in a large variety of programs. I use several of the subroutines in my Morse code program for the AIM 65 (available from me for $\$ 3.50$ ). The subroutines might be useful in computer assisted instruction programs that require interaction of the computer with the operator. Or they might be useful in testing reading and comprehension speed in certain psychological tests of perception and cognition.

The read/write (RAM) memory locations from \$A438 to \$A43B, memory locations which are available on an off-the-shelf AIM 65, are used to store the ASCII characters to be displayed. We call these locations the display buffer. These 20 locations are filled with ASCII spaces by the subroutine CLEAR starting at address \$03A0. Subroutine DISPLAY, starting at address $\$ 0360$, transfers the ASCII characters in the display buffer to the AIM 65 display. It does this by making use of a subroutine in the AIM 65 monitor called OUTDD1 tht is located at \$EF7B.

Subroutine OUTDD1 in the AIM 65 ROM is very usef $\mu$ in working with the 20 -character display. The content of the $X$ register addresses the display in the sense that $X=\$ 00$ is the leftmost character on the display, and $X=\$ 13$ (19) is the right-most character on the display.

The accumulator, A , must contain the ASCII representation of the character to be displayed betore the jump to the OUTDD1 subroutine is made. The accumulator must also be ORAed with $\$ 80$ before the subroutine call, or the cursor will be displayed. With the accumulator properly loaded and the appropriate "address' in the $X$ register, a subroutine jump to OUTDD1 will display the character.

A jump to subroutine CLEAR, at $\$ 03 A 0$, followed by a jump to subroutine DISPLAY will clear the display. To put some information in the display and scroll it to the left, subroutine MODIFY (starting at address \$0372) is used.

Subroutine MODIFY stores the contents of the accumulator in location \$A44C. Then it proceeds to shift the contents of $\$ 4439$ to $\$ 4438, \$$ A 43 A to \$A439, and so on until it finishes by shifting the contents of location \$A44C to \$A44B.

Once the display buffer is properly modifjed by subroutine MODIFY, then a subroutine call to DISPLAY will cause the down-shifted ASCII characters in the display buffer to appear as left-shifted characters on the AIM 65 display.

The sequence of events, starting at the beginning of the main program, is as follows: First, the display buffer is cleared by subroutine CLEAR. The message buffer from $\$ 0200$ to $\$ 02 \mathrm{FF}$ is cleared (loaded with ASCII spaces).

Next, an AIM 65 monitor subroutine, READ, is called to get a character from the keyboard. As long as no key is depressed, the monitor stays in this subroutine. A key depression results in a return to the main program with the ASCll representation of the character in the accumulator. The contents of the accumulator are transferred to the message buffer, using $Y$ as an index for the buffer's base address of $\$ 0200$, unless it is the ASCII character for RETURN, DEL, or the F1 key.

The F1 key starts the entire program over. The DEL key removes the last character from the message buffer, and it backspaces (scrolls right) the display buffer and the display itself. The RETURN key starts the message, and this key should be pressed only when the desired message has been placed in the message buffer.

If a character is placed in the message buffer, then it is also displayed by calling subroutines MODIFY and DISPLAY in succession. If the message buffer is filled, or if the RETURN key is pressed, then the program wili proceed to scroll the entire message across the display.

The message is displayed by getting characters from the message buffer, starting with location \$0200, and then calling subroutines MODIFY and DISPLAY in succession. A time delay is in-

0010:
0020: * MAIN PROGRAM
0040: 0300 ORG \$0300
0050: 030020 A0 03 JSR \$03A0
0060: 0303 A0 00 LDYIM $\$ 00$
0070: 03058400 STY \$00
0080: 0307 A9 20 LDAIM $\$ 20$
0090: 0309 990002 STAY \$0200
0110: 030D DO FA BNE $\$ 0309$
0120: 030F 20 3C E9 JSR \$E93C
$\begin{array}{lllll}\text { 0130: 0312 C9 OD } & \text { CMPIM \$0D } \\ \text { 0140: } 0314 \text { F0 1C } & \text { BEQ } \$ 0332\end{array}$
0150: 0316 C9 5B CMPIM \$5B
0160: 0318 F0 E6 BEQ \$0300
0170: 031A C9 7F CMPIM \$7F
0180: 031C DO 06 BNE \$0324
0190: 031E A9 20 LDAIM \$20
0200: 032088 DEY
0210: 0321208503 JSR \$0385
0220: 0324990002 STAY \$0200
0230: 0327 B0 E6 BCS \$030F
0240: 0329207203 JSR $\$ 0372$
0250: 032C 206003 JSR $\$ 0360$
0270: 0330 D0 DD BNE \$030F
$\begin{array}{llllll}\text { 0280: } 0332 & \text { AO } 00 & \text { LDYIM } \$ 00 \\ \text { 0290: } 0334 & \text { B9 } 00 & 02 & \text { LDAY } \$ 0200\end{array}$
0300: 0337 C9 20 CMPIM \$20
0310: 0339 D0 08 BNE \$0343
0320: 033B A5 00
0340: 033F E6 00 INC \$00
0350: 0341 D0 07 BNE \$034A
0360: 0343 A9 00 LDAIM \$00
0370: 03458500 STA $\$ 00$
0380: 0347 B9 00 02 LDAY \$0200
0390: 034A 207203 JSR \$0372
0400: 034D 206003 JSR $\$ 0360$
0410: 0350 A9 FF LDAIM $\$$ FF
0420: 0352 8D 97 A4 STA \$A497
0430: 0355 2C 97 A4 BIT $\$$ A 497
0440: 0358 10 FB BPL $\$ 0355$
0460: 035B 18 CLC
0470: 035C 90 D6
0480:
0490:
0500:
0501: 0360
0510: 0360 A2 13
serted (\$FF is loated into the divide-by-1024 counter on the 6532 chip) unless more than one space occurs in succession. In that case, the subroutines and the time delay are not used at all, and the program keeps searching through the message buffer until it finds another non-space ASCII character, in which case subroutines MODIFY and DISPLAY are called again.

One subroutine that remains to be mentioned is BACKSPACE used by the DEL key. It starts at $\$ 0385$ and its effect is to backspace the display buffer, replacing the leftmost character with the appropriate character from the message buffer. It then calls subroutine DISPLAY to show the typist that the character has, in fact, been deleted and the entire message has been backspaced.

Again, I think the subroutines MODIFY, DISPLAY, CLEAR, READ, and OUTDD1 will be of considerable use if you are writing programs that use the keyboard or the display on the AIM 65. All of them are quite short, and a little study will show how they work. Most involve only simple loops and nothing more complicated than indexed addressing. Mimic or echo your display on your computer storefront and you will have something that will really catch the eye, but don't ask me where to get the appropriate neon sign elements.

A summary of the subroutines follows:

DISPLAY Takes the contents of locations \$AS438 to \$A44B and transfers them to the AIM 65 display. A is modified, and $\mathrm{X}=0$ on return.

MODIFY Successively shifts the contents of locations $\$$ A439 to \$A44C to locations in memory whose addresses are one less. The contents of the accumulator, when the subroutine is called, will be stored in location \$A438. A and $X$ are modified.

CLEAR Loads $\$ 20$ in the display buffer, locations \$A438 to $\$ \mathrm{~A} 44 \mathrm{~B}$. A and X are modified.

BACKSPACE Reverses the effects found in MODIFY and, in addition, loads location \$A438 with the contents of the message buffer in $\$ 0200+(Y-\$ 13)$. $Y$ points to the last entry made in the message buffer. $X$ and $A$ are modified.

## clab notos

The MicroComputer Investor's Association is a non-profit, professional organization which was founded three years ago to enable members to share data and information. For an information packet, send $\$ 1.00$ to:

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The NW PET Users' Group is attempting to locate persons in the Oregon/Washington area, interested in a local users ${ }^{\text { }}$ group. If interested, please write or call:

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The Honolulu APPLE Users Society supports a newsletter containing the latest up to date-information concerning the APPLE.. program tips and techniques, listings, reviews, etc. The club is interested in excbanging information and software with other clubs. Contact:

## Bill Mark

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The APPLE $I I$ users in the South Florida, Miami area have formed the Miami APRLE Users'Group, president, Steve Pierce. The club was formed to share soft-
ware and technical information and to help new APPLE users use their APPLES. They plan to establish a quarterly newsletter and anticipate installing an on line system where anyone can have access to club information. If you wish to correspond or join, contact:

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# Applesoft Renumbering 

# Here is a fast and reliable utility for APPLE programmers who do not have disks. It can be adapted to the PET and other Microsoft BASIC systems. 

J.D. Childress<br>5108 Springlake Way<br>Baltimore, MD 21212

The need for a program written in Applesoft to renumber Applesoft programs is moot now that APPLE has made available the 3.2 version of its disk operating system, that is, if one has a disk system. I wrote the present renumbering program while my disk drive was out of action, before the release of the 3.2 version, and after reading Mr. Carpenter's program in MICRO 12:45 based in turn on a PET program by Jim Butterfield, MICRO 8:33. Since some people do not have disks and since Applesoft programs can be adapted to the PET and other systems using Microsoft BASIC, my renumbering program still may find users.

## Comparison

This Applesoft renumbering program (hereafter called RENUMB) is dreadfully slow; it took 7.9 minutes to renumber a 8.5 K program. Even at that, it's faster than Mr. Carpenter's program, which took 13.2 minutes to renumber the same 8.5 K program (and also had a problem with one THEN). In comparison, the 3.2 disk renumber program did the job in 7.8 seconds.

Like Mr . Carpenter's program, RENUMB cannot change the line number after a GOTO, a GOSUB, or a THEN equivalent of a GOTO when the new line number has more digits than the old one. The program prints a list of these changes which must be made by hand. If there is not enough space, RENUMB inserts only the least significant digits. For example, the line

```
100 ON L GOTO 180, 190
```

with a line number shift upwards by 1005 would be given as

$$
1105 \text { ON L GOTO 185, } 195
$$

With the manual change instructions shown here:

LINE 1105: INSERT 1185 AFTER GOTO.
LINE 1105: INSERT 1195 AFTER COMMA
If there is more space than needed, RENUMB inserts leading zeros. (Note that the Applesoft interpreter preserves such leading zeros whereas the 3.2 disk renumber program does not.)

RENUMB has one useful feature in common with the 3.2 disk renumber program, namely the capability of renumbering only a specified portion of a program. This feature must be used with care since one can renumber a part of a program with line numbers equal to or in between some of the line numbers of the remaining part of the program.
Unlike the 3.2 disk program, RE-NUMB does not order such lines into the proper sequence. If you really want that, you must run RENUMB first then use the screen/cursor editing controls to copy the out-of-sequence lines through the Applesoft interpreter. The reader is left with the nontrivial problem of getting rid of the still remaining out-of-sequence lines.

## Operation

To use RENUMB, one needs to append RENUMB to the program to be renumbered. The machine language APPEND program and procedure given by Mr. Carpenter are recommended. After the two programs are properly loaded, renumbering is accomplished by a RUN 63000 command. Give the requested information, then be patient; remember that RENUMB is numbingly slow.

Copy carefully all the manual changes listed. If you want to see them again, you can do so by a GOTO 63360 command provided you have done nothing to clear the variables, i.e., have not given any RUN commands or changed any line of the program.

You may use the SPEED command to slow up the display and the CTRL-C command to interrupt the display without clearing the variables. Once the variables have been cleared, there is nothing you can do except start from the beginning, that is, load the programs again.

At the beginning of the program run, you are asked for a rough estimate of the number of program lines (numbered lines) to be renumbered. Be generous, within limits of available memory. If your estimate is too small, you will get a

## ?BAD SUBSCRIPT ERROR IN 630X0

where $X=6,7$, or 8 since your estimate is used for array dimensioning. Unless your program is especially rich in branches, an estimate, say, about $50 \%$ greater than the number of line numbers will suffice.

## Program Design

The design of RENUMB is quite simple. First RENUMB searches the program being renumbered for line numbers (and their memory locations) and the line numbers (and memory locations) after GOTO's, GOSUB's, THEN's, and COMMA's in multiple branches. This search is done by lines 63040-63090 and for branches, the subroutine at 63250 . Lines 63130 and 63140 make the changes at the branches and line 63180 at the labels. The routine beginning at 63350
prints out those changes that must be made by hand.

All else is bookkeeping. Note: In line 63030, START is the address in memory of the beginning of the program. This is probably the only thing that needs to be changed for RENUMB to run on the PET (try START@1025 per Butterfield) and possibly on other systems using Microsoft BASIC. Finally, if you write very GOTOy and GOSUBy programs, you may want to change the definition of DD in line 63030.

## Applesoft

Butterfield gives considerable information about the insight into the structure of Microsoft BASIC. What is even handier is your own APPLE II. Let it be your textbook and teacher. For example, starting fresh with Applesoft in the computer, enter

## 1 PRINT: GOTO 521

## 521 PRINT "FREE": LIST 521

While this little program runs without error, that is not necessary. You can enter anything you want to see how Applesoft handles it.

Now go to the monitor and look at
801-0C 080100 BA 3A AB 35323100
80C-10 080902 BA 224652454522 3A
BC 35323100
810.0000
for ROM Applesoft ( 1001 for RAM Applesoft). In the above lines, arranged here for clarify, OC, 08, 1008 , and the final 0000 point to the next instruction in memory, the 0000 pointer labelling the end of the program. 0100 and 0902 are
the line numbers, 1 and 521 respectively. BA is the token for PRINT; 3A is the ASCII code for the colon; AB is the token for GOTO; 353231 gives the line number for the GOTO; and 00 indicates the line ending. 224652454522 is a direct ASCII code rendition of "FREE". Finally BC is the token for LIST and 353231 is the line number 521 after LIST.

Study of the above paragraph shows that Applesoft puts things into memory almost exactly the way you type them on the keyboard, except that the interpreter removes spaces, puts in instruction addresses, translates its command words into tokens, and uses ASCII code and hexidecimal, low-order bit first notation.

I think we can be confident that Microsoft has written most of their BASIC interpreters in as similar a fashion as possible. After all, why not exploit one's own good work. $\mu$

```
LISTING--APPLESOFT RENIMBERING
        PQOGRAM
    62999 END
    63000 HOME : VTAB (3): PDINT "
        RENUMBERING PPOGRAM": DOMNT
    63010 PRINT "LINES TO BE DENImPE
        RED:": IMPUT " PEGIMING LI
        NE--";BGN: INPUT " ENDIMG
        LIME--":TOM: |NPITT " TOTAL
        NUMBER OF LINES (ROUGHLY)--
        ":D: PRINT
    53020 IVPUT "RENUMBFPED BEGINNIN
        G LINE--";SK: INPIIT "INCDEME
        NT--":ADD
63030 START = 256 * PEEX (104) +
        PEEK (103):M = STAPT + 2:DD
        = INT (D/4): DIM LS(D),L
        N(DD),LM(DD),LOC(DD),NAS(DD)
        ,ND(DD),INS(OD),IMS(DD)
63040L=L+1:LS(L)=M:LC = 25
        6 * PEEK (M + 1) + PEEK (M
        ): |F LC > 62900 THEN 63100
63050 FOR J =M + 2 TO M + 255:T
        ST : PEEK (J): IF TST = O THEN
        M = J + 3: GOTO 63040
63050 IF TST = 171 THEN NAS(K +
        1) = "GחTO": GOSUB 63250
63070 IF TST = 176 THEN NAS(K +
        1) = "GOSUB": GOSUB 63250
63080 IF TST = 196 ANN PEEK (J +
        1) > 47 ANO PEEK (J+1)<
        58 THEN NA$(K + 1) = "THEN":
        gosub 63250
63090 NEXT
63100 FOP J = 1 TO L:LNU = 258*
        OEEK (LS(J) + 1) + DEEK (L
    S(J)): IF LNU > TDM OR LNU >
    62900 THEN PRINT : POIMT "R
    ENUMREDING COMPLETED THPOUGH
        LINE ":LIN;".": GCTN 63350
63110 IF LNII < BGN THEN 63100
6312J SK$ = "0000" + STOS (SK):S
    K! = DIEHT$ (5K5,5)
```

```
63130 FOR 1 = 1 TO K: IF LNU < >
        INS(1) THEN NEXT : GOTO 631
        80
63140 FOR KA = 1 TO ND(1): POKE
    LOC(1) + 1 + NC(1) - KA, YAL
        (MIDS (SK$,E - KA,1)) + 48:
        NEXT
63150 IF LNU = INS(1) THEN IMS(I
        )=SK
63160 1F LEN ( STDS (SK)) > NO(
    1) THEN PCR = 1
53170 MEXT
63180 SO = INT (SK / 255): POKF
        (LS(J) + 1).SO: POKE (LS(J))
        ,Sk - 256 - S0
63190 FOR I = 1 TO K: IF LNU = L
        N(1) THEN LM(1) = SK: IF LNU
        < BGN THEN LM(I) = LNU
63200 NEXT
63210 SK = SK + ADD:LJN = LNU
63220 NEXT
63250 K = K + 1:LN(K) = LC:SU = DEEK
        (J + 1) - 48
63260 FOR KA * J + 2TO J + 6:CP
        R= PEEK (KA): IF CPO = O OR
        CPR = 58 OR CPR = 44 THEN GOTR
        63290
63270 SU = 10 * SU + CPR - 48
6 3 2 8 0 ~ N E X T ~
63290 LOC(K) = J:NO(K) = KA - 1-
        J:INS(K) = SU:J = KA - 1: IF
        CPR = 44 THEN NAS(K + 1) ="
        COMMA":J = KA: GOTD 63250
63300 PEETURN
63310 ENO
63350 IF PCP < > 1 THEN ENO
63360 POINT : PDINT "NOTE: YOU M
        UST MAKE THE FOLLOWIMG CHAN-
        ": PRINT "GES MANUALLY:": POINT
63370 FOR I = 1 TC K: IF LEN ( STOS
        (IMS(1))) < = ND(1) THEN NEXT
        : END
63380 PDINT "LINE ";LM(1);": IMS
        ERT ";IMS(1);" AETER ";NAS(I
        );"."
63390 NEXT : END
```


# MOVE IT: Relocating PET Source Programs and Object: Code 

> A useful program need not perform the entire task. If ten percent of the total coding effort achieves ninisty-nine percent of the desired result, perhaps manual intervention will be more efficient than additional programming.

MICRO readers probably know that when a PET program is saved on cassette tape it normally loads back into the same area of memory. Several times recently I wished that was not the case because I found the need to relocate information already saved.

For example, I originally assigned source code for an assembler to what later turned out to be an inconvenient area of memory. Being naturally lazy, I had no desire to retype the long source program into the newly assigned memory region. Let the PET move it, I said-and it did. This article tells how.

Information one might wish to relocate falls into three categories. I have already mentioned ASCII source code which would require no modifications after being moved. The next category also requires no extra work. BASIC programs which you might want to append, relocate, or relocate, do indeed have address links which need to be modified. Fortunately for us the PET has routines which do this automatically.

Finally, machine language programs are not always located where they do the most good and it could become necessary to move them to more useful areas. In this case many changes probably will be necessary. Instructions which use absolute addressing modes and indexed pointers are the principle culprits. Finding the necessary changes can be difficult without a source listing of the original program.

The first method I considered to move programs (the first step in relocation) was a modified program from the First Book of KIM (MOVEIT, p. 127). I rejected this approach for a number of reasons, but mainly because I was convinced the PET had the routines already built in. An article by Jim Butterfield (bless his bones) in the PET User's Notes (Vol. 2, \#1, p. 7) gave me the concept I needed to have the PET operating system help relocate code already saved on tape.

This method has the decided advantage of not requiring the old memory locations to be present. A program originally located at hex 6000 and saved
on tape in another PET can be loaded into an 8 K machine at hex 41)0 if desired.

After placing the mach ne language program in a new area of memory, it is necessary to make varijus address changes. These modifica ions can be made with a machine language program. See The First Book of KIM, p. 130, for an example. Since I feel more comfortable working in BASIC, I develcped a simple BASIC program to do most of the address modifications.

The program is not perfect, so any remaining changes or corrections need to be done with a monito program. I deliberately used an easy-to-write (slightly flawed) program in combination with manual correction, ins tead of spending lots of time writing all elegant program which did everything. I felt this approach gave the best results because the total time to accomplish a task is what really counts.

In summary, the relociting method discussed here can be brolien down into three essential steps:

1. Loading the informetion on the cassette tape into the new area of memory.
2. Running a BASIC program which makes mast of the address changes.
3. Manually correcting e rors, using a monitor program, and making other necessary changes $n$ issed by the simple minded BASIC program.

As an example, I have picked what I hope is a useful exercise: relocating Commodore's machine lariguage monitor. It is important to hive available monitors which are tocated in different areas of memory. When we want to modify low areas of memory, it is necessary to use a monitor in high memory, and vice versa.

Furthermore, the top of memory is consistently changing as PET owners add extra memory. It is a decided disadvantage to be stuck witt only a low monitor, as supplied by Commodore. The latest PET's have a menitor in ROM,

Professor Harvey B. Herman Department of Chemistry The University of North Carolina Greensboro, NC 27412



Location
(Ibject Code

## Source Code and Notes

| Original | Modified | Orig nal | Modified |  |
| :--- | ---: | :--- | ---: | :--- |
| 0447 | 1 C 47 | 20F204 | 20F21C | JSR CRLF (1) |
| 0484 | 1 C84 | DDO:205 | DD021D | CMP CMDS, X (2) |
| 0414 | $1 C 14$ | A90 | A91C | LDA \# BRKE (3) |
| 0511 | 1011 | 06 | $1 E$ | .BYT ZZ8 (4) |
| $073 E$ | $1 F 3 E$ | C906 | C912 | CMP \#6 (5) |
|  |  |  |  |  |

(1) Absolute address identified and changed by BASIC MODIFY program.
(2) Address not changed by BASIC program. Changed manuaily with the monitor.
(3) High order byte of address of the break vector stored at \$21C must be changed.
(4) Jump table value (address of command) has to be relocated.
(5) Code which was erron $\begin{gathered}\text { ously changed because of preceeding }\end{gathered}$ hex 20. Changed back manually with the monitor.

## Typical Changes While Relocating Monitor Program


but the general ideas presented here will still be useful to owners of those machines in other applications.

The article by Jim Butterfield showed a procedure which loaded the tape into the screen memory area. I wanted to move the monitor program to the top of memory, 8 K in my PET. This procedure is shown in Figure 1. Step 2 loads the tape header. I used the monitor program, in low memory, to modify the tape address from the header in steps 4 and 5. Moving the program on tape to the new area of memory occurs in step 7. After protecting the program, step 8 , it is necessary to make address modifications before the program can be run successfully.

> Most of the address modifications can be made with the BASIC program. The program looks for JSR (hex 20/dec 32) and JMP (hex 4C/dec 76) values in the new memory locations. The majority of changes necessary were in those two instructions alone. When the program finds dec 32 or dec 76 followed by a location in pages 4 through 7 (where the original program was located), it modifies the page number to the relocated values, 28 through 31 respectively. This program is quite a bit slower than a machine language version, but it certainly runs faster than I could type in the changes.

> Since the BASIC program has flaws, it is important to check for errors. The
relocated monitor program contained two unnecessary changes which were easy to find and change back. I manually corrected these errors using a low monitor and looked for other locations which needed to be changed. All instructions besides JSR and JMP that have an absolute addressing mode referring to relocated addresses must be modified. Much harder to find are table values and page zero references. A source listing or disassembler output listing is essential.

I had the advantage of having the source code for the monitor (PET User Manual, p. 100) and changes were easier to identify. However, I have successfully relocated code with just a dissassembled listing and no comments or mnemonic variable names. A few examples of what to look for are shown in Figure 2.

The trickiest part of relocating involves indirect instructions. The instruction itself does not have to be changed, but the numbers stored as page 0 pointers may have to be. Somewhere in the code, there may be a combination like LDA $\$ 05 /$ STA $\$ 35$, which would have to be changed to LDA \#1D/STA

| $1 E 00$ | $9 A$ | $4 C$ | $8 B$ | $C 3$ | $A 2$ | 01 | DD | 02 |
| :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- |
| $1 E 08$ | $A 2$ | 09 | B5 | 10 | 48 | B5 | 11 | 20 |
| $1 E 10$ | 13 | $1 E$ | 68 | 48 | $4 A$ | $4 A$ | $4 A$ | $4 A$ |
| $1 E$ | 18 | 20 | $2 B$ | $1 E$ | $A A$ | 68 | 29 | $0 F$ |

$\$ 35$. The monitor program did not contain examples of these instructions.

A hex listing of the reloc ated monitor is shown in Figure 3. All functions have been checked and appear to be working.

The BASIC modify program changed 72 locations (2 of which were in error). I have underlined the correct changes and put an asterisk beside the corrected errors. Fourteen locations needed to be changed manually, and these have been boxed. By my count, more than 3/4 of the changes were made by the BASIC program. I was satisfied, but others may wish to write a more comprehensive utility.

Once properly moved and relocated, the monitor can be run (SYS 7183) and saved on tape ( $\$ 01,1 \mathrm{C} 00,1 \mathrm{F6B}$ ). The break vector is set automatically on entering the program. After the first run, the program can be restarted with SYS 1024 which is easier to remember. That trick takes advantage of the zero (BRK) first byte in every BASIC program.

Moving and relocating programs can be fun as well as useful. In some respects it's like a game or puzzle. I believe this is the aspect that appeals to me. I would enjoy hearing from other PETters about their success or failures in relocating programs (SASE for reply).
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# Life in the Fast Lane 

Richard R. Auricchio<br>1596 Stapleton Court<br>San Jose, CA 95118

> This high speed version of the game of LIFE uses lo-res graphics on the APPLE II. A clean assembly language implementation makes it easy to enhance or adapt.

What? Yet another game of LIFE? Yes, this one's for the APPLE II computer, and it's a fairly quick one. The game runs in Lo-Res graphics using a $32 \times 32$ array. The current version is black-and-white, but adding coler should not prove too difficult a task. The assembly language module which actually computes the generations is capable of running off about three per second on the APPLE screen.

The program is designed to utilize both of the APPLE's graphic screen buffers to avoid the ripple effect which occurs when the display is updated. When both buffers are used, the new image is created in the buffer not currently being displayed on the screen; after the complete image is created, the buffers are swapped via the hardware controls in the APPLE.

In addition to the two screen buffer areas, the actual LIFE generations are performed in a $32 \times 32$ array ( 1 K bytes). Separating the screen and LIFE array makes it easier to interface with the LIFE program from BASIC; in addition, a speed increase was realized because it was not necessary to "read" the screen points to compute the next generation. The code to perform an assembly language SCRN( $X, Y$ ) function, although short, requires computation of screen coordinates. This computation would cost valuable compute-time.

## Program Organization

There are two entry points to the LIFE program. One, which performs initialization, is used to clear the LIFE array and set up the screen buffer pointers. The second screen buffer is then blanked out by copying the first into the second.

The second entry is the "run" entry to perform LIFE generations. This is the main part of the LIFE program. It runs until either the screen becomes completely blank, or the user hits any key on the APPLE keyboard. The program will not detect a stable LIFE partern. It will keep running more generations even though the display does not appear to change.

The LIFE program makes two passes over the LIFE array to compute each generation. The first pass sets up pending births and deaths within the array. This is done by accessing cells (neighbors) which border the current cell being examined. The array is allowed to wrap around and going off one edge brings you back in on the other side. The second pass completes the birth/death process, displays the cells in the inactive screen buffer, and swaps the screens. This process continues until all cells die or a key is hit. In either event, a return is made to the BASIC program; the screen and LIFE array are not altered. This allows the BASIC program
to actually edit the LIFE array, say, to add/delete cells or to center the image on the screen.

## Driving the Program from BASIC

A simple Integer BASIC driver is included here: It allows one to type in points until ( 0.0 ) is entered, and then calls the LIFE program to display generations on the screen. No fancy editing facilities have been coded, but they're easy enough to add if you find them useful.

## Structuring the Code

The LIFE program was coded using straightforward techniques. No tricks or shortcuts were used to save a byte here, a microsecond there. Comments have been sprinkled throughout the listing to enable changes or customization of the module, and coding tricks might have made that next to impossible.

## Use with APPLE DOS

The LIFE program is completely compatible with APPLE DOS (both versions 3.1 and 3.2). There are no memory areas used which will conflict with DOS usage, and no DOS features are affected by running LIFE. Users with DOS systems should BSAVE the LIFE module and insert an appropriate BLOAD command at the beginning of the BASIC driver program.



| APTR | * | \$0000 | LIFE ARRAY POINTER (LO) |
| :---: | :---: | :---: | :---: |
| NP'TR | * | \$0002 | NEIGHBOR CELL POINTER (LO) |
| NNUM | * | \$0004 | NUMBER NEIGHBOR CHECKS |
| NCNT | * | \$0005 | NEIGHBOR COUNT |
| NCELLS | * | \$0006 | NUMBER LIVE CELLS |
| CRT | * | \$0007 | CRT OFFSET: $00=1 \mathrm{ST}, 04=2 \mathrm{ND}$ |
| COLOR | * | \$0030 | PLOT COLOR |
| GBASH | * | \$0027 | GRAPHIC BASE ADDRESS (HI) |
| A1L | * | \$003C | MONITOR WORK BYTES |
| A 1 H | * | \$003D |  |
| A2L | * | \$003E |  |
| A2H | * | \$003F |  |
| A4L | * | \$0042 |  |
| A 4 H | * | \$0043 |  |
| ASTART | * | \$000C | START PAGE FOR ARRAY |
| AEND | * | \$0010 | END Page for array |
| KB | * | \$C000 | KEYBOARD INPUT ADDRESS |
| KBS | * | \$C010 | KEYBOARD STROBE CLEAR |
| CRTFLI | * | \$C054 | C054/C055 FLIPS CRT |
| gBaSCA | * | \$F847 | CALCULATE PLOT ADDRESS |
| MOVE | * | \$F32C | BLOCK MOVE ROUTINE |

- MEMORY LAYOUT:

| * | PAGE (S) | CONTENT |
| :--- | :--- | :--- |
| * | $04-07$ | CRT\#1 |
| * | $08-0 B$ | CRT 2 |
| * | OC-OF | LIFE ARRAY |
| * | $10-11$ | PROGRAM |
|  |  |  |
|  |  |  |
|  |  | $\$ 1000$ |- call to tnit utle clear the life array

* and set up the appropriate crt pointer
100020 DA 10 INIT JSR ORIGIN SET ARRAY POINTER
1003 A9 04 LDAIM $\$ 04$ SET CRT TO SECOND
10058507 STA CRT FOR THE FIRST GENERATION
1007 AO 00 LDYIM $\$ 00$ ZERO INDEX
100998
100A 9100
100C 20 E5 10
100F 90 F8
CLRA TYA
STAIY APTR CLEAR ARRAY BYTE
JSR BUMP BUMP TO NEXT BYTE
BCC CLRA $=>M O R E$ TO DO
    * CLEAR SECOND CRT buFFER

| 1011 | A2 00 | LDXIM $\$ 00$ |  |
| :--- | :--- | :--- | :--- |
| 1013 | 86 | $3 C$ | STX A1L | | SET UP ADDRESSES |
| :--- |
| 1015 |
| 86 |
| 42 |


-
PASS2 WILL DISPLAY THE

- array by plotting points
- in CRT \#1 OR \#2 and
* WILL then Swap screens
* 

1082 B1 00
1086 OA
10879100
1089 F0 02
108B A2 FF
108D 8630
108F A9 1 F
10912500
109318
10946904
1096 A8
1097 A5 00
1099 4A
109A 4A
109 B 4 A
109C 4a
109D 4A
109E 8504
10A0 A9 03
10A2 2501
10A4 OA
10A5 0A
10A6 OA
10A7 0504
10A9 18
10AA 6904
10AC 20 F9 10



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Very often it is desired to measure the time between two events, such as the start and end of a race or the time taken to respond to a given stimulus. The time between events can occur from a given start pulse to each succeeding pulse as follows:


This article will use the SYM-1 board's 6532 timer and keyboard display to create a device which can measure up to 50 time intervals, store them in memory and then read them out one at a time.

The first segment of the program is a loop loop to look for the start pulse, set up the 6532 timer and then look for the stop pulse. To measure the time between events, the 6532 generates an interrupt whenever it times out. The interrupt routine increments a 6-digit counter which counts the number of time intervals until the stop pulse is found. With minor modifications the program can accomplish both types of event measurement mentioned earlier.

For example, the program listing shown will measure time events per Figure 2. In order to measure as per Figure 1, change the BNE (24B) instruction to jump to STOP 1. The number of time events is fixed by $N$. The last segment of the program is the readout routine. This routine will read out each time interval from 1 to $N$, stopping so that the answer can be written down before going on automatically to the next. After completing the routine, the program jumps back to the beginning.

The time interval increments can be changed by accessing different dividers of the 6532 and changing the timer count

To operate the program the following steps are necessary:

1. Enter in location $N$ the number of time intervals to be measured. (In HEX, less than 31.)
2. Decide what type of time intervals are to be measured (i.e. Figure 1 or Figure 2).
3. Decide time interval needed and enter VAL from Table 1.
4. Start program at location 200.
5. Display results by hitting 1 on the keyboard.

The interface hardware to the event timer can be a 556 timer connected as shown in Figure 3.
The input signals can be derived from switches, light coupled devices or transducers. The output pulses are 50 microseconds wide and positive going. This conditions the input pulses so the software can look for a minimum width pulse. The only other hardware required is a SYM-1 board, cassette and power supply.


Figure 3: Connecting a 556 timer for use as an interface to the event timer.


## Table 1: Time Interval Data

TIME INTERVAL
100 MICROSECONDS
1 MILLISECOND
10 MILLISECONDS
100 MILLISECONDS

VALUE
49
7A
9 C
62

ADDRESS
(253 of Program)
A41C
A41D
A41E
A41F

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## AIM-65 in the Ham Shack

Have a field day with this message transmitter ancl keyer. It will accept and save messages to be broadcast automatically, as needed, in response to a single keystroke.


Figure 1: Buffer circuit to key the transmitter. Some transmitters may require the keying relay. Only two ICs, a 74LS00 and a 7402 are required to construct this interface and the keyer interface in Figure 2. Both ICs have pin 14 as +5 V and pin 7 as GND.

Dr. Marvin L. De Jong Department of Mathematics and Physics The School of the Ozarks Point Lookout, MO 65726

Contest operating is a lot easier if standard messages such as "CQ CQ VQ DE KOEI KOEI K" can be sent automatically. The program listed in Table I in the AIM 65 disassembly format allows you to do just that. It has the following features:

1) Three different messages, called $\mathrm{A}, \mathrm{B}$, and C , may be stored in one page of memory. The total number of characters, including spaces, may not exceed 256 characters.
2) The messages are composed and edited using the AIM 65 keyboard. As the message is typed on the keyboard, it appears on the display and scroils left. The delete key allows corrections to be made. The carriage return key signals that the message is complete, and the display blanks in preparation for the next message.
3) When all three messages are entered the display blanks again, and you enter your code speed in words per minute (in decimal). The code speed will remain displayed until new messages are entered by restarting the program.
4) With the messages and code speed loaded, a depression of the A key will result in message A being sent, the $B$ key will cause the $B$ message to be sent, and the $C$ key will cause the $C$ message to be sent.
5) The code speed can be changed at the end of any message by pressing the S key. This display will blank, and a new code speed can be entered.
6) A simple interface circuit and an interrupt routine allow the same program to be used as a keyer, operating at the same speed as the speed entered on the keyboard. You must provide paddle, or modify a bug to make the mechanical connections.
7) Code speeds from 5 wpm to 99 wpm are possible for the message sender and keyer, though it is


Figure 2: Interface circuit for the keyer. All connections are to the Applications Connector on the AIM 65.
unlikely that any of us will ever send 99 wpm with a keyer.

Before illustrating how the program might be used, let me point out that a similar program for the KIM-1 is scheduled to appear in the September or October issue of 73 Magazine, and the details of its operation are described there. Only a few features unique to the AIM 65 version will be given here. Also, most of the display routines have been described in a companion article in MICRO, and they will not be described again.
Let me describe how the keyer and message sender might be used for Field Day. You would start the program, then load message A as follows: "CQ CQ CQ FD DE KOEI/O KOEI/O K" Expecting someone to respond to your CQ, message B would be "DE KOEI UR MO MOBK" To use message B, you would first send the other guy's call with the keyer and then hit key B on the keyboard. The blanks, spaces inserted by pressing the keyboard, would be filled by you, again using the keyer, to give the proper signal report. Message C might read "QSL ES TU OM" and would be sent after you received his signal report and section correctly. It would not be difficult to modify the program for sweepstakes or


| 0232 | C9 00 | CMPIM | \$00 |
| :---: | :---: | :---: | :---: |
| 0234 | FO 13 | BEQ | \$0249 |
| 0236 | 990001 | STAY | \$0100 |
| 0239 | 8A | TXA |  |
| 023A | 48 | PHA |  |
| 0238 | 890001 | LDAY | \$0100 |
| 023E | 207203 | JSR | \$0372 |
| 0241 | 206003 | JSR | \$0360 |
| 0244 | 68 | PLA |  |
| 0245 | AA | TAX |  |
| 0246 | C8 | INY |  |
| 0247 | DO CE | BNE | \$0217 |
| 0249 | 8A | TXA |  |
| 024A | 48 | PHA |  |
| 024B | 209803 | JSR | \$039B |
| 024E | 206003 | JSR | \$0360 |
| 0251 | 68 | PLA |  |
| 0252 | AA | TAX |  |
| 0253 | 88 | DEY |  |
| 0254 | 9404 | STYZX | \$0004 |
| 0256 | C8 | INY |  |
| 0257 | E8 | INX |  |
| 0258 | E0 03 | CPXIM | \$03 |
| 025A | 90 B9 | BCC | \$0215 |
| 025C | 209803 | JSK | \$0398 |
| 025F | 206003 | JSR | \$0360 |
| 0262 | 20 3C E9 | JSR | \$E93C |
| 0265 | 48 | PHA |  |
| 0266 | 207203 | JSR | \$0372 |
| 0269 | 206003 | JSR | \$0360 |
| 026C | 68 | PLA |  |
| 026D | 38 | SEC |  |
| 026E | E9 30 | SBCIM | \$30 |
| 0270 | OA | ASLA |  |
| 0271 | OA | ASLA |  |
| 0272 | OA | ASLA |  |
| 0273 | OA | ASLA |  |
| 0274 | 8511 | STAZ | \$0011 |
| 0276 | 20 3C E9 | JSR | \$E93C |
| 0279 | 48 | PHA |  |
| 027A | 207203 | JSR | \$0372 |
| 027D | 206003 | JSR | \$0360 |
| 0280 | 68 | PLA |  |
| 0281 | 38 | SEC |  |
| 0282 | E9 30 | SBCIM | \$30 |
| 0284 | 18 | CLC |  |
| 0285 | 6511 | ADCZ | \$11 |
| 0287 | 48 | PHA |  |
| 0288 | 29 FO | ANDIM | \$F0 |
| 028A | 4A | LSRA |  |
| 028B | 8510 | STAZ | \$0010 |
| 028D | 4 A | LSRA |  |
| 028E | 4A | LSRA |  |
| 028F | 18 | CLC |  |
| 0290 | 6510 | ADCZ | \$0010 |
| 0292 | 8510 | STAZ | \$00 10 |
| 0294 | 68 | PLA |  |
| 0295 | 29 OF | ANDIM | \$0F |
| 0297 | 6510 | ADCZ | \$0010 |
| 0299 | 8510 | STAZ | \$0010 |
| 029B | 38 | SEC |  |
| 029C | A2 00 | LDXIM | \$00 |
| 029E | A9 94 | LDAIM | \$94 |
| 02A0 | 8508 | STAZ | \$0008 |
| 02A2 | A9 04 | LDAIM | \$04 |
| 02 A 4 | 8509 | STAZ | \$0009 |
| 02A6 | A5 08 | LDAZ | \$0008 |
| 02A8 | E5 10 | SBCZ | \$0010 |
| 02AA | 8508 | STAZ | \$0008 |
| 02AC | A5 09 | LDAZ | \$0009 |
| 02 AE | E9 00 | SBCIM | \$00 |
| 02B0 | 8509 | STAZ | \$0009 |
| 02B2 | E8 | INX |  |
| 02B3 | B0 F1 | BCS | \$02Áó |


other contests. Any time you viant to insert something in a message, Je sure to leave enough time, in ASCII spaces, to key in the insert. You soon get the hang of working so smoothly that no one will recognize your insert for what it is.

Some notes about the proyram may be useful if you want to modify it for your own purposes. Instructions starting at $\$ 0200$ and ending at $\$ 025 \mathrm{~F}$ are used to load the three messages. The instructions starting at $\$ 0262$ and $3 n d i n g$ at $\$ 0285$ are used to enter the code speed in decimal, convert the ASCII representations to decimal numbers, and store the result in location $\$ 0011$. The instructions starting at $\$ 0287$ and 3 nding at $\$ 0299$ are used to convert this decimal number to a hexadecimal number and store it in location $\$ 0010$. The instructions starting at $\$ 029 \mathrm{~B}$ and ending at \$02B5 are used to convert the :ipeed to a number to be loaded into the divide-by-1024 timer. The remainder of the program tests for A, B, or C key depressions, and it calls on var ous subroutines to send the message If you do not want to use the AIM 65 as a keyer, then you may omit the interrupt routine.

Note that in my program listings I used page one, addresses $\$ 0100$ and upward to store the message. I would not recommend this, but since I have only 1K of memory, I could not usie $\$ 0400$ to $\$ 04 F F$. If you have more than 1 K of memory, I would urge you to change all the $\$ 0100$ addresses in the program to $\$ 0400$ or the page of your own choosing. These instructions are located at $\$ 0221$, \$0236, \$023B, and \$02EF.

The interface circuits are shown in Figures 1 and 2. Figure 1 gives a circuit that simply buffers the output of the PBO pin to key my transmitter. A optional relay may be required for other transmitters. The NOR gates were used because I needed a NOR gate in the keyer circuit, and the NOR gates allow you to OR your own keyer to the message sender circuit. The keyer circuit is shown in Figure 2. Basically it debounces the dot and dash paddle connections, and it may be reset with a pulse frory pin CB2.

When the key is put in either the dot or dash position, a negative going signal is produced at pine one of the 7402. A negative pulse on CB1 produces an interrupt, so the program jumpss to process the interrupt routine. In the interrupt routine Port B is read (LDA A000), producing a negative pulse on CB2. This negative pulse will reset the crosscoupled NAND gates if the sey is up, otherwise pin one of the 7402 will stay at logic zero. As long as it is at logic zero the program continues to serd dots (or dashes). Reading Port B also clears the interrupt flag on the 6522. As soon as PB6 is set to logic one by the negative pulse from CB2, the interrupt routine is completed and execution continues in the main program.

| 033A 8D 97 A4 |  | STA | $\$$ A 497 |  |
| :--- | :--- | :--- | :--- | :--- |
| 033D 2C 97 A4 |  | BIT | $\$$ A 497 |  |
| 0340 | 10 FB |  | BPL | $\$ 033 \mathrm{D}$ |
| 0342 | 60 |  | RTS |  |
|  |  |  |  |  |
|  |  |  | INTERRUPT | ROUTINE |


| 034348 | PHA |  |
| :---: | :---: | :---: |
| 03448 A | TXA |  |
| 034548 | PHA |  |
| 0346 AD 00 A0 | LDA | \$A000 |
| 03493006 | BMI | \$0351 |
| 034 B 201 A 03 | JSR | \$031A |
| 034 E 4 C 5403 | JMP | \$0354 |
| 0351203303 | JSR | \$0333 |
| 0354 AD 00 AO | LDA | \$ ${ }^{\text {0 }} 000$ |
| 0357 OA | ASLA |  |
| 035810 EC | BPL | \$0346 |
| 035A 68 | PLA |  |
| 035B AA | TAX |  |
| 035C 68 | PLA |  |
| 035D 40 | RTI |  |

$\#$

* DISPLAY
* SUBROUTINU

| 0360 |  |
| :---: | :---: |
| 0360 | A2 13 |
| 0362 | 8A |
| 0363 | 48 |
| 0364 | BD 38 A 4 |
| 0367 | 0980 |
| 0369 | 20 7B EF |
| 036C | 68 |
| 036D | AA |
| 036E | CA |
| 036F | 10 F 1 |
| 0371 | 60 |

* 
* MODIFY SUBROUTINE

0372 8D 4C A4
0375 A2 01
0377 BD 38 A4
037A CA
037 B 9 D 38 A 4
037E E8
037F E8
0380 E0 15
038290 F3
038460

| STA | \$A44C |
| :---: | :---: |
| LDXIM | \$01 |
| LDAX | \$ 4438 |
| DEX |  |
| STAX | \$A438 |
| INX |  |
| INX |  |
| CPXIM | \$15 |
| BCC | \$0377 |
| RTS |  |
| - backspace |  |
| - Subroutine |  |
| LDXIM | \$12 |
| LDAX | \$A438 |
| INX |  |
| STAX | \$A438 |
| DEX |  |
| DEX |  |
| BPL | \$0387 |
| LDAIM | \$20 |
| STA | \$A438 |
| JSR | \$0360 |
| RTS |  |
|  |  |
| * Clear subro | UTINE |
| LDXIM | \$13 |
| LDAIM | \$20 |
| STAX | \$ 4438 |
| DEX |  |
| BPL | \$039F |
| RTS |  |

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## mJopobes

Stephen Bach of Rt 2, Box 50A1, Scottsville, VA reports that the 24 Hour AIM Clock program on MICRO 10:7 should contain F8 (SED) at location 0305, rather than 38 (SEC) as published.

Lt. Robert Carlson speculates that his article on A Baudot Teletype Driver for the APPEE II in MICRO $14: 5$ was mutilated by the editorial staff. It's true: location 037C should contain 68 (RORA), rather than $6 E$ ( $R O R$ ), and the spurious operand bytes at 037 D and 037 E should be removed to close up the code. In addition, the following lookup table should follow the program code, beginning at location 0381, immediately afier the RTS which moved upward two bytes:

```
0381-0245 OA 41 20 53 49
0388-55 OD 44 52 4A 4E 46 43
0390-4B 54 5A 4C 57 48 59 50
0398-51 4F 42 47 06 4D 58 56
03A0-00 99 3399 2D 99 07 38
03A8- 3703 24 34 27 2C 21 3A
0380-28 35 22 29 32 23 36 30
03B8-31 39 3F 26 99 2E 2F 3B
03C0-99 00 00 co 00
```

Roger Cohen, 100 Nimbus Road, Holbrook, NY 11741 reported the same editorial slip-up.

Several readers reported problems with the AMPERSORT article on MICRO 14:47. G. Lewis Scott of 6220 Colchester Place, Gharlotte, NC $282 \%$ sent in seven corrections to the assembly language source:

Line 0370 should be 5201-20 E7 54
Line 0400 should be 5207- DD 2D 55
Line 1180 should be 5298- BD 3355
Line 1240 should be 52A5- 20 OA 55
Line 2810 should be 53CA- 20 C2 54
Line 2870 should be 53D8- 20 c2 54
Line 3320 should be 5425-20 OA 55
and Mr. Scott noted some additional problems once he got the program running. William $G$. Trawick of the Georgia State University Dept. of Chemistry in Atlanta, GA 30033 reported some of the same microbes. Mark Crosby of Washington APPLE Pie and 1373 E Street SE, Washington DC 20003 is also working on these difficulties. Alan G. Hill, the author of AMPERSORT, is collating corrections to these problems, most of which developed when last minute enhancements were integrated into the source. If you have keyed in AMPERSORT, save that tape! Any final patches will appear next month.

Peter J. Sleggs of 1208 Hal Moon Lane, Oakville Ontarto L6F $2 E 5$ reports that the EKIM Extension to the KIM monitor in MIGRO 11:20 will not perform as expected in the autoincrement and branch modes. He suggests changing 17D1 from BO AD (BCS START) to BO B4 (BCS GETK). Mr. Sleggs included an insightful enhancement to this routir e; however, another very elegant enhancement arrived from

Gary A. Focte reports that his article on Sorting with tre APFLE, in MiCRO 13:22, should have line 80 ret ding:
80 I=J=K=L $=M=X=T=Z=L L=I I=L M=B M=W=N$ whereas, ir the original article, the $N^{n}$ was inadvertent ly omitted. Also, for 48 K system operation, line 90 should be changed so that it does not ex ceed the 32767 1imit. It should be: 90 LM=PEEK (204) + PEEK (205):256: $\mathrm{HM}=32767$

Aalph W. Leiper of 18 Alberta Street, Vindsor Locks, CT 18096 noticed a microbe in Harmonic Analysis fir the APPLE, MICRO 13:5, which works perfectly inless one of the harmonics happens
to be off scale. His fix is easy, Change line 1290 and $1: 00$ to read:
1290 S=70: REM SETTING INITIAL SCALE
1300 PRINT: PRINT: PRINT PRLOT OE INPUT DATA Calcu ated to rifth harmonic> 1 at 100 $=\mathrm{H}$ IT: $\mathrm{H}=0$ : HGR
He also mace the following changes to improve readability of the graphics output:
Add: 1325 日PLOT 0,79
Chg: 1360 HPLOT TO k, 79- Y
This plots the original curve as a solid line which will stand out from the harmonics.

William 0. Taylor writes of an error in his article, Tre Basic Morse Keyboard, MICRO 13:13. The tone bcard schematic bas output from the computer ard $+5 V$ power reversed! Exchange PBO and +5 t correct. Although the parts list shows a 50 yf cap while the schematic shows 35 yf for 12 , either value will work. Finally, line 5 of the BASIC program should include keyword PRI NT before the output string.

The articlu, APPLE II Serial Output Made Simple In MICRO 1655 contains a full page of extraneous code in 15:7. A11 of page 15:7 should be removed frum this artiele. This was another example of editorial staff confusion, hopefully exhausting our guota for many months.

You may wr te or telephone MICRO to obtain the current stitus of any published program.

# Speech Processor for the PET 

> A speech processor unit samples audio waveforms and digitizes the input signal. Digitized speech can be stored, catalog. ed, processed as discrete data, and output through a D/A converter. The output speech quality rivals that of a CB radio.

Within the past year a low cost speech processor unit has appeared on the market. This device designed for the computer hobbyist can be used in a variety of applications from voice augmentation to computer games to direct computer-to-phone modem implementations.

This article will briefly describe the device and how the unit can be interfaced to a PET computer. A software driver program capable of storing the digitized sound, playing it back, saving the processed information on cassette files, and then reloading it will also be presented. The article will conclude with an illustration of how this device might be used with a home computer system.

## The Speech Processor Unit

The speech procesor unit used in this article is the DATA-BOYTM Speech Processor developed by Mimic Electronics Company. This processor is an extremely low-cost audio signal processing system designed for the hobbyist. The speech processor is essentially a speech "digitizer" which uses a proprietary signal processing technique to convert the human voice into a single bit stream, and vice versa. "Digitized" speech is typically thought of as speech which has been sampled with an analog-to-digital form, and then reconverted to analog form by a digital-to analog (D/A) converter.

By using certain characteristics of the speech waveform, especially the fact that the amplitude components tend to decrease with increasing frequency, the resolution of the A/D and D/A converters required can be decreased from, say, 8 bits down to a single bit while maintaining intelligibility. When this bit stream is sampled at a rate of 8000 samples per second, highly intelligible speech can be obtained. The speech quality is close to that which is given by a CB radio.

## Speech Processor Interface

Figure 1 describes the components necessary to support the speech processor and its interface with the PET Computer. To digitize and then reproduce speech the speech processor unit requires the use of an additional
speaker, microphone and poiver supply. The speech processor unit i.s designed on a 3 inch by 5 inch printed circuit board. The author's unit was built into a 9 by $51 / 2$ inch box which also contained the power supply. The simple power supply design was taken directly from the users manual provided with he speech processing unit.

In addition to the interfaces shown in Figure 1, the author added two addjtional features to his unit. To determine when the squelch threshold level was exceeded one side of a light emitting diode was connected to the DATA F EADY line. The other side of the LED was connected to the +5 volt supply through a 300 ohm registor. When the squelch threshold level is exceeded the DATA READY line goes low and LE[' glows.

A computer bypass switct was also added to the author's unit. This switch allows the TO COMPUTER ind FROM COMPUTER lines to be dirsctly interconnected or interfaced to the computer. This feature allows t ie speech processor system to be tested independent of the computer. It also allows the user to demonstrate the difference in intelligibility produced by the somputers quanitization effects.

The Speech Processor Unit is interconnected to the PET Computer by three lines. Each line is accompanied by a ground to provide some degre: of shielding. The TO COMPUTER lire is connected to PAO (Pin C) on the IJSER Connector. This line will be samfled by the processor at the proper da a rate to quanitize the input data st eam. The digital output data stream wil be returned to the speech processor init on the line marked FROM COMPUTEI2. This line is attached to PA7 (Pin L) on the USER Connector. A third line termec the DATA READY line is used to indicale if the input signal level exceeds the threshold established in the speech processor unit. The DATA READY line is zonnected to PA1 (Pin D) on the PET USER Connector.

## Software Description

The software used to drive the speech processor device is written in two parts: A User Interface Program written in BASIC and a pair of Speech Prjcessor to

Charles R. Husbands
24 Blackhorse Drive
Acton, MA 01720

Computer Interface Programs written in machine language. The User Interface Program is designed to allow the user to interact easily with the speech processor. This program provides four user options: RECORD, PLAYBACK, SAVE and LOAD.

The RECORD Program calls one of the machine language interface programs which samples the state of the speech processor bit stream and stores the sampled input data into sequential locations of buffer memory.

The PLAYBACK process is the direct counterpart of the RECORD process. In this mode each word in the buffer memory is unpacked and returned to the speech processor to reproduce the speech data examined during the RECORD sequence. The PLAYBACK process like the RECORD process calls a supporting machine language program.

The SAVE routine is a data file storage program which allows the user to save all or some portion of the recorded data on tape for later use.

The LOAD routine is a data file retrieval program which allows digital date files stored on tape by the SAVE program to be restored into the computers memory. Both the SAVE and LOAD routines allow the user to designate the beginning and ending address of the data to be manipulated. With this facility data words stored in memory can be saved and rearranged in order to build a data base where the beginning of each utterance or sound is uniquely defined.

An illustration of the memory map organization used to support the speech processor unit is shown in Figure 2. From this map it can be seen that the machine language programs required to support the BASIC programs are stored in tape buffer \#2. In order to establish sufficient buffer memory to store the digitized speech information, a cap was placed on the BASIC program. By forcing the BASIC Interpreter into thinking it is operating with a 4 K memory limitation, the upper 4 K of memory can be used for storing the recorded digitized speech. A small number of bytes in zero page working storage are used for pass-


Figure 3: Speech processor flowchart


Figure 1: Speech processor components


Figure 2: Speech processor memory map
ing variables between the machine language programs and their BASIC counterparts.

## The Record Program

The RECORD process is entered by pressing " R ". This action causes the pointing vector corresponding to the beginning address of the RECORD machine language program to be placed into locations 1 and 2. A value of 02 is also placed in decimal location 60 , which is the squelch mask value to be used in the machine language routine. The machine language program is then entered by executing the USR instruction.

A flow diagram of the RECORD Program is shown in Figure 3 and a machine language listing of the process is also given. After initialization, the program loops waiting for the DATA READY line to go low. This action occurs when the amplitude of the voice level exceeds the squeich threshold set on the speech processor board. Once the squelch level is sensed, the program proceeds and the record program initialization commences.

A machine language listing of the SQUELCH Process is shown. If the user wishes not to implement the squelch test, the values in line 610 of the program can be changed to:

610 POKE 01, 58 : POKE 02,03 and the record program will be entered directly.

After initialization the record process beings. The value of TO COMPUTER line is sampled at PAO of the user's port and stored into the LSB of the buffer location DATAWD. A counter (BITCNT) is then tested to see if 8 samples have been sensed. If 8 samples have not yet been sensed, DATAWD is shifted left one place and a delay loop is executed before the value of the next bit is sampled. When a full byte of data has been received, the byte is stored away in the next memory locatlon. The values of DATAWD and BITCNT are reinitialized. A short delay loop is executed and the process is repeated.

At the time that DATAWD is stored away the location into which it is being stored is checked against an upper bound pointer in memory. When the two address correspond, the process has run out of available buffer space for the record process and the machine language routine returns control to the BASIC program. Completion of the RECORD process is indicated by the line "RECORD PROCESS COMPLETED" appearing on the display screen.

## Playback Program

The PLAYBACK program is entered by pressing " $P$ ". This action forces the pointing vector corresponding to the
beginning address of the playback machine language program tc be placed into locations 1 and 2. This machine language program is then entired by the execution of the USR instruction. A flow diagram of the PLAYBACK program is shown in Figure 4. A machine language listing of the PLAYBACK prog am is also given.

The PLAYBACK program repeats the same basic process developed in the record process. As each new byte is retrieved from the buffer mamory, the state of the most significant bit is outputted to the speech processor unit. After a finite delay, the D.ATAWD is shifted left one position and the state of the new MSB is sent to the speech processor. When all the bits have been examined, the next byte in buffer memory is retrieved. When all of the bytes in the data buffer memory have been examined, the PLAYBACK process is completed and, the message "PLAYBACK PROCESS COMPLETED' appea's on the monitor screen.

## Save Process

The SAVE process is a EiASIC program written to allow the user to dump portions of the buffer memo'y on tape for later use. The process is antered by pressing " S ". A prompting message asks the user to enter the $d \in$ sired starting address and ending address in buffer memory to be saved. The contents of the memory locations between the two selected locations is then written on tape and upon completion of :his operation the message "SAVE PROCESS COMPLETED" appears on the monitor.

## Load Process

The LOAD program is also a BASIC routine designed to load into memory a tape prepared by the SAVE program. To enter the LOAD process the user presses the " $L$ " key. A message will prompt the user to enter the slarting and ending address of the data file to be stored. When the LOAD process is completed the message "LOAD PROCESS COMPLETED" will appear on the screen.

## Typical Application

For an illustration of how these programs might be employed, let's assume the user wants to have his computer automatically dial up telephone numbers. Using the speech processor and the RECORD process, each dual tor e multiple frequency (DTMF) output is recorded from a standard touch tone telephone. As each tone is recorded, a de.ta file can be written using the SAVE prc gram. The starting locating for each tone would be the beginning of buffer memory. The ending address could be set at the beginning of memory plus, say 200 bytes. After all ten tones have been recirded, the data files collected by the SAVE program can be stacked consecutively on

| 600 | REM. , RECORD MODE. |
| :---: | :---: |
| 610 | POKE 01,210: POKE 02,03 |
| 615 | POKE 60,02 |
| 620 | LET X $=$ USR (R) |
| 630 | PRINT" RECORD PROCESS COMPLETED" |
| 640 | GOTO 420 |
| 700 | REM. PLAYBACK MODE. |
| 710 | POKE 01,130: POKE 02,03 |
| 720 | LET X=USR (R) |
| 730 | PRINT" PLAYBACK PROCESS COMPLETED" |
| 740 | GOTO 420 |
| 800 | PRINT"**SAVE PROCESS INITATED" |
| 805 | INPUT" FILE NAME"; N\$ |
| 810 | INPUT'' INPUT STARTING ADDRESS'; S |
| 820 | INPUT'" INPUT ENDING ${ }^{\text {a }}$ (DRESS" ; $\Xi$ |
| 825 | POKE 243,122: POKE 244,02 |
| 830 | OPEN 1,1,1,N\$ |
| 840 | FOR I $=0$ 20 (E-S) |
| 850 | PRINT \#1, PEEK(S+I) |
| 860 | NEXT I |
| 870 | CLOSE 1 |
| 880 | PRINT"*ぇSAVE PROCESS COMPLETED**" |
| 890 | GOTO 420 |
| 900 | PRINT"**LQAD PROCESS INT TATED" |
| 905 | INPUT'' FILE NAME'; N\$ |
| 910 | INPUT' INPUT STARTING ADDRESS'; S |
| 920 | INPUT' ${ }^{\prime \prime}$ INPUT ENDING ADDRESS'; E |
| 925 | POKE 243, 122:POKE 244,02 |
| 930 | OPEN 1,1,0,N\$ |
| 940 | FOR I=0 50 (E-S) |
| 950 | INPUT \#1,A |
| 960 | POKE (S+I) , 4 |
| 970 | NEXT I |
| 980 | CLOSE 1 |
| 990 | PRINT'"**LQAD PROCESS COMPIETED**' |
| 995 | GOTO 420 |
| 1000 | END |

200 byte boundaries using the LOAD program. We would now have a data base in buffer storage with each tone starting and ending on a known boundary.

In order to now dial any number, a small BASIC program wouid be required to call the PLAYBACK program with the appropriate starting boundary and ending boundary addresses in the required sequence. The resulting tones developed through the speech processor would then be acoustically coupled to the telephone to complete the process.

## Conclusions

This paper was designed to illustrate how a low cost speech processor might be interfaced with a PET Computer. However, the same machine language software could be used to interface the device to any 6502 based processor with only slight modifications. The use of BASIC in this application provided an easy method of mechanizing the manmachine interface. The application of voice or sound feedback in computing is almost limitless and it is hoped that this article illustrates one method of achieving this goal.
$\mu$

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| 03CD | datawd | * | \$0036 |  |
| :---: | :---: | :---: | :---: | :---: |
| O3CD | BITCNT | * | \$0037 |  |
| 03 CD | WDE | * | \$0038 |  |
| 03CD | DLYCNT | * | \$003A |  |
| 03CD | endbuf | * | \$003B |  |
| 03CD | MASK | * | \$003C |  |
| 03 CD | PADD | * | \$5843 |  |
| $03 C D$ | PAD | * | \$E34F |  |
| 03jA |  | ORG | \$033A |  |
| 033A A9 00 | INIT 1 | LDAIM | \$00 | SET UP DIRECTION REG |
| 033C 8D 43 E8 |  | STA | Padd | as an input |
| 033F 8535 |  | STA | DATAWD | DATAWD $=0$ |
| 03418538 |  | STA | WDB |  |
| 0343 A8 |  | tay |  |  |
| 0344 A9 08 |  | LDAIM | \$08 |  |
| 03468537 |  | STA | BITCNT | BITCNT $=8$ |
| 0548 A9 OC |  | LDAIM | \$0C |  |
| 034A 853 A |  | STA | DLYCNT | DLYCNT $=12$ |
| 03.4C A9 OC |  | LDAIM | \$0C |  |
| $034 E 8539$ |  | STA | WDB | +01 WDE+1 = 12 |
| 0350 A9 1E |  | LDAIM | \$1E |  |
| 0352853 B |  | STA | Endeuf | ENDBUF $=$ \$1E |
| 0354 LE 4F E8 | LOCP 1 | LSR | PAD | PICK UP DATA BIT |
| 03572636 |  | ROL | DATAMD | Store in lsb of datamd |
| 0355 C6 37 |  | DEC | BITCNT |  |
| 0358 A5 37 |  | LDA | Bitcnt |  |
| 035D F0 OC |  | BEQ | STRG1 |  |
| 035F A5 3A | DLY 1 | LDA | DLYCNT | delay for 8Khiz Rate |
| 0361 AA |  | TaX |  |  |
| 0362 CA |  | DEX |  |  |
| 0365 8A |  | TXA |  |  |
| 0364 DO FC |  | BNE | DLY 1 | +03 |
| 0366 EA |  | NOP |  |  |
| 05674 C 5403 |  | JMP | LOOP 1 |  |
| 036A EA |  | NOP |  |  |
| 036B A5 36 | STRG 1 | LDA | DATAWD |  |
| 036D 9138 |  | STAIY | WDB |  |
| 036F EA |  | NOP |  |  |
| 0370 C8 |  | INY |  |  |
| 0371 A9 08 |  | LDAIM | \$08 |  |
| 03738537 |  | STA | BITCNT |  |
| 037598 |  | TYA |  |  |
| 0376 DO DC |  | BNE | LOOP1 |  |
| 0378 E6 39 |  | INC | WDB | +09 |
| 037A A5 39 |  | LDA | WDE | +01 |
| 037 C C5 3E |  | CMP | ENDBUF |  |
| 037 E D0 D4 |  | BNE | LOOP 1 |  |
| 038060 |  | RTS |  |  |
| 0381 EA |  | NOP |  |  |
| 03D2 |  | ORG | \$03D2 |  |
| 03D2 A9 00 | SQUELC | LDAIM | \$00 |  |
| 03D4 8D 43 E8 |  | STA | PADD |  |
| 03D7 AD 4F E8 |  | LDA | Pad |  |




| SYMBOL TABLE 20002066 |  |  |  |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| BITCNT | 0037 | Datawd | 0036 | DLYCNT | 003A | DLYQ | 035F |
| DLYR | 03A2 | ENDBUF | 003B | INITQ | 033A | INITR | 0382 |
| LOOPQ | 0354 | LOOPR | 03AA | MASK | 003 C | PADD | E843 |
| PAD | E84F | SQUELC | 03D2 | STRGQ | 036E | STRGR | 03 E 7 |

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| OFF | APPEND | DUMP |
| FIND |  |  |

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```
HELP
    500 J = SQR(A* B/C)
```


## READY

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[^0]
# Tiny PILOT: <br> An Educational Language for the 6502 

> PILOT is a higher level language used for coniputer aided instruction. This version includes an editor and an interpreter. It requires fewer than $\mathbf{8 0 0}$ bytes of miemory.

Nicholas Vrtis 5863 Pinetree S.E Kentwood, MI 49508



The editor doesn't have a provision for inserting a line between existing lines, but it is possible to change a line, provided you replace it with one of the same length or shorter. The percent key fills from the current position to the next end of line with delete characters, hex FF. Since most terminals ignore these, it works effectively as a delete to the end of the line. The program has to check for these during MATCH and COMPUTE statement processing, since they represent the logical end of line.

The carriage return, entered as the end of line, is converted to a zero by the editor. This simplifies looking for the end of each line, later on, since the zero flag is set as the byte gets loaded. The SYM monitor routine CRLF outputs both the carriage return and the line feed, so one doesn't save anything by keeping the return in the line to output it.

The locations CURAD and CURAD + 1 address the start of each PILOT line. initially, this is set to $\$ 500$ by the routine SETBGN. The $Y$ register is incremented $o$ access the next character in the line. At the end of each line, subroutine SCURAD bumps $Y$ one more time to get past the end of line character, and then adds the resulting $Y$ value to the current address and resets $Y$ to zero.

This sets things up for the start of the next line. Performing the line scan in this way saves two bytes each time I need to get to the next character because an INY is used instead of a JSR, and it also makes it easy to check for a line too




| 0297 | A9 | $3 F$ |  | takein | LDAIM | \$3F | DISPLAY $\quad$ ? ${ }^{\text {\% PROMPTING CHARACTER }}$ |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| 0299 | 20 | 47 | 8A |  | JSR | OUTCHR |  |
| 029C | A2 | 27 |  |  | LDXIM | \$27 | CHRS GETS STORED EACKWARDS |
| 029E | 20 | 18 | 8A | ACHR | JSR | INCHR | GET AN INPUT CHARACTER |
| 02 A 1 | C9 | 08 |  |  | CMPIM | \$08 | IS IT A BACKSPACE? |
| 02A3 | D0 | 03 |  |  | BNE | ACHR 1 | BRANCH IF NOT |
| 02A5 | E8 |  |  |  | INX |  | ELSE FORGET about last character in |
| 02A6 | D0 | 56 |  |  | BNE | ACHR | UNCONDITIONAL |
| 02A8 | C9 | CD |  | ACHR 1 | CMPIM | CR | WAS IT A Carriage returns |
| 02AA | D0 | 02 |  |  | ENE | ACHR2 | NO - SXIP AHEAD |
| 02AC | A9 | 00 |  |  | LDAIM | \$00 | YES - CONVERT CR TO END OF LINE |
| 02AE | 95 | 03 |  | ACHR2 | STAX | CHRS | and save it for match statement |
| 0230 | 24 | 87 |  |  | BIT | IFLAG | SEE if getting name field |

long. If $Y$ is minus after it has been incremented, more than 128 characters have gone by since the start of the line.
The editor inserts an end of line at this point and continues on. If this occurs during line print or scan for end of line, it probably means that the PILOT program has gone off the end, so these routines branch to SETBGN to start at the beginning again. This does not prevent the PILOT program from looping while looking for an undefined label, but it does prevent printing some garbage.
The first character on a line is not necessarily useful for executing a PILOT statement. There might be a line feed or some other control character present there. The asterisk and the label are not used except as a destination for a USE or JUMP statement. If we do find one of these, we not only need to skip it, but we must also skip the next character, since that is the label. The routine SKPJNK takes care of skipping over everything but the asterisk, since the same routine is used by both normal command start and by the label search routine.
Once the program has searched out the first probable command character on the line, the next thing it has to do is look for a conditional flag. This will determine whether it must examine the rest of the line. $A$ " $Y$ " or an " $N$ " is a conditional, and if the character of one of these lines, it is checked against the current value in FLG. If they do match, the program simply increments $Y$ to point to the following character, and also starts again, but this time $Y$ is pointing to the operation code following the conditional.
Most of the other operations execute in a similar manner. They look at the current character in $A$, do their processing if it is their turn, or branch to the next routine if it isn't theirs. There are some exceptions to this (naturally). The TEXT command is last because, if the character isn't a valid statement, the whole line must be printed anyway. One of the other exceptions is the processing for ENTER NAME (?:) and ACCEPT statements, which share much of the same code. Another is the code for JUMP and USER statements, which also share common code.

Logically, the only difference between the "?:" statement and the "A:" statement is that the "?:" inputs characters into both CHRS and into NAME, while the "A:" saves the starting address of the line for use in " $J: A$ " (jump to last accept) processing. In fact, the processing of the ENTER NAME statement merely involves setting the high order bit of IFLAG on and skipping the save of the line address that the ACCEPT statement performs. The high order bit of IFLAG is normally turned off by storing the ASCII command character in it. The code for the ACCEPT statement checks the high
order bit of IFLAG and stores the input character in NAME if the bit is on.

One thing to note is that data saved in NAME and CHRS are stored backwards, with the first input character in CHRS +39 ,the second in CHRS +38 , etc. Since I have to initialize the $X$ register anyway, I could initialize it with zero and count up, or with 39 and count down. If I am counting up, though, I need to do a compare to see if I have reached the maximum value. If I am counting down, the minus flag will automatically set when I reach the end.

The COMPUTE statement uses decimal arithmetic. Each variable is two bytes long, with the high order first. The high order decimal digit (bits $0-3$ of the first byte) are used to indicate the sign. A value of 8 or 9 indicates a negative number, while anything else is considered positive. It works out to be tens' complement arithmetic. To illustrate, assume I want to calculate 1 minus 2, which everybody knows is -1 . The actual result from the decimal subtract is $\$ 9999$, much as it would be \$FFFF in binary.

In order to display this as - 1, we have to subtract $\$ 9999$ from zero to get $\$ 0001$. Using decimal arithmetic does have some disadvantages, particularly the fact that the range of numbers is -2000 to +7999 ( $\$ 8000$ to $\$ 7999$ ) for two bytes instead of -32768 to +32767 for binary. Another disadvantage is that INC is not a decimal instruction.
The primary advantage of using decimal mode is the ease of translating from ASCII to internal and back. The ASCII characters zero through nine are $\$ 30$ through $\$ 39$ in hex. Multiplying by 10 in order to accept the next digit into a number is also very easy, since it only requires a four bit shift left. Converting to display merely means shifting each digit to the low order four bits. ANDing off the high order part, and ORing in $\$ 30$.
The MATCH statement is the most complicated statersent apart from COMPUTE. In theory, all that has to be done is compare the characters in CHRS against those in the MATCH statement line, and then set FLG to $Y$ if they match, and to N if they don't. This works fine if they match. The problems come when they are different. Before the flag gets set to $N$, we have to determine why they did not match.

For one thing, it might be the end of the MATCH statement line. Since all the characters up to that point have matched, the program treats this condition as a complete match. PILOT uses the comma as a seperator in the match statement to indicate alternate possible matches, so if the mismatch character is a comma, it is treated as the end of line, and FLG is set to $Y$.



There is also the possibility it might be caused by a request to match against the current value of a variable. To per form variable matching, the program calls CNVDSP which converts the variable to display format with leading zeros suppressed. It then matches the display format against the characters in CHRS If the variable value matches, the program continues checking the rest of the MATCH statement.

If, even after all this, we still have a nomatch condition, all is not lost yet. We have to scan forward in the MATCH statement, to look for a comma or the end of line. If we find the end of line, then FLG gets set to N. If we find a comma, the program starts the whole match process over again, from the character after the comma in the MATCH statement and from the beginning of CHRS All this sounds confusing but, for example, the statement "M:YE,OK,SUR" will provide a $Y$ indication for most affirmative responses such as YES or YES SIR or YEP or SURE WILL or OK

As I mentioned earlier, the USE subroutine statement shares much of its code with the JUMP statement. The main difference is that the USE statement must save the address of the start of the next statement, while the JUMP statement doesn't need to. Note that the USE statement does not nest levels (sorry about that).
There are two reserved labels in PILOT. The first is the asterisk, which is used to completely restart the PILOT program (including zeroing the variables). The second reserved label is " $A$ ". This label indicates a JUMP (or USE) to the last ACCEPT statement. If the label in the statement is not one of the reserved labels, the program sets CURAD back to the start of the PILOT program via a call to SETBGN + 3 and starts the search for that label.

The STOP statement is trivial. It merely requires a jump back to the start of the editor.

Processing of the EXIT from subroutine statement is slightly more complex. It involves a check of the high order byte of the address contained in RETURN. If it is zero, then there was no USE statement executed to get there, and the program merely advances to the next line. The high order byte can never be zero, since all the lines are stored above $\$ 500$. After restoring the return address to CURAD, the program resets the high order byte to zero. This means that the PILOT program can either "fall through" a subroutine, or use it in a normal fashion.

The REMARKS processing rivals that of the STOP statement for complexity. It merely invoives advancing to the next
statement. One final PILOT statement is the TYPE statement. It is also the default statement if none of the above sections processed it. If the statement is not a true TYPE statement, $Y$ is backed up twice, so the whole line will be printed. Otherwise, the line is printed following the " $T:$ ".

The remainder of the program consists of subroutines used by various PILOT statements. The routine PRT prints the current line to the end. It uses the high order bit of IFLAG to see if the program is in editor mode. If it is, then all characters are printed, instead of being checked for a " $\$$ " to indicate a variable. After the line has been printed, a carriage return and line feed are output. It then falls through to FWD1.

The purpose of this routine is to advance to the end of the current line, and set up CURAD for the next line. Since it checks for end of line first, before incrementing Y , the fall through from PRT will immediately exit this routine, thus saving a branch in PRT.

FWD1, in turn, exits to a routine called SCURAD. This adds one to $Y$, and adds the result to CURAD as the start of the next line. Finally, this routine falls through to SKPJNK, which skips over any unwanted junk at the start of the line and executes the return.

With the exception of CNVDSP, the remaining routines are short and pretty much to the point. The VTRANS routine must transfer the high order byte of the variable last, so it sets the sign flag for CNVDSP. The format of the NUMDSP array is set up in the same "backward" manner used for CHRS and NAME, and it is the output of CNVDSP. If the variable is negative, a " - " is inserted as the first character.

The high order bit of SIGNIF is used to keep track of whether a non-zero digit has been encountered in the number being converted. If the bit is off and the current digit is zero, the index is not decremented, but the zero is stored anyway. If the bit is on, the digit gets stored regardless of its value. Any nonzero digit turns on the high order bit, just to make sure. An end of line zero is inserted after the last digit.

There are three SYM monitor routines used in this program. If you pian to bring Tiny PILOT up on another system you will have to change the addresses for these routines. They are all fairly standard, so most systems should have equivalents. INCHR gets one ASCII character from the terminal into the A register, without parity; OUTCHR outputs one ASCII character from A; and CRLF outputs a carriage return then a line feed. Tiny PILOT assumes that all registers are preserved by these routines.
$\mu$

$\begin{array}{llll}0434 & 20 & 18 & 04\end{array}$ 04371204

0439 B5 90 0438 F0 15 043 D 20478 A 0440 CA 044110 F5

0443 A2 27 $0445 \quad 85 \quad 28$ $0447=009$ 044920478 A 044 C CA 044D 10 F6

044F $20 \quad 47$ 8A 0452 C8 $0453 \quad 10$ CC $045530 \quad 2 \mathrm{C}$ $0457 \quad 20 \quad 4 \mathrm{D} 83$

045A B1 97
045C F0 05
045E C8
045 F 10 F 9 34613020

JSR CNVDSP CONVERT VAnIABLE TO DISDLAY LDXIM $\$ 04$ GOT 5 BYTES POSSIBLE

VBDISP LDAX NUMDSF GET A CHARACTER
BEQ CHROUT +03 GRANCH IF TO END OF VARIABLE
JSR OUTCHR ELSE OUTPUT IT AND COUNT IT
BPL VBDISP UNCONDITIONAE LOOP

NAMEO LDXIM $\$ 27$ REMEMBER - IT CAME IN BACKWARDS
LDAX NAME
GEQ CHROUT +03 BRANCH IF TO END OF NAME
JSR OUTCHR
BPL NAMEO -02 UNCONDITIONAL
CHROUT JSR OUTCHR
PRT LOOP IF NOT TOO MANY
BMI SETEGN RESET TO BEGINNING IF FAST THE END LINEND JSR CRLF OUTPUT A CR AND THE LINE FEED

* enter here to skip a line without print
- AND INITIALIZE FOR THE NEXT LINE

FWD1 LDAIY CURAD GET A CHARACTER
BEQ SCURAD BRANCH IF END OF LINE
INY
ELSE BUMP TO NEXT ONE
EPL FWD1 LOOP IF NOT TOO MANY
BMI SETBGN RESET TO BEGINNING IF PAST THE END

* HERE FIXES UP CURAD TO pOINT TC bEGINNING OF a LINE
* CURAD SHOULD INDEX END OF LINE (WITH Y) ON ENTRY
- 

| 0463 | $C 8$ |  |
| :--- | :--- | :--- |
| 0464 | 98 |  |
| 0465 | 18 |  |
| 0466 | 65 | 97 |
| 0468 | 85 | 97 |
| $045 A$ | 90 | 02 |
| $046 C$ | $E 6$ | 98 |

SCURAD INY
BUMP FAST THE CR $\begin{array}{ll}\text { TYA } & \text { MOVE COUNT TO A } \\ \text { CLC } & \text { CLEAR CARRY FOR ADD }\end{array}$ $\begin{array}{ll}\text { TYA } & \text { MOVE COUNT TO A } \\ \text { CLC } & \text { CLEAR CARRY FOR ADD }\end{array}$
ADC CURAD ADD TO LOW ORDER FIRST STA CURAD AND SAVE RESULT BCC SKPJNK SKIP IF NO CARRY FORWARD INC CURAD +01 ELSE BUMP HIGH ORDER

- here to skip past leeading junk on a line

SKPJNK LDYIM \$FF SET UP Y THIS WAY
SJLOOP INY INCREMENT TO NEXT CHARACTER
BIT IFLAG SEE IF IN EDIT MODE
$\begin{array}{ll}0470 & \text { C8 } \\ 0471 & 2487\end{array}$
047330 OC
$0475 \quad 3197$
047730 ล7
0479 C9 2A
0478 F0 04
047D C9 3F
$047 F 90$ EF
048138
048250



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-

- convert a variaele to display corm

04A8 209094 04AB $10 \quad 17$ O4AD A9 2D 04 AF 8594 048158 048238 0483 A9 00 04 BF ES 8 A 0487858 A 0489 A9 00 048 E E5 89 04BD 8589 04BF D8
04CO A2 03 04 C 2 DO 02
$04 C 4$ A2 04
$04 \mathrm{C} \quad 18$
$04 C 7668 \mathrm{E}$ $04 C 9$ A5 89 04 CB 20 E6 04 04 CE A5 8A 04DO 4A O4D 14 A O4D2 4A 04D3 4A 04D4 20 E6 04 04D7 as 8a 04D9 20 E6 04 04DC 248 E O4DE 3001 OUEC CA $04 E 1$ A9 00 OUE3 9590 $04 E 560$

04 E 629 OF $04 E 80930$ O4EA 9590 O4EC 248 E OUEE 30 OE $04 F 0$ C9 30 $04 F 2$ DO 01 045460

045538 $04 F 6668 \mathrm{E}$ $04 \mathrm{~F}_{8} \mathrm{CA}$ $04 F 960$
BPL TRAAS MOVE TO ROARA
BPL ISPLCS BRANCH IF POSATIVE
LDAIM \$2D EL.SE PUT IN MINUS SIGN
STA
SED
SEC
$\begin{array}{lll}\text { SBC } & \text { WORK } & +01 \\ \text { SIA } & \text { WORK } & +01\end{array}$
LDAIM $\$ 00$
SBC WORK
CLD NOK
LDXIM $\$ 03$
SKIP INDEX SET
ISPL1 CLC TURN OFF SIUNIFICANCE INDICATOR
LDA WORK GET FTRST DIGIT
R TOOU' PUT TO OUTPUT AREA
MOVE TO LOW ORDER
RA
LDA WORK +01 LOW ORDER IS THIRD DIGIT
JSR TOOU1
bit Signif see lf had any significant chans
ISPĽ SKIP NEXT TF YES
ISPL2 LDAIM $\$ 00$ INSERT END OE LINE MARKEF
STAX NUMDSP
and return
-

- Convert curaent value to ascti and fut to output area
TOOUT
$\$ 0 \mathrm{~F}$
MARE IT ASCII
STAX NUMDSP SAVE REGARDLESS
bIT SIGN1F SEE IF SIGNIFICANCE STARTED
SETSIG YES - ALL are important NOW
CMPIM $\$ 30$ ELSE SEE IF SHOULD START NOW
bNE SETSIG LMPORTANT IF NOT ZERO
SEC
ALWAYS
and then return

| CNVDSP | JSR | vtrans | MOVE TO WORK AREA |
| :---: | :---: | :---: | :---: |
|  | BPL | ISPLIS | brance if posative |
|  | LDAIM | \$2D | ELSE PUT IN MINUS SIGN |
|  | STA | NUMDS $P$ | +04 |
|  | SED |  | SET DECIMAL MODE INDICATOR |
|  | SEC |  |  |
|  | LEATM | $\$ 00$ | SUBTRACT FROM ZERO TO COMPLEMENT |
|  | SBC | WORK | +01 |
|  | STA | WORK | +01 |
|  | LDAIM | \$00 |  |
|  | SBC | WORK |  |
|  | STA | WORK |  |
|  | CLD |  | CLEAR dECImal mode |
|  | LDXIM | \$03 | ONLY 4 POSITIONS LEFT |
|  | ENE | ISPL | SKIP INDEX SET |
| $\begin{aligned} & \text { ISPLUS } \\ & \text { ISPL } 1 \end{aligned}$ | LDXIM | \$04 | PLUS HAS FIVE POSITIONS Available |
|  | CLC |  | TURN OFF SIENTFICANCE INDICATOR |
|  | ROR | SIGN:F |  |
|  | LDA | WORK | GET FIRST DIGIT |
|  | JSR | T00u' | PUT TO OUTPUT AREA |
|  | LDA | WORK | +O1 SECOND DIGIT LS HIGH ORDER OF THIS |
|  | LSRA |  | MOVE TO LOW ORDER |
|  | LSRA |  |  |
|  | LSRA |  |  |
|  | LSRA |  |  |
|  | JSR | TOOU' |  |
|  | LDA | WORK | +O1 LOW ORDER LS THIRD DIGIT |
|  | JSR | T00U1 |  |
|  | BIT | SIGN]F | SEe lf had any SIcnificant chans |
|  | EMI | ISPL: | SKIP NEXT TF YES |
|  | DEX |  | ELSE KEEP THE LAST ZERO THERE |
| iSPL2 | LDAIM | \$00 | INSERT END OF LINE MARKER |
|  | STAX | NUMDS P |  |
|  | RTS |  | AND RETURN |
| - Convert current value to ascit and fut to output area |  |  |  |
| TOOUT | ANDIM | \$0F | KEEP ONLY LOW ORDER |
|  | ORAIM | \$30 | MAKE IT ASCII |
|  | STAX | NUMDS $P$ | Save regardless |
|  | BIT | SIGN1F | SEE If SIGNIFICANCE STARTED |
|  | EMI | SETSIG | yeS - all are important now |
|  | CMPIM | \$30 | ELSE SEE IF SHOULD START NOW |
|  | BNE | SETSIC | Important if not zero |
|  | RTS |  | ELSE RETURN |
| SETSIC | SEC |  | SET SIGNIFICANCE BIT ON |
|  | HOR | SIGNIF | ALWAYS |
|  | DEX |  | and point to next available position |
| PGMEND | RTS |  | and then return |

LDA WORK +O1 SECOND DIGIT LS HIGH ORDER OF THIS
ELSE KEEP THE LAST ZERO THERE

| SYMBOL | TABLE | $0002215 A$ |  |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| ACHR | -299E | ACHRQ | 02A8 | ACHRR | 02as | ACHRS | 0286 |
| ADONE | 02BD | ANSX | 008D | BITROL | 02EF | CHAR | 023F |
| CHARQ | 0245 | CHKCOII | 026D | CHROUT | 044F | CHRS | 0003 |
| CMPDON | 0334 | CMPLO: | 02DA | CNVDSP | 04A8 | CR | ODOD |
| CRLF | 834D | CURAD | 0097 | EGET | 020C | ELINE | 0207 |
| EXEC | 0252 | FLG | 0002 | FMNEXT | 03 E 8 | FNDMRK | 03DB |
| FWD | 0279 | FWDQ | 045A | GETIDX | 0494 | holdy | 0088 |
| Iflag | 0087 | ILNEX:' | 0418 | INCHR | 8 A 1 B | irest | 03ED |
| ISOPR | 02FF | ISPLQ | 04C6 | ISPLR | 04E1 | ISPLUS | 04C4 |
| JDO | 03 C 4 | JF | 03D8 | LINEND | 0457 | LSTART | 0263 |
| LST | 0000 | MCHK | 035E | MCHKX | 0358 | MCOMMA | 0376 |
| MCOMX | 0381 | MNUMB | 0385 | MX | 03A5 | MXDIFF | 039E |
| MXNMCH | 036E | MXNOLI | 0391 | MXSETN | 03A3 | MXY | 036A |
| NAME | 0028 | nameo | 0443 | NOTNMB | 02F9 | NUMDSP | 0090 |
| OPMNUS | 0317 | OPRATt | 008F | OPWRAP | 0324 | OUTCHR | 8a47 |
| PADLOP | 0231 | PGMENI | 04F9 | P $\mathrm{fT}^{\text {T }}$ | 0421 | RESTRQ | 025E |
| RESTRT | 0255 | RESUL ${ }^{\text {d }}$ | 008B | RETURN | 0095 | SCURAD | 0463 |
| SETBCN | 0483 | SETNL | 0240 | SETSIG | 0455 | SIGNIF | 008E |
| SJLOOP | 0470 | SJRTS | 0481 | SKPJNK | 046E | SXPNXI | 026A |
| Start | 0200 | STRTS1 | 027E | takein | 0297 | TALOOP | 0342 |
| TE | 0418 | trlag | 0275 | toout | 04E6 | TOVRIB | 034B |
| TRYDSP | 0224 | TRYREF | 0220 | Varibs | 0053 | VBDISP | 0439 |
| virans | 049C | WORK | 0089 | XA | 028B | XC | 02 C 3 |
| XCa | 02CA | XE | 03F7 | XFWD | 0353 | X. | 03EE |
| XM | 0356 | XOUES 1 | 0282 | XR | 04CB | XS | 03 FO |
| XT | 0412 | XU | 03A9 | XXFWD | 040F |  |  |

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# THE MICRO SOFTWARE CATALOG: XII 

Mike Rowe<br>P.O. Box 6502<br>Chelmsford, MA 018 :4

Name: AIM 65 Morse Code Send Program
System: AIM 65
Memory: Less than 1 K
Language: Assembly Language
Hardware: One IC (inverter), one relay (no relays necessary for solid state transmitters)
Description: This program converts the AIM 65 keyboard input into Morse code characters that are then output to pin PAO on the applications connector. A suitable buffer (two 7404 inverters) will key the transmitter or drive a relay. The following feature are provided:

1) The characters are displayed on the AIM 65 display as they are typed on the keyboard. Up to 20 characters may be displayed, with a 20 character overflow buffer, giving the ability to type 40 characters ahead.
2) The display is updated (scrolled to the left) as the characters are sent.
3) The display is updated as the characters are entered.
4) The DEL key allows characters to be deleted (backspace and delete).
5) Code speed is set and controlled digitally by entering the speed (in decimal) on the keyboard. Speeds from 05 to 99 wpm are possible.
6) The speed can be changed at any time by pressing the ESC key, followed by entering the speed in words per minute.
7) A message being sent may be halted at any time by means of the carriage return key.

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Includes: Source listing in the AIM 65 dissasembly format interface description, and instructions for operation.

## Author: Marvin L. De Jong

Available from: Marvin L. De Jong
S.R. 2, Box 364A

Branson, MO 65616

Namı: MONITOR-II
Syste in: APPLE-II
Mem iry: 3K + DOS 3.2 requirements
Lang age: Machine Language
Hard r'are: APPLE-II, DISK-II
(Supp irted) High speed serial card, Programmer's Aid ROM Applesoft ROM
Desc ption: MONITOR-II is an extension to Apple's ROM Monitor that adds an interactive command langt age. MONITOR-II provides the user: Extended cursor c introl-Named program load, initialize, and execute
from lape or disk-Programmer's Aid ROM \#1 interface and :ommands-Transient area management-Variable speer listings-Split screen display-Special I/O routine supp. irt-User extensible interactive commands-Integer BASI:; Variables Utility-Resident supervisor-much more
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Price $\$ 45.00$ on disk (Introductory)
Inclu les: System disk, user's guide
Optic 'al: Assembly listings, systems guide
Authe :: W.C. Deegan
Avail .ble from: W.C. Deegan
2 Fairfax Towne
Southfield, MI 48075

## Name Single Drive Copy

Syste II: APPLE
Memciy: 16K
Langt age: Integer BASIC
Descr otion: Allows you to copy a diskette using one drive. Automatically adjusts for available memory and come with the option of not initializing disk.
Copie :: 10
Price: : $\$ 19.95+\$ 1.00$ postage $\&$ handling (PA residents add 6\% sales tax).
Incluc as: Cassette with instructions.
Authc : Vince Corsetti
Availe lJle from: Progressive Software
P.O. Box 273

Ply. Mtg., PA 19462

## Name: CLASS ATTENDANCE CHURCH ATTENDANCE

## System: PET

Memory: $\mathbf{8 K}$ or more
Language: BASIC
Hardware: PET, 8K or larger
Description: Class Attendance \& Church Attendance maintain attendance records for any group which meets regularly, using data tapes. The school version records 0 to 5 days' attendance for each of up to 39 weeks. The church version does the same for 0 to 5 times for each of 12 months.

Attendance automatically sorts entires alphabetically within \& between data tapes. Though presently dimensioned for up to 10 tapes of $70-85$ names each, there is no limit to the number of tapes that can be used.

Commands include: ADD, DELETE, LIST, UPDATE, END \& CATEGORY. Within CATEGORY there are 8 subcommands for frequency of attendance, (perfect attendance this month, for example). There is also a Help command to escape to the main menu from anywhere in the program, and a Back-up command to correct mistakes in updating. With four SIMPLE line changes, PET's with the upcoming roms can use Attendance also.

## Copies sold: Just released.

Price: $\$ 12.95$ (either version)
Includes: Program cassette with sample Directory \& sample data, 2 blank C -10 tapes for data, and a 6 page instruction booklet with lists of variables used \& location of major routines.

## Author: James Strasma

Available from: Dr. Daley
425 Grove Av.
Berrien Springs, MI 49103
(616) 471-5514 (Sun-Thurs, Noon-9PM)
(Master Charge \& Bank Americard OK)

## Name: Omni Plotting Package <br> System: APPLE <br> Memory: 32K <br> Language: BASIC <br> Hardware: Disk and Applesoft on ROM

Description: 7 data sets and 2 exec file examples 12 basic programs, employ the High Resolution Mode. No shape tables are used, data base is Cartesian coordinates which can be sent over the phone or to hardcopy plotters. Package allows ease of intricate graphics generation supported by easy manipulation of graphics after generation. Package is excellent for map making, drafting, artistry and any other purely visual application.
Price: $\$ 19.00+\$ 5.00$ for diskette or you send one. We pay postage.
Includes: 12 basic programs, 7 data sets, 2 examples, 19 page illustrated users manual with step by step instructions.

Author: P.S. Truax
Available from: Omni Plotting Package
c/o P.S. Truax
237 Star Rte.
Santa Barbara, CA 93105

Name: Applesoft BASIC-Optimization Library System: APPLE II
Memory: 113K
Language: 6502 Assembly Language
Hardware: Standard (Applesoft ROM card optional)
Description: The Library consists of two 1.3K assemblylanguage programs (VAROPT \& REMOUT) that will work in any APPLE II with APPLOSOFT IIa, VAROPT renames all variables to unique 1-2 character variable names and displays (prints) a cross-reference listing with new name, old name, and all line numbers where the variable was referenced. REMOUT removes remarks, removes extra coloris, renumbers from 1 by 1 , and concatenates short lines into a reduced number of long lines.

Together, these programs will convert a verbose, welldocumented development version of an Applesoft program into an extremely memory-efficient, more-secure production version.
Copies: Just Released
Price: \$15/Dassette, \$20/Disk
Includes: Classette/disk with hex listing \& instructions
Available from: Sensible Software
P.O. Box 2395

Dearborn, MI 48123

Name: APPLE XFR
System: APPLE II with disk
Memory: 3:K
Language: Machine Language
Hardware: Apple II, Disk II, D.C. Hayes Micromodem II
Description: This program will establish a session between two Apples and allow transfer of any text file from one to another. All text file I/O is written in machine language. Programs are also included to facilitate the conversion of any Integer or Applesoft program to a text file.
Copies: Just released
Price: \$15.! 95 on diskette
Includes: Ciskette and documentation
Author: Travis Johns
Available from: Travis Johns
1642 Heritage Cr.
Anaheim, CA 92804

Name: OTHELLO
System: AFPLE
Memory: 16K
Language: Integer BASIC
Description: A game played by one or two players. Once a piece is flayed the color may be reversed many times, with sudden reverses of luck. Can win with a single move. Computer keeps all details and flips the pieces.
Copies: 10
Price: $\$ 9.95+\$ 1.00$ postage $\&$ handling (PA residents add $6 \%$ sales tax).
Includes: C;assette with instructions.

## Author: Virice Corsetti

Available from: Progressive Software
P.O. Box 273

Ply. Mtg., PA 19462

# 8080 Simulation with a 6502 

## The design for an 8080 simulator running on the $\$ 3502$ illustrates how your micro can assist program development for other machines, master new applications, and double the quantity of software it will exerute.

Dann McCreary<br>Box 16435-M<br>San Diego, CA 92116



## Why Bother to Simulate?

While many advantages of simulating one microprocessor with another might be cited, there are several which I believe stand out above the rest.

Educators and students can use simulation software as an enhancement to introductory courses in microprocessing. Such courses often make use of single board microcomputers like the Commodore $\mathrm{KIM}-1$. These computers provide invaluable hands-on experience. The addition of simulation software can multiply their effectiveness by enabling the study of alternate architectures and instruction sets without the expense of purchasing more hardware.
The entrepreneur and the hobbyist are typically owners of systems based on a single type of processor. Should a situation arise in which they would like to develop software for some other processor, they are faced with another significant capital investment. The availability of simulation packages which can run on their present hardware can make it economical to design and debug code for other processors.

Applications software fulfilling particular functions is sometimes hard to come by. Some might claim that the availability of a given application varies inversely with the need for a version written for the microprocessor available to
run it on. Enter simulation software and your choice of applications can be easily doubled. One good exampl: might be the use of an inexperisive 8080 assembler for a one-time :ask rather than going to the time or expianse of producing a cross-assembler.

The experimenter, never quite satisfied with the status quo, can use simulation techniques to try out his theories about an optimized instruction set. He can, in software, model the processor of his design and by doing so he can gather actual data about the validity of his ideas.

The major and most obvious drawback to simulating one micri:processor with another is the large spesd penalty. In the Cosmac 1802 Simulator which I have implemented on the 650: , about fifty 6502 instructions are execi ted in the course of executing one 18 C 2 instruction. In my 8080 Simulator, twice as many or more are required for each 8080 instruction executed. High sijeed realtime code or applications requiring precise timing relationshif:; derived from instruction cycle timing are clearly outside the scope of this tecinnique.

A somewhat lesser proble in involved is the space occupied by the simulator program, which must be co-*sident in memory with the application program. Careful design here can inake the
simulator quite compact but it does take up a finite amount of space.
For a majority of applications, I feel that the advantages of using a simulator overshadow the drawbacks, making this type of modeling very worthwhile.

## Optimizing the Approach

A simulation of sorts could be accomplished by compiling or translating the code of an 8080 into 6502 code. This approach would in fact be advantageous from an execution speed standpoint and would be a good choice if running application software were the only consideration. It would, however, generate large amounts of code and would not meet some of the other objectives I had for an 8080 simulator.

The interpretive approach seemed to best fulfill my self-imposed requirements. It would provide an accurate model of the 8080 processor, complete with all internal registers and duplicating all 8080 instructions. It would allow for single stepping or tracing through an 8080 program invaluable for debugging and for educational purposes. An interpreter could be very codeefficient, not only using little memory itself but also allowing 8080 object code to run unmodified in a 6502 environment.

I could have taken a "brute force" approach to interpretation, using perhaps
a table lookup scheme and transfering to a separate routine for each 8080 opcode. This offered some advantages in simplicity and execution speed but it required far more memory than I cared to use.

A careful analysis of the 8080 instruction set suggested that the 256 table entries and routines required by a "brute force" technique could be reduced by 25 by grouping the 8080 op-codes into categories sharing common functions.

In addition, certain judicious tradeoffs could be made between simplicity and ideal features, taking best advantage of the addressing modes and features of the 6502. For instance, the 6502 stack resides in page one and many of the 6502's instructions and addressing modes make use of page zero. To avoid memory use conflicts it would have been nice to simulate 8080 memory starting at 0200 HEX, making that address equivalent internally to 0000 HEX . This would have required a great deal of overhead in the form of a special monitor to show addresses minus the 200 HEX offset.

The addresses being used by the 8080 program while running would have to be converted dynamically, and in order to use indirect addressing a special set of simulated registers would have to be maintained in page zero. Besides requiring much more code, this would slow execution speed down considerably. I decided instead to simply require the user to patch around the small areas in page zero and page one being used by the simulator.

## Final Design Overview

Laying out the 8080 instruction set graphically on a hexadecimal grid, as illustrated, reveals some interesting features. Four major divisions are apparent, neatly dividing the instruction set into quadrants. The second quadrant is composed alomst entirely of MOV instruction op-codes. This MOV group most clearly illustrates the way that 8080 op-codes break down into source and destination fields, and suggests the best way to organize simulated 8080 registers in memory.

With simulated registers arranged properly in memory, source and destination field data can be extracted from the op-code and used as indexes to the registers involved. In every case where instructions act upon individual registers their order, as dtermined by this source/destination indexing scheme is $B . C, D, E, H, L, M, A-w h e r e M$ is not an actual register but rather the content of the mrmory location pointed to by the HL register pair.

This order suggests a general method for accessing individual registers with some slight exceptional logic for the $M$
"pseudo-register". By inverting the source and destination indexes and reversing the order of the 8080 registers in memory it becomes possible tc use the HL register pair directly as an indirect pointer to memory. Addins the Stack Pointer and Program Counter to the register array in the same reversed order completes the simulated recilister set.

Looking again at the instruction set grid it can be seen that a symme ry exists based on the source field of the opcode. For instance, all INR instruztions have source fields containing $0<$. HEX while all DCR instructions have 0.5 HEX as their source field. The fisurth quadrant exhibits similar symmetry. The third quadrant is more logically defined by the destination field, but still civides into 8 groups of similar instructicins as do the first and fourth quadrants. ${ }^{--}$ese, along with the entire MOV quaclrant, total 25 groups of similar instructions. A major task, then, of the simulator mainline is to determine from the opcode which of the 25 groups it belor gs in so tht control can be transfered to the proper routine to interpret it.

To keep the simulator as compact as possible it is advantageous to pu'form as many common operations as frossible in the mainline. Fetching the opcode, extracting source and desti ation indexes from it and incrementirig the Program Counter are fundamentál. The mainline also fetches the contis of memory pointed to by the HL re!gister pair, clears a flag used by nany simulator routines, saves data from the register pointed to by the destination index for later operations, tests for and handles interrupts and handles other "housekeeping" type functions.

At the end of the mainline the address of the selected interpreter routine is pushed onto the stack along with a preset status. A 6502 RTI instruction is executed, transferring control to the proper module entry.

It is the responsibility of each module to correctly interpret all op-codes ivhich result in a call to that module. Each module is constructed as a subrostine, returning control to the mainline via an RTS. This also enables certain modules to be used as subroutines by other modules. A brief look at the modules and their support subroutines will hels to illustrate their functions.

MOV. While encompassing the largest number of op-codes of any module, MOV has perhaps one of the simplest tessks. It merely takes the content of the rimister indicated by the source index and $s$ tores it in the register pointed to $b y$ the destination index. No condition $f$ ags in the PSW (Processor Status Word) aire affected. The only slight compliciation whether the destination is memory, in
which case the HL register pair is used as an indirect pointer to store the result in memory.

INXIDCX. This module must increment or decrement a selected register pair. The least significant bit of the destination index is tested to determine whether the instruction is an increment or a decrement instruction. The bit is then dropped and what remains is an index to the proper register pair-except for the cases of 33 HEX and 3 B HEX when the Stack Pointer is the register pair of interest. In these cases, the proper index for the Stack Pointer is substituted.

With the proper index set, a call is made to INCDEC. INCDEC is a 16 bit adder designed to add two zero page 16 bit operands. With the 6502's $X$ and $Y$ indexes properly set at the entry to this support routine, the content of a double precision one ( 0001 HEX ) or a double precision minus one (FFFF HEX) is added to the chosen register pair, performing the increment or decrement.

The proper register pair is selected in the same fashion for the DAD and LXI instructions also.

INR, DCR, MVI. These instructions are very consistent in their use of the destination index for determining which register (or memory as the case may be) they operate on. INR and DCR have the added complication of modifying the PSW condition bits, with the exception of the 8080 Carry.

Rotates. This is a mixture of quite different instructions lumped into one module. Proper execution depends on separating the Rotate instructions from the DAA, STC, CMC and CMA instructions, providing special logic for each and insuring the proper setting of PSW flags.

PUSH/POP. While handling register pairs somewhat like INX, DCX, DAD and LXI do, these instructions differ in substituting a register pair made up of the 8080 Accumulator and PSW for the Stack Pointer. The simulator handles this by looking for the specia! case and then decrementing the destination index to the proper position. The Stack Pointer is then incremented or decremented appropriately and the register pair data transferred to or from the stack as required.

Several support routines come into play, including INCDEC and various routines for transferring the content of register pairs between each other and memory. An intermediate register pair (not illustrated) is utilized as a temporary storage location during the exchange of register pairs. l've labeled it simply "SCR", though I believe it bears an actual hardware analog in the 8080 in the form of a hidden register pair, temporary registers $W$ and $Z$.


CALL and RETURN. These also manipulate the stack, using it as a storage location for the content of the Program Counter. The same set of support routines are used to get the transfer address from memory (for the CALL instruction) and to move data to and from the stack memory. RST is treated like a CALL instruction, except that the transfer address is computed from the destination field of the op-code rather than taken from memory. Conversely, JUMP gets its transfer address from memory, but does not save any return address on the stack.

Condition Codes. CALL, RET, and JMP all make use of a subroutine called CONDIT. CONDIT examines the destination index derived from the op-code and subdivides it into. a condition index and a True/False indicator bit. The index is used to select a PSW bit mask from a
table of masks. These masks align with the appropriate bit in the PSW. Based on the state of the selected PSW bit and the True/False indicator bit, CONDIT returns an indication of whether or not the JMP, CALL, or RET should take place.

Arithmetic and Logic. These instructions occupy the third quadrant of the instruction set. Rather than being grouped vertically by their source fields they are grouped horizontally by their destination fields. This is due to the fact that while they may have different sources of data, they all have one implied destina-tion-the 8080 Accumulator. The CMP instruction is the only one of this group which does not place its results in the Accumulator. It merely discards the result, setting only the PSW flags accordingly. This is accomplished by forcing the destination index to point to a scratchpad location.

Probably one of the most difficult things to simulate successfully is the proper setting of the Processor Status Word. Different instruction groups affect different subsets of PSW flags but the Arithmetic and Logic group affect all the flags. Zero, Sign and Parity flags are
always affected as a group. A routine called STATUS sets these three flags simultaneously when a result is passed to it. Carry and Auxilliary Carry are handled separately as they may be affected in isolation by some instructions and not affected at all by others.

Special Features. For the purpose of using the simulator as a debugging tool,

I chose to trap unimplementec opcodes. When the 8080 Simulator determines that the current instruction is an illegal op-code it forces a jump to the system monitor. This can be used to advantage as a simple type of break|ooint. Alternately, a table of breakpoint addresses may be set up in memory. After each instruction, the 8080 Prcgram Counter is compared to each address in the breakpoint table. If a match is $f$ fund, a jump is forced to the system monitor. This makes it possible to step from breakpoint to breakpoint, seeinc! the result of groups of steps rather thail only individual steps.

I/O Instructions. I/O is also handied via a table of addresses. Each ertry in the table is the address of a port $n$ the 6502 system. The entries in the tatle are associated with 8080 ports in sequential ascending order. Setting of the Data Direction Register, as in a 6530 FIO, is handled transparently to the user.

Call 65.1 have "borrowed" one of the 8080's unimplemented op-codes for a special purpose function-calling 6502 subroutines from an 8080 prograr. This enables you to use existing system I/O routines and other utilities. All tha is required is to add brief header and :railer routines to transfer the required parameters to and from simulator registers
and 6502 registers used by the subroutine. The CALL 65 instruction may also be useful for handling time dependent code segments.

## Summary

Modeling one microprocessor with another is a technique which provides many potential benefits. It has certain significant drawbacks, most notable of which is a large penalty in execution speed. These drawbacks, however, are not of paramount importance in a large number of applications in instructional, personal and experimental use.

Designing such a simulator involves tradeoffs between the complexity and quantity of the coding required for the task on one hand, and the features and execution speed of the final product on the other. I chose to minimize the quantity of code, emphasizing commonality of functions within the simulator.

Simulators for the 8080 and Cosmac 1802 microprocessors are available from the author in versions designed to run on the Commodore/MOS Technology KIM-1.

Thanks to Gary Davis for his generous support in the form of access to his 8080 system and his assistance in running comparison tests.

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# Who writes for MICRO? Subscribers just like yourself! How does one go about it? Read on! 

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The kind of material published in MICRO can be broken down into three general categories: application notes describing hardware or software projects, tutorials conveying general information about specific subjects, and reviews presenting informed opinion. The division into categories is not hard and fast; one easy way to get published is to write a piece whose very novelty defies categorization. Yet most articles published in MICRO and other magazines describe a project, or a concept, or a product, and can be labelled accordingly.
The label serves a purpose by reminding the writer that abstract theory may be out of place in an application description; working laboratory projects may detract from a tutorial; and opinion, the mainstay of reviews, is alien to both other forms.
This article is a tutorial on the subject of writing technical articles. It illustrates how most anyone can write a piece that will be received gratefully by any number of magazines, including MICRO, and it explains exactly why one would want to do so. Because it is a tutorial, and not an application note, it will not present step by step instructions that could be executed in sequence, with the usual backtracking and microbe debugging, to produce a working (publishable) project. If it did, about ten thousand readers would promptly deluge MICRO with drafts of essentially identical manuscripts.

Instead, we will examine ealch of the three forms of technical prose, describe some easy ways to get startec।, mention a few techniques that may $t \in$ applied along the way, and encouragle you to rush the result directly to MICF.O.

## Application notes

Many personal computers are much like the H.O. railroad train toys of two decades ago: expensive initulgences that occupy a few delightful hours on Christmas day and spend the: next six months gathering dust on a clos:set shelf. A decent model railroad used tis run big bucks, what with engines and sars and lots of track segments, tur riels and crossings and enough plastic rie brakemen to simulate featherbeddirig.

Yet once assembled, it served only one useful purpose. Like model trains, the personal computer may prowide little more than entertainment the $d y y$ it is unpacked and assembled. It cal play all kinds of games, and play most of them exceedingly well. It comes complete with a formidable library of reireational software. Whether any individual machine ever rises above rerreational applications depends entirely upon the dilligence and ingenuity of its cwner.

This explains why many pro!!lamming buffs scorn "personal coirputers." Quite a few data processing professionals actually eschew the term itself,

perhaps because there is little that is personal in computing and less that is computational in pure and simple gamesmanship. The measure of any computer lies in its ability to implement true data processing applications.

Recreation is a legitimate application, of course, but no one doubts that personal computers can be recreational. The question is, "Can they be applied to other problems?" Can they afford economical, effective solutions to the classical problems of data management? Can they admit to new applications reflecting the unsolved problems raised by recent technology?

We suspect that the answer is a resounding yes, and we can demonstrate this by describing applications that are served by personal computers. Each application implemented successfully enhances the versatility of the machine that was used to perform the task and, perhaps more important, each makes the next application all that much easier to implement.

A 6502 application note will benefit the entire 6502 community if it describes the solution to an open problem or an application never before implemented on a particular machine. Beneficial articles might also report new or unusual approaches to problems that have been solved using different methods. The novelty, general applicability, and overall ellegance of the solution are quite important because, after all, brand new applications that solve open problems are very rare.

It is easy to write an article that describes a computer application. This is fortunate because the application is not fully implemented until it has been described in writing. An application note should describe the problem that was solved, the method of solution, and the implications of the method. It should answer the questions: "What?" and "So what?"

It is impossible to overrate the value of a problem description. Serious computer scientists are constantly refining their ability to evaluate problems, assign them to categories or classes, distinguish those solved in the past from those that remain open, separate the easy from the difficult, generalize the solution to other applications, and extract specific techniques that might serve well in future projects.

This skill derives from exposure to problems, as well as solutions. So state the problem clearly. Describe the situation that caused the problem. Indicate its analogs in related situations. Outline previous attempts to solve the problem, and mention the measure of their success or the reason for their failure.

You will only have to deal with your solution once, now that it is fully implemented, yet whether you are describing a simple memory test routine or a mind boggling speech synthesizer, yet another fast Fourier transform or the first algorithm to play a competitive game of GO, you will undoubtedly encounter your problem over and over again. Few people understand the problem as well as you do, now that you have solved it once. Take the time to describe it well, so that you and everyone else will recognize all of its manifestations.

The problem solution is most often a program or a piece of logic. Software solutions and hardware solutions have much in common. Most interesting problems admit to solutions of either type.

The best presentation of a software solution reproduces a working source of the actual computer program; that is, the assembler or compiler output listing of a program that was loaded and tested thoroughly immediately after assembly or compilation.

Listings that were transcribed or manually corrected imply that the person who made the copy or revision is less prone to error than the computer. The entire article is likely to be viewed with the same scepticism anyone would accord this implication. As a rule, let the computer generate the program listing.

It goes without saying that any program worth coding is worth commenting. Or does it? The first self explanatory computer program remains to be written. If and when it finally appears, odds are that it will contain comments.

The program description is perhaps the least important part of an application note. The program is right there, after all, Gleanly coded and with ample commentary. The big question, "What?", has been answered by the problem description and the source listing.

Authors stress program descriptions because they provide an effective mechanism for answering the question, "So what?" The description illustrates why an application is deserving of study. It points out noteworthy aspects of the designer's methodology, perspective or approach. It identifies techniques that may be generalized to solve other problems. In much the way a map enhances appreciation of unfamiliar terrain, it uncovers pitfalls, highlights the points of interest, and distinguishes one parti-
cular application from the variety of similar programs that almost always exist as equivalent solutions. Is your memory test routine any different 1 rom a thousand others written since Baboage named the game? If so, the program description is the place to point this out.

A hardware application note vill require a logic schematic in lieu of a program listing, block diagrams in pliace of flowcharts, pin out lists instead cf calling sequences, and perhaps a photograph. That photo might not preivide much hard and fast information, tut it relates your article to the real woild.

Schematics and block diagrams are almost always drawn by hand and, therefore, susceptible to errors no proofreader will ever catch. Unlike soltware designers, whose computer gen erated source listings instill a measure of reader confidence, the hardware designer must rely on manual reproduction techniques to express his implementation. That extraneous pricto is one exception to this rule. Like computers, cameras might not alwa/si tell the truth, but they never make mistakes.

## Tutorials

A tutorial is a short, complete and entertaining explanation of a technical subject. Unlike application notes, tutorials need not describe operational programs or projects that may be constructed to perform useful tasks. Although they may include program segments or circuit details, by way of illustration, tutorials present techniques for solving general problems, instead, and avoid specific problem solutions.

The subject of a tutorial must be selected carefully to resolve the conflicting demands of brevity and completeness. A single chip, such as the 6522 or 6532, might make a good subject. A major subsystem, such as a video driver or a tape $1 / O$ package, might be too complex to describe in sufficient detail. The general subject of subroutine calling sequences would make a good tutorial; however, the subject of floating point math packages is much too broad.

Because of its limited size, a tutorial may employ writing techniques that are not appropriate in other types of


technical prose. Use of the first person is common, for example, and casual or vernacular writing may be effective. These techniques help make the tutorial entertaining, fun to read; their use gives tutorials a big advantage over longer technical articles, which can tend to be rather dull.

Of course, a tutorial must present more than warmed over material from the manufacturer's documentation or a clever rehash of material excerpted from a textbook. Like any other form of writing, its impact depends upon the author's originality. Here again, careful selection of subject and perspective is the key to success. A fresh, innovative point of view applied to the right topic yields a tutorial that will practically write itself.

Analogy is an effective technique that sets tutorials apart from run of the mill technical documentation. Virtually all significant hardware and software problems have analogs outside of computer science. Textbooks cannot develop analogies for more than a few aspects of the material they treat without ranging far afield. Yet the tutorial, because of its limited scope, responds perfectly to the use of analogy.
Historical perspective is another useful trick. A general solution is only as
interesting as the general prcblem it solves, and the tutorial provides an ideal format for discussion of problems, as well as solutions.

Innovative modelling can also pay off, to the extent that it is effective; but this little trick is fraught with risk. The author who devises his own paradigm has guaranteed originality and a fresh perspective right off the bat. If the model is effective, he might just become as famous as Hollerith with punched cards; Baudot with character codes; or Hamming, whose simple concept of "distance" sold thousands of books. However, if the model is not effective, the tutorial fails. It is as simple as that, and the 'e is no middle ground, but perhaps simmeone will print that questionable model as a humerous bit of satire.
The writing style matters, in a cutorial, because virtually identical information is available from many other sources. Dr. De Jong's application note in this issue provides the only description of an AIM Notepad you will find anywhere; in contrast, there are countless articles on even such an unlikely tutorial suljject as writing articles. If you survived that freshman English composition course, enough said. But if you opted ins:ead for a tensor calculus elective, sorre common sense guidelines will make a whole lot of difference.

Short sentences win big. Use first person, present tense, active voice. Avoid any grammatical construction you can't identify by name. Resist all impulse to employ parentheses, quotation marks, footnotes or dashes. And when in doubt, triple space the manuscript, leaving your editor plenty of room to ply his trade.

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## Reviews

MICRO has published very few product reviews, in the past, largely because of uncertainty about how reviews should be solicited, prepared and presented. MICRO will publish many more software, hardware and book reviews in future issues. This is how we plan to go about it.

All product reviews must be solicited by the magazine. MICRO will not publish a review that simply shows up in the mail, because the act of writing an unsolicited review implies that the author has strong feelings about the product, one way or another, else he would not have troubled to prepare an unsolicited manuscript.
The product must be submitted for review by the manufacturer. This is only fair, because a reviewer should feel free to make negative comments, and a manufacturer should be able to enjoy his monthly MICRO without encountering those negative comments completely unexpectedly.
The manufacturer of a product, or the author of a book or program, must receive a copy of the review, prior to publication. Although manufacturers will not have the right to modify or suppress unfavorable reviews, they will be able to make comments or rebuttals and offer additional insights that the reviewer might have missed.

Software and books submitted for review will become the property of the reviewer. Hardware will be provided, and the reviewer will have the option of purchasing this hardware at dealer cost.

## What's in it for me?

More and more frequently, of late, manuscripts have arrived from writers who wished to retain exclusive ownership of their articles. MICRO has received copyrighted manuscripts, and a few authors have declined to fill out the ominously worded MS cover sheet.

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## What if I don't get published?

Rejecting an article is the mos: difficult task any editor will be called נpon to perform, especially when the copy deadline draws near and space remains to be filled. The only common reasons for rejecting articles submittecl to MICRO are:

Too short
Nothing new

## Incomplete

and, very rarely, just entirely too difficult to prepare for publication. These piffalls are surely easy enough to avoid. If your article is at least one page long and reflects original work, it will be putlished. If it is incomplete, you will be asked to supply additional material prior to publication. This might involve answers
to questions that were raised and left open, comments to accompany program code, or background information most readers would require.

Every so often an otherwise excellent manuscript simply jams the production machinery. One author's draft stubbornly refused to pass through the copying machine. Another included several yards of program listing, in a language the MICRO systems lab was not equipped to reproduce, all printed using blue ink which is invisible to photographic platemaking equipment. By all means avoid blue ink and electrostatic copies. More to the point, take a minute and consider what is required to convert your manuscript into a magazine article. Have you supplied the basic input required? If so publication is all but guaranteed.

The editor wishes to thank these persons who contributed their thoughts and assistance: Keating Wilcox, Dann McCreary, Dr. Marvin L. De Jong, Philip K. Hooper, Robert M. Tripp. Illustrations by Bruce Conley.


## The Basic Switch

## Attention "Old" Pet" ${ }^{\text {m }}$ Owners:

Not sure about the ROM Retrofit Kit from Commodore?
Now you can use both sets of Commodore ROMs and others as well.
The Basic Switch allows switch selection of elther FiOM set (your original set or your retrofit set) from Commodore. Plus, Model 15-A includes an additional zero insertion force socket allowing easy use of ROMs like the BASIC Programmer's Toolkit ... concurrently.
Model 14-E The economy model of The Basic Switch. Stand alone board and harness without case and case hardware. The free standing unit is ready to accept your ROMs.
Model 14-D Same as Model 14-E but includes attractive protective case and mounted Basic Switch board.
Note that Model 14 Series does not allow for expansion ROMs like the BASIC Programmer's Toolkit.
Model 15-A The Basic Switch plus ... includes expianded cable assembly and zero insertion force socket. Your 15th FiOM simply plugs in ... enabled while either ROM set is selected. Socket 15 may be readdressed by the user for additional flexibility.
The Basic Switch is sold in assembled form only. All rnodels are designed for easy attachment to your Pet with a convenient cable as:embly. No soldering or drilling is required. The Basic Switch mates with a cable assembly at your primary board, and does not use the physical connectors of any Pet ports.
Model 15-A allows you to use the BASIC Programme's Toolkit without the need for the additional $\$ 25.00$ board or tying up your ports. And since we've designed the 15 th socket to be readdressable, watch for more: ROM pacs later in the Fall.

| The Basic Switch: |  | With installed RCM <br> Retrofit Kit from <br> Commodore: | With BASIC <br> Programmer's Toolkit |
| :--- | :--- | :--- | :--- |
| Model 14-E | $\$ 64.95$ | $\$ 149.95$ | - |
| Model 14-D | $\$ 77.95$ | $\$ 162.95$ | - |
| Model 15-A | $\$ 99.95$ | $\$ 184.95$ | $\$ 149.95$ |

Model 15-A with installed ROM Retrofit and Basic Programmer's Toolkit: $\$ 229.95$
Model 15-A with installed ROM Retrofit and both Tocilkits: $\$ 274.95$
"Old" Pets were shipped with 24 or 28 pin ROMs. You rnust check which you have, and specify at time of order.

The Basic Switch ${ }^{\text {M }}$ is a product of
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