

# PRIMERGY RX4770 M1

# System configurator and order-information guide

**June 2014** 

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## **PRIMERGY Server**



## Instructions

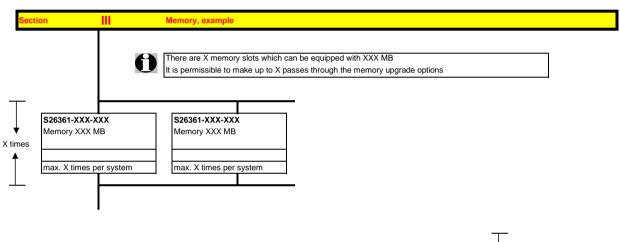
This document contains basic product and configuration information that will enable you to configure your system via PC-/System-Architect

Only these tools will ensure a fast and proper configuration of your PRIMERGY server or your complete PRIMERGY Rack system.

You can configure your individual PRIMERGY server in order to adjust your specific requirements.

The System configurator is divided into several chapters that are identical to the current price list and PC-/System-Architect.

Please follow the lines. If there is a junction, you can choose which way or component you would like to take. Go through the configurator by following the lines from the top to the bottom.



In one chapter you can only select as many components (here 4x) as the arrow indicates.



Please note that there are information symbols which indicate necessary information.



#### Further information in the internet see:

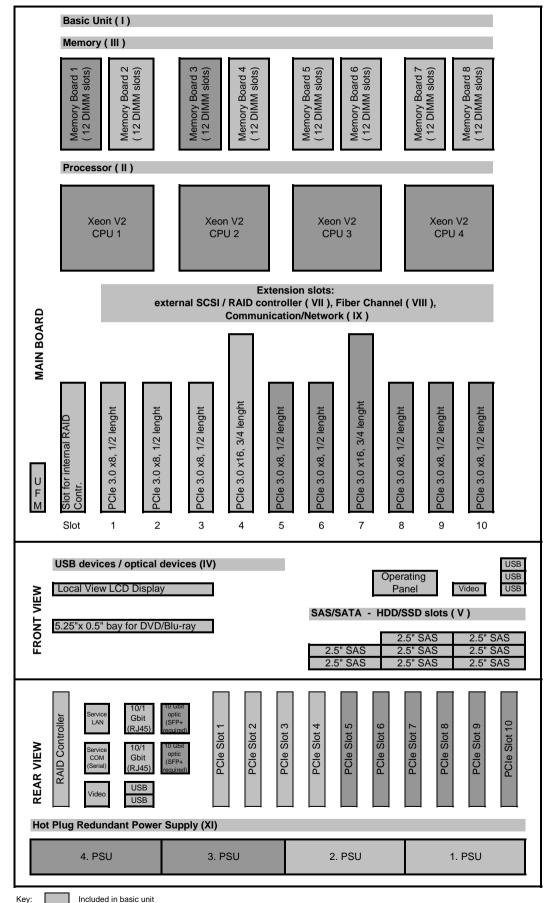
http://ts.fujitsu.com/products/standard\_servers/index.html

(internet)

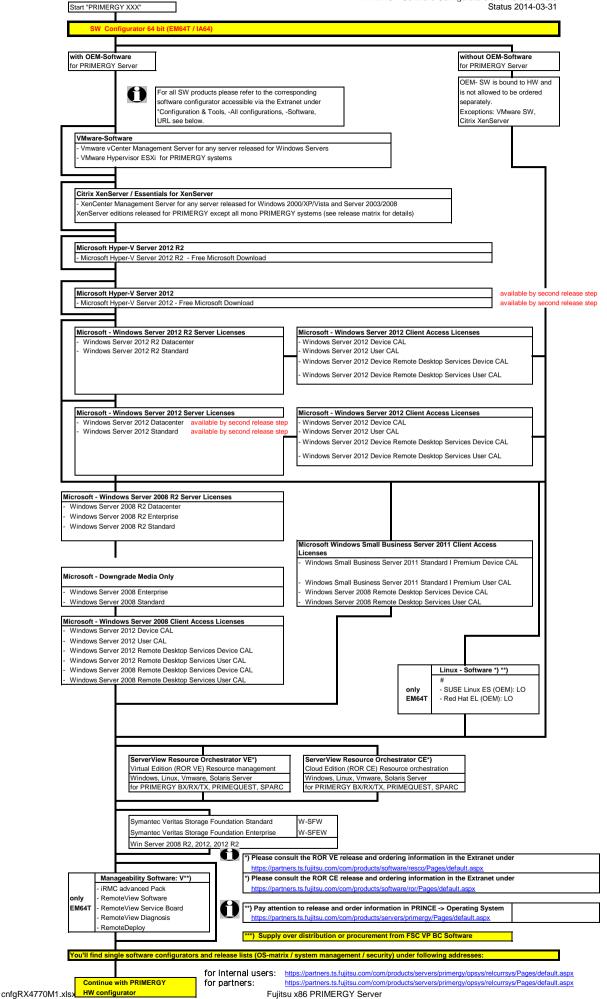
https://partners.ts.fujitsu.com/com/order-supply/configurators/primergy\_config/current/P (extranet)

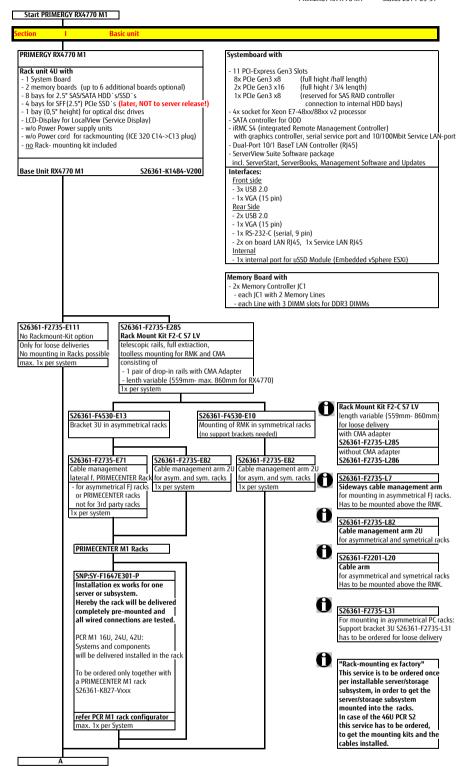
Prices and availability see price list and PC-/System-Architect. Subject to change and errors excepted.

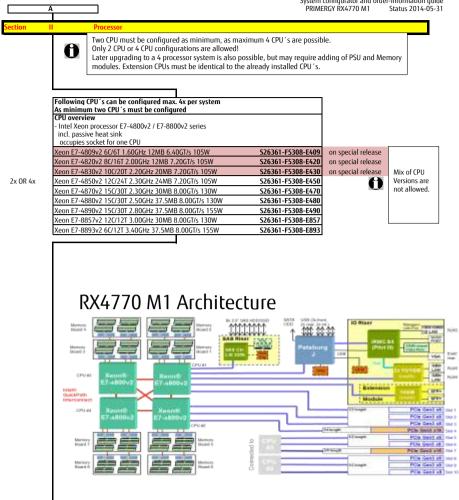
## **Configuration diagram PRIMERGY RX4770 M1**



Option

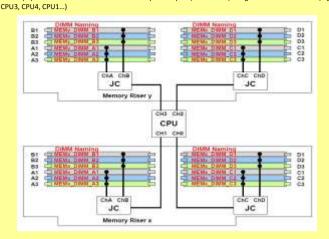






Basically DIMM population orders:

DIMM population order for each CPU is done by DIMM pair (2x DIMMSs) using Round Robin scheme (e.g. CPU1, CPU2,



#### BTO Order for DIMM population (DIMM installation order)

DIMM population limitation in platform is listed as below.

- Mixing of LRDIMM with any other DIMM type is not allowed per platform.
- Mixing of DDR3 voltages is not validated within a socket or across sockets. (If 1.35V (DDR3L) and 1.50V (DDR3) DIMMs are mixed, the DIMMs will run at 1.50V.)
- Mixing of DDR3 operating frequencies is not validated within a socket or across. (If DIMMs with different frequencies are mixed, all DIMMs will run at the common lowest frequency.)
- A maximum of 8 logical ranks (ranks seen by the host) per channel is allowed. Mixing of Independent and Lockstep channel mode is not allowed per platform.
- Mixing of Non-Mirrored and Mirrored mode is not allowed per platform.
- Mixing of Sparing and Non-Sparing mode is not allowed per platform.

ration modes with minimum need of DIMMs ner CPII and further Stenning (see Population #):

Population #	SMI2 Channel Mode	Memory Mirror Mode	Memory Spare Mode	DIMM population #
1	independent mode	Off	Off	. 2
2	Independent mode	On	Off	2
.3	Independent mode	Off	On	4
4	Lockstep mode	Off	Off	4
5	Lockstep mode	On	Off.	4
6	Lockstep mode	Off	Qn .	8

- Server Rules for Combination CPU and Memory Boards:

   A minimum of one Memory Board per each CPU must be populated.

   If two Memory Boards per CPU will be used, each CPU have to be populated with two Memory Boards.

   Following Configurations of CPU and Memory Boards per Server are possibble:
- --- Two CPU with two Memory Boards
- Two CPU with four Memory Boards
- --- Four CPU with four Memory Moards.
  --- Four CPU with eight Memory Moards

Memory population Table:

SMI2 Channel modes are selectable in BIOS setup menu.

Lockstep Mode is default Mode if HW configuration makes it possible.

Performance Mode can be switched on.

#### Lockstep Mode:

- Lockstep --> delault mode, parallel Thrughput to both MEM Lines of one SMI2.

   Bus frequency ratio SMI2 to MEM Line is 1:1
- Max. SMI2 frequency are 2666 MHz, means in Lockstep-Mode **1600 MHz DIMMs can also used with 1600 MHz.**Memory Interlieving functionate only via 2 level, required for interleaving are same Memory capacity on DDR channels.

Mirror Mode and Sparing Mode can be combined.
In Lockstep Channel Mode, each memory access is a 128-bit data access that spans Channel 0 and Channel 1, and Channel 2 and Channel 3. Lockstep Channel mode allows SDDC/DDDC. Lockstep Channel Mode requires that Channel 0 and Channel 1, and Channel 2 and Channel 3 must be populated identically with regards to size and organization. DIMM slot populations within a channel do not have to be identical but the same DIMM slot location across Channel 0 and Channel 1 and across Channel 2 and Channel 3 must be populated the same

#### Intel Independent Mode:

- Bus frequency ratio SMI2 to MEM Line is 2:1

  Max. SMI2 frequency are 2666 MHz, means in Independent -Mode fast 1600 MHz **DIMMs can be used with Max. with**
- · Memory Interlieving functionate via all 3 memory level what will be result in faster memory troughput than Lockstep Mode, required for interleaving are same Memory capacity on DDR channels.
- Mirror Mode and Sparing Mode can be combined.
- Mirror Widde and a paring widde can be commissed.
   Channels can be populated in any order in Independent Channel Mode. All four channels may be populated in any order and have no matching requirements. All DDR channels must run at the same interface frequency but individual channels

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#### Max Capacity / Features

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Be system can be equipped with up to 96 DIMMs (distributed on 8 memory boards)

Each DIMM slot can optionally be equipped with 8GB, 16GB, 32GB or 64GB DDR3 LV DIMM modules, so the maximal memory size is 6144 GB with 64GB modules.

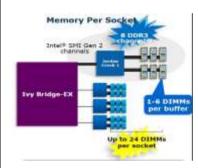
Max. Memory Speed depends on CPU QPI Speed and Memory Type, but is limited to 1600 MHz. See also description above

Memory Controller Independent Mode and Lockstep Mode can be switched by BIOS setup menue.
Independent Mode (higher I/O, B/W)
Lockstep Mode (highest DDR3 speeds)
Can be combined with in ordering Mirroring Mode or Spare Mode!

IMM population tables will be dadded later!

#### acknround for access able Memory capacity calculation

Memory Mirroring Mode:
In Mirrored Channel Mode, the memory contents are mirrored between SMI2 Channel 0 and SMI
Channel 1 and also between SMI2 Channel 2 and SMI2 Channel 3. As a result of the mirroring, the
total physical memory available to the system is half of what is populated. Mirrored Channel Mode
requires that SMI2 Channel 0 and SMI2 Channel 1, and SMI2 Channel 2 and SMI2 Channel 3 must be populated identically with regards to size and organization



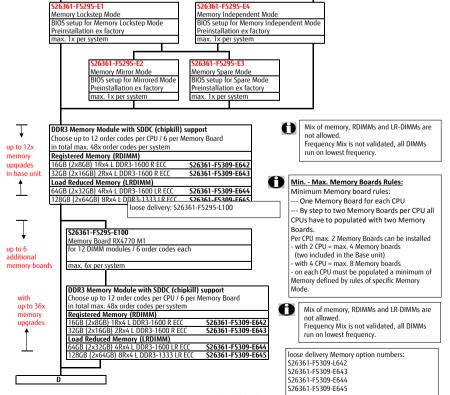
Memory Sparing Mode:
Sparing will be done by Rank Sparing within the same Memory Line (DDR channel).
For Ivy-Bridge Processors, Rank Sparing can be achieved if there are 2Ranks in each DDR channel.
DIMM number is unrelated.
Memory Controller on CPU can handle up to 8 logical RANKs per DDR3 channel. Requirement to

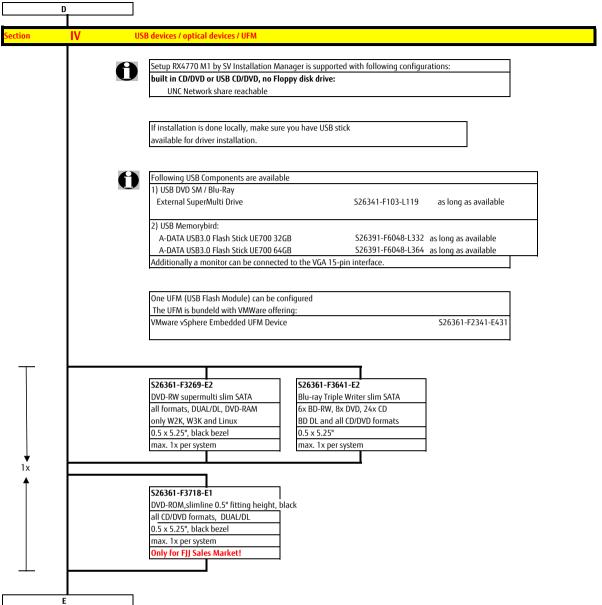
internity Controller on CPO Carl Infantie up to 6 logical NAMS per DDR3 channel. Requirement to configure RANK-sparing is as follows.

- In case of 1R/2R RDIMM, at least two DIMMs should be populated on the DDR3 channel.

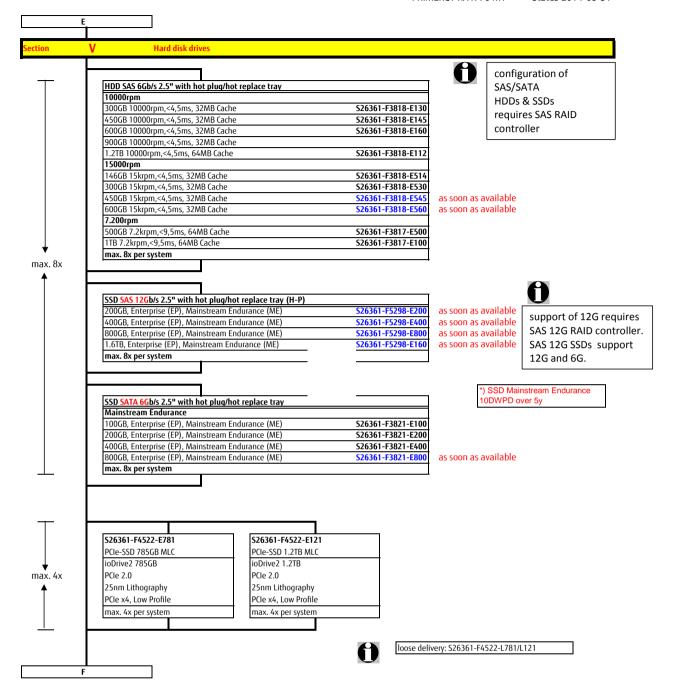
- In case of 4R RDIMM or LR-DIMM, one DIMM population is allowed.
In Spare Mode the used Ranks as Spare Ranks shrinked the direct access able Memory:
Example: Rank Information in Memory Order Number description: xxGB (2xxxGB) 2Rx4 L DDR3-1600 R ECC

The total number of spared physically rank on a DDR channel					
	Populated DIMM slots in Channel:				
	1DPC	2DPC	3DPC		
	Not possible,				
8 GB R DIMM(1pr)	because min. 2	1	1		
	DIMMs				
16 GB R DIMM(2pr)	1	1	1		
32GB LR DIMM(4pr)	1	1	2		
64GB LR DIMM(8pr)	4	4	4		





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S26361-F5295-L610

max. 1x per Controller

SAS cable 1-2 (Lynx2, Cougar 3)

to internaly HDD/SSD bay)

(SAS Cable from Controller

Loose Delivery Options ofinternal SAS RAID 0/1 and RAID5 Controller for HDD's

Loose delivery for LSI SAS 2008: S26361-F3554-L8

Loose delivery for LSI SAS 2208: S26361-F3669-L4

**RAID Advanced Software Option** 

S26361-F3669-L660

RAID Advanced Software Options License Activation Key for CacheCade 2.0 and FastPath

for 1 Controller

Lose delivery TFM option

S26361-F3669-L100

TFM Module for FBU option (flash and FBU control logic) max. 1x per Controller

FBU max. 2x per system possible

S26361-F3669-L110

Flash Backup Unit with 25, 55, 70 cable set

max. 1x per Controller

S26361-F5295-L610

SAS cable 1-2 (Lynx2, Cougar 3) (SAS Cable from Controller to internaly HDD/SSD bay)

max. 1x per Controller

as soon as available

Loose delivery for LSI SAS 3108: S26361-F5243-L1

Lose delivery TFM option

S26361-F5243-L100 PRAID EP400i - TFM Module for FBU option

max. 1x per Controller

FBU max. 2x per system possible

S26361-F5243-L110

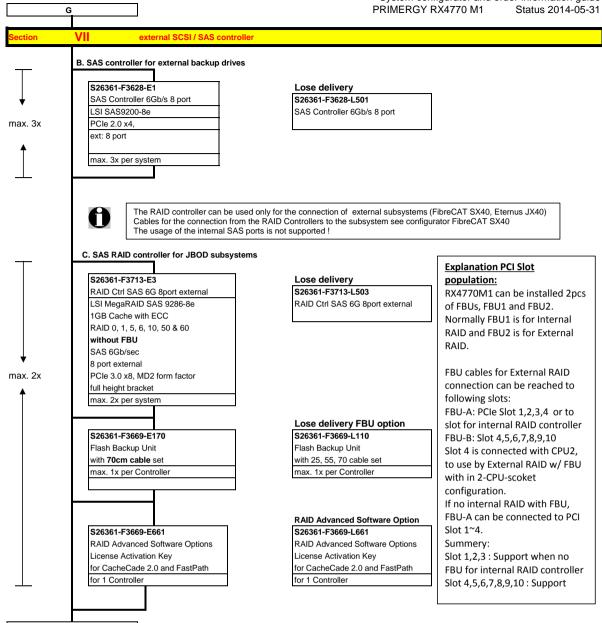
Flash Backup Unit with 25, 55, 70 cable set

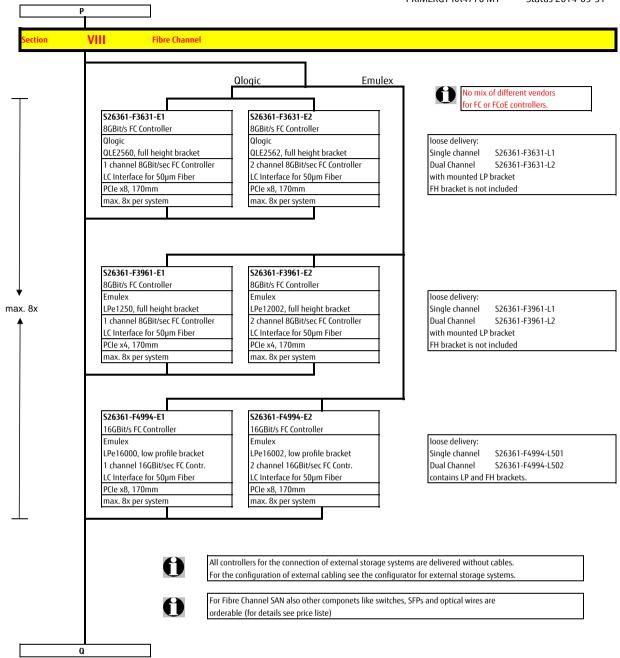
nax. 1x per Controller

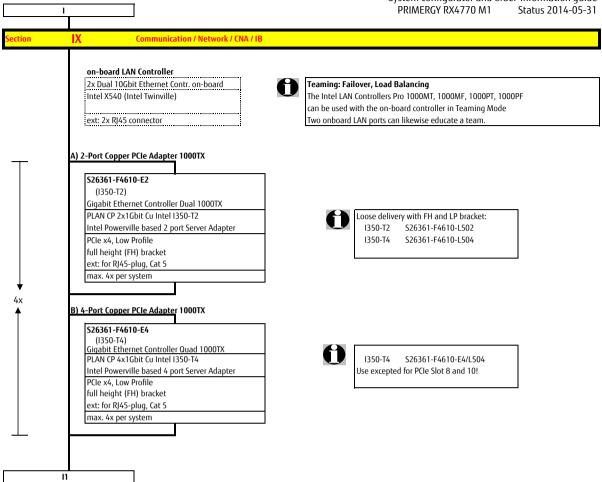
S26361-F5295-L620

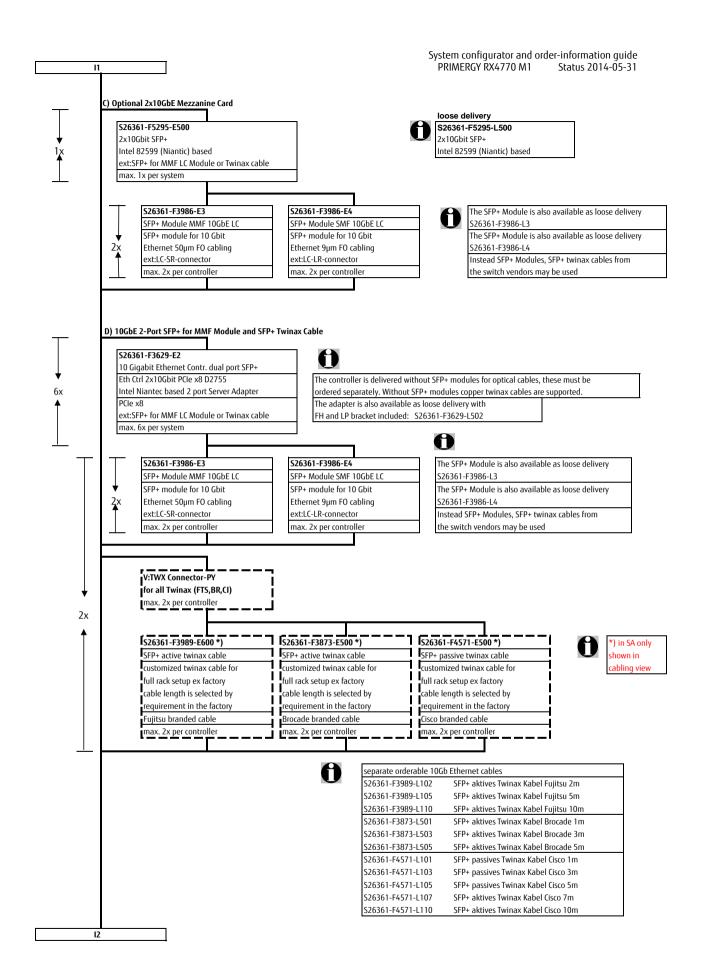
SAS cable 1-2 Cougar 4 (SAS Cable from Controller to internaly HDD/SSD bay)

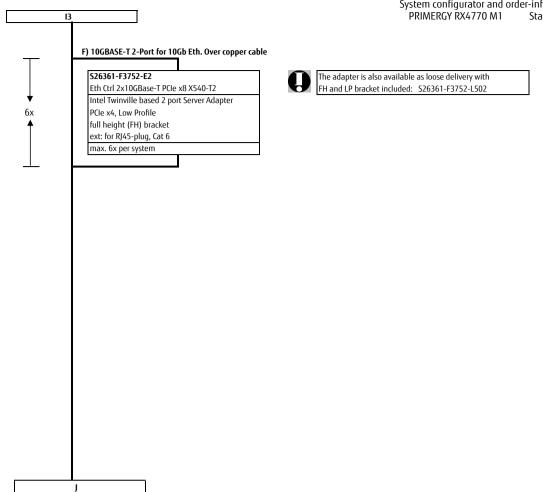
max. 1x per Controller

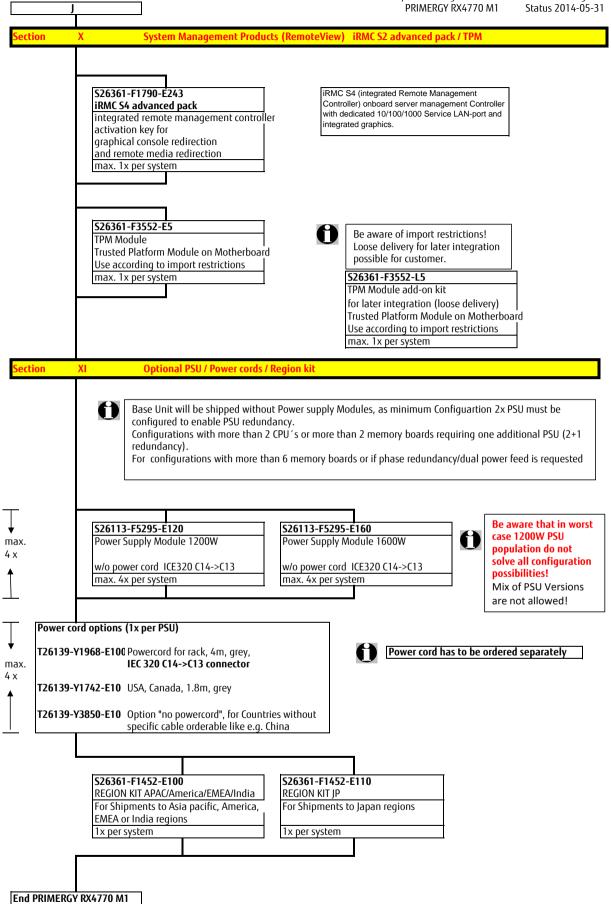












# **Change Report**

Date	Order number	Changes	
2014-05-20	S26361-F5308-E409	Added another 6 Core CPU on special release (Xeon E7-4809v2)	
2014-04-03	Corrected Basic Unit order number S26361-F4530-E10 and S26361-F4530-E13		
2014-03-31		First Version	