

Intel® Server System R2000GZ/GL Product Family

Technical Product Specification

Revision 2.2

May 2014

Intel® Server Boards and Systems

Revision History

Date	Revision Number	Modifications	
January 2012	1.0	First Production Release	
April 2012	1.0	 Updated Features Table Added support for processors with 135W TDP Updated Environmental Limits Table Added ASHRAE support details Add GPU support section Updated supported riser card sections Updated Thermal Management sections Updated Chapter 6 – Storage Controller Options 	
		 Revised / Corrected SAS expander usage samples Updated embedded SCU and RAID support sections Added sections for product errata definition and FRUSDR use Added PCIe layout diagram Updated Reference Documents list 	
June 2012	1.2	 Corrected LCP front Panel port definition Corrected LCP product code Added advisory note to AXXVRAIL feature list Corrected IO Module product code definitions in Table 1 Added note describing support requirements for AXXRMFBU2 accessory kit Added sections for RMM4 support Added BBU9 Installation Diagram 	
January 2013	1.3	 Front bezel badge option diagrams added to section 2.5 Rail Kit Caution and Advisory notes added to section 2.6 Added DC power supply specification content to chapter 3 Added Chapter 5 – Intel[®] Xeon Phi™ card and GPGPU add-in card support Updated RSTe support sections in chapter 7 Removed sections in Chapter 9 – LCP support. Added reference to published LCP TPS Corrected Appendix B - POST Code LED Decoder – (E0h – E3h) Added Appendix D. – System Configuration Table for Thermal Compatibility 	
January 2013	1.3.1	Additional changes made to Appendix D	
February 2013	1.3.2	 Removed all content and references to DC power supply option Updated section 5.3 to include required system sw stack revisions 	
August 2013	2.0	 Added Intel[®] Xeon[®] processor E5-2600 v2 product family support. Updated System Feature Set Table #1 Updated Thermal Config Table in Appendix D Updated system rail kit support 	
February 2014	2.1	 Added DC power supply specification content to chapter 3 Added sections detailing add-in card support Added 2.5" SSD support content to 3.5" drive configurations Added volumetric air flow requirements table 	
May 2014	2.2	 Added DC 750W Gold for Power Supply Options to chapter 2 Added the Disclaimer Note for the System Environmental Limits Summary table Update figure 10 – update the jumper order Added the footnote for the Power/Sleep LED Functional States table Added the Intel® Xeon® processor E5-2600 v2 product family support in 	

ii Revision 2.2

Intel® Server System R2000GZ/GL Product Family

Date	Revision Number	Modifications
		Appendix A

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iv Revision 2.2

Table of Contents

1.	Introduc	ction	1
1	.1	Chapter Outline	1
1	.2	Server Board Use Disclaimer	1
1	.3	Product Errata	2
2.	Product	Family Overview	3
2	2.1	Chassis Dimensions	6
2	2.2	System Level Environmental Limits	7
2	2.3	System Features and Options Overview	8
	2.3.1	Hot Swap Hard Drive Bay and Front Panel Options	8
	2.3.2	Back Panel Features	10
	2.3.3	Front Control Panel Options	10
2	2.4	Server Board Features Overview	11
2	2.5	Available Front Bezel Support	13
2	2.6	Available Rack and Cabinet Mounting Kit Options	15
3.	Power S	Subsystem	16
3	3.1	Mechanical Overview	16
3	3.2	Power Connectors	18
	3.2.1	Power Supply Module Card Edge Connector	18
	3.2.2	Riser Card Power Connectors	18
	3.2.3	Hot Swap Backplane Power Connector	
	3.2.4	Optical Drive and SSD Power Connector	19
3	3.3	Power Supply Module Efficiency	20
3	3.4	Power Cord Specification Requirements	
3	3.5	Optional Chassis Grounding Support	21
3	3.6	AC Power Supply Input Specifications	
	3.6.1	Power Factor	
	3.6.2	AC Input Voltage Specification	
	3.6.3	AC Line Isolation Requirements	
	3.6.4	AC Line Dropout / Holdup	
	3.6.5	AC Line Fuse	
	3.6.6	AC Inrush	
	3.6.7	AC Line Transient Specification	
	3.6.8	Susceptibility Requirements	
	3.6.9	Electrostatic Discharge Susceptibility	
	3.6.10	Fast Transient/Burst	
	3.6.11	Radiated Immunity	
	3.6.12	Surge Immunity	
	3.6.13	Power Recovery	
	3.6.14	Voltage Interruptions	
	3.6.15	Protection Circuits	
3	3.7	DC Power Supply Input Specifications	
	3.7.1	DC Input Voltage	
	3.7.2	DC Input Fuse	26

Intel® Server System R2000GZ/GL Product Family TPS

3.7.3	DC Inrush Current	26
3.7.4	DC Input Under Voltage	26
3.7.5	DC Holdup Time and Dropout	26
3.7.6	DC Line Surge Voltages (Line Transients)	26
3.7.7	Susceptibility Requirements	27
3.7.8	Protection Circuits	27
3.8	Cold Redundancy Support	28
3.8.1	Powering on Cold Standby supplies to maintain best efficiency	29
3.8.2	Powering on Cold Standby supplies during a fault or over current condition	29
3.8.3	BMC Requirements	29
3.8.4	Power Supply Turn On Function	29
3.9	Closed Loop System Throttling (CLST)	30
3.10	Smart Ride Through (SmaRT)	30
3.11	Power Supply Status LED	30
4. Therma	al Management	31
4.1	Thermal Operation and Configuration Requirements	31
4.2	Thermal Management Overview	32
4.2.1	Set Throttling Mode	33
4.2.2	Altitude	33
4.2.3	Set Fan Profile	33
4.2.4	Fan PWM Offset	33
4.2.5	Quiet Fan Idle Mode	33
4.2.6	Thermal Sensor Input for Fan Speed Control	33
4.3	System Fans	35
4.4	Power Supply Module Fan	37
4.5	FRUSDR Utility	37
5. Intel [®])	(eon Phi [™] Coprocessor and GPGPU Add-in Card Support	38
5.1	Using High Power Add-in Cards with Active Cooling Solutions	38
5.2	Using High Power Add-in Cards with Passive Cooling Solutions	39
5.3	Intel® Xeon Phi™ Coprocessor Card – System Configuration Requirements	40
6. Systen	n Storage and Peripheral Drive Bays Overview	41
6.1	2.5" Storage Device Support	41
6.1.1	2.5" Drive Hot-Swap Backplane Overview	43
6.1.2	Cypress* CY8C22545 Enclosure Management Controller	44
6.2	3.5" Drive Support	
6.2.1	3.5" Drive Hot-Swap Backplane Overview	47
6.2.2	Cypress* CY8C22545 Enclosure Management Controller	48
6.3	Optical Drive Support	49
6.4	Internal – Fixed Mount Solid State Drive (SSD) Support	50
6.5	Low Profile eUSB SSD Support	51
6.6	SATA DOM Support	51
7. Storag	e Controller Options Overview	52
7.1	Embedded SATA / SAS Controller support	52
7.2	Embedded Software RAID Support	53
7.2.1	Intel® Embedded Server RAID Technology 2 (ESRT2)	53
7.2.2	Intel® Rapid Storage Technology (RSTe)	

$\mathsf{Intel}^{\tiny{\textcircled{\tiny{\$}}}} \, \mathsf{Server} \, \, \mathsf{System} \, \, \mathsf{R2000GZ/GL} \, \, \mathsf{Product} \, \, \mathsf{Family} \, \, \mathsf{TPS}$

7.3	Intel® Integrated RAID Module Support (Available Option)	54
7.4	Intel® SAS Expander Support	55
7.4.1	24-Port PCIe Intel® RAID Expander Card RES2SV240	56
7.4.2	24-Port and 36-Port Internal Intel RAID Expander Cards	56
7.4.3	SAS Expander Usage Guidelines	58
7.4.4	Sample SAS Expander Configurations	58
8. Front C	Control Panel and I/O Panel Overview	60
8.1	I/O Panel Features	60
8.2	Control Panel Features	60
9. Intel [®] L	ocal Control Panel	64
10. PCI Ris	ser Card Support	65
10.1	Riser Slot Overview	65
10.2	Riser Card Assembly	67
10.3	Riser Card Options	68
10.3.1	3-Slot PCIe Riser Card	68
10.3.2	2-Slot PCIe Riser Card	69
10.3.3	3-Slot PCIx / PCIe Riser Card	69
10.4	PCIe Add-in card support	
10.4.1	PCIe Gen3 support – Systems configured with an Intel® Xeon® proces	ssor E5-2600 product family
10.4.2 family	PCIe Gen3 support – Systems configured with an Intel® Xeon® proce 70	essor E5-2600 V2 product
11. Mezzar	nine Module Support	74
11.1	IO Module Support	74
11.2	Intel® Remote Management Module 4 (RMM4) Lite and Management	NIC Support74
Appendix A	A: Integration and Usage Tips	76
	3: POST Code Diagnostic LED Decoder	
	C: POST Code Errors	
Appendix I	D: System Configuration Table for Thermal Compatibility	87
Glossary		90
Reference I	Jocuments	91

List of Figures

Figure 1. Chass	sis Dimensions	6
Figure 2. Syste	m Components Overview	8
Figure 3. 3.5" F	Hard Drive Bay - 8 Drive Configuration	8
Figure 4. 3.5" F	Hard Drive Bay - 12 Drive Configuration	9
Figure 5. 2.5" F	Hard Drive Bay - 8 Drive Configuration	9
Figure 6. 2.5" F	Hard Drive Bay - 16 Drive Configuration	9
Figure 7. 2.5" F	Hard Drive Bay - 24 Drive Configuration	9
Figure 8. Back	Panel Feature Identification	10
Figure 9. Front	Control Panel Options	10
Figure 10. Intel	® Server Board S2600GZ	12
Figure 11. Intel	[®] Light-Guided Diagnostic LEDs - Server Board	13
Figure 12. Fron	t Bezel accessory with optionally installed wave feature	13
Figure 13. Fron	nt Bezel accessory with optionally installed wave and ID badge (1)	14
Figure 14. Fron	nt Bezel accessory with optionally installed wave and ID badge (2)	14
Figure 15. Fron	nt Bezel accesssory ID Badge mechanical drawings	15
Figure 16. Pow	er Supply Module Mechanical Drawing	17
Figure 17. Pov	ver Supply Module	17
Figure 18. AC a	and DC Power Supply - Connector Views	17
Figure 19. AC F	Power Cord	20
Figure 20. DC I	Power Cord	21
Figure 21. Cha	ssis Grounding Studs	21
Figure 22. Fan	Control Model	34
Figure 23. Syst	em Fan Identification	35
Figure 24. Syst	em Fan Assembly	36
Figure 25. Serv	ver Board System Fan Connector Locations	36
Figure 26. Intel	Accessory Kit AGZCOPRODUCT - Air Duct	40
Figure 27. 2.5"	Drive Bay - 8 & 16 Drive Configuration	41
Figure 28. 2.5"	Drive Bay - 24 Drive Configuration	42
Figure 29. 3.5"	Drive Bay - 8 Drive Configuration	45
Figure 30. 3.5"	Drive Bay - 12 Drive Configuration	45
Figure 31. Opti	on to install 2.5" SSD into a 3.5" drive blank	46
Figure 32. Opti	cal Drive Support	49
Figure 33. 2.5"	Solid State Drive (SSD) Mounting Option	50
Figure 34. Low	Profile eUSB SSD Support	51
Figure 35. Inno	Disk* Low Profile SATA DOM	51
Figure 36. AXX	BBU9 Installation	55
Figure 37. A2U	BKTMFBUSSD Bracket + AXXRMFBU2 Installation	55
Figure 38. 24 P	Port PCIe Intel [®] RAID Expander Card RES2SV240	56
Figure 39. Inter	nal SAS Expander Installation	57
Figure 40. Inter	nal 24-Port SAS Expander Card (RES2CV240)	57
	Port Expander SAS Connector/Drive Identification Block Diagram	
Figure 42. Inter	rnal 36-Port SAS Expander Card (RES2CV360)	58

Intel® Server System R2000GZ/GL Product Family TPS

Figure 43. 36-Port Expander SAS Connector/Drive Identification Block Diagram	58
Figure 44. Front I/O Panel Features	60
Figure 45. Front Control Panel Features	61
Figure 46. Intel Local Control Panel Option	64
Figure 47. Riser Slot Architecture	65
Figure 48. Intel [®] Server Board S2600GZ/GL PCI Bus Layout Diagram	66
Figure 49. Riser Card Assembly	67
Figure 50. Full Height / Full Length Add-in Card Support	67
Figure 51. Intel® RMM4 Dedicated Management NIC Installation	75
Figure 52. Intel [®] RMM4 Lite Activation Key Installation	75
Figure 53. POST Diagnostic LED Location	77

İХ

List of Tables

Table 1. System Feature Set	4
Table 2. System Environmental Limits Summary	7
Table 3. Power Supply Module Output Power Connector Pin-out	18
Table 4. Riser Slot Power Pin-out ("OPT_12V_PWR_#")	19
Table 5. Hot Swap Backplane Power Connector Pin-out ("HSBP PWR")	19
Table 6. Peripheral Drive Power Connector Pin-out ("ODD/SSD PWR")	19
Table 7. 460 Watt AC Power Supply Efficiency (Gold)	20
Table 8. 750 Watt AC Power Supply Efficiency (Platinum)	20
Table 9. 750 Watt DC Power Supply Efficiency (Gold)	20
Table 10. AC Power Cord Specifications	20
Table 11. DC Power Cable Connector Pin-out	21
Table 12. AC Input Voltage Range	22
Table 13. AC Line Sag Transient Performance	23
Table 14. AC Line Surge Transient Performance	23
Table 15. Performance Criteria	24
Table 16. 460 Watt Power Supply Over Current Protection	25
Table 17. 750 Watt Power Supply Over Current Protection	25
Table 18. Over Voltage Protection (OVP) Limits	25
Table 19. DC Input Rating	26
Table 20. Line Voltage Transient Limits	27
Table 21. Over Current Protection	28
Table 22. Over Voltagge Protection Limits	28
Table 23. Example Load Share Threshold for Activating Supplies	29
Table 24. LED Indicators	30
Table 25. 2U System Volumetric Air Flow Requirements	35
Table 26. System Fan Connector Pin-out	37
Table 27. Drive Status LED States	42
Table 28. Drive Activity LED States	43
Table 29. Intel® RAID C600 Upgrade Key Options	52
Table 30. Supported Intel® Integrated RAID Modules	54
Table 31. System Status LED State Definitions	62
Table 32. Power/Sleep LED Functional States	63
Table 33. Riser Slot #1 – PCIe Port Routing	71
Table 34. Riser Slot #2 – PCIe Port Routing	72
Table 35. Enabling Advanced Management Features	75
Table 36. POST Progress Code LED Example	77
Table 37. Diagnostic LED POST Code Decoder	78
Table 38. MRC Progress Codes	80
Table 39. MRC Fatal Error Codes	81
Table 40. POST Error Messages and Handling	82
Table 41. POST Error Beep Codes	86
Table 42. Integrated BMC Beep Codes	86

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Revision 2.2 xi

1. Introduction

This Technical Product Specification (TPS) provides system level information for the Intel® Server System R2000GZ and Intel® Server System R2000GL product families. The system level features of both these product families are common, however the server board integrated into them is different. The Intel® Server System R2000GZ product family is integrated with an Intel® Server Board S2600GZ and the Intel® Server System R2000GL product family is integrated with the Intel® Server Board S2600GL.

This document will describe the functions and features of the integrated server system which includes the chassis layout, system boards, power sub-system, cooling sub-system, storage sub-system options, and available installable options. Server board specific detail can be obtained by referencing the *Intel® Server Board S2600GZ/S26000GLTechnical Product Specification*.

In addition, design-level information related to specific server board components / subsystems can be obtained by ordering External Product Specifications (EPS) or External Design Specifications (EDS) related to this server generation. EPS and EDS documents are made available under NDA with Intel and must be ordered through your local Intel representative. See the Reference Documents section at the end of this document for a list of available documents.

1.1 Chapter Outline

This document is divided into the following chapters:

- Chapter 1 Introduction
- Chapter 2 Product Family Overview
- Chapter 3 Power Subsystem
- Chapter 4 Thermal Management
- Chapter 5 Intel[®] Xeon Phi[™] Coprocessor card and GPGPU add-in card support
- Chapter 6 System Storage and Peripherals Drive Bay Overview
- Chapter 7 Storage Controller Options Overview
- Chapter 8 Front Control Panel and I/O Panel Overview
- Chapter 9 Intel[®] Local Control Panel
- Chapter 10 PCI Riser Card Support
- Chapter 11 Mezzanine Module Support
- Appendix A Integration and Usage Tips
- Appendix B POST Code Diagnostic LED Decoder
- Appendix C Post Code Errors
- Appendix D System Configuration Table for Thermal Compatibility
- Glossary
- Reference Documents

1.2 Server Board Use Disclaimer

Intel Corporation server boards support add-in peripherals and contain a number of high-density VLSI and power delivery components that need adequate airflow to cool. Intel[®] ensures through its own chassis development and testing that when Intel[®] server building blocks are used together, the fully integrated system will meet the intended thermal requirements of these components. It is the responsibility of the system integrator who chooses not to use Intel[®]-developed server building blocks to consult vendor datasheets and operating parameters to determine the amount of airflow required for their specific application and environmental conditions. Intel Corporation cannot be held responsible if components fail or the server board does not operate correctly when used outside any of their published operating or non-operating limits.

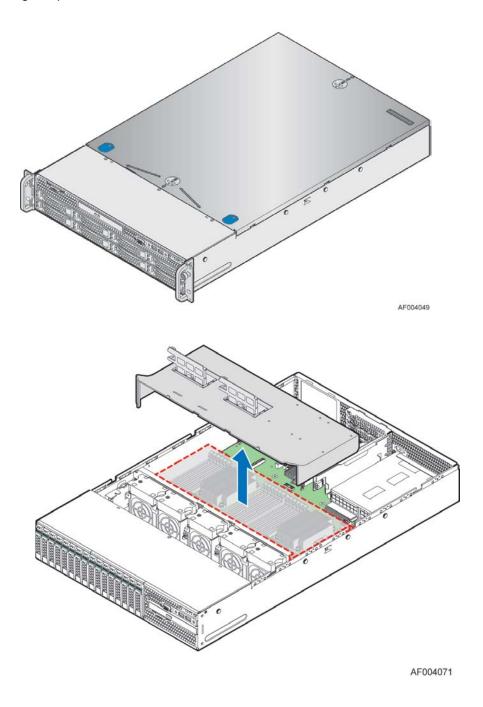
1.3 Product Errata

The products described in this document may contain design defects or errors known as errata which may cause the product to deviate from published specifications. Product Errata are documented in the *Intel® Server Board S2600GZGL, Intel® Server System R1000GZGL, Intel® Server System R2000GZGL Monthly Specification Update* which can be downloaded from Intel.com

2. Product Family Overview

This generation of Intel 2U server platforms offers a variety of system options to meet the varied configuration requirements of high-density high-performance computing environments. The Intel® Server System R2000GZ/GL product family includes several available 2U rack mount server systems that are integrated with either an Intel® Server Board S2600GZ or Intel® Server Board S2600GL.

This chapter provides a high-level overview of the system features and available options as supported in different platform SKUs within this server family. Greater detail for each major system component or feature is provided in the following chapters.



Note: The following table lists features common to both server product families. Features that are unique to one product family will be identified by either denoting the server system name or the integrated server board name.

Intel® Server System R2000GZ/GL Product Family TPS

Table 1. System Feature Set

Server System	Integrated Server Board
Intel® Server System R2000GZ product family	Intel [®] Server Board S2600GZ
Intel® Server System R2000GL product family	Intel [®] Server Board S2600GL

Feature	Description
Processor Support	 Support for one or two processors: Intel[®] Xeon[®] processor E5-2600 product family with TDP support up to 135 W^{1,2} Intel[®] Xeon[®] processor E5-2600 v2 product family with TDP support up to 130 W
Memory	 S2600GL - 16 DIMM slots – 2 DIMMs / Channel – 4 memory channels per processor S2600GZ - 24 DIMM slots – 3 DIMMs / Channel – 4 memory channels per processor Unbuffered DDR3 (UDIMM), Registered DDR3 (RDIMM), and Load Reduced DDR3 (LRDIMM) Memory DDR3 data transfer rates of 800, 1066,1600, and 1866³ MT/s DDR3 standard I/O voltage of 1.5V and DDR3 Low Voltage of 1.35V
Chipset	Intel® C602 chipset with support for optional Storage Option Select keys
External I/O connections	 Video – Back Panel + Front Panel on Non-Max Hard drive SKUs RJ-45 Serial- A Port Four RJ-45 Network Interface Connectors supporting 10/100/1000Mb USB 2.0 connectors - 3 on back panel + 2 on front panel on non-max hard drive SKUs
Internal I/O connectors / headers	One Type-A USB 2.0 connectorOne DH-10 Serial-B port connector
I/O Module Accessory Options	The following I/O modules utilize a single proprietary on-board connector. An installed I/O module can be supported in addition to standard on-board features and any add-in expansion cards. • AXX4P1GBPWLIOM – Quad port 1 GbE based on Intel® Ethernet Controller I350 • AXX10GBTWLIOM – Dual RJ-45 port 10GBase-T I/O Module based on Intel® Ethernet Controller x540 • AXX10GBNIAIOM – Dual SFP+ port 10GbE module based on Intel® 82599 10 GbE controller • AXX1FDRIBIOM – Single Port FDR 56GT/S speed InfiniBand module with QSFP connector • AXX2FDRIBIOM – Dual port FDR 56GT/S speed infiniband module with QSFP connector
System Fans	Five managed system fansOne power supply fan for each installed power supply module
Riser Cards	Support for two riser cards. Each riser card slot has support for the following riser card options: 3-slot PCle Riser Card: (Slots 1 & 2) – PCle x16 slot, x8 lanes, (Slot 3) – PCle x8 slot, x8 lanes 2-slot PCle Riser Card: (Slot 1) – PCle x16 slot, x16 lanes, (Slot 2) – PCle x8 slot, x8 lanes 3-slot PClx/PCle Riser Card: (Slots 1 & 2) – PClx 64-bit, (Slot 3) – PCle x8 slot, x8 lanes
Video	Integrated 2D Video Controller16 MB DDR3 Memory
On-board storage controllers and options	 One eUSB 2x5 pin connector to support 2mm low-profile eUSB solid state devices Two 7-pin single port AHCI SATA connectors capable of supporting up to 6 GB/sec Two SCU 4-port mini-SAS connectors capable of supporting up to 3 GB/sec SAS/SATA SCU 0 Port (Enabled standard) SCU 1 Port (Requires Intel RAID C600 Upgrade Key) Intel[®] Integrated RAID module support (Optional) Intel[®] RAID C600 Upgrade Key support providing optional expanded SATA / SAS RAID capabilities
Security	Intel® TPM Module - AXXTPME5 (Accessory Option)
Server Management	 Integrated Baseboard Management Controller, IPMI 2.0 compliant Support for Intel® Server Management Software Intel® Remote Management Module 4 Lite – Accessory Option Intel® Remote Management Module 4 Management NIC – Accessory Option

Intel® Server System R2000GZ/GL Product Family TPS

Power Supply Options	 The server system can have up to two power supply modules installed, providing support for the following power configurations: 1+0, 1+1 Redundant Power, and 2+0 Combined Power Two power supply options: AC 460W Gold AC 750W Platinum DC 750W Gold
Storage Bay Options	 8x - 3.5" SATA/SAS Hot Swap Hard Drive Bays + Optical Drive support 12x - 3.5" SATA/SAS Hot Swap Hard Drive Bays 8x - 2.5" SATA/SAS Hot Swap Hard Drive Bays + Optical Drive support 16x - 2.5" SATA/SAS Hot Swap Hard Drive Bays + Optical Drive support 24x - 2.5" SATA/SAS Hot Swap Hard Drive Bays 2x - internal fixed mount SSDs (All SKUs)
Supported Rack Mount Kit Accessory Options	 AXXPRAIL – Tool-less rack mount rail kit – 800mm max travel length AXXPRAIL755 – Tool-less rack mount rail kit – 755mm max travel length AXXVRAIL – Value rack mount rail kit – 424mm max travel length AXXELVRAIL – Enhanced value rack mount rail kit - 424mm max travel length AXX1U2UCMA – Cable Management Arm – (*supported with AXXPRAIL only) AXX2POSTBRCKT – 2-post fixed mount bracket kit

Notes:

- With a system fan failure, processor throttling may occur
 Processor throttling may occur with systems configured using the following Intel[®] Xeon[®] E5-2600 product family processors: E5-2690, E5-2643
 Intel[®] Xeon[®] processor E5-2600 v2 product family only

Revision 2.2

5

2.1 Chassis Dimensions

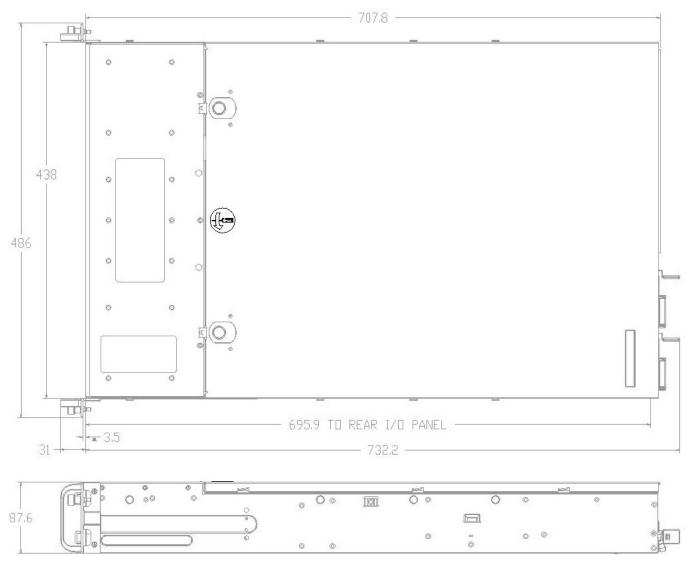


Figure 1. Chassis Dimensions

2.2 System Level Environmental Limits

The following table defines the system level operating and non-operating environmental limits.

Table 2. System Environmental Limits Summary

erating pping erating pping erating packaged	ASHRAE Class A2 – Continuous Operation. 10° C to 35° C (50° F to 95° F) with the maximum rate of change not to exceed 10°C per hour ASHRAE Class A3 – Includes operation up to 40C for up to 900 hrs per year. ASHRAE Class A4 – Includes operation up to 45C for up to 90 hrs per year. -40° C to 70° C (-40° F to 158° F) Support operation up to 3050m with ASHRAE class deratings. 50% to 90%, non-condensing with a maximum wet bulb of 28° C (at temperatures from 25° C to 35° C)			
pping erating pping erating	rate of change not to exceed 10°C per hour ASHRAE Class A3 – Includes operation up to 40C for up to 900 hrs per year. ASHRAE Class A4 – Includes operation up to 45C for up to 90 hrs per year. -40° C to 70° C (-40° F to 158° F) Support operation up to 3050m with ASHRAE class deratings. 50% to 90%, non-condensing with a maximum wet bulb of 28° C (at temperatures from 25° C			
erating pping erating	ASHRAE Class A4 – Includes operation up to 45C for up to 90 hrs per year. -40° C to 70° C (-40° F to 158° F) Support operation up to 3050m with ASHRAE class deratings. 50% to 90%, non-condensing with a maximum wet bulb of 28° C (at temperatures from 25° C			
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erating pping erating	Support operation up to 3050m with ASHRAE class deratings. 50% to 90%, non-condensing with a maximum wet bulb of 28° C (at temperatures from 25° C			
pping	50% to 90%, non-condensing with a maximum wet bulb of 28° C (at temperatures from 25° C			
pping	50% to 90%, non-condensing with a maximum wet bulb of 28° C (at temperatures from 25° C			
erating				
erating				
•				
•				
oackaged	Half sine, 2g, 11 mSec			
	Trapezoidal, 25 g, velocity change is based on packaged weight			
ckaged	Product Weight: ≥ 40 to < 80			
	Non-palletized Free Fall Height = 18 inches			
	Palletized (single product) Free Fall Height = NA			
l	F. He to FOO He . O OO to PMO to to do to			
oackaged	5 Hz to 500 Hz 2.20 g RMS random			
ckaged	5 Hz to 500 Hz 1.09 g RMS random			
togo	90 Hz to 132 V and 180 V to 264 V			
-	47 Hz to 63 Hz No loss of data for power line drop-out of 12 mSec			
	Unidirectional			
erating and erating	Officirectional			
e to earth Only	AC Leads 2.0 kV			
	I/O Leads 1.0 kV			
	DC Leads 0.5 kV			
Disaban	40.0127			
	12.0 kV			
ntact charge	8.0 kV			
wer in Watts	<300 W ≥300 W ≥600 W ≥1000 W			
vers/Rack unt BA	7.0 7.0 7.0 7.0			
	Discharged htact charge ver in Watts vers/Rack			

Disclaimer Note: Intel ensures the unpackaged server board and system meet the shock requirement mentioned above through its own chassis development and system configuration. It is the responsibility of the system integrator to determine the proper shock level of the board and system if the system integrator chooses different system configuration or different chassis. Intel Corporation cannot be held responsible, if components fail or the server board does not operate correctly when used outside any of its published operating or non-operating limits.

See Appendix D in this document or the *Intel® S2600GZGL Product Family Power Budget and Thermal Configuration Tool* for system configuration requirements and limitations.

2.3 System Features and Options Overview

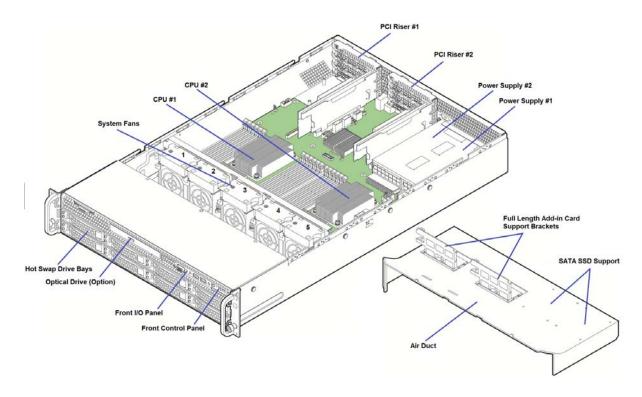


Figure 2. System Components Overview

2.3.1 Hot Swap Hard Drive Bay and Front Panel Options

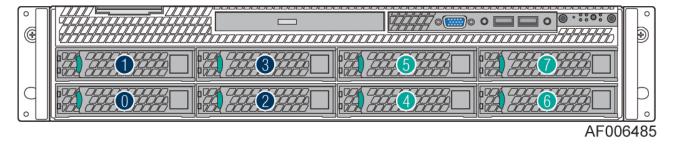


Figure 3. 3.5" Hard Drive Bay - 8 Drive Configuration

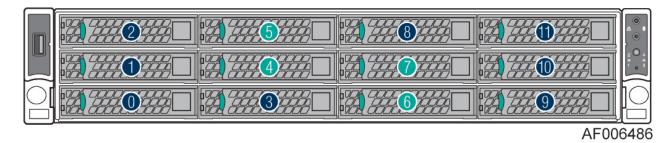


Figure 4. 3.5" Hard Drive Bay - 12 Drive Configuration

Note: Each set of four drives with a common color device ID represents a common SATA/SAS cable interface connector on the backside of the backplane.

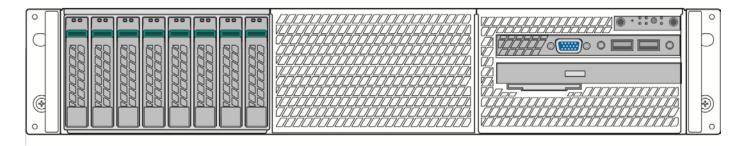


Figure 5. 2.5" Hard Drive Bay - 8 Drive Configuration

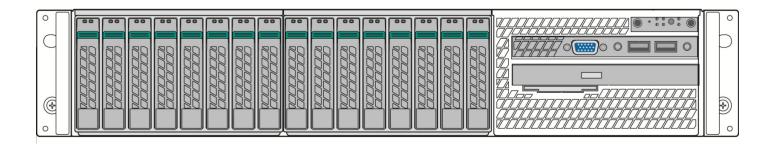


Figure 6. 2.5" Hard Drive Bay - 16 Drive Configuration

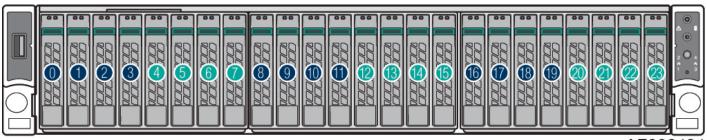


Figure 7. 2.5" Hard Drive Bay - 24 Drive Configuration

Revision 2.2

AF006484

Note: Each set of four drives with a common color device ID represent a common SATA/SAS cable interface connector on the backside of the backplane.

2.3.2 Back Panel Features

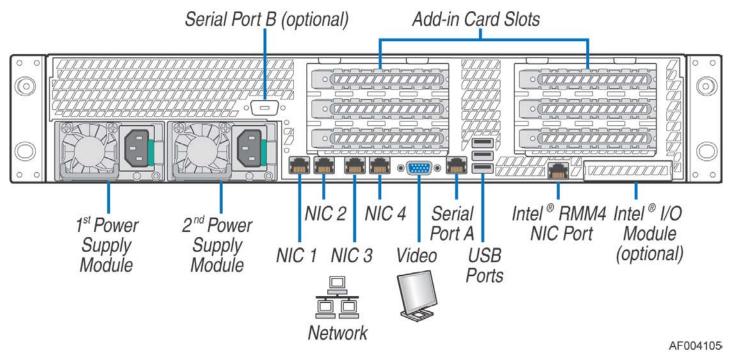
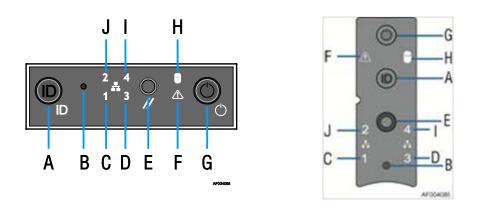


Figure 8. Back Panel Feature Identification

2.3.3 Front Control Panel Options



Label	Description	Label	Description
Α	System ID Button w/Integrated LED	F	System Status LED
В	NMI Button (recessed, tool required for use)	G	Power Button w/Integrated LED
С	NIC-1 Activity LED	Н	Hard Drive Activity LED
D	NIC-3 Activity LED	I	NIC-4 Activity LED
Е	System Cold Reset Button	J	NIC-2 Activity LED

Figure 9. Front Control Panel Options

2.4 Server Board Features Overview

The following illustration provides a general overview of the server board, identifying key feature and component locations. The majority of the items identified are common between the Intel[®] Server Board S2600GL and S2600GZ.

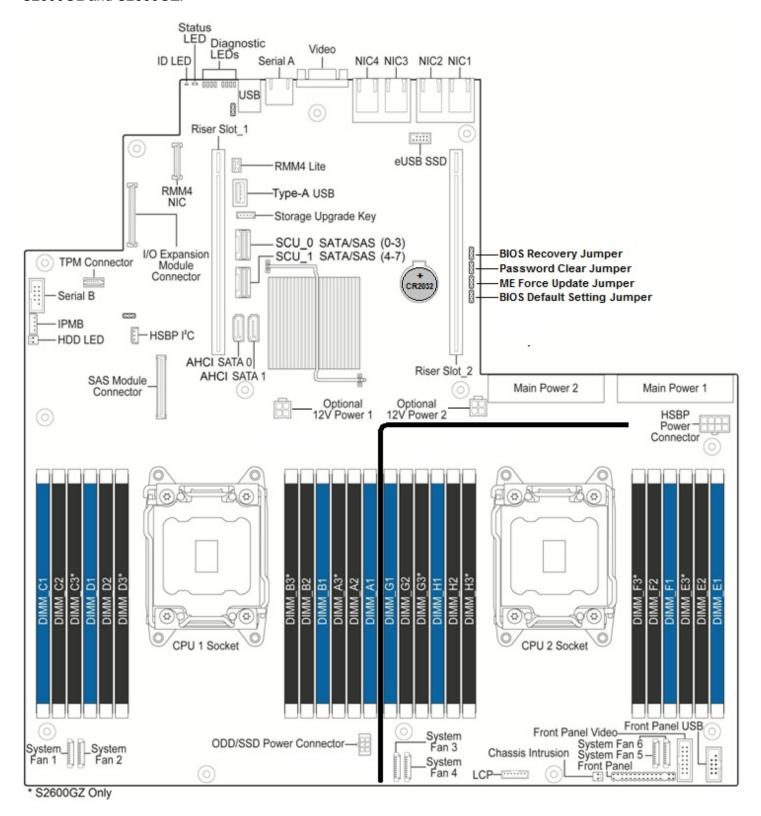
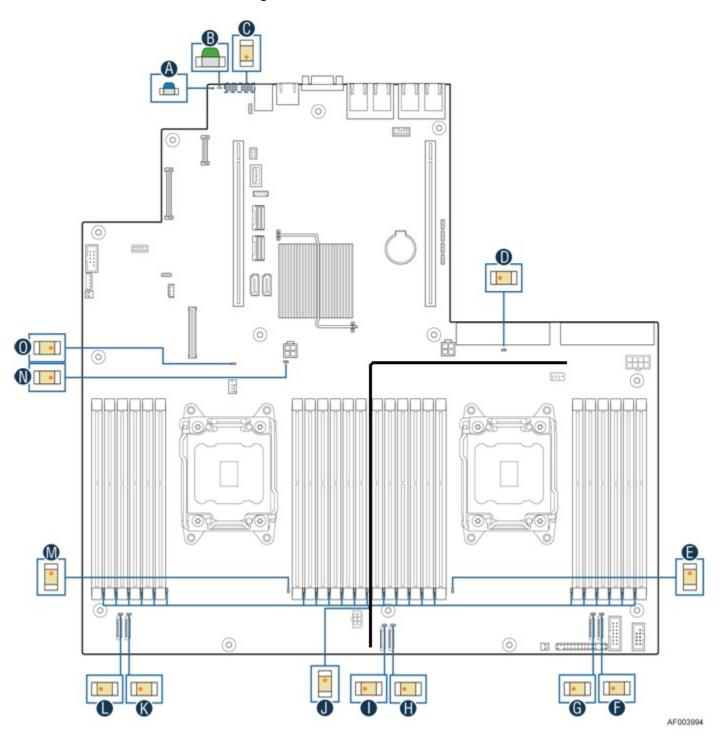


Figure 10. Intel[®] Server Board S2600GZ



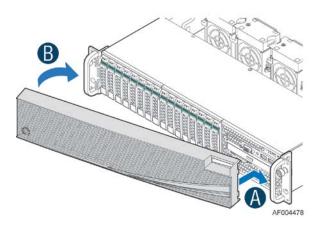
Label	Description	Label	Description
Α	System ID	ı	System Fan #3 Fan Fault
В	System Status	J	Memory Fault
С	POST Code Diagnostics	K	System Fan #2 Fan Fault
D	12V Stand-by Power Present	L	System Fan #1 Fan Fault
Е	CPU-2 Fault	М	CPU-1 Fault
F	System Fan #6 Fan Fault	N	CATERR
G	System Fan #5 Fan Fault	0	System Power Good
Н	System Fan #4 Fan Fault		

Figure 11. Intel[®] Light-Guided Diagnostic LEDs - Server Board

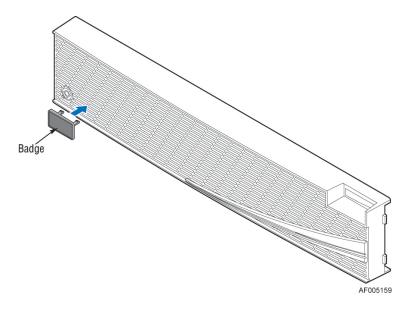
2.5 Available Front Bezel Support

The optional front bezel is made of Black molded plastic and uses a snap-on design. When installed, its design allows for maximum airflow to maintain system cooling requirements.

(Intel Product Order Code – A2UBEZEL)



The face of the bezel assembly includes snap-in identification badge options and a wave feature option to allow for customization.



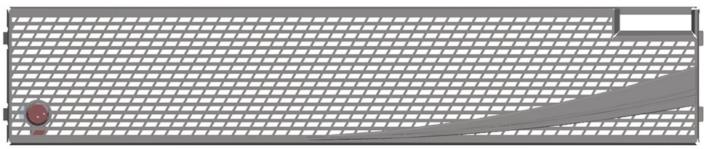


Figure 12. Front Bezel accessory with optionally installed wave feature



Figure 13. Front Bezel accessory with optionally installed wave and ID badge (1)



Figure 14. Front Bezel accessory with optionally installed wave and ID badge (2)

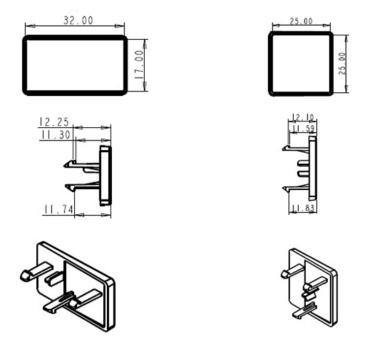


Figure 15. Front Bezel accesssory ID Badge mechanical drawings

2.6 Available Rack and Cabinet Mounting Kit Options

<u>Advisory Note</u> – Available rack and cabinet mounting kits are not designed to support shipment of the server system while installed in a rack. If you choose to do so, Intel advises you verify your shipping configuration with appropriate shock and vibe testing, before shipment. Intel does not perform shipping tests which cover the complex combination of unique rack offerings and custom packaging options.

<u>Caution:</u> Exceeding the specified maximum weight or misalignment of the server may result in failure of the rack rails, resulting in damage to the system or personal injury. The use of mechanical assists to install and align the server into the rack rails is highly recommended.

- AXXPRAIL Tool-less rack mount rail kit
 - o 1U and 2U compatible
 - o 800mm max travel length
 - o 54 lbs (24 Kgs) max support weight
 - o Tool-less installation
 - o Full extension from rack
 - Drop in system install
 - o Optional cable management arm support
- AXXPRAIL755 Tool-less rack mount rail kit
 - o 1U and 2U compatible
 - 755mm max travel length
 - o 54 lbs (24 Kgs) max support weight
 - o Tool-less installation
 - o Full extension from rack
 - o Drop in system install
- AXXVRAIL Value rack mount rail kit
 - o 1U to 4U compatible
 - o 130 lbs (59 Kgs) max support weight
 - o Tool-less chassis attach
 - o Tools required to attach to rails to rack
 - o 2/3 extension from rack
- AXXELVRAIL Enhanced Value rack mount rail kit
 - o 1U to 4U compatible
 - 130 lbs (59 Kgs) max support weight
 - o Tool-less chassis attach
 - o Tools required to attach to rails to rack
 - o 2/3 extension from rack
 - o Improved robustness over AXXVRAIL, same mechanical spec
- AXX1U2UCMA Cable Management Arm *supported with AXXPRAIL only
- AXX2POSTBRCKT 2-Post Fixed mount bracket kit
 - o 1U and 2U compatible
 - Tools required to attach components to rack

3. Power Subsystem

This chapter provides a high level overview of the power management features and specification data for the power supply options available for this server product. Specification variations will be identified for each supported power supply.

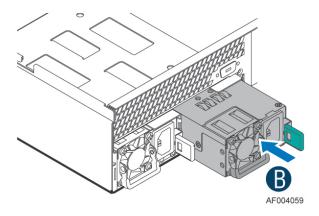
The server system can have upto two power supply modules installed, supporting the following power supply configurations: 1+0 (single power supply), 1+1 Redundant Power, and 2+0 Combined Power (non-redundant). 1+1 redundant power and 2+0 combined power configurations are automatically configured depending on the total power draw of the system. If the total system power draw exceeds the power capacity of a single power supply module, then power from the 2nd power supply module will be utilized. Should this occur, power redundancy is lost. In a 2+0 power configuration, total power available maybe less then twice the rated power of the installed power supply modules due to the amount of heat produced with both supplies providing peak power. Should system thermals exceed programmed limits, platform management will attempt to keep the system operational. See *Closed Loop System Throttling (CLST)* later in this chapter, and Chapter 4 *Thermal Management*, for details.

There are three power supply options available for this server product: 460W AC, 750W AC, 750W DC.

Caution: Installing two Power Supply Units with different wattage ratings in a system is not supported. Doing so will not provide Power Supply Redundancy and will result in multiple errors being logged by the system.

The power supplies are modular, allowing for tool-less insertion and extraction from a bay in the back of the chassis. When inserted, the card edge connector of the power supply mates blindly to a matching slot connector on the server board.

In the event of a power supply failure, redundant 1+1 power supply configurations have support for hot-swap extraction and insertion.



The AC input is auto-ranging and power factor corrected.

3.1 Mechanical Overview

The physical size of the power supply enclosure is 39/40mm x 74mm x 185mm. The power supply contains a single 40mm fan. The power supply has a card edge output that interfaces with a 2x25 card edge connector in the system. The AC plugs directly into the external face of the power supply

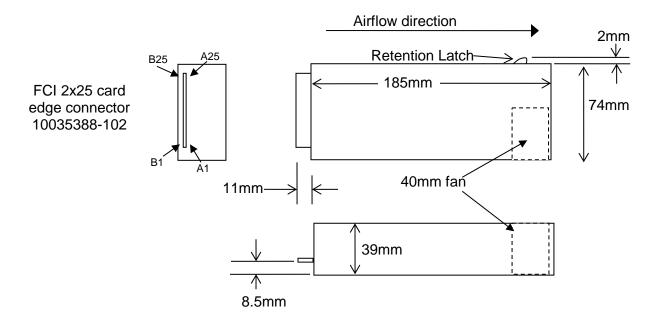


Figure 16. Power Supply Module Mechanical Drawing

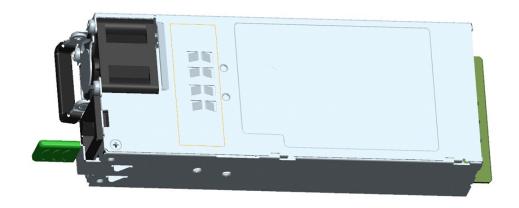


Figure 17. Power Supply Module



Figure 18. AC and DC Power Supply - Connector Views

3.2 Power Connectors

3.2.1 Power Supply Module Card Edge Connector

Each power supply module has a single 2x25 card edge output connection that plugs directly into a matching slot connector on the server board. The connector provides both power and communication signals to the server board. The following table defines the connector pin-out.

Table 3. Power Supply Module Output Power Connector Pin-out

Pin	Name	Pin	Name
A1	GND	B1	GND
A2	GND	B2	GND
A3	GND	В3	GND
A4	GND	B4	GND
A5	GND	B5	GND
A6	GND	B6	GND
A7	GND	B7	GND
A8	GND	B8	GND
A9	GND	B9	GND
A10	+12V	B10	+12V
A11	+12V	B11	+12V
A12	+12V	B12	+12V
A13	+12V	B13	+12V
A14	+12V	B14	+12V
A15	+12V	B15	+12V
A16	+12V	B16	+12V
A17	+12V	B17	+12V
A18	+12V	B18	+12V
A19	PMBus SDA	B19	A0 (SMBus address)
A20	PMBus SCL	B20	A1 (SMBus address)
A21	PSON	B21	12V stby
A22	SMBAlert#	B22	Cold Redundancy Bus
A23	Return Sense	B23	12V load share bus
A24	+12V remote Sense	B24	No Connect
A25	PWOK	B25	Compatibility Check pin*

The server board provides several connectors to provide power to various system options. The following subsections will identify the location; provide the pin-out definition; and provide a brief usage description for each.

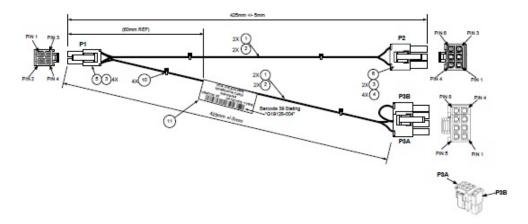
3.2.2 Riser Card Power Connectors

The server board includes two white 2x2-pin power connectors that provide supplemental power to high power PCIe x16 add-in cards (GPGPU) that have power requirements that exceed the 75W maximum power supplied by the PCIe x16 riser slot. A cable from this connector may be routed to a power connector on the given add-in card. Maximum power draw for each connector is 225W, but is also limited by available power provided by the power supply and the total power draw of the rest of the system. A power budget for the complete system should be performed to determine how much supplemental power is available to support any high power add-in cards.

Each connector is labeled as "OPT_12V_PWR_1" and "OPT_12V_PWR_2" on the server board. The following table provides the pin-out for both connectors.

Table 4. Riser Slot Power Pin-out ("OPT_12V_PWR_#")

Signal Description	Pin#	Pin#	Signal Description
P12V	3	1	GROUND
P12V	4	2	GROUND



The power cable (as shown above) for the OPT_12V_PWR_# connector is included in the 2-slot Riser Card accessory kit (A2UL16RISER) and can support both 6 and 8 pin GPU card 12V AUX power connectors.

3.2.3 Hot Swap Backplane Power Connector

The server board includes one white 2x4-pin power connector that is cabled to provide power for hot swap backplanes. On the server board, this connector is labeled as "HSBP PWR". The following table provides the pin-out for this connector.

Table 5. Hot Swap Backplane Power Connector Pin-out ("HSBP PWR")

Signal Description	Pin#	Pin#	Signal Description
P12V_240VA	5	1	GROUND
P12V_240VA	6	2	GROUND
P12V_240VA	7	3	GROUND
P12V_240VA	8	4	GROUND

3.2.4 Optical Drive and SSD Power Connector

The server board includes one brown 2x3-pin power connector intended to provide power to optionally installed optical drive and up to two Solid State Devices (SSDs) mounted to the top side of the air duct. On the server board this connector is labeled as "ODD/SSD PWR". The following table provides the pin-out for this connector.

Table 6. Peripheral Drive Power Connector Pin-out ("ODD/SSD PWR")

Signal Description	Pin#	Pin#	Signal Description
P12V	4	1	P5V
P3V3	5	2	P5V
GROUND	6	3	GROUND

3.3 Power Supply Module Efficiency

The following tables provide the required minimum efficiency level at various loading conditions. These are provided at three different load levels: 100%, 50% and 20%. Efficiency is tested over an AC input voltage range of 115 VAC to 220 VAC.

Table 7. 460 Watt AC Power Supply Efficiency (Gold)

Loading	100% of maximum	50% of maximum	20% of maximum	10% of maximum
Minimum Efficiency	88%	92%	88%	80%

Table 8. 750 Watt AC Power Supply Efficiency (Platinum)

Loading	100% of maximum	50% of maximum	20% of maximum	10% of maximum
Minimum Efficiency	91%	94%	90%	82%

Table 9. 750 Watt DC Power Supply Efficiency (Gold)

Loading	100% of maximum	50% of maximum	20% of maximum	10% of maximum
Minimum Efficiency	88%	92%	88%	80%

3.4 Power Cord Specification Requirements

The AC power cord used must meet the specification requirements listed in the following table.

Table 10. AC Power Cord Specifications

Cable Type	SJT
Wire Size	16 AWG
Temperature Rating	105°C
Amperage Rating	13 A
Voltage Rating	125 V

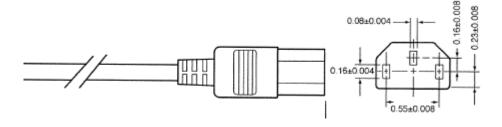


Figure 19. AC Power Cord

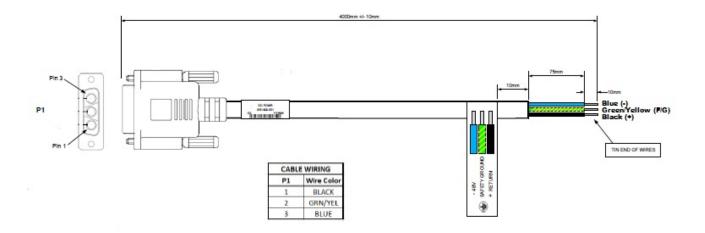


Figure 20. DC Power Cord

Table 11. DC Power Cable Connector Pin-out

Pin	Definition
1	+ Return
2	Safety Ground
3	- 48V

3.5 Optional Chassis Grounding Support

The system provides 10-32 threaded grounding studs on the back panel of the chassis, allowing for optional system grounding via a grounding strap.

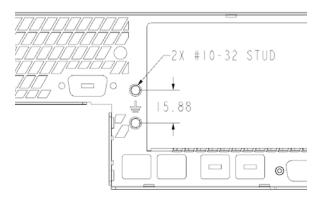


Figure 21. Chassis Grounding Studs

Note: Product Safety Regulations pertaining to the use of DC power supplies require that chassis grounding studs be used for all DC power supply configurations. In the event that chassis grounding studs are not available on a given server chassis, systems must be configured with two DC power supplies, with each connected to separate ground wires while the system is operational.

3.6 AC Power Supply Input Specifications

The following sections provide the AC Input Specifications for systems configured with AC power supply modules.

3.6.1 Power Factor

The power supply must meet the power factor requirements stated in the Energy Star® Program Requirements for Computer Servers. These requirements are stated below.

Output power	10% load	20% load	50% load	100% load
Power factor	> 0.65	> 0.80	> 0.90	> 0.95

Tested at 230Vac, 50Hz and 60Hz and 115VAC, 60Hz

3.6.2 AC Input Voltage Specification

The power supply must operate within all specified limits over the following input voltage range. Harmonic distortion of up to 10% of the rated line voltage must not cause the power supply to go out of specified limits. Application of an input voltage below 85VAC shall not cause damage to the power supply, including a blown fuse.

PARAMETER	MIN	RATED	VMAX	Start up VAC	Power Off VAC
Voltage (110)	90 Vrms	100-127 Vrms	140 Vrms	85VAC +/- 4VAC	70VAC +/- 5VAC
Voltage (220)	180 Vrms	200-240 Vrms	264 Vrms		
Frequency	47 Hz	50/60	63 Hz		

Table 12. AC Input Voltage Range

- 1. Maximum input current at low input voltage range shall be measured at 90VAC, at max load.
- 2. Maximum input current at high input voltage range shall be measured at 180VAC, at max load.
- 3. This requirement is not to be used for determining agency input current markings.

3.6.3 AC Line Isolation Requirements

The power supply shall meet all safety agency requirements for dielectric strength. Transformers' isolation between primary and secondary windings must comply with the 3000Vac (4242Vdc) dielectric strength criteria. If the working voltage between primary and secondary dictates a higher dielectric strength test voltage the highest test voltage should be used. In addition the insulation system must comply with reinforced insulation per safety standard IEC 950. Separation between the primary and secondary circuits, and primary to ground circuits, must comply with the IEC 950 spacing requirements.

3.6.4 AC Line Dropout / Holdup

An AC line dropout is defined to be when the AC input drops to 0VAC at any phase of the AC line for any length of time. During an AC dropout the power supply must meet dynamic voltage regulation requirements. An AC line dropout of any duration shall not cause tripping of control signals or protection circuits. If the AC dropout lasts longer than the hold up time the power supply should recover and meet all turn on requirements. The power supply shall meet the AC dropout requirement over rated AC voltages and frequencies. A dropout of the AC line for any duration shall not cause damage to the power supply.

Loading	Holdup time		
70%	12msec		

3.6.4.1 AC Line 12VSBHoldup

The 12VSB output voltage should stay in regulation under its full load (static or dynamic) during an AC dropout of **70ms min** (=12VSB holdup time) whether the power supply is in ON or OFF state (PSON asserted or deasserted).

3.6.5 AC Line Fuse

The power supply shall have one line fused in the **single line fuse** on the line (Hot) wire of the AC input. The line fusing shall be acceptable for all safety agency requirements. The input fuse shall be a slow blow type. AC inrush current shall not cause the AC line fuse to blow under any conditions. All protection circuits in the power supply shall not cause the AC fuse to blow unless a component in the power supply has failed. This includes DC output load short conditions.

3.6.6 AC Inrush

AC line inrush current shall not exceed **55A peak**, for up to one-quarter of the AC cycle, after which, the input current should be no more than the specified maximum input current. The peak inrush current shall be less than the ratings of its critical components (including input fuse, bulk rectifiers, and surge limiting device).

The power supply must meet the inrush requirements for any rated AC voltage, during turn on at any phase of AC voltage, during a single cycle AC dropout condition as well as upon recovery after AC dropout of any duration, and over the specified temperature range (T_{op}).

3.6.7 AC Line Transient Specification

AC line transient conditions shall be defined as "sag" and "surge" conditions. "Sag" conditions are also commonly referred to as "brownout", these conditions will be defined as the AC line voltage dropping below nominal voltage conditions. "Surge" will be defined to refer to conditions when the AC line voltage rises above nominal voltage.

The power supply shall meet the requirements under the following AC line sag and surge conditions.

Table 13. AC Line Sag Transient Performance

AC Line Sag (10sec interval between each sagging)				
Duration	Sag	Operating AC Voltage	Line Frequency	Performance Criteria
0 to 1/2 AC cycle	95%	Nominal AC Voltage ranges	50/60Hz	No loss of function or performance
> 1 AC cycle	>30%	Nominal AC Voltage ranges	50/60Hz	Loss of function acceptable, self recoverable

Table 14. AC Line Surge Transient Performance

AC Line Surge				
Duration	Surge	Operating AC Voltage	Line Frequency	Performance Criteria
Continuous	10%	Nominal AC Voltages	50/60Hz	No loss of function or performance
0 to ½ AC cycle	30%	Mid-point of nominal AC Voltages	50/60Hz	No loss of function or performance

3.6.8 Susceptibility Requirements

The power supply shall meet the following electrical immunity requirements when connected to a cage with an external EMI filter which meets the criteria defined in the SSI document EPS Power Supply Specification. For further information on Intel standards please request a copy of the Intel Environmental Standards Handbook

Table 15. Performance Criteria

Level	Description
Α	The apparatus shall continue to operate as intended. No degradation of performance.
В	The apparatus shall continue to operate as intended. No degradation of performance beyond spec limits.
С	Temporary loss of function is allowed provided the function is self-recoverable or can be restored by the operation of the controls.

3.6.9 Electrostatic Discharge Susceptibility

The power supply shall comply with the limits defined in EN 55024: 1998/A1: 2001/A2: 2003 using the IEC 61000-4-2: Edition 1.2: 2001-04 test standard and performance criteria B defined in Annex B of CISPR 24.

3.6.10 Fast Transient/Burst

The power supply shall comply with the limits defined in EN55024: 1998/A1: 2001/A2: 2003 using the IEC 61000-4-4: Second edition: 2004-07 test standard and performance criteria B defined in Annex B of CISPR 24.

3.6.11 Radiated Immunity

The power supply shall comply with the limits defined in EN55024: 1998/A1: 2001/A2: 2003 using the IEC 61000-4-3: Edition 2.1: 2002-09 test standard and performance criteria A defined in Annex B of CISPR 24.

3.6.12 Surge Immunity

The power supply shall be tested with the system for immunity to AC Unidirectional wave; 2kV line to ground and 1kV line to line, per EN 55024: 1998/A1: 2001/A2: 2003, EN 61000-4-5: Edition 1.1:2001-04. The pass criteria include: No unsafe operation is allowed under any condition; all power supply output voltage levels to stay within proper spec levels; No change in operating state or loss of data during and after the test profile; No component damage under any condition.

The power supply shall comply with the limits defined in EN55024: 1998/A1: 2001/A2: 2003 using the IEC 61000-4-5: Edition 1.1:2001-04 test standard and performance criteria B defined in Annex B of CISPR 24.

3.6.13 Power Recovery

The power supply shall recover automatically after an AC power failure. AC power failure is defined to be any loss of AC power that exceeds the dropout criteria.

3.6.14 Voltage Interruptions

The power supply shall comply with the limits defined in EN55024: 1998/A1: 2001/A2: 2003 using the IEC 61000-4-11: Second Edition: 2004-03 test standard and performance criteria C defined in Annex B of CISPR 24.

3.6.15 Protection Circuits

Protection circuits inside the power supply cause only the power supply's main outputs to shut down. If the power supply latches off due to a protection circuit tripping, an AC cycle OFF for 15 seconds and a PSON* cycle HIGH for one second reset the power supply.

3.6.15.1 Over-current Protection (OCP)

The power supply shall have current limit to prevent the outputs from exceeding the values shown in table below. If the current limits are exceeded the power supply shall shutdown and latch off. The latch will be cleared by toggling the PSON[#] signal or by an AC power interruption. The power supply shall not be damaged from repeated power cycling in this condition. 12VSB will be auto-recovered after removing OCP limit.

Table 16. 460 Watt Power Supply Over Current Protection

Output Voltage	Input voltage range	Over Current Limits
+12V	90 - 264VAC	47A min; 55A max
12VSB	90 - 264VAC	2A min; 2.5A max

Table 17. 750 Watt Power Supply Over Current Protection

Output Voltage	Input voltage range	Over Current Limits
+12V	90 – 264VAC	72A min; 78A max
12VSB	90 – 264VAC	2.5A min; 3.5A max

3.6.15.2 Over-voltage Protection (OVP)

The power supply over voltage protection shall be locally sensed. The power supply shall shutdown and latch off after an over voltage condition occurs. This latch shall be cleared by toggling the PSON* signal or by an AC power interruption. The values are measured at the output of the power supply's connectors. The voltage shall never exceed the maximum levels when measured at the power connectors of the power supply connector during any single point of fail. The voltage shall never trip any lower than the minimum levels when measured at the power connector. 12VSB will be auto-recovered after removing OVP limit.

Table 18. Over Voltage Protection (OVP) Limits

Output Voltage	MIN (V)	MAX (V)
+12V	13.3	14.5
+12VSB	13.3	14.5

3.6.15.3 Over-temperature Protection (OTP)

The power supply will be protected against over temperature conditions caused by loss of fan cooling or excessive ambient temperature. In an OTP condition the PSU will shutdown. When the power supply temperature drops to within specified limits, the power supply shall restore power automatically, while the 12VSB remains always on. The OTP circuit must have built in margin such that the power supply will not oscillate on and off due to temperature recovering condition. The OTP trip level shall have a minimum of 4°C of ambient temperature margin.

3.7 DC Power Supply Input Specifications

The following sections provide the DC Input Specifications for systems configured with DC power supply modules.

NOTE: Product Safety Regulations pertaining to the use of DC power supplies require that chassis grounding studs be used for all DC power supply configurations. In the event that chassis grounding studs are not available on a given server chassis, systems must be configured with two DC power supplies, with each connected to separate ground wires while the system is operational.

3.7.1 DC Input Voltage

The power supply must operate within all specified limits over the following input voltage range.

Table 19. DC Input Rating

PARAMETER	MIN	RATED	MAX
DC Voltage	-40.5 VDC	-48VDC/-60VDC	-75VDC
Input Current	24A		12.5A

3.7.2 DC Input Fuse

The power supply shall have the –48VDC input fused. The fusing shall be acceptable for all safety agency requirements. DC inrush current shall not cause the fuse to blow under any conditions. No protection circuits in the power supply shall cause the DC fuse to blow unless a component in the power supply has failed. This includes DC output load short conditions.

3.7.3 DC Inrush Current

Maximum inrush current from power-on shall be limited to a level below the surge rating of the input line cable; input diodes, fuse, and EMI filter components. To allow multiple power cycling events and DC line transient conditions max I²t value shall not exceed 20% of the fuse max rating. Repetitive ON/OFF cycling of the DC input line voltage should not damage the power supply or cause the input fuse to blow.

3.7.4 DC Input Under Voltage

The power supply shall contain protection circuitry (under-voltage lock-out) such that the application of an input voltage below the specified minimum specified, shall not cause damage (overstress) to the power supply unit (due to over-heating or otherwise).

3.7.5 DC Holdup Time and Dropout

Loading	Holdup time	
750W (100%)	0.2msec	

During a DC dropout of **0.2ms** or less the power supply must meet dynamic voltage regulation requirements for every rated load condition. A DC line dropout of 0.2ms or less shall not cause tripping of control signals or protection circuits. Repeated every **10 seconds** starting at the min input voltage DC line dropout shall not damage the power supply under any specified load conditions. The PWOK signal shall not go to a low state under these conditions. DC dropout transients in excess of 0.2 **milliseconds** may cause shutdown of the PS or out of regulation conditions, but shall not damage the power supply. The power supply should recover and meet all turn on requirements for DC dropouts that last longer than **0.2ms**. The power supply must meet the DC dropout requirement over rated DC voltages and output loading conditions.

3.7.6 DC Line Surge Voltages (Line Transients)

The Power Supply should demonstrate tolerance for transients in the input DC power line caused by switching or lightning. The power supply shall be primarily tested and must be compliant with the requirements of EN61000-4-5: "Electrical Fast transients / Burst Requirements and Surge Immunity Requirements" for surge withstand capability. The test voltage surge levels are to be: 500Vpk for each Line to Primary Earth Ground test (none required between the L1 and L2). The exact description can be found in Intel Environmental Standards Handbook 2001.

Table 20. Line Voltage Transient Limits

Duration	Slope/Rate	Output	Performance criteria
200µs max	-48V → -30V w/ +2V/µs	Rated DC Voltages	No loss of function or performance
	-30V → -48V w/ -2V/µs	Rated DC Voltages	No loss of function or performance

3.7.7 Susceptibility Requirements

The power supply shall meet the following electrical immunity requirements when connected to a cage with an external EMI filter which meets the criteria defined in the SSI document EPS Power Supply Specification. For further information on Intel standards please request a copy of the Intel Environmental Standards Handbook.

Level	Description
А	The apparatus shall continue to operate as intended. No degradation of performance.
В	The apparatus shall continue to operate as intended. No degradation of performance beyond spec limits.
С	Temporary loss of function is allowed provided the function is self-recoverable or can be restored by the operation of the controls.

3.7.7.1 Electrostatic Discharge Susceptibility

The power supply shall comply with the limits defined in EN 55024: 1998 using the IEC 61000-4-2:1995 test standard and performance criteria B defined in Annex B of CISPR 24. Limits shall comply with those specified in the Intel Environmental Standards Handbook.

3.7.7.2 Fast Transient/Burst

The power supply shall comply with the limits defined in EN55024: 1998 using the IEC 61000-4-4:1995 test standard and performance criteria B defined in Annex B of CISPR 24. Limits shall comply with those specified in the Intel Environmental Standards Handbook.

3.7.7.3 Radiated Immunity

The power supply shall comply with the limits defined in EN55024: 1998 using the IEC 61000-4-3:1995 test standard and performance criteria A defined in Annex B of CISPR 24. Limits shall comply with those specified in the Intel Environmental Standards Handbook. Additionally, must also comply with field strength requirements specified in GR 1089 (10V/meter).

3.7.7.4 Surge Immunity

The power supply shall be tested with the system for immunity, per EN 55024:1998, EN 61000-4-5:1995 and ANSI C62.45: 1992.

The pass criteria include: No unsafe operation is allowed under any condition; All power supply output voltage levels to stay within proper spec levels; No change in operating state or loss of data during and after the test profile; No component damage under any condition.

The power supply shall comply with the limits defined in EN55024: 1998 using the IEC 61000-4-5:1995 test standard and performance criteria B defined in Annex B of CISPR 24. Limits shall comply with those specified in the Intel Environmental Standards Handbook.

3.7.8 Protection Circuits

Protection circuits inside the power supply shall cause only the power supply's main outputs to shutdown. If the power supply latches off due to a protection circuit tripping, an DC cycle OFF for 15sec and a PSON# cycle HIGH for 1sec shall be able to reset the power supply.

3.7.8.1 Current Limit (OCP)

The power supply shall have current limit to prevent the outputs from exceeding the values shown in table below. If the current limits are exceeded the power supply shall shutdown and latch off. The latch will be cleared by toggling the PSON# signal or by an DC power interruption. The power supply shall not be damaged from repeated power cycling in this condition. 12VSB will be auto-recovered after removing OCP limit.

Table 21. Over Current Protection

Output VOLTAGE	Input voltage range	OVER CURRENT LIMITS
+12V		72A min; 78A max
12VSB		2.5A min; 3.5A max

3.7.8.2 Over Voltage Protection (OVP)

The power supply over voltage protection shall be locally sensed. The power supply shall shutdown and latch off after an over voltage condition occurs. This latch shall be cleared by toggling the PSON# signal or by an DC power interruption. The values are measured at the output of the power supply's connectors. The voltage shall never exceed the maximum levels when measured at the power connectors of the power supply connector during any single point of fail. The voltage shall never trip any lower than the minimum levels when measured at the power connector. 12VSBwill be auto-recovered after removing OVP limit.

Table 22. Over Voltagge Protection Limits

Output Voltage	MIN (V)	MAX (V)
+12V	13.3	14.5
+12VSB	13.3	14.5

3.7.8.3 Over Temperature Protection (OTP)

The power supply will be protected against over temperature conditions caused by loss of fan cooling or excessive ambient temperature. In an OTP condition the PSU will shutdown. When the power supply temperature drops to within specified limits, the power supply shall restore power automatically, while the 12VSB remains always on. The OTP circuit must have built in margin such that the power supply will not oscillate on and off due to temperature recovering condition. The OTP trip level shall have a minimum of 4 of ambient temperature margin

 $\Box C$

3.8 Cold Redundancy Support

Power supplies that support cold redundancy can be enabled to go into a low-power state (that is, cold redundant state) in order to provide increased power usage efficiency when system loads are such that both power supplies are not needed. When the power subsystem is in Cold Redundant mode, only the needed power supply to support the best power delivery efficiency is ON. Any additional power supplies; including the redundant power supply, is in Cold Standby state

Each power supply has an additional signal that is dedicated to supporting Cold Redundancy; CR_BUS. This signal is a common bus between all power supplies in the system. CR_BUS is asserted when there is a fault in any power supply OR the power supplies output voltage falls below the Vfault threshold. Asserting the CR_BUS signal causes all power supplies in Cold Standby state to power ON.

Enabling power supplies to maintain best efficiency is achieved by looking at the Load Share bus voltage and comparing it to a programmed voltage level via a PMBus command.

Whenever there is no active power supply on the Cold Redundancy bus driving a HIGH level on the bus all power supplies are ON no matter their defined Cold Redundant roll (active or Cold Standby). This guarantees that incorrect programming of the Cold Redundancy states of the power supply will never cause the power

subsystem to shutdown or become over loaded. The default state of the power subsystem is all power supplies ON. There needs to be at least one power supply in Cold Redundant Active state or Standard Redundant state to allow the Cold Standby state power supplies to go into Cold Standby state.

3.8.1 Powering on Cold Standby supplies to maintain best efficiency

Power supplies in Cold Standby state shall monitor the shared voltage level of the load share signal to sense when it needs to power on. Depending upon which position (1, 2, or 3) the system defines that power supply to be in the cold standby configuration; will slightly change the load share threshold that the power supply shall power on at.

 Table 23. Example Load Share Threshold for Activating Supplies

	Enable Threshold for	Disable Threshold for	CR_BUS De-asserted / Asserted
	V _{CR_ON_EN}	V _{CR_ON_DIS}	States
Standard	NA; Ignore dc/dc_ active# signal; power supply is always ON		OK = High
Redundancy			Fault = Low
Cold Redundant	NA; Ignore dc/dc_ active# signal; power supply is always ON		OK = High
Active			Fault = Low
Cold Standby 1 (02h)	3.2V (40% of max)	$3.2V \times 0.5 \times 0.9 = 1.44V$	OK = Open
			Fault = Low
Cold Standby 2 (03h)	5.0V (62% of max)	$5.0V \times 0.67 \times 0.9 = 3.01V$	OK = Open
·			Fault = Low
Cold Standby 3 (04h)	6.7V (84% of max)	$6.7V \times 0.75 \times 0.9 = 4.52V$	OK = Open
			Fault = Low

Notes:

Maximum load share voltage = 8.0V at 100% of rated output power

These are example load share bus thresholds; for a given power supply, these shall be customized to maintain the best efficiency curve for that specific model.

3.8.2 Powering on Cold Standby supplies during a fault or over current condition

When an active power supply asserts its CR_BUS signal (pulling it low), all parallel power supplies in cold standby mode shall power on within $100\mu sec$

3.8.3 BMC Requirements

The BMC uses the Cold_Redundancy_Config command to define/configure the power supply's roll in cold redundancy and to turn on/off cold redundancy.

The BMC shall schedule a rolling change for which PSU is the Active, Cold Stby1, Cold Stby 2, and Cold Stby 3 power supply. This allows for equal loading across power supply over their life.

Events that trigger a re-configuration of the power supplies using the Cold Redundancy Config command.

- AC power ON
- o PSON power ON
- o Power Supply Failure
- Power supply inserted into system

3.8.4 Power Supply Turn On Function

Powering on and off of the cold standby power supplies is only controlled by each PSU sensing the Vshare bus. Once a power supply turns on after crossing the enable threshold; it lowers its threshold to the disable threshold. The system defines the 'position' of each power supply in the Cold Redundant operation. It will do this each time the system is powered on, a power supply fails, or a power supply is added to the system.

The system is relied upon to tell each power supply where it resides in the Cold Redundancy scheme.

3.9 Closed Loop System Throttling (CLST)

The server system has support for Closed Loop System Throttling (CLST). CLST prevents the system from crashing if a power supply module is overloaded. Should system power reach a pre-programmed power limit, CLST will throttle system memory and/or processors to reduce power. System performance will be impacted should this occur. For more in depth information about CLST implementation, please refer to the *SmaRT* & *CLST Architecture on "Romley" Systems and Power Supplies Specification* (IBL Reference # 461024).

3.10 Smart Ride Through (SmaRT)

The server system has support for Smart Ride Through Throttling (SmaRT). This feature increases the reliability for a system operating in a heavy power load condition, to remain operational during an AC line dropout event. See section 3.5.4 AC Line Dropout / Holdup for power supply hold up time requirements for AC Line dropout events.

When AC voltage is too low, a fast AC loss detection circuit inside each installed power supply asserts an SMBALERT# signal to initiate a throttle condition in the system. System throttling reduces the bandwidth to both system memory and CPUs, which in turn reduces the power load during the AC line drop out event

3.11 Power Supply Status LED

There is a single bi-color LED to indicate power supply status. The LED operation is defined in the following table.

Power Supply Condition LED State Output ON and OK GREEN No AC power to all power supplies OFF AC present / Only 12VSB on (PS off) or PS in Cold 1Hz Blink GREEN redundant state AC cord unplugged or AC power lost; with a second AMBER power supply in parallel still with AC input power. Power supply warning events where the power supply continues to operate; high temp, high power, high current, 1Hz Blink Amber slow fan.

AMBER

2Hz Blink GREEN

Power supply critical event causing a shutdown; failure,

OCP, OVP, Fan Fail
Power supply FW updating

Table 24. LED Indicators

4. Thermal Management

The fully integrated system is designed to operate at external ambient temperatures of between 10°C- 35°C with limited excursion based operation up to 45°C, as specified in *Table 2. System Environmental Limits Summary*. Working with integrated platform management, several features within the system are designed to move air in a front to back direction, through the system and over critical components to prevent them from overheating and allow the system to operate with best performance.

The Intel® Server System R2000GZ/GL product family supports short-term, excursion-based, operation up to 45°C (ASHRAE A4) with limited performance impact. The configuration requirements and limitations are described in the configuration matrix found in Appendix D of this document or in the *Intel® S2600GZGL Product Family Power Budget and Thermal Configuration Tool*, available as a download online at http://www.intel.com/support.

The installation and functionality of several system components are used to maintain system thermals. They include five managed 60mm system fans, one integrated 40mm fan for each installed power supply module, an air duct, populated hard drive carriers, and installed CPU heats sinks. Hard drive carriers can be populated with a hard drive or supplied drive blank. In addition, it may be necessary to have specific DIMM slots populated with DIMMs or supplied DIMM blanks.

4.1 Thermal Operation and Configuration Requirements

To keep the system operating within supported maximum thermal limits, the system must meet the following operating and configuration guidelines:

- The system operating ambient is designed for sustained operation up to 35°C (ASHRAE Class A2) with short term excursion based operation up to 45°C (ASHRAE Class A4).
 - o The system can operate up to 40°C (ASHRAE Class A3) for up to 900 hours per year
 - o The system can operate up to 45°C (ASHRAE Class A4) for up to 90 hours per year
 - System performance may be impacted when operating within the extended operating temperature range
 - o There is no long term system reliability impact when operating at the extended temperature range within the approved limits.
- Specific configuration requirements and limitations are documented in the configuration matrix found in Appendix D of this document or in the Intel® Server Board S2600GZGL product family Power Budget and Thermal Configuration Tool, available as a download online at Intel.com.
- The CPU-1 processor + CPU heat sink must be installed first. The CPU-2 heat sink must be installed at all times, with or without a processor installed.
- Memory Slot population requirements –
- NOTE: Some system configurations may come with pre-installed DIMM blanks. DIMM blanks should only be removed when installing a DIMM in the same DIMM slot. Memory population rules apply when installing DIMMs.
 - DIMM Population Rules on CPU-1 Install DIMMs in order; Channels A, B, C, and D¹. Start with1st DIMM (Blue Slot) on each channel, then slot 2, then slot 3¹. Only remove factory installed DIMM blanks when populating the slot with memory.
 - o **DIMM Population on CPU-2** Install DIMMs in order; Channels E, F, G, and H¹. Start with1st DIMM (Blue Slot) on each channel, then slot 2, then slot 3¹. Only remove factory installed DIMM blanks when populating the slot with memory.
 - The following system configurations require that specific memory slots be populated at all times using either a DIMM or supplied DIMM Blank
 - ❖ System Configuration 24x 2.5" hard drive bay or 12x 3.5" hard drive bay configuration + Intel® Server Board S2600GZ (24 DIMM server board)
 - Memory slots 2 and 3 populated on all memory channels

Revision 2.2

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¹ 24-DIMM Intel® Server Board S2600GZ only

- ❖ System Configuration 16x 2.5" hard drive bay or 8x 3.5" hard drive bay configuration + Intel[®] Server Board S2600GZ (24 DIMM server board)
 - Memory slot 3 populated on all memory channels
- ❖ System Configuration 24x 2.5" hard drive bay or 12x 3.5" hard drive bay configuration + Intel® Server Board S2600GL (16 DIMM server board)
 - Memory slot 2 populated on all memory channels
- All hard drive bays must be populated. Hard drive carriers can be populated with a hard drive or supplied drive blank.
- With the system operating, the air duct must be installed at all times
- In single power supply configurations, the 2nd power supply bay must have the supplied filler blank installed at all times.
- The system must be configured with dual power supplies for the system to support fan redundancy.
- Thermally, the system can support the following PCI add-in cards.
 - Add-in cards with a minimum 100 LFM (0.5 m/s) air flow requirement can be installed in any available add-in card slot in both Riser Card #1 and Riser Card #2
 - Add-in cards with a minimum 200 LFM (1 m/s) air flow requirement can be installed in any available add-in card slot on Riser Card #2 and the bottom add-in card slot on Riser Card #1.
 Middle and Top add-in card slots on Riser Card #1 cannot support PCI add-in cards with air flow requirements greater than 100 LFM.
 - o Add-in cards with a >200 LFM air flow requirement cannot be supported.
 - <u>Note:</u> Most PCI add-in cards have minimum air flow requirements of 100 LFM (0.5m/s). Some high power add-in cards have minimum air flow requirements of 200 LFM (1 m/s). System integrators should verify PCI add-in card air flow requirements from vendor specifications when integrating add-in cards into the system.
- The system top-cover must be installed at all times when the system is in operation. The only exception
 to this requirement is to hot replace a failed system fan, in which case the top cover can be removed for
 no more than 3 minutes at a time

4.2 Thermal Management Overview

In order to maintain the necessary airflow within the system, all of the previously listed components and top cover need to be properly installed. For best system performance, the external ambient temperature should remain below 35°C and all system fans should be operational. The system is designed for fan redundancy when the system is configured with two power supplies. Should a single system fan fail (System fan or Power Supply Fan), integrated platform management will: change the state of the System Status LED to flashing Green, report an error to the system event log, and automatically adjust fan speeds as needed to maintain system temperatures below maximum thermal limits.

Note: All system fans are controlled independent of each other. The fan control system may adjust fan speeds for different fans based on increasing/decreasing temperatures in different thermal zones within the chassis.

In the event that system temperatures should continue to increase with the system fans operating at their maximum speed, platform management may begin to throttle bandwidth of either the memory subsystem or the processors or both, in order to keep components from overheating and keep the system operational. Throttling of these sub-systems will continue until system temperatures are reduced below preprogrammed limits.

Should system thermals increase to a point beyond the maximum thermal limits, the system will shut down, the System Status LED will change to a solid Amber state, and the event will be logged to the system event log.

<u>Note:</u> Sensor data records (SDRs) for any given system configuration must be loaded by the system integrator for proper thermal management of the system. SDRs are loaded using the FRUSDR utility.

An intelligent Fan Speed Control (FSC) and thermal management technology (mechanism) is used to maintain comprehensive thermal protection, deliver the best system acoustics, and fan power efficiency. Options in <F2>

BIOS Setup (BIOS>Advanced>System Acoustic and Performance Configuration) allow for parameter adjustments based on the actual system configuration and usage. Refer to the following sections for a description of each setting.

4.2.1 Set Throttling Mode

This option is used to select the desired memory thermal throttling mechanism. Available settings include: **[Auto]**, [DCLTT], [SCLTT] and [SOLTT].

[Auto] – Factory Default Setting - BIOS automatically detects and identifies the appropriate thermal throttling mechanism based on DIMM type, airflow input, and DIMM sensor availability.

[DCLTT] - Dynamic Closed Loop Thermal Throttling: for the SOD DIMM with system airflow input

[SCLTT] – Static Close Loop Thermal Throttling: for the SOD DIMM without system airflow input

[SOLTT] – Static Open Loop Thermal Throttling: for the DIMMs without sensor on dimm (SOD)

4.2.2 Altitude

This option sets the proper altitude that the system will be used. Available settings include: [300m or less], [301m-900m], [901m-1500m], [Above 1500m].

Selecting an altitude range that is lower than the actual altitude the system will be operating at, can cause the fan control system to operate less efficiently, leading to higher system thermals and lower system performance. If the altitude range selected is higher than the actual altitude the system will be operating at, the fan control system may provide better cooling but with higher acoustics and higher fan power consumption. If the altitude is not known, selecting a higher altitude is recommended in order to provide sufficient cooling.

4.2.3 Set Fan Profile

This option sets the desired Fan Profile. Available settings include: **[Performance]** and [Acoustic].

The Acoustic mode offers the best acoustic experience and appropriate cooling capability supporting the majority of the add-in cards used. Performance mode is designed to provide sufficient cooling capability covering all kinds of add-in cards on the market.

4.2.4 Fan PWM Offset

This option is reserved for manual adjustment to the minimum fan speed curves. The valid range is from [0 to 100] which stands for 0% to 100% PWM adding to the minimum fan speed. This feature is valid when Quiet Fan Idle Mode is at Enabled state. The default setting is [0]

4.2.5 Quiet Fan Idle Mode

This feature can be [Enabled] or **[Disabled]**. If enabled, the fans will either shift to a lower speed or stop when the aggregate sensor temperatures are satisfied, indicating the system is at ideal thermal/light loading conditions. When the aggregate sensor temperatures are not satisfied, the fans will shift back to normal control curves. If disabled, the fans will never shift into lower fan speeds or stop, regardless of whether the aggregate sensor temperatures are satisfied or not. The default setting is [Disabled]

Note: The above feature may or may not be in effect and depends on the actual thermal characteristics of the specified system.

4.2.6 Thermal Sensor Input for Fan Speed Control

The BMC uses various IPMI sensors as inputs to fan speed control. Some of the sensors are actual physical sensors and some are "virtual" sensors derived from calculations.

The following IPMI thermal sensors are used as input to fan speed control:

- Front Panel Temperature Sensor¹
- CPU Margin Sensors^{2,4,5}
- DIMM Thermal Margin Sensors^{2,4}
- Exit Air Temperature Sensor^{1, 7, 9}
- PCH Temperature Sensor^{3,5}
- On-board Ethernet Controller Temperature Sensors^{3, 5}
- Add-In Intel SAS/IO Module Temperature Sensors^{3, 5}
- PSU Thermal Sensor^{3, 8}
- CPU VR Temperature Sensors^{3, 6}
- DIMM VR Temperature Sensors^{3, 6}
- BMC Temperature Sensor^{3, 6}
- Global Aggregate Thermal Margin Sensors⁷
- Hot Swap Backplane Temperature Sensors
- I/O module Temperature Sensor (With option installed)
- Intel[®] ROC Module (With option installed)

Notes:

- 1. For fan speed control in Intel chassis
- 2. Temperature margin from throttling threshold
- 3. Absolute temperature
- 4. PECI value or margin value
- 5. On-die sensor
- 6. On-board sensor
- 7. Virtual sensor
- 8. Available only when PSU has PMBus
- 9. Calculated estimate

The following diagram illustrates the fan speed control structure.

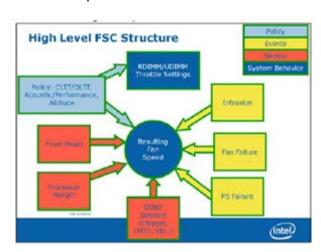


Figure 22. Fan Control Model

4.3 System Fans

Five 60x38-mm fans and an embedded fan for each installed power supply, provide the primary airflow for the system. The following table provides the volumetric air flow requirements for different 2U system configurations.

Table 25. 2U System Volumetric Air Flow Requirements

System Configuration	Air Flow Range (CFM)
3.5" x 12 drives	20 – 96 CFM
2.5" x 24 drives	26 – 113 CFM
All other 2U system configurations	11 – 80 CFM

The system is designed for fan redundancy when configured with two power supply modules. Should a single fan fail (system fan or power supply fan), platform management will adjust air flow of the remaining fans and manage other platform features to maintain system thermals. Fan redundancy is lost if more than one fan is in a failed state.

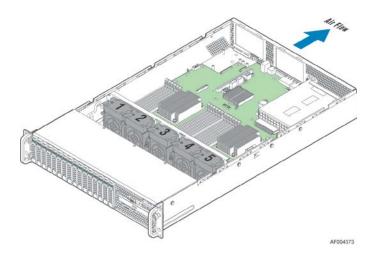


Figure 23. System Fan Identification

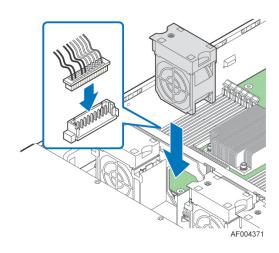




Figure 24. System Fan Assembly

The system fan assembly is designed for ease of use and supports several features.

- Each fan is hot-swappable.
- Each fan is designed for tool-less insertion and extraction from the fan assembly. For instructions on installing or removing a fan module, see the *Intel® Server System R2000GZ/GL Service Guide*.
- Fan speed for each fan is controlled by the integrated BMC on the server board. As system thermals fluctuate high and low, the integrated BMC firmware will increase and decrease the speeds to specific fans within the fan assembly to regulate system thermals.
- Each fan has a tachometer signal that allows the integrated BMC to monitor their status.
- On top of each fan is an integrated fault LED. Platform management illuminates the fault LED for the failed fan.
- Each fan has a10-pin wire harness that connects to a matching connector on the server board.

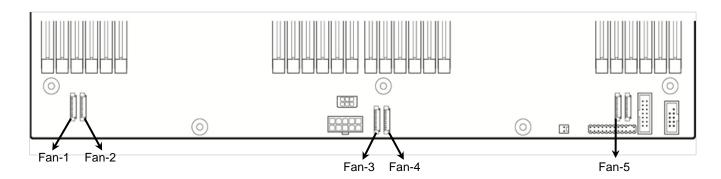


Figure 25. Server Board System Fan Connector Locations

Table 26. System Fan Connector Pin-out

SYS_FAN 1		SYS_FAN 2		SYS_FAN 3	
Signal Description	Pin#	Signal Description	Pin#	Signal Description	Pin#
FAN_TACH1_IN	1	FAN_TACH3_IN	1	FAN_TACH5_IN	1
FAN_ BMC_PWM0_R_BUF	2	FAN_BMC_PWM1_R_BUF	2	FAN_ BMC_PWM2_R_BUF	2
P12V_FAN	3	P12V_FAN	3	P12V_FAN	3
P12V_FAN	4	P12V_FAN	4	P12V_FAN	4
FAN_TACH0_IN	5	FAN_TACH2_IN	5	FAN_TACH4_IN	5
GROUND	6	GROUND	6	GROUND	6
GROUND	7	GROUND	7	GROUND	7
FAN_SYS0_PRSNT_N	8	FAN_SYS1_PRSNT_N	8	FAN_SYS2_PRSNT_N	8
LED_FAN_FAULT0_R	9	LED_FAN_FAULT1_R	9	LED_FAN_FAULT2_R	9
LED_FAN0	10	LED_FAN1	10	LED_FAN2	10
SYS_FAN 4		SYS_FAN 5		SYS_FAN 6 (Not Used)	
Signal Description	Pin#	Signal Description	Pin#	Signal Description	Pin#
0.g = 000p	FIII#	Signal Description	,	Orginal Boooription	,
FAN_TACH7_IN	1	FAN_TACH9_IN	1	FAN_TACH11_IN	1
				-	
FAN_TACH7_IN	1	FAN_TACH9_IN	1	FAN_TACH11_IN	1
FAN_TACH7_IN FAN_BMC_PWM3_R_BUF	1 2	FAN_TACH9_IN FAN_BMC_PWM4_R_BUF	1 2	FAN_TACH11_IN FAN_BMC_PWM5_R_BUF	1 2
FAN_TACH7_IN FAN_BMC_PWM3_R_BUF P12V_FAN	1 2 3	FAN_TACH9_IN FAN_BMC_PWM4_R_BUF P12V_FAN	1 2 3	FAN_TACH11_IN FAN_BMC_PWM5_R_BUF P12V_FAN	1 2 3
FAN_TACH7_IN FAN_BMC_PWM3_R_BUF P12V_FAN P12V_FAN	1 2 3 4	FAN_TACH9_IN FAN_BMC_PWM4_R_BUF P12V_FAN P12V_FAN	1 2 3 4	FAN_TACH11_IN FAN_BMC_PWM5_R_BUF P12V_FAN P12V_FAN	1 2 3 4
FAN_TACH7_IN FAN_BMC_PWM3_R_BUF P12V_FAN P12V_FAN FAN_TACH6_IN	1 2 3 4 5	FAN_TACH9_IN FAN_BMC_PWM4_R_BUF P12V_FAN P12V_FAN FAN_TACH8_IN	1 2 3 4 5	FAN_TACH11_IN FAN_BMC_PWM5_R_BUF P12V_FAN P12V_FAN FAN_TACH10_IN	1 2 3 4 5
FAN_TACH7_IN FAN_BMC_PWM3_R_BUF P12V_FAN P12V_FAN FAN_TACH6_IN GROUND	1 2 3 4 5 6	FAN_TACH9_IN FAN_BMC_PWM4_R_BUF P12V_FAN P12V_FAN FAN_TACH8_IN GROUND	1 2 3 4 5 6	FAN_TACH11_IN FAN_BMC_PWM5_R_BUF P12V_FAN P12V_FAN FAN_TACH10_IN GROUND	1 2 3 4 5 6
FAN_TACH7_IN FAN_BMC_PWM3_R_BUF P12V_FAN P12V_FAN FAN_TACH6_IN GROUND GROUND	1 2 3 4 5 6 7	FAN_TACH9_IN FAN_BMC_PWM4_R_BUF P12V_FAN P12V_FAN FAN_TACH8_IN GROUND GROUND	1 2 3 4 5 6 7	FAN_TACH11_IN FAN_BMC_PWM5_R_BUF P12V_FAN P12V_FAN FAN_TACH10_IN GROUND GROUND	1 2 3 4 5 6 7

4.4 Power Supply Module Fan

Each installed power supply module includes one embedded (non-removable) 40-mm fan. It is responsible for airflow through the power supply module. This fan is managed by the fan control system. Should this fan fail, the power supply will continue to operate until its internal temperature reaches an upper critical limit. The power supply will be protected against over temperature conditions caused by loss of fan cooling or excessive ambient temperature. In an over-temperature protection condition, the power supply module will shutdown.

4.5 FRUSDR Utility

The purpose of the embedded platform management and fan control systems is to monitor and control various system features, and to maintain an efficient operating environment. Platform management is also used to communicate system health to supported platform management software and support mechanisms. The FRUSDR utility is used to program the server board with platform specific environmental limits, configuration data, and the appropriate sensor data records (SDRs), for use by these management features.

The FRUSDR utility must be run as part of the initial platform integration process before it is deployed into a live operating environment. It must be run with the system fully configured and each time the system configuration changes.

The FRUSDR utility for the given server platform can be run as part of the *Intel® Server Deployment Toolkit* and *Management* DVD that ships with each Intel server, or can be downloaded from http://downloadcenter.intel.com.

Note: The embedded platform management system may not operate as expected if the platform is not updated with accurate system configuration data. The FRUSDR utility must be run with the system fully configured and each time the system configuration changes for accurate system monitoring and event reporting.

5. Intel[®] Xeon Phi[™] Coprocessor and GPGPU Add-in Card Support

Some (not all) system configurations of the Intel[®] Server System R2000GZ/GL product family have thermal and mechanical support for Intel[®] Xeon Phi[™] Coprocessor cards and non-Intel GPGPU add-in cards. Which card and system configurations can be supported is dependent on power and thermal limits of the system, and power and thermal requirements of the add-in card. This chapter will provide an overview of the system configuration criteria needed to support these high power cards.

The following sections will define support criteria based on the cooling solutions utilized on the add-in card; Passive Cooling (heat sink only) vs. Active Cooling (heat sink with fan attached).

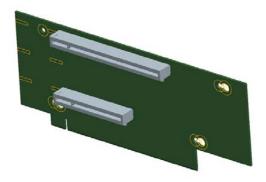
Advisory Note: Shipping a fully configured system that includes the Intel[®] Xeon Phi[™] card or other high end PCI graphics card is NOT supported using the original Intel packaging that the system came in. If the original Intel packaging is re-used, Intel recommends installing the Intel[®] Xeon Phi[™] card or other high end PCI graphics card into the system after it reaches its final destination. Shipping a fully integrated system that includes the Intel[®] Xeon Phi[™] card or other high end PCI graphics card using Intel's original system packaging may result in system or component damage.

5.1 Using High Power Add-in Cards with Active Cooling Solutions

 Due to air flow and thermal limitations, high power add-in cards with active cooling solutions will only be supported in the following base system SKUs: R2308GZ/GL####, R2208GZ/GL####, and R2216GZ/GL####. See Appendix D at the end of this document for additional configuration information.

NOTE: Only high power add-in cards with active cooling (heat sink with mounted fan) will be supported in system configurations installed with the standard (default) air duct. Due to thermal and air flow limitations, High power add-in cards (≥ 75W) with passive heat sinks cannot be supported in systems configured with the standard (default) air duct. See section 5.2 for support requirements needed to use high power add-in cards with passive cooling solutions.

• High power add-in cards with active cooling solutions can only be supported when installed in a 2-slot riser card (Intel Accessory Kit Part Number - A2UL16RISER).



Slot #	Description	PCle Lane Routing Riser Slot #1	PCle Lane Routing Riser Slot #2
Slot-1 (Top)	PCIe x16 lanes, x16 slot	CPU1	CPU2
Slot-2 (Bottom)	PCIe x8 lanes, x8 slot	CPU2	CPU2

With a 2-slot riser card installed, the embedded fan speed control will operate system fans at a higher speed. **3-slot riser cards (as shipped in the standard platform configuration) cannot be used due to air flow and thermal limitations**.

NOTE: The latest posted system software updates must be installed on the system to ensure proper fan speed control is enabled. The latest system update package can be downloaded from the following Intel web site: http://downloadcenter.intel.com.

- High power add-in cards with active cooling solutions that require up to 300W, can be supported. A
 total system power budget should be calculated to determine if the power draw of the desired system
 configuration meets the power limits of the installed power supplies. A power budget tool for the
 specified system can be downloaded from http://www.intel.com/support.
- Riser cards can support a maximum combined (all PCIe slots) power draw of up to 75W. Add-in cards with power requirements above 75W will require the additional power to be drawn from either of two 2x2 pin "OPT_12V_ PWR" connectors on the server board. See section 3.2.2 Riser Card Power Connectors, for the Optional12V power connector usage and power cable specification. Power cables for these connectors are included in the A2UL16RISER accessory kit and can support both 6 and 8 pin 12V AUX power connectors located on the add-in cards.
- Configuration Note: In order to support a GPGPU card in Riser Slot #1, the Auxiliary 12V power connector of the add-in card MUST be located on the back edge of the card. Aux 12V power connectors located on the top edge of the add-in card will interfere with the chassis side wall.

5.2 Using High Power Add-in Cards with Passive Cooling Solutions

Due to air flow and thermal limitations, high power add-in cards with passive cooling solutions will only
be supported in the following base system SKUs: R2308GZ##### and R2208GZ#####, and only
when configured with the contents included in the Intel Accessory Kit – AGZCOPRODUCT. This
accessory kit includes the following: new air duct design, two 2-slot PCIe riser cards, two 12V Auxiliary
power cables. See Appendix D at the end of this document for additional configuration information.

NOTE: Due to thermal and air flow limitations, High power add-in cards (> 75W) with passive heat sinks cannot be supported in systems configured with the standard (default) air duct or the 3-slot PCIe riser cards that are included in the standard shipping configuration.

NOTE: Intel Accessory Kit AGZCOPRODUCT is NOT compatible with any of the Intel[®] Server System R2000GL product family. (System integrated with the 16-DIMM Intel[®] Server Board S2600GL).

- High power add-in cards with passive cooling solutions that require up to 300W, can be supported. A
 total system power budget should be calculated to determine if the power draw of the desired system
 configuration meets the power limits of the installed power supplies. A power budget tool for the
 specified system can be downloaded from http://www.intel.com/support.
- Riser cards can support a maximum combined (all PCle slots) power draw of up to 75W. Add-in cards with power requirements above 75W will require the additional power to be drawn from either of the two 2x2 pin "OPT_12V_ PWR" connectors on the server board. Power cables for these connectors are included in the A2UL16RISER accessory kit and can support both 6 and 8 pin 12V AUX power connectors located on the add-in cards. See section 3.2.2 Riser Card Power Connectors, for the Optional12V power connector usage and power cable specification.
- Configuration Note: In order to support a GPGPU card in Riser Slot #1, the Auxiliary 12V power
 connector of the add-in card MUST be located on the back edge of the card. Aux 12V power
 connectors located on the top edge of the add-in card will interfere with the chassis side wall.

- A system configured with the contents of Intel Accessory Kit **AGZCOPRODUCT** can support the following add-in card configurations.
 - o Add-in cards with minimum 100 LFM (0.5 m/s) or 200 LFM (1m/s) air flow requirement can be installed in any available add-in card slot in both Riser Card #1 and Riser Card #2.
 - o Add-in cards with a >200 LFM air flow requirement cannot be supported.
 - When installing a double wide add-in card, remove the plastic knock-out from the air duct from the side on which the add-in card is installed. Only remove both plastic knock outs when installing two double wide cards (1 per riser card) into the system.

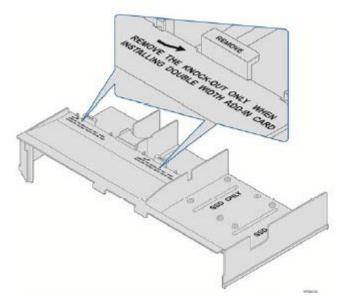


Figure 26. Intel Accessory Kit AGZCOPRODUCT - Air Duct

Note: System integrators should verify GPGPU add-in card air flow requirements from vendor specifications before integrating them into the system.

5.3 Intel[®] Xeon Phi[™] Coprocessor Card – System Configuration Requirements

For best system performance and proper fan speed control, ensure the system is configured as follows:

- The minimum system software revisions installed must be:
 - System BIOS 01.06.0002 or later
 - BMC Firmware 01.17.4207 or later
 - FRUSDR 1.09 or later

Intel highly recommends that the latest available system software be installed. The latest system update package can be downloaded from the following Intel web site: http://downloadcenter.intel.com

- The following <F2> BIOS Setup Utility Options must be set:
 - ➤ MMIO over 4GB = Enabled
 - MMIO Size = 256GB

6. System Storage and Peripheral Drive Bays Overview

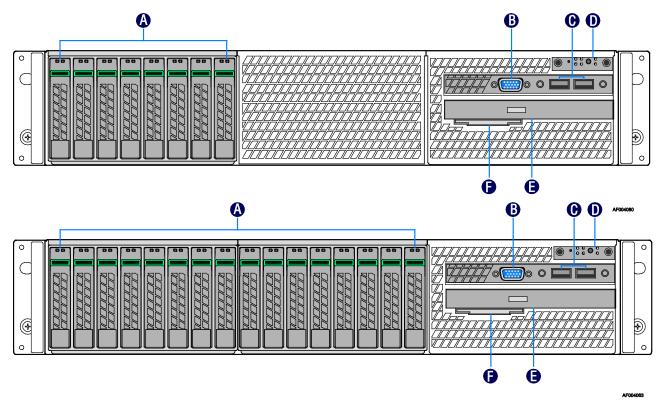
The Intel® Server System R2000GZ/GL product family has support for many storage device options, including:

- Hot Swap 2.5" Drives
- Hot Swap 3.5" Drives
- SATA Optical Drive
- SATA Solid State Devices (SSDs)
- SATA DOMs
- Low Profile (2mm) eUSB Solid State Device (eUSB SSD)

Support for different storage and peripheral device options will vary depending on the system SKU. This section will provide an overview of each available option.

6.1 2.5" Storage Device Support

The server is available to support a 2.5" device form factor with configurations of 8, 16, and 24 devices (Hard Disk or SSD) as illustrated below.



Label	Description
Α	2.5" Hard Drive Bays
В	Video Connector
С	USB Ports
D	Front Control Panel
Е	SATA Optical Drive
F	System Label Pull-out

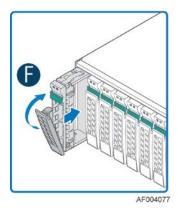
Figure 27. 2.5" Drive Bay - 8 & 16 Drive Configuration



Figure 28. 2.5" Drive Bay - 24 Drive Configuration

The drive bay can support either SATA or SAS hard disks or SSDs. Mixing of drive technologies (SATA/SAS) within a common hot swap backplane is not supported. Systems with multiple hot swap backplanes can support different drive type configurations as long as the drives attached to a common backplane are the same and the installed controller attached to the given backplane can support the drive type. Drive type is dependent on the type of host bus controller used, SATA only or SAS.

Each 2.5" device is mounted to a drive tray, allowing for hot swap extraction and insertion. Drive trays have a latching mechanism that is used to extract and insert drives from the chassis, and lock the tray in place.



Note: To maintain system thermals, all drive bays must be populated with a drive tray mounted with a hard drive, SSD, or supplied drive blank.

Light pipes integrated into the drive tray assembly direct light emitted from Amber drive status and Green activity LEDs located next to each drive connector on the backplane, to the drive tray faceplate, making them visible from the front of the system.



Table 27. Drive Status LED States

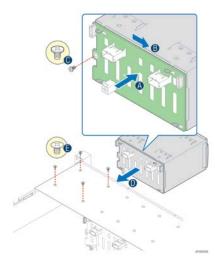
	Off	No access and no fault	
Amber Solid On Hard Drive Fault has occured		Hard Drive Fault has occured	
	Blink	RAID rebuild in progress (1 Hz), Identify (2 H	

Table 28. Drive Activity LED States

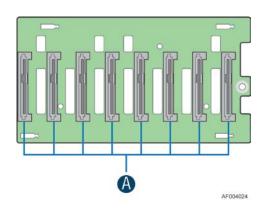
	Condition	Drive Type	Behavior
	Power on with no drive activity	SAS	LED stays on
	Fower on with no drive activity	SATA	LED stays off
	Power on with drive activity	SAS	LED blinks off when processing a command
Green	en Power on with drive activity	SATA	LED blinks on when processing a command
	Power on and drive spun down	SAS	LED stays off
		SATA	LED stays off
	Power on and drive spinning up	SAS	LED blinks
	rower on and unive spirining up	SATA	LED stays off

6.1.1 2.5" Drive Hot-Swap Backplane Overview

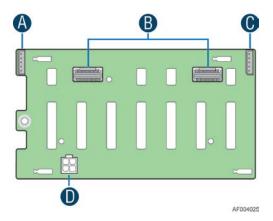
Depending on the number of storage devices supported by a given system SKU, a system can be configured with 1, 2, or 3 eight drive backplanes. Each backplane is attached to the back of the drive bay assembly.



On the front side of each backplane are mounted eight storage device interface connectors (A), each providing both power and I/O signals to attached storage device.



On the backside of each backplane are several connectors. The following illustration identifies each.



Label	Description
Α	SMBus-Out cable connector for multi-backplane support
В	4-port Mini-SAS cable connectors
С	SMBus-In cable connector – From Server board or other backplane
D	Power connector

A and C – SMBus Cable Connectors – The backplane includes two 1x5 cable connectors used as a management interface between the server board and the installed backplanes. In systems configured with multiple backplanes, a short jumper cable is attached between backplanes, with connector B used on the first board and connector D used on the second board, extending the SMBus to each installed backplane.

B – Multi-port Mini-SAS Cable Connectors – The backplane includes two multi-port mini-SAS cable connectors, each providing SGPIO and I/O signals for four SAS/SATA storage devices on the backplane. Cables can be routed from matching connectors on the server board, installed add-in SAS/SATA RAID cards, or optionally installed SAS expander cards for drive configurations of greater than 8 storage devices.

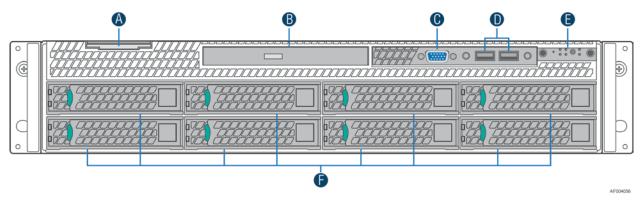
D – Power Harness Connector – The backplane includes a 2x2 connector supplying power to the backplane. Power is routed to each installed backplane via a multi-connector power cable harness from the server board.

6.1.2 Cypress* CY8C22545 Enclosure Management Controller

The backplanes support enclosure management using a Cypress* CY8C22545 Programmable System-on-Chip (PSoC*) device. The CY8C22545 drives the hard drive activity/fault LED, hard drive present signal, and controls hard drive power-up during system power-on.

6.2 3.5" Drive Support

The server is available in 3.5" form factor storage device configurations of 8 and 12 devices as illustrated below.



Label	Description
Α	System Label Pull-out Tab
В	SATA Optical Drive
С	Video Connector
D	USB Ports
Е	Front Control Panel
F	3.5" Hard Drive Bays

Figure 29. 3.5" Drive Bay - 8 Drive Configuration

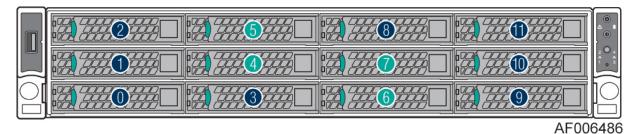
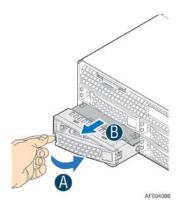


Figure 30. 3.5" Drive Bay - 12 Drive Configuration

The drive bay can support either SATA or SAS hard disk drives or SSDs. Mixing of drive types (SAS/SATA) within the hard drive bay is not supported. Storage device type is dependent on the type of host bus controller used, SATA only or SAS. Each 3.5" storage device is mounted to a drive tray, allowing for hot swap extraction and insertion. Drive trays have a latching mechanism that is used to extract and insert storage devices from the chassis, and lock the tray in place.



Note: To maintain system thermals, all drive bays must be populated with a drive tray mounted with a hard disk drive, SSD, or supplied drive blank.

The provided 3.5" drive blank can also be used as a 2.5" device bracket, allowing a 2.5" SSD to be installed into a 3.5" device carrier.

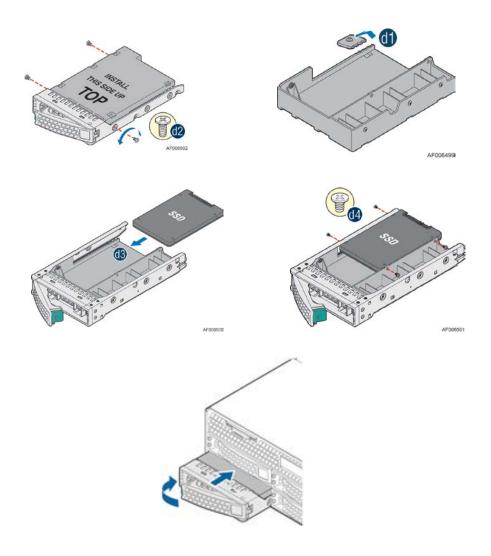
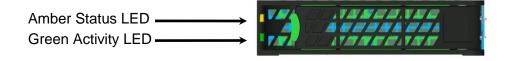


Figure 31. Option to install 2.5" SSD into a 3.5" drive blank

Note: Due to degraded performance and reliability concerns, the use of the 3.5" drive blank as a 2.5" device bracket is intended to support SSD type storage devices only. Installing a 2.5" hard disk drive into the 3.5" drive blank cannot be supported.

Light pipes integrated into the drive tray assembly direct light emitted from Amber drive status and Green activity LEDs located next to each drive connector on the backplane, to the drive tray faceplate, making them visible from the front of the system.



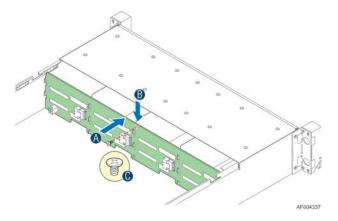
	Off	No access and no fault
Amber	mber Solid On Hard Drive Fault has occured	
	Blink	RAID rebuild in progress (1 Hz), Identify (2 Hz)

	Condition	Drive Type	Behavior
	Power on with no drive activity	SAS	LED stays on
	Fower on with no drive activity	SATA	LED stays off
	Power on with drive activity	SAS	LED blinks off when processing a command
Green		SATA	LED blinks on when processing a command
	Power on and drive spun down	SAS	LED stays off
		SATA	LED stays off
	Power on and drive spinning up	SAS	LED blinks
	rower on and unive spirining up	SATA	LED stays off

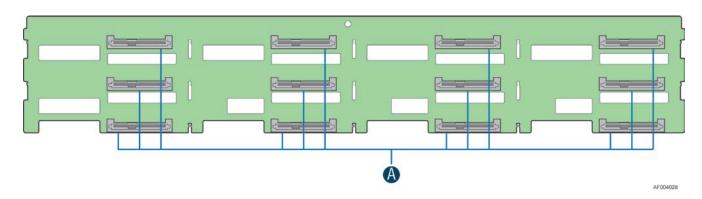
6.2.1 3.5" Drive Hot-Swap Backplane Overview

Systems with 8 or 12 drive configurations have their own unique backplane. Both 8 and 12 drive backplanes share identical features. The following will be used to describe the features of both backplanes. Differences between the two will be noted.

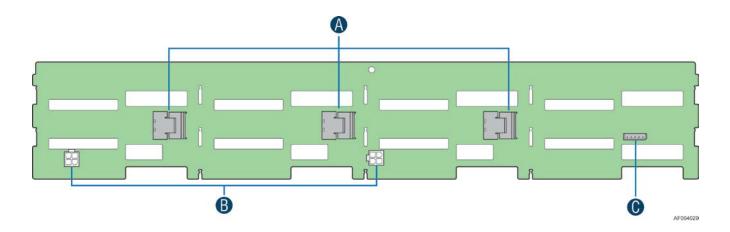
The backplanes mount to the back of the drive bay assembly.



On the front side of each back plane are mounted eight or twelve hard disk drive interface connectors, each providing both power and I/O signals to attached hard disk drives.



On the backside of each backplane are several connectors. The following illustration identifies each.



Label	Description
Α	4-port mini-SAS connectors
В	Power connectors
С	SMBus connector

A – 4-port Mini-SAS Connectors – The backplane includes two or three multi-port mini-SAS cable connectors, each providing SGPIO and I/O signals for four SAS/SATA hard drives on the backplane. Cables can be routed from matching connectors on the server board, add-in SAS/SATA RAID cards, or an optionally installed SAS expander card. Each mini-SAS connector will include a silk-screen identifying which drives the connector supports; Drives 0-3, Drives 4-7, and Drives 8-11.

B – Power Harness Connector - The backplane includes a 2x2 connector supplying power to the backplane. Power is routed to the backplane via a power cable harness from the server board

C- SMBus Cable Connectors – The backplane includes a 1x5 cable connector used as a management interface to the server board.

6.2.2 Cypress* CY8C22545 Enclosure Management Controller

The backplanes support enclosure management using a Cypress* CY8C22545 Programmable System-on-Chip (PSoC*) device. The CY8C22545 drives the hard drive activity/fault LED, hard drive present signal, and controls hard drive power-up during system power-on.

6.3 Optical Drive Support

Systems configured with eight 3.5" hard drive bays or up to sixteen 2.5" hard drive bays also include support for an optical drive bay 'A' as illustrated below.

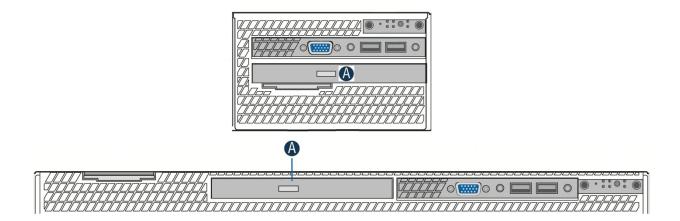
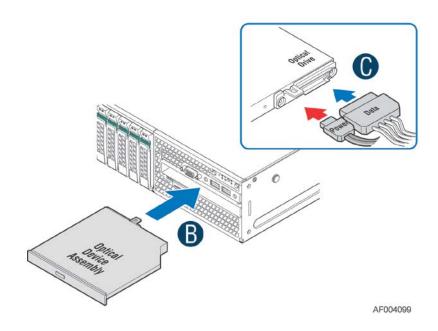


Figure 32. Optical Drive Support

A 2x3 pin power connector on the server board labeled "ODD/SSD PWR" is designed to provide power to two SATA SSDs or a single SATA SSD and a SATA optical drive. Using the supplied power harness, one power lead is routed to the optical drive bay, and two longer power leads are routed to the SSDs. SATA signals for one SSD and one optical drive or two SSDs, are cabled from the two white single port AHCI SATA connectors on the server board.



6.4 Internal – Fixed Mount Solid State Drive (SSD) Support

The system can support up to two internal fixed mount 2.5" Solid State Drives (SSD). Each drive is attached to the top side of the air duct as illustrated below.

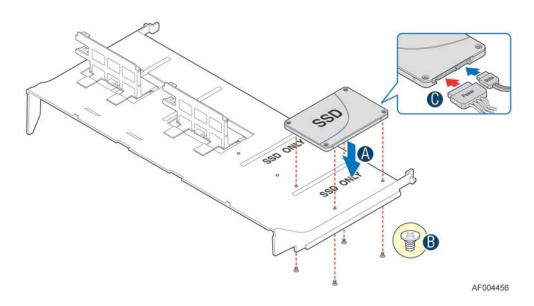


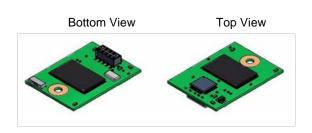
Figure 33. 2.5" Solid State Drive (SSD) Mounting Option

A 2x3 pin power connector on the server board labeled "ODD/SSD PWR" is designed to provide power to both SSDs or a single SSD and an optical drive. Using the supplied power harness, one power lead is routed to the optical drive bay, and two longer power leads are routed to the SSDs. SATA signals for one SSD and one optical drive or two SSDs, are cabled from the two white 7-pin AHCI SATA connectors on the server board.

NOTE: Mounting standard hard disk drives to the CPU air duct is NOT a supported configuration. Mounting hard disk drives to the CPU air duct may cause data loss, performance degradation, and premature drive failures.

6.5 Low Profile eUSB SSD Support

The system provides support for a low profile eUSB SSD storage device. A 2mm 2x5-pin connector labeled "eUSB SSD" near the rear I/O section of the server board is used to plug this small flash storage device into.



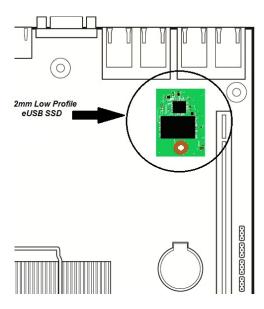


Figure 34. Low Profile eUSB SSD Support

eUSB features include:

- 2 wire small form factor Universal Serial Bus 2.0 (Hi-Speed USB) interface to host
- Read Speed up to 35 MB/s and write Speed up to 24 MB/s
- Capacity range from 256 MB to 32 GB
- Support USB Mass Storage Class requirements for Boot capability

6.6 SATA DOM Support

The system has support for up to two vertical low profile Innodisk* SATA Disk-on-Module (DOM) devices.

Each installed SATA DOM plugs directly into one of the 7-pin AHCI SATA ports on the server board, which provide both power and I/O signals.



Figure 35. InnoDisk* Low Profile SATA DOM

SATA DOM features include:

- Ultra low Profile
- High Speed and Capacity
- Built-in VCC at pin 7

Note: Visit http://www.intel.com/support for a list of supported InnoDisk SATA DOM parts.

7. Storage Controller Options Overview

The server platform supports many different embedded and add-in SATA/SAS controller and SAS Expander options to provide a large number of possible storage configurations. This section will provide an overview of the different options available.

7.1 Embedded SATA / SAS Controller support

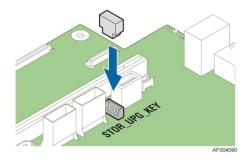
Integrated on the server board is an Intel[®] C602 chipset that provides embedded storage support via two integrated controllers: AHCI and SCU.

The standard server board (with no additional storage options installed) will support up to six SATA ports:

- Two single 6 Gb/sec SATA ports routed from the AHCI controller to two white 7-pin SATA ports labeled "SATA-0" and "SATA-1" on the server board. Embedded RAID levels 0 and 1 supported.
- Four 3 Gb/sec SATA ports routed from the SCU controller to the multi-port mini-SAS connector labeled "SCU_0 (0-3)".

Note: The mini-SAS connector labeled "SCU_1 (4-7)" is NOT functional by default and is only enabled with the addition of an Intel[®] RAID C600 Upgrade Key option supporting 8 SAS/SATA ports.

With the addition of one of several available Intel[®] RAID C600 Upgrade Keys, the system is capable of supporting additional embedded SATA, SAS, and software RAID options. Upgrade keys install onto a 4-pin connector on the server board labeled "STOR_UPG_KEY".



The following table identifies available upgrade key options and their supported features.

Table 29. Intel® RAID C600 Upgrade Key Options

Intel [®] RAID C600 Upgrade Key Options (Intel Product Codes)	Key Color	Description
Default – No option key installed	-	4 Port SATA with Intel [®] ESRT RAID 0,1,10 and Intel [®] RSTe RAID 0,1,5,10
RKSATA4R5	Black	4 Port SATA with Intel [®] ESRT2 RAID 0,1, 5, 10 and Intel [®] RSTe RAID 0,1,5,10
RKSATA8	Blue	8 Port SATA with Intel® ESRT2 RAID 0,1, 10 and Intel® RSTe RAID 0,1,5,10
RKSATA8R5	vvnite	8 Port SATA with Intel® ESRT2 RAID 0,1, 5, 10 and Intel [®] RSTe RAID 0,1,5,10
RKSAS4	Green	4 Port SAS with Intel® ESRT2 RAID 0,1, 10 and Intel® RSTe RAID 0,1,10
RKSAS4R5	Yellow	4 Port SAS with Intel® ESRT2 RAID 0,1, 5, 10 and Intel® RSTe RAID 0,1,10
RKSAS8		8 Port SAS with Intel® ESRT2 RAID 0,1, 10 and Intel® RSTe RAID 0,1,10
RKSAS8R5	Purple	8 Port SAS with Intel® ESRT2 RAID 0,1, 5, 10 and Intel® RSTe RAID 0,1,10

Additional information for the on-board RAID features and functionality can be found in the *Intel® RAID Software Users Guide* (Intel Document Number D29305-015).

7.2 Embedded Software RAID Support

The system includes support for two embedded software RAID options:

- Intel[®] Embedded Server RAID Technology 2 (ESRT2) based on LSI* MegaRAID SW RAID technology
- Intel[®] Rapid Storage Technology (RSTe)

Pressing the <F2> key to enter the BIOS Setup Utility during system POST, options are available to enable/disable SW RAID, and select which embedded software RAID option to use.

7.2.1 Intel[®] Embedded Server RAID Technology 2 (ESRT2)

Features of the embedded software RAID option Intel® Embedded Server RAID Technology 2 (ESRT2) include the following:

- Based on LSI* MegaRAID Software Stack
- Software RAID with system providing memory and CPU utilization
- Supported RAID Levels 0,1,5,10
 - 4 & 8 Port SATA RAID 5 support provided with appropriate Intel[®] RAID C600 Upgrade Key
 - o 4 & 8 Port SAS RAID 5 support provided with appropriate Intel® RAID C600 Upgrade Key
- Maximum drive support = 8
 - NOTE: ESRT2 has no SAS Expander Support
- Open Source Compliance = Binary Driver (includes Partial Source files)
 - Meta data is also recognized by MDRAID layer in Linux (No direct Intel support, not validated by Intel)
- OS Support = Windows 7*, Windows 2008*, Windows 2003*, RHEL*, SLES*, other Linux variants using partial source builds.
- Utilities = Windows* GUI and CLI, Linux GUI and CLI, DOS CLI, and EFI CLI

7.2.2 Intel® Rapid Storage Technology (RSTe)

Features of the embedded software RAID option Intel® Rapid Storage Technology (RSTe) include the following:

- Software RAID with system providing memory and CPU utilization
- Supported RAID Levels 0.1.5.10
 - 4 Port SATA RAID 5 available standard (no option key required)
 - 8 Port SATA RAID 5 support provided with appropriate Intel[®] RAID C600 Upgrade Key
 - No SAS RAID 5 support
- Maximum drive support = 32 (in arrays with 8 port SAS), 16 (in arrays with 4 port SAS), 128 (JBOD)
- Open Source Compliance = Yes (uses MDRAID)
- MDRAID supported in Linux. (Does not require a driver)
- OS Support = Windows 7*, Windows 2008*, Windows 2003*, RHEL*1, SLES*1, VMWare 5.x.
- Utilities = Windows* GUI and CLI, Linux CLI, DOS CLI, and EFI CLI
- <u>NOTE:</u> Boot drive support to targets attached through SAS expander card requires BIOS update. Must connect expander to SCU_0 and drives to ports 0&1 on RES2SV240 expander or A&B on RES2CV**0 for boot support.

Note 1) See latest product errata list for Linux support status. Product Errata are documented in the Intel® Server Board S2600GZGL, Intel® Server System R1000GZGL, Intel® Server System R2000GZGL Monthly Specification Update which can be downloaded from http://www.intel.com/support.

Visit http://www.intel.com/support for a list of supported operating systems.

7.3 Intel® Integrated RAID Module Support (Available Option)

The system has support for many Intel and 3rd party PCIe add-in RAID adapters which can be installed in available PCIe add-in cards slots. For system configurations with limited add-in card slot availability, an optional Intel[®] Integrated RAID mezzanine module can be installed onto a high density 80-pin connector (labeled "SAS Module") on the server board.

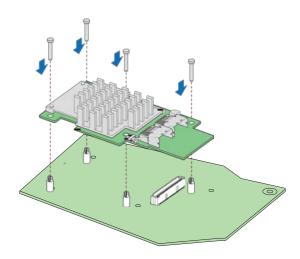


Table 30. Supported Intel® Integrated RAID Modules

External Name	Description	Product Code
Intel [®] Integrated RAID Module RMS25CB080	8 Port SAS-2.1, Full HW RAID (ROC), 1GB, IOM Slot RAID Levels 0,1,10, 5, 50, 6, 60	RMS25CB080
Intel [®] Integrated RAID Module RMS25CB040	4 Port SAS-2.1, Full HW RAID (ROC), 1GB, IOM Slot RAID Levels 0,1,10, 5, 50, 6, 60	RMS25CB040
Intel [®] Integrated RAID Module RMT3CB080	8 Port SATA-3, Full HW RAID (ROC), 512MB, IOM Slot RAID Levels 0,1,10, 5, 50, 6, 60	RMT3CB080
Intel [®] Integrated RAID Module RMS25JB080	8 Port SAS-2.1, Entry-level HW RAID, IOM Slot RAID Levels 0,1,1E	RMS25JB080
Intel [®] Integrated RAID Module RMS25JB040	4 Port SAS-2.1, Entry-level HW RAID, IOM Slot RAID Levels 0,1,1E	RMS25JB040

Features of this option include:

- Custom on-board system interface connector. Does not utilize a PCIe slot on the riser cards
- SKU options to support full or entry level hardware RAID
- 4 or 8 port, SAS/SATA, or SATA-only Module options
- ROC SKU options to support 512MB or 1GB embedded memory
- Support for RAID Battery Backup Unit (AXXRBBU9)
- ROC support for the Intel[®] Raid Maintenance Free Backup Unit (AXXRMFBU2)
 - Accessory Support Note: Due to mechanical limits, the AXXRMFBU2 accessory cannot be
 mounted on the side wall attach feature used for the AXXRBBU9. To support the AXXRMFBU2
 in the 2U chassis, an optional mounting bracket must be installed over the power supply bay.
 The Intel part number for the 2U system AXXRMFBU2 mounting bracket is A2UBKTMFBUSSD.

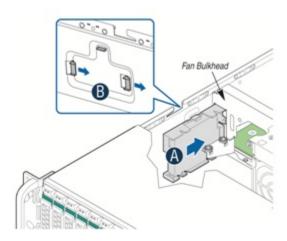


Figure 36. AXXBBU9 Installation

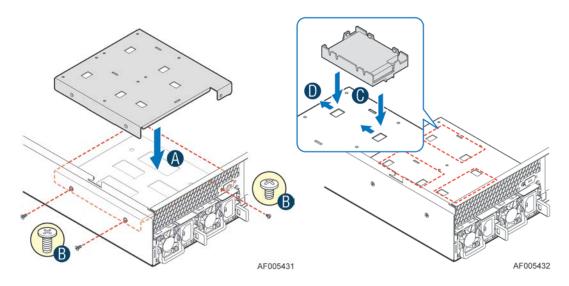


Figure 37. A2UBKTMFBUSSD Bracket + AXXRMFBU2 Installation

For additional product information, please reference the following Intel documents:

- Intel Integrated RAID Module RMS25PB040, RMS25PB080, RMS25CB040, and RMS25CB080 Hardware / Installation Users Guide
- Intel Integrated RAID Module RMT3PB080 and RMT3CB080 Hardware / Installation Users Guide
- Intel Integrated RAID Module RMS25KB040, RMS25KB080, RMS25JB040, and RMS25JB080 Hardware / Installation Users Guide
- Intel® Raid Maintenance Free Backup Unit AXXRMFBU2 User's Guide

7.4 Intel[®] SAS Expander Support

To support system configurations of more than eight hard disk drives, several Intel[®] SAS expander card options are available.

Intel [®] SAS Expander	Installation type	Hard Drive Configuration
24 Port PCIe Intel® RAID Expander Card RES2SV240	PCIe add-in card	3.5" Hard Disk drive bay configurations
24 Port Internal Intel [®] RAID Expander Card RES2CV240	Internal mount	2.5" Hard Disk drive bay configurations
36 Port Internal Intel® SAS Expander Card RES2CV360	Internal mount	2.5" Hard Disk drive bay configurations

Intel® RAID Expander Features:

- SAS protocol, described in the Serial Attached SCSI (SAS) Standard, version 2.0
- Serial SCSI Protocol (SSP) to enable communication with other SAS devices
- Serial Tunneling Protocol (STP) support for SATA II through expander interfaces
- Serial Management Protocol (SMP) to share topology management information with expanders
- Supports SES for enclosure management
- Output mini-SAS connectors support sideband SGPIO as per SFF-8485 specification
- Supports both Serial Attached SCSI and Serial ATA device targets
- 6.0 Gbit/s, 3.0 Gbit/s, and 1.5 Gbit/s data transfer rate
- SFF-8087 mini-SAS connectors
- Provides a low-latency connection to create and maintain transparent access to each connected SAS/SATA physical drive
- Staggered spin-up
- Hot Plug
- Native Command Queuing
- Allows multiple initiators to address a single target (in a fail-over configuration)

7.4.1 24-Port PCIe Intel® RAID Expander Card RES2SV240

For large storage configurations that include a drive bay supporting twelve 3.5" hard disk drives, a 24-Port PCIe Intel® RAID Expander Card RES2SV240 may be installed into any available PCIe add-in slot.

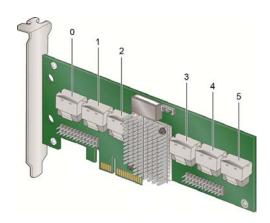


Figure 38. 24 Port PCIe Intel® RAID Expander Card RES2SV240

For additional information on the Intel[®] RAID Expander Card RES2SV240, please reference the *Intel*[®] RAID Expander Card RES2SV240 Hardware Users Guide, Intel Document Order # E93121-003.

7.4.2 24-Port and 36-Port Internal Intel RAID Expander Cards

For large storage configurations that include a drive bay supporting more than eight 2.5" hard disk drives, the system may be configured with an internally mounted 24-port or 36-port SAS expander card mounted directly behind the drive bay assembly as shown in the following illustration. SAS expander selection is dependent on the desired SAS controller configuration.

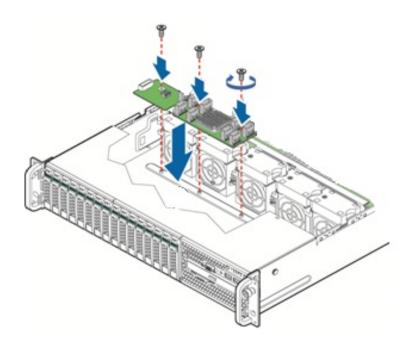


Figure 39. Internal SAS Expander Installation

The following diagrams are used to help identify the mini-SAS connectors found on the SAS expander cards. Care should be taken when connecting connectors from the SAS expander to the connectors on the backplane because each connector is pre-programmed at the factory to provide specific drive identification mapping. Improper connections may provide undesirable drive mappings.

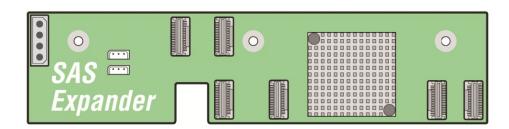


Figure 40. Internal 24-Port SAS Expander Card (RES2CV240)

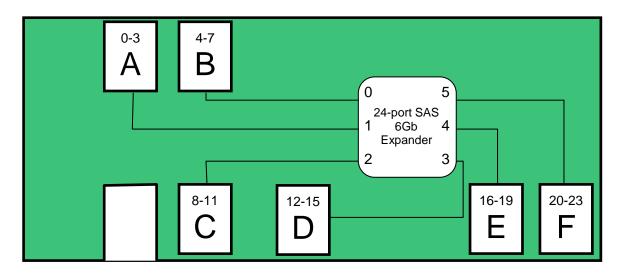


Figure 41. 24-Port Expander SAS Connector/Drive Identification Block Diagram

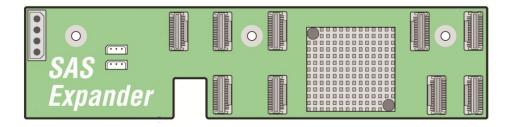


Figure 42. Internal 36-Port SAS Expander Card (RES2CV360)

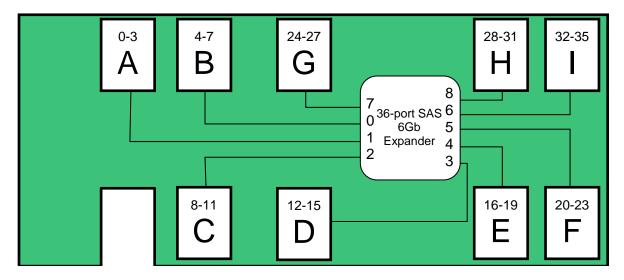


Figure 43. 36-Port Expander SAS Connector/Drive Identification Block Diagram

Each connector on the SAS expander card can be used as a "cable in" (SAS Controller to SAS Expander) or "cable out" (SAS Expander to Hot Swap Backplane) type connector. However, for contiguous drive mapping (0 - 16 or 0 - 24), cable routing differs when using a x8 wide-port capable 6 Gb SAS / SAS RAID Controller vs. using the embedded SCU ports.

7.4.3 SAS Expander Usage Guidelines

- SCU controller limitations prevent both SCU mini-SAS connectors on the server board from being attached to the same expander card.
- When attaching a SAS expander to one of the SCU mini-SAS connectors on the server board, the embedded RAID option must be configured as RSTe. The ESRT2 stack does not have SAS expander support.
- The RSTe stack supports booting from drives connected to an expander when the EFI Optimized Boot option is enabled in <F2> BIOS Setup (often referred to as UEFI boot). There are no restrictions on this configuration.
- Using Legacy mode BIOS for booting to targets attached through SAS expander card requires a BIOS update. Must connect the expander to SCU_0. Drives must be connected to ports 0&1 on RES2SV240 expander or A&B on RES2CV**0 for legacy mode boot support
- All Intel supported 6Gb SAS/RAID adapters can connect both quad-port connectors (x8 wide port) to the expander card.
- All Intel supported 3Gb SAS/RAID adapters may only connect a single quad-port connector (x4 wide port) to the expander card

7.4.4 Sample SAS Expander Configurations

Please reference the *Intel® Server System R2000GZ/GL Product Family Service Guide* for cable routing diagrams illustrating a variety of different storage configurations.

7.4.4.1 Cable Routing using the both embedded SCU ports and a SAS Expander Card (RSTe only)

SCU controller limitations prevent both SCU mini-SAS connectors on the server board from being attached to the same expander card. Therefore, in order to use both embedded SCU ports with a SAS expander card, one SCU port is connected directly to backplane and one SCU port is connected to the expander card.

To ensure contiguous drive mapping, cables should be routed as follows:

Config Option #1:

- The SCU 0 (0-3) connector is cabled to first connector on the hot swap backplane
- The SCU_1 (4-7) connector is cabled to Connector A (RES2CV240 or RES2CV360) or Connector 0 (RES2SV240) on the SAS expander card
- Cables from the SAS Expander to the hot swap backplane are connected in order starting with Connector B (RES2CV240 or RES2CV360) or Connector 1 (RES2SV240)

Config Option #2:

- The SCU_0 (0-3) connector is cabled the expander. Do not use connectors A&B (RES2CV240 or RES2CV360) or connectors 0&1 (RES2SV240) for the SCU.
- The SCU_1 (4-7) connector is not used, or can be connected directly to the backplane.
- Cables from the SAS Expander to the hot swap backplane are connected in order starting with Connector A (RES2CV240 or RES2CV360) or Connector 0 (RES2SV24

7.4.4.2 Cable Routing using a x8 wide-port capable 6 Gb SAS / SAS RAID Controller and an expander card

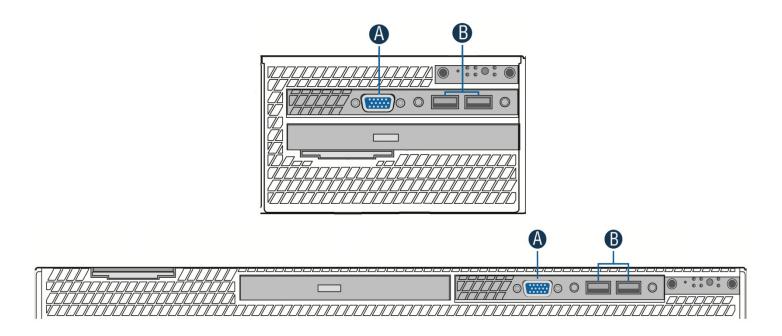
To ensure contiguous drive mapping when using a x8 wide-port capable 6 Gb SAS / SAS RAID Controller with the a SAS expander card, the system should may be cabled as follows:

- Cables from the SAS Expander to the hot swap backplane should be connected in order starting with Connector A (RES2CV240 or RES2CV360) or Connector 0 (RES2SV240)
- Once all connections from the SAS expander card to the backplane are made, the cables from the SAS
 controller can be attached to any of the remaining connectors on the SAS expander card

8. Front Control Panel and I/O Panel Overview

All system configurations will include a Control Panel on the front of the system providing push button system controls and LED indicators for several system features. Systems configured with eight 3.5" hard drive bays or up to sixteen 2.5" hard drive bays will also include an I/O Panel providing additional system I/O features. This section describes the features and functions of both front panel options.

8.1 I/O Panel Features



Label	Description
Α	Video connector
В	USB ports

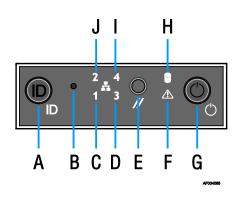
Figure 44. Front I/O Panel Features

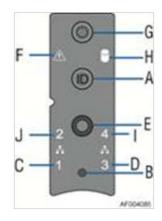
A – Video connector – The front I/O Panel video connector gives the option of attaching a monitor to the front of the system. When BIOS detects that a monitor is attached to the front video connector, it disables the video signals routed to the on-board video connector on the back of the system. Video resolutions from the front video connector may be lower than that of the rear on-board video connector. A short video cable should be used for best resolution. The front video connector is cabled to a 2x7 header on the server board labeled "FP Video".

B – USB Ports – The front I/O panel includes two USB 2.0 ports. The USB ports are cabled to a 2x5 connector on the server board labeled "FP USB".

8.2 Control Panel Features

The system includes a control panel that provides push button system controls and LED indicators for several system features. Depending on the hard drive configuration, the front control panel may come in either of two formats; however, both provide the same functionality. This section will provide a description for each front control panel feature.





Label	Description	Label	Description
Α	System ID Button w/Integrated LED	F	System Status LED
В	NMI Button (recessed, tool required for use)	G	Power / Sleep Button w/Integrated LED
С	NIC-1 Activity LED	Н	Hard Drive Activity LED
D	NIC-3 Activity LED	I	NIC-4 Activity LED
Е	System Cold Reset Button	J	NIC-2 Activity LED

Figure 45. Front Control Panel Features

A – System ID Button w/Integrated LED – Toggles the integrated ID LED and the Blue server board ID LED on and off. The System ID LED is used to identify the system for maintenance when installed in a rack of similar server systems. The System ID LED can also be toggled on and off remotely using the IPMI "Chassis Identify" command which will cause the LED to blink for 15 seconds.

B – NMI Button – When the NMI button is pressed, it puts the server in a halt state and issues a non-maskable interrupt (NMI). This can be useful when performing diagnostics for a given issue where a memory download is necessary to help determine the cause of the problem. To prevent an inadvertent system halt, the actual NMI button is located behind the Front Control Panel faceplate where it is only accessible with the use of a small tipped tool like a pin or paper clip.

C, D, I and J – Network Activity LEDs – The Front Control Panel includes an activity LED indicator for each on-board Network Interface Controller (NIC). When a network link is detected, the LED will turn on solid. The LED will blink once network activity occurs at a rate that is consistent with the amount of network activity that is occurring.

E – System Cold Reset Button – When pressed, this button will reboot and re-initialize the system.

F – System Status LED – The System Status LED is a bi-color (Green/Amber) indicator that shows the current health of the server system. The system provides two locations for this feature; one is located on the Front Control Panel, the other is located on the back edge of the server board, viewable from the back of the system. Both LEDs are tied together and will show the same state. The System Status LED states are driven by the on-board platform management sub-system. The following table provides a description of each supported LED state.

Table 31. System Status LED State Definitions

Color	State	Criticality	Description
Off	System is	Not ready	System is powered off (AC and/or DC).
	not		2. System is in EuP Lot6 Off Mode.
	operating		3. System is in S5 Soft-Off State.
	0 " 1	01	4. System is in S4 Hibernate Sleep State.
Green	Solid on	Ok	Indicates that the System is running (in S0 State) and its status is 'Healthy'.
			The system is not exhibiting any errors. AC power is present and BMC has
0	4 11= 5 15-15	Decine de de content	booted and manageability functionality is up and running.
Green	~1 Hz blink	Degraded - system	System degraded:
		is operating in a degraded state	Redundancy loss, such as power-supply or fan. Applies only if the associated platform sub-system has redundancy capabilities.
		although still	Fan warning or failure when the number of fully operational fans is more
		functional, or	than minimum number needed to cool the system.
		system is	Non-critical threshold crossed – Temperature (including HSBP temp),
		operating in	voltage, input power to power supply, output current for main power rail
		a redundant state	from power supply and Processor Thermal Control (Therm Ctrl) sensors.
		but with an	Power supply predictive failure occurred while redundant power supply
		impending failure	configuration was present.
		warning	Unable to use all of the installed memory (one or more DIMMs
			failed/disabled but functional memory remains available)
			Correctable Errors over a threshold and migrating to a spare DIMM
			(memory sparing). This indicates that the user no longer has spared DIMMs
			indicating a redundancy lost condition. Corresponding DIMM LED lit.
			Uncorrectable memory error has occurred in memory Mirroring Mode,
			causing Loss of Redundancy.
			Correctable memory error threshold has been reached for a failing DDR3
			DIMM when the system is operating in fully redundant RAS Mirroring Mode. Battery failure.
			BMC executing in uBoot. (Indicated by Chassis ID blinking at Blinking at
			3Hz). System in degraded state (no manageability). BMC uBoot is running
			but has not transferred control to BMC Linux. Server will be in this state 6-8
			seconds after BMC reset while it pulls the Linux image into flash
			BMC booting Linux. (Indicated by Chassis ID solid ON). System in
			degraded state (no manageability). Control has been passed from BMC
			uBoot to BMC Linux itself. It will be in this state for ~10-~20 seconds.
			BMC Watchdog has reset the BMC.
			Power Unit sensor offset for configuration error is asserted.
			HDD HSC is off-line or degraded.
Amber	~1 Hz blink	Non-critical -	Non-fatal alarm – system is likely to fail:
		System is	Critical threshold crossed – Voltage, temperature (including HSBP temp),
		operating in a	input power to power supply, output current for main power rail from power
		degraded state	supply and PROCHOT (Therm Ctrl) sensors.
		with an impending	VRD Hot asserted.
		failure warning, although still	Minimum number of fans to cool the system not present or failed Hard drive fault
		functioning	Power Unit Redundancy sensor – Insufficient resources offset (indicates not
		Tariotioning	enough power supplies present)
			In non-sparing and non-mirroring mode if the threshold of correctable errors
			is crossed within the window
			Correctable memory error threshold has been reached for a failing DDR3
			DIMM when the system is operating in a non-redundant mode

Intel® Server System R2000GZ/GL Product Family TPS

Amber	Solid on	Critical, non-	Fatal alarm – system has failed or shutdown:
		recoverable -	CPU CATERR signal asserted
		System is halted	MSID mismatch detected (CATERR also asserts for this case).
			CPU 1 is missing
			CPU Thermal Trip
			No power good – power fault
			DIMM failure when there is only 1 DIMM present and hence no good memory present ¹ .
			Runtime memory uncorrectable error in non redundant mode.
			DIMM Thermal Trip or equivalent
			SSB Thermal Trip or equivalent
			CPU ERR2 signal asserted
			BMC\Video memory test failed. (Chassis ID shows blue/solid-on for this condition)
			Both uBoot BMC FW images are bad. (Chassis ID shows blue/solid-on for this condition)
			240VA fault
			Fatal Error in processor initialization:
			Processor family not identical
			Processor model not identical
			Processor core/thread counts not identical
			Processor cache size not identical
			Unable to synchronize processor frequency
			Unable to synchronize QPI link frequency

G – Power/Sleep Button – Toggles the system power on and off. This button also functions as a sleep button if enabled by an ACPI compliant operating system. Pressing this button will send a signal to the integrated BMC, which will either power on or power off the system. The integrated LED is a single color (Green) and is capable of supporting different indicator states as defined in the following table.

Table 32. Power/Sleep LED Functional States

State	Power Mode	LED	Description
Power-off	Non-ACPI	Off	System power is off, and the BIOS has not initialized the chipset.
Power-on	Non-ACPI	On	System power is on
S5	ACPI	Off	Mechanical is off, and the operating system has not saved any context to the hard disk.
S4	ACPI	Off	Mechanical is off. The operating system has saved context to the hard disk.
S3-S1	ACPI	Slow blink ¹	DC power is still on. The operating system has saved context and gone into a level of low-power state.
S0	ACPI	Steady on	System and the operating system are up and running.

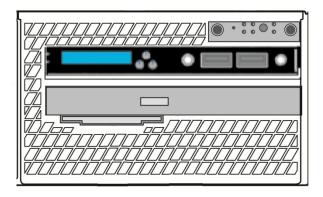
Note:

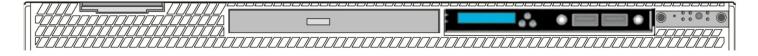
1. The LED blinks at a rate of 1 Hz with a 50% duty cycle.

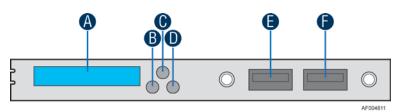
H- Drive Activity LED - The drive activity LED on the front panel indicates drive activity from the on-board hard disk controllers. The server board also provides a header giving access to this LED for add-in controllers.

9. Intel[®] Local Control Panel

The Intel[®] Local Control Panel option (Intel Product Order Code – **A1U2ULCP**) utilizes a combination of control buttons and LCD display to provide system accessibility and monitoring.







Label	Description	Functionality
Α	LCD Display	one line 18 character display
В	Left Control Button	moves the cursor backward one step or one character
С	"Enter" Button	selects the menu item highlighted by the cursor
D	Right Control Button	moves the cursor forward one step or one character
E	USB 2.0 Port	
F	USB 2.0 Port	

Figure 46. Intel Local Control Panel Option

The LCD (Local Control Display) is a one line character display that resides on the front panel of the chassis. It can display a maximum of 18 characters at a time. This device also contains 3 buttons (Left, Right and Enter). The user can select the content that needs to be displayed on the LCD screen by operating these buttons.

For a complete description of the LCP accessory, please reference the *Intel® Local Control Panel for Platforms Based on Intel® Xeon® Processor E5 4600/2600/2400/1600/1400 Product Families Technical Product Specification.* (Intel document order number G83726-001).

10. PCI Riser Card Support

The system includes two riser card slots on the server board. Available riser cards can be used in either slot. This section will provide an overview of each available riser card and describe the server board features and architecture supporting them.

10.1 Riser Slot Overview

The server board includes two riser card slots labeled "RISER 1" and "RISER 2". The following diagram illustrates the general server board architecture supporting these two slots.

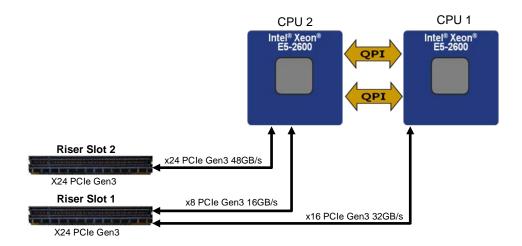


Figure 47. Riser Slot Architecture

Riser slot-1 includes a total of 24 PCIe Gen3 bus lanes; 16 routed from CPU-1 and 8 routed from CPU-2. Riser slot-2 has 24 PCIe Gen3 bus lanes routed from CPU-2. Each riser slot is capable of supporting several different types of multi-slot riser cards. In order to support the maximum number of add-in cards, both CPU-1 and CPU-2 must be populated. With only CPU-1 installed, riser slot-2 has no functionality and depending on the riser card installed, riser slot-1 will be limited to supporting a single x16 PCIe add-in card or two x8 PCIe add-in cards. See Figure 48. Intel® Server Board S2600GZ/GL PCI Bus Layout Diagram, to determine PCI bus lane routing for each riser card.

NOTE: The riser card slots on the server board are designed to support riser cards only. Inserting a PCle add-in card directly into the riser card slot on the server board will result in damage to the server board, the add-in card, or both. PCle add-in cards should only be installed into a supported riser card assembly.

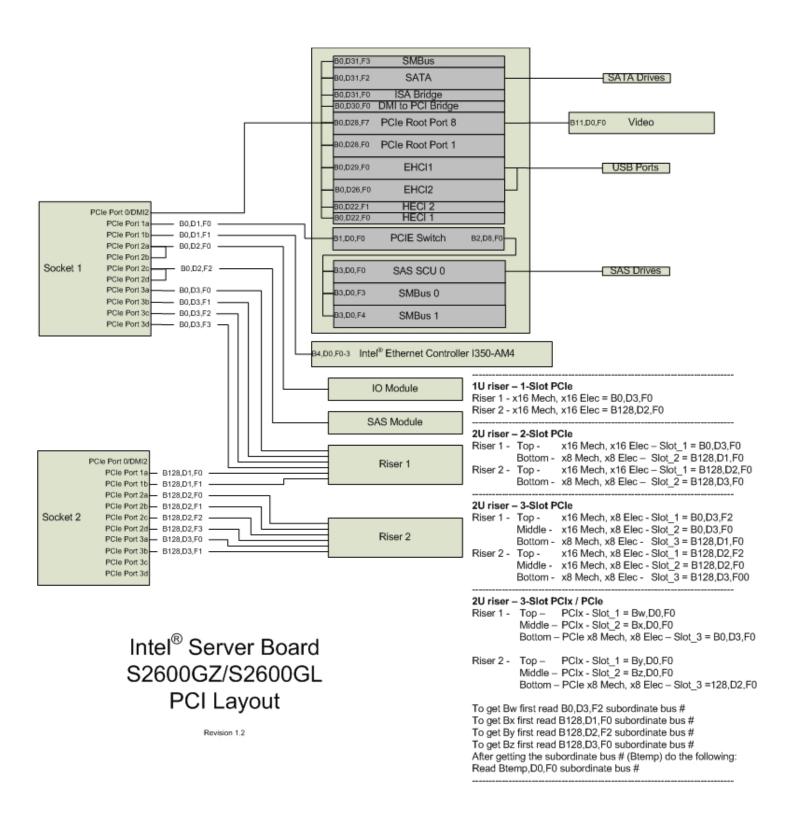


Figure 48. Intel[®] Server Board S2600GZ/GL PCI Bus Layout Diagram

10.2 Riser Card Assembly

The system includes two riser card assemblies which include a riser card bracket and a riser card. The riser card bracket will accept riser cards with up to three add-in card slots

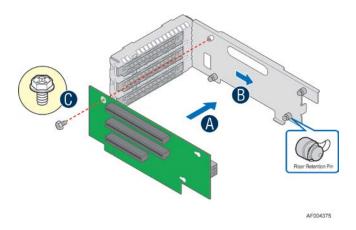


Figure 49. Riser Card Assembly

Depending on the riser card option installed, the system can support up to four full height full length add-in cards (top and middle slots on each riser) and two full height ½ length add-in cards (bottom slot on each riser).

Note: Add-in cards that exceed the PCI specification for ½ length PCI add-in cards (167.65mm or 6.6in) may interfere with other installed devices on the server board when installed in the bottom add-in card slot.

Two card guide brackets on the air duct provide support for Full Height / Full Length add-in cards when installed in either the middle or top add-in card slots of each installed riser card assembly.

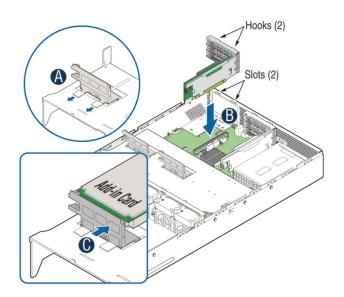


Figure 50. Full Height / Full Length Add-in Card Support

Thermally, the system can support the following PCI add-in cards.

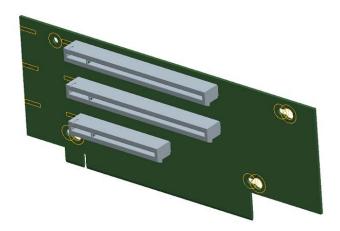
- Add-in cards with a minimum 100 LFM (0.5 m/s) air flow requirement can be installed in any available add-in card slot in both Riser Card #1 and Riser Card #2
- Add-in cards with a minimum 200 LFM (1 m/s) air flow requirement can be installed in any available add-in card slot on Riser Card #2 and the bottom add-in card slot on Riser Card #1. Middle and Top add-in card slots on Riser Card #1 cannot support PCI add-in cards with air flow requirements greater than 100 LFM.
- Add-in cards with a >200 LFM air flow requirement cannot be supported.

<u>Note:</u> Most PCI add-in cards have minimum air flow requirements of 100 LFM (0.5m/s). Some high power add-in cards have minimum air flow requirements of 200 LFM (1 m/s). System integrators should verify PCI add-in card air flow requirements from vendor specifications when integrating add-in cards into the system

10.3 Riser Card Options

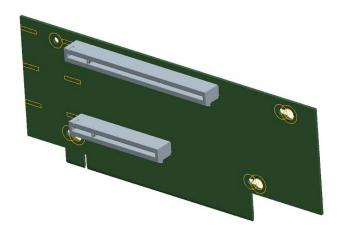
Several multi-slot PCI riser card options are available for this server product family. All available riser card options can be installed in either riser card slot on the server board.

10.3.1 3-Slot PCle Riser Card



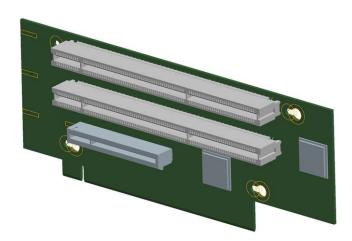
Slot #	Description	PCIe Lane Routing Riser Slot # 1	PCIe Lane Routing Riser Slot #2
Slot-1 (Top)	PCIe x8 lanes, x16 slot	CPU1	CPU2
Slot-2 (Middle)	PCIe x8 lanes, x16 slot	CPU1	CPU2
Slot-3 (Bottom)	PCIe x8 lanes, x8 slot	CPU2	CPU2

10.3.2 2-Slot PCle Riser Card



Slot #	Description	PCIe Lane Routing Riser Slot #1	PCIe Lane Routing Riser Slot #2
Slot-1 (Top)	PCIe x16 lanes, x16 slot	CPU1	CPU2
Slot-2 (Bottom)	PCIe x8 lanes, x8 slot	CPU2	CPU2

10.3.3 3-Slot PCIx / PCIe Riser Card



Slot #	Description	PCIe Lane Routing Riser Slot #1	PCIe Lane Routing Riser Slot #2
Slot-1 (Top)	PCIx 133MHz	CPU1	CPU2
Slot-2 (Middle)	PCIx 133MHz	CPU2	CPU2
Slot-3 (Bottom)	PCIe x8 lanes, x8 slot	CPU1	CPU2

10.4 PCle Add-in card support

The PCIe sub-system of the server board has support for PCIe add-in cards that follow the PCIe Gen1, Gen2 and Gen3 specifications. However, the performance of some PCIe Gen3 cards may be forced to operate at Gen2 speeds due to electrical signaling characteristic limitations that exist between the server board and some PCIe Gen 3 add-in cards.

Intel has implemented the following PCIe Gen 3 support model for this generation of its server boards and server systems.

10.4.1 PCle Gen3 support – Systems configured with an Intel[®] Xeon[®] processor E5-2600 product family

For a server system configured with one or more Intel[®] Xeon[®] processor E5-2600 product family, the system BIOS will use an embedded PCIe Gen 3 compatibility list which identifies all PCIe Gen 3 add-in cards tested by Intel to operate reliably at Gen 3 speeds on the given server system. During POST, the system BIOS will compare installed PCIe Gen 3 add-in cards with those included in the embedded compatibility list. If BIOS matches an installed card to one listed on the compatibility list, the BIOS will configure the device to operate at PCIe Gen 3 speeds. If the BIOS cannot match an installed PCIe add-in card with any device included in the list, the BIOS will force the device to operate at PCIe Gen2 speeds.

Note: The latest available BIOS should be installed on the system to ensure the most up to date embedded PCle Gen 3 compatibility list is being used.

Visit the following Intel web site for a list of Intel tested PCIe Gen 3 compatible cards included in the BIOS embedded compatibility list – http://intel.com/support/motherboards/server/sb/CS-034157.htm

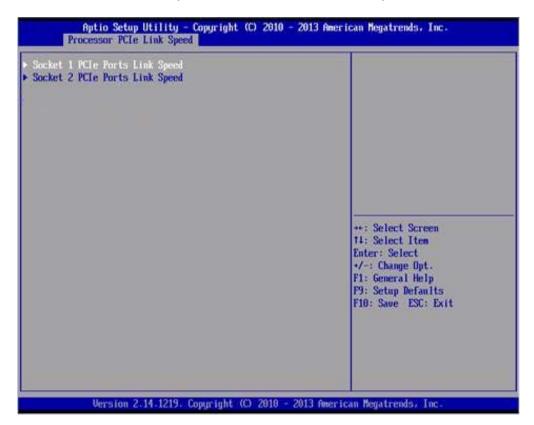
10.4.2 PCIe Gen3 support – Systems configured with an Intel® Xeon® processor E5-2600 V2 product family

For a server system configured with one or more Intel[®] Xeon[®] processor E5-2600 V2 product family, the system BIOS will configure all installed PCIe Gen 3 compatible add-in cards to operate at PCIe Gen 3 speeds by default. For a list of Intel tested PCIe Gen 3 add-in cards, review the Tested Hardware and OS list (THOL) using Intel's Server Configurator Tool at the following web site:

https://serverconfigurator.intel.com

The following tables identify the PCIe port routing for each add-in card slot on all supported riser cards as installed in either Riser Slot # 1 or Riser Slot #2. Note the specific processor, PCIe port ID, and number of PCIe bus lanes supporting each add-in card slot.

Depending on the riser card installed, specific PCle ports routed from the processor IIO module provide the PCle interface to each riser card slot.



The "Processor PCIe Link Speed" menu will display selectable options for each installed processor, identified as "Socket #", where # identifies the CPU number.

Using information provided in *the following tables, select* the processor associated with the PCIe port to be configured.

Table 33. Riser Slot #1 - PCle Port Routing

Riser Slot #1 – Riser Card Options			
2U - PCIx Riser Card	2U – 3 PCIe Slot Riser Card	2U – 2 PCIe Slot Riser Card	
Top PCIx Slot (CPU #1 – Port 3C)	Top PCIe Slot (CPU #1 – Port 3C) (x8 elec, x16 mech)	Top PCIe Slot	
Middle PCIx Slot (CPU #2 – Port 1B)	Middle PCle Slot (CPU #1 – Port 3A) (x8 elec, x16 mech)	(CPU #1 – Port 3A) (x16 elec, x16mech)	
Bottom PCIe Slot (CPU # 1 – Port 3A) (x8 elec, x8 mech)	Bottom PCIe Slot (CPU #2 – Port 1A) (x8 elec, x8 mech)	Bottom PCIe Slot (CPU #2 – Port 1A) (x8 elec, x8 mech)	

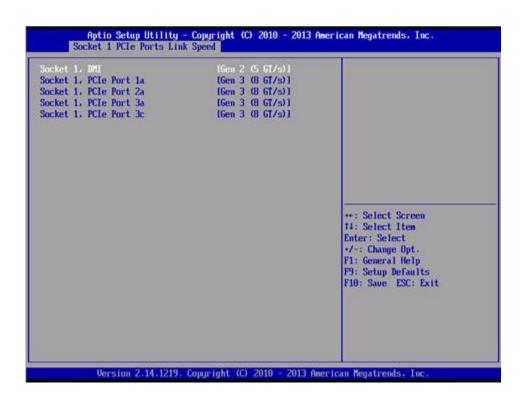
Table 34. Riser Slot #2 - PCle Port Routing

Riser Slot #2 – Riser Card Options				
2U - PCIx Riser Card	2U – 3 PCle Slot Riser Card	2U – 2 PCIe Slot Riser Card		
Top PCIx Slot (CPU #2 – Port 2C)	Top PCIe Slot (CPU #2 – Port 2C) (x8 elec, x16 mech)	Top PCIe Slot		
Middle PCIx Slot (CPU #2 –Port 3A)	Middle PCle Slot (CPU #2 – Port 2A) (x8 elec, x16 mech)	(CPU #2 – Port 2A) (x16 elec, x16 mech)		
Bottom PCIe Slot (CPU #2 – Port 2A) (x8 elec, x8 mech)	Bottom PCIe Slot (CPU #2 – Port 3A) (x8 elec, x8 mech)	Bottom PCIe Slot (CPU #2 – Port 3A) (x8 elec, x8 mech)		

The "Socket # PCIe Ports Link Speed" window displays selectable options for each configurable PCIe port associated with the current system configuration.

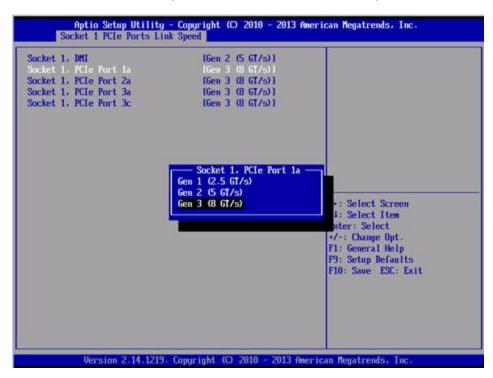
Note: The illustrations below are for reference purposes only. Actual PCle port data displayed in the "Socket # PCle Ports Link Speed" window may be different than what is shown here.

Using the arrow keys, move the cursor down to the PCIe port to be changed.



Once a port is selected, a port configuration window appears and provides options to configure the specified PCIe port to operate at a specified PCIe Gen level.

Intel® Server System R2000GZ/GL Product Family TPS

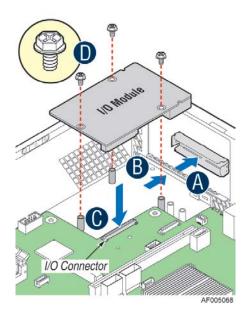


Select the desired PCIe Gen level. After making all desired changes in BIOS setup, be sure to save the changes and reboot the system.

11. Mezzanine Module Support

11.1 IO Module Support

In addition to the embedded I/O features of the server board, and those available with the addition of a PCIe add-in card, the server also provides concurrent support of an optionally installed mezzanine I/O module.



The following table lists the Intel® I/O modules available for this server.

Product Code & iPN	Description
AXX10GBNIAIOM	Dual SFP+ port 10GbE IO Module based on Intel® 82599 10GbE Ethernet Controller
AXX10GBTWLIOM	Dual RJ-45 port 10GBase-T I/O Module based on Intel® Ethernet Controller x540
AXX1FDRIBIOM	Single Port FDR 56GT/S speed InfiniBand module with QSFP connector
AXX2FDRIBIOM	Dual port FDR 56GT/S speed infiniband module with QSFP connector
AXX4P1GBPWLIOM	Quad Port 1GbE I/O Module based on Intel® Ethernet Controller I350

11.2 Intel[®] Remote Management Module 4 (RMM4) Lite and Management NIC Support

The system has support for the Intel® Remote Management Module 4 (RMM4). Additional information for the RMM4 option can be found in the following documents: Intel® Remote Management Module 4 Technical Product Specification and the Intel® Remote Management Module 4 and Integrated BMC Web Console Users Guide.

Intel Product	Description	Kit Contents	Benefits
Code			
AXXRMM4LITE	Intel® Remote Management	RMM4 Lite	Enables KVM & media
	Module 4 Lite	Activation Key	redirection via onboard NIC
AXXRMM4R	Intel® Remote Management	RMM4 Lite	Dedicated NIC for management
	Module 4	Activation Key	traffic. Higher bandwidth
		Dedicated NIC Port	connectivity for KVM & media
		Module	Redirection with 1Gbe NIC.

On the server board each Intel® RMM4 component is installed at the following locations.

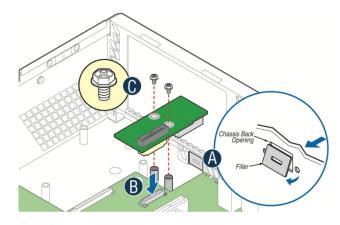


Figure 51. Intel® RMM4 Dedicated Management NIC Installation

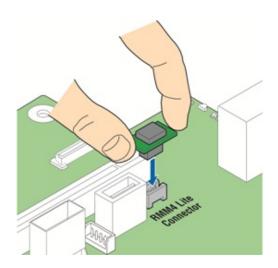


Figure 52. Intel® RMM4 Lite Activation Key Installation

Table 35. Enabling Advanced Management Features

Manageability Hardware	Benefits
Intel® Integrated BMC	Comprehensive IPMI based base manageability features
Intel® Remote Management Module 4 – Lite Package contains one module – 1- Key for advance Manageability features.	No dedicated NIC for management Enables KVM & media redirection via onboard NIC
Intel® Remote Management Module 4 Package includes 2 modules — 1 - key for advance features 2 - Dedicated NIC (1Gbe) for management	Dedicated NIC for management traffic. Higher bandwidth connectivity for KVM & media Redirection with 1Gbe NIC.

For further RMM4 information, please refer to the following documents:

- Intel® Server Board S2600GZ/GL Technical Product Specification
- Intel[®] Remote Management Module 4 Technical Product Specification
- Intel® Remote Management Module 4 and Integrated BMC Web Console Users Guide

Appendix A: Integration and Usage Tips

This section provides a list of useful information that is unique to the Intel[®] Server System R2000GZ/GL Product Family and should be kept in mind while configuring your server system.

- Only the Intel[®] Xeon[®] processor E5-2600 and the Intel[®] Xeon[®] processor E5-2600 V2 product families are supported in this Intel[®] Server System. Previous generation Intel[®] Xeon[®] processors are not supported.
- For best system performance, follow memory population guidelines as specified in the *Intel® Server Board S2600G4Z / S2600GL Technical Product Specification*.
- For best system performance, follow all thermal configuration guidelines as specified in this document.
- The CPU-1 processor + CPU heat sink must be installed first. The CPU-2 heat sink must be installed at all times, with or without a processor installed
- DIMM slots E1 thru H3 are only supported when CPU-2 is installed.
- PCI Riser Slot-2 is only functional when two CPUs are installed.
- The Mini-SAS connector labeled "SCU_1(4-7)" on the server board is only functional when an appropriate Intel[®] RAID C600 Upgrade Key is installed.
- Many integrated on-board SAS and RAID options are available by installing any of several available Intel® RAID C600 Upgrade Keys.
- The embedded platform management system may not operate as expected if the platform is not updated with accurate system configuration data. The FRUSDR utility must be run with the system fully configured and each time the system configuration changes for accurate system monitoring and event reporting.
- Make sure the latest system software is loaded on the server. This includes System BIOS, BMC Firmware, ME Firmware and FRUSDR. The latest system software can be downloaded from http://downloadcenter.intel.com.

Appendix B: POST Code Diagnostic LED Decoder

As an aid to assist in trouble shooting a system hang that occurs during a system's Power-On Self Test (POST) process, the server board includes a bank of eight POST Code Diagnostic LEDs on the back edge of the server board.

During the system boot process, Memory Reference Code (MRC) and System BIOS execute a number of memory initialization and platform configuration processes, each of which is assigned a specific hex POST code number. As each routine is started, the given POST code number is displayed to the POST Code Diagnostic LEDs on the back edge of the server board.

During a POST system hang, the displayed post code can be used to identify the last POST routine that was run prior to the error occurring, helping to isolate the possible cause of the hang condition.

Each POST code is represented by eight LEDs; four Green and four Amber. The POST codes are divided into two nibbles, an upper nibble and a lower nibble. The upper nibble bits are represented by Amber Diagnostic LEDs #4, #5, #6, #7. The lower nibble bits are represented by Green Diagnostics LEDs #0, #1, #2 and #3. If the bit is set in the upper and lower nibbles, the corresponding LED is lit. If the bit is clear, the corresponding LED is off.

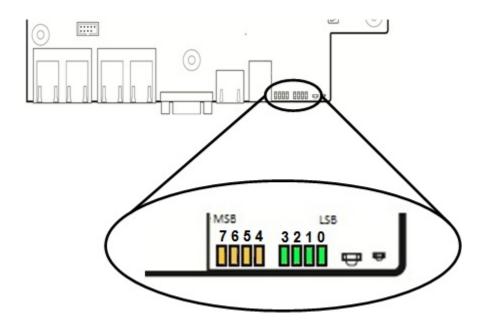


Figure 53. POST Diagnostic LED Location

In the following example, the BIOS sends a value of ACh to the diagnostic LED decoder. The LEDs are decoded as follows:

	l	Jpper Nibble	AMBER LED	S	Lower Nibble GREEN LEDs			
LEDs	MSB							LSB
LEDS	LED #7	LED #6	LED #5	LED #4	LED #3	LED #2	LED #1	LED #0
	8h	4h	2h	1h	8h	4h	2h	1h
Status	ON	OFF	ON	OFF	ON	ON	OFF	OFF
Results	1	0	1	0	1	1	0	0
Results		Α	h			C	h	

Table 36. POST Progress Code LED Example

Upper nibble bits = 1010b = Ah; Lower nibble bits = 1100b = Ch; the two are concatenated as ACh.

Table 37. Diagnostic LED POST Code Decoder

	Diagnostic LED Decoder								
	1 = L								
Checkpoint	Uppe					er Nik	ble		
onoonpoint	MSB							LSB	
	8h	4h	2h	1h	8h	4h	2h	1h	
LED #		#6	#5	#4	#3	#2	#1		Description
SEC Phase									
01h	0	0	0	0	0	0	0	1	First POST code after CPU reset
02h	0	0	0	0	0	0	1	0	Microcode load begin
03h	0	0	0	0	0	0	1	1	CRAM initialization begin
04h	0	0	0	0	0	1	0	0	Pei Cache When Disabled
05h	0	0	0	0	0	1	0	1	SEC Core At Power On Begin.
06h	0	0	0	0	0	1	1	0	Early CPU initialization during Sec Phase.
07h	0	0	0	0	0	1	1	1	Early SB initialization during Sec Phase.
08h	0	0	0	0	1	0	0	0	Early NB initialization during Sec Phase.
09h	0	0	0	0	1	0	0	1	End Of Sec Phase.
0Eh	0	0	0	0	1	1	1	0	Microcode Not Found.
0Fh	0	0	0	0	1	1	1	1	Microcode Not Loaded.
PEI Phase									DELO.
10h	0	0	0	1	0	0	0	0	PEI Core
11h	0	0	0	1	0	0	0	1	CPU PEIM
15h 19h	0	0	0	1	0	0	0	1	NB PEIM SB PEIM
	•	•			1 00 Co			1	executed - See Table 28
PEI Phase co			KC P	rogre	SS CC	ue Se	equer	ice is	executed - See Table 26
31h	0	0	1	1	0	0	0	1	Memory Installed
32h	0	0	1	1	0	0	1	0	CPU PEIM (Cpu Init)
33h	0	0	1	1	0	0	1	1	CPU PEIM (Cache Init)
34h	0	0	1	1	0	1	0	0	CPU PEIM (BSP Select)
35h	0	0	1	1	0	1	0	1	CPU PEIM (AP Init)
36h	0	0	1	1	0	1	1	0	CPU PEIM (CPU SMM Init)
4Fh	0	1	0	0	1	1	1	1	Dxe IPL started
DXE Phase									
60h	0	1	1	0	0	0	0	0	DXE Core started
61h	0	1	1	0	0	0	0	1	DXE NVRAM Init
62h	0	1	1	0	0	0	1	0	SB RUN Init
63h	0	1	1	0	0	0	1	1	Dxe CPU Init
68h	0	1	1	0	1	0	0	0	DXE PCI Host Bridge Init
69h	0	1	1	0	1	0	0	1	DXE NB Init
6Ah	0	1	1	0	1	0	1		DXE NB SMM Init
70h	0	1	1	1	0	0	0	0	DXE SB Init
71h	0	1	1	1	0	0	0	1	DXE SB SMM Init
72h	0	1	1	1	0	0	1	0	DXE SB devices Init
78h 79h	0	1	1	1	1	0	0	0 1	DXE ACPI Init DXE CSM Init
90h	1	0	0	1	0	0	0	0	DXE BDS Started
91h	1	0	0	1	0	0	0	1	DXE BDS stated DXE BDS connect drivers
92h	1	0	0	1	0	0	1	0	DXE PCI Bus begin
93h	1	0	0	1	0	0	1	1	DXE PCI Bus HPC Init
94h	1	0	0	1	0	1	0	0	DXE PCI Bus enumeration
95h	1	0	0	1	0	1	0	1	DXE PCI Bus resource requested
96h	1	0	0	1	0	1	1	0	DXE PCI Bus assign resource
97h	1	0	0	1	0	1	1	1	DXE CON_OUT connect
98h	1	0	0	1	1	0	0	0	DXE CON_IN connect
99h	1	0	0	1	1	0	0	1	DXE SIO Init
9Ah	1	0	0	1	1	0	1	0	DXE USB start
9Bh	1	0	0	1	1	0	1	1	DXE USB reset
9Ch	1	0	0	1	1	1	0	0	DXE USB detect
9Dh	1	0	0	1	1	1	0	1	DXE USB enable
A1h	1	0	1	0	0	0	0	1	DXE IDE begin
A2h	1	0	1	0	0	0	1	0	DXE IDE reset

	Diago	nosti	c I Fr) Dec	oder				
		Diagnostic LED Decoder 1 = LED On, 0 = LED Off					1		
Checkpoint	Uppe			LLD	Lower Nibble				1
Olleckpoliti	MSB				LSB				
	8h	4h	2h	1h	8h	4h	2h	1h	1
LED#		#6	#5	#4	#3	#2	#1		Description
A3h	1	0	1	0	0	0	1	1	DXE IDE detect
A4h	1	0	1	0	0	1	0	0	DXE IDE enable
A5h	1	0	1	0	0	1	0	1	DXE SCSI begin
A6h	1	0	1	0	0	1	1	0	DXE SCSI reset
A7h	1	0	1	0	0	1	1	1	DXE SCSI detect
A8h	1	0	1	0	1	0	0	0	DXE SCSI enable
A9h	1	0	1	0	1	0	0	1	DXE verifying SETUP password
ABh	1	0	1	0	1	0	1	1	DXE SETUP start
ACh	1	0	1	0	1	1	0	0	DXE SETUP input wait
ADh	1	0	1	0	1	1	0	1	DXE Ready to Boot
AEh	1	0	1	0	1	1	1	0	DXE Legacy Boot
AFh	1	0	1	0	1	1	1	1	DXE Exit Boot Services
B0h	1	0	1	1	0	0	0	0	RT Set Virtual Address Map Begin
B1h	1	0	1	1	0	0	0	1	RT Set Virtual Address Map End
B2h	1	0	1	1	0	0	1	0	DXE Legacy Option ROM init
B3h	1	0	1	1	0	0	1	1	DXE Reset system
B4h	1	0	1	1	0	1	0	0	DXE USB Hot plug
B5h	1	0	1	1	0	1	0	1	DXE PCI BUS Hot plug
B6h	1	0	1	1	0	1	1	0	DXE NVRAM cleanup
B7h	1	0	1	1	0	1	1	1	DXE Configuration Reset
00h	0	0	0	0	0	0	0	0	INT19
S3 Resume									
E0h	1	1	1	0	0	0	0	0	S3 Resume PEIM (S3 started)
E1h	1	1	1	0	0	0	0	1	S3 Resume PEIM (S3 boot script)
E2h	1	1	1	0	0	0	1	0	S3 Resume PEIM (S3 Video Repost)
E3h	1	1	1	0	0	0	1	1	S3 Resume PEIM (S3 OS wake)
BIOS Recove	ry								,
F0h	1	1	1	1	0	0	0	0	PEIM which detected forced Recovery condition
F1h	1	1	1	1	0	0	0	1	PEIM which detected User Recovery condition
F2h	1	1	1	1	0	0	1	0	Recovery PEIM (Recovery started)
F3h	1	1	1	1	0	0	1	1	Recovery PEIM (Capsule found)
F4h	1	1	1	1	0	1	0	0	Recovery PEIM (Capsule loaded)
									, , ,

POST Memory Initialization MRC Diagnostic Codes

There are two types of POST Diagnostic Codes displayed by the MRC during memory initialization; Progress Codes and Fatal Error Codes.

The MRC Progress Codes are displays to the Diagnostic LEDs that show the execution point in the MRC operational path at each step.

Table 38. MRC Progress Codes

	Diagnostic LED Decoder 1 = LED On, 0 = LED Off									
Checkpoint		Upper	Nibble			Lower	Nibble	<u> </u>	j	
,	MSB							LSB	Description	
	8h	4h	2h	1h	8h	4h	2h	1h		
LED	#7	#6	#5	#4	#3	#2	#1	#0		
MRC Progress	MRC Progress Codes									
B0h	1	0	1	1	0	0	0	0	Detect DIMM population	
B1h	1	0	1	1	0	0	0	1	Set DDR3 frequency	
B2h	1	0	1	1	0	0	1	0	Gather remaining SPD data	
B3h	1	0	1	1	0	0	1	1	Program registers on the memory controller level	
B4h	1	0	1	1	0	1	0	0	Evaluate RAS modes and save rank information	
B5h	1	0	1	1	0	1	0	1	Program registers on the channel level	
B6h	1	0	1	1	0	1	1	0	Perform the JEDEC defined initialization sequence	
B7h	1	0	1	1	0	1	1	1	Train DDR3 ranks	
B8h	1	0	1	1	1	0	0	0	Initialize CLTT/OLTT	
B9h	1	0	1	1	1	0	0	1	Hardware memory test and init	
BAh	1	0	1	1	1	0	1	0	Execute software memory init	
BBh	1	0	1	1	1	0	1	1	Program memory map and interleaving	
BCh	1	0	1	1	1	1	0	0	Program RAS configuration	
BFh	1	0	1	1	1	1	1	1	MRC is done	

Memory Initialization at the beginning of POST includes multiple functions, including: discovery, channel training, validation that the DIMM population is acceptable and functional, initialization of the IMC and other hardware settings, and initialization of applicable RAS configurations.

When a major memory initialization error occurs and prevents the system from booting with data integrity, a beep code is generated, the MRC will display a fatal error code on the diagnostic LEDs, and a system halt command is executed. Fatal MRC error halts do NOT change the state of the System Status LED, and they do NOT get logged as SEL events. The following table lists all MRC fatal errors that are displayed to the Diagnostic LEDs.

Table 39. MRC Fatal Error Codes

	Diagnostic LED Decoder								
	1 = L	ED O	n, 0 =	: LED	Off				
Checkpoint	Uppe	er Nib	ble		Lowe	er Nib	ble		
	MSB							LSB	Description
	8h	4h	2h	1h	8h	4h	2h	1h	
LED	#7	#6	#5	#4	#3	#2	#1	#0	
MRC Fatal E	rror C	odes		•					
E8h	1	1	1	0	1	0	0	0	No usable memory error 01h = No memory was detected via SPD read, or invalid config that causes no operable memory. 02h = Memory DIMMs on all channels of all sockets are disabled due to hardware memtest error. 3h = No memory installed. All channels are disabled.
E9h	1	1	1	0	1	0	0	1	Memory is locked by Intel Trusted Execuiton Technology and is inaccessible
EAh	1	1	1	0	1	0	1	0	DDR3 channel training error 01h = Error on read DQ/DQS (Data/Data Strobe) init 02h = Error on Receive Enable 3h = Error on Write Leveling 04h = Error on write DQ/DQS (Data/Data Strobe
EBh	1	1	1	0	1	0	1	1	Memory test failure 01h = Software memtest failure. 02h = Hardware memtest failed. 03h = Hardware Memtest failure in Lockstep Channel mode requiring a channel to be disabled. This is a fatal error which requires a reset and calling MRC with a different RAS mode to retry.
EDh	1	1	1	0	1	1	0	1	DIMM configuration population error 01h = Different DIMM types (UDIMM, RDIMM, LRDIMM) are detected installed in the system. 02h = Violation of DIMM population rules. 03h = The 3rd DIMM slot can not be populated when QR DIMMs are installed. 04h = UDIMMs are not supported in the 3rd DIMM slot. 05h = Unsupported DIMM Voltage.
EFh	1	1	1	0	1	1	1	1	Indicates a CLTT table structure error

Appendix C: POST Code Errors

Most error conditions encountered during POST are reported using **POST Error Codes**. These codes represent specific failures, warnings, or are informational. POST Error Codes may be displayed in the Error Manager display screen, and are always logged to the System Event Log (SEL). Logged events are available to System Management applications, including Remote and Out of Band (OOB) management.

There are exception cases in early initialization where system resources are not adequately initialized for handling POST Error Code reporting. These cases are primarily Fatal Error conditions resulting from initialization of processors and memory, and they are handed by a Diagnostic LED display with a system halt.

The following table lists the supported POST Error Codes. Each error code is assigned an error type which determines the action the BIOS will take when the error is encountered. Error types include Minor, Major, and Fatal. The BIOS action for each is defined as follows:

- Minor: The error message is displayed on the screen or on the Error Manager screen, and an error is logged to the SEL. The system continues booting in a degraded state. The user may want to replace the erroneous unit. The POST Error Pause option setting in the BIOS setup does not have any effect on this error.
- Major: The error message is displayed on the Error Manager screen, and an error is logged to the SEL.
 The POST Error Pause option setting in the BIOS setup determines whether the system pauses to the
 Error Manager for this type of error so the user can take immediate corrective action or the system
 continues booting.

Note that for 0048 "Password check failed", the system halts, and then after the next reset/reboot will displays the error code on the Error Manager screen.

• Fatal: The system halts during post at a blank screen with the text "Unrecoverable fatal error found. System will not boot until the error is resolved" and "Press <F2> to enter setup" The POST Error Pause option setting in the BIOS setup does not have any effect with this class of error.

When the operator presses the **F2** key on the keyboard, the error message is displayed on the Error Manager screen, and an error is logged to the SEL with the error code. The system cannot boot unless the error is resolved. The user needs to replace the faulty part and restart the system.

NOTE: The POST error codes in the following table are common to all current generation Intel server platforms. Features present on a given server board/system will determine which of the listed error codes are supported.

Table 40. POST Error Messages and Handling

Error Code	Error Message	Response
0012	System RTC date/time not set	Major
0048	Password check failed	Major
0140	PCI component encountered a PERR error	Major
0141	PCI resource conflict	Major
0146	PCI out of resources error	Major
0191	Processor core/thread count mismatch detected	Fatal
0192	Processor cache size mismatch detected	Fatal
0194	Processor family mismatch detected	Fatal
0195	Processor Intel(R) QPI link frequencies unable to synchronize	Fatal
0196	Processor model mismatch detected	Fatal
0197	Processor frequencies unable to synchronize	Fatal
5220	BIOS Settings reset to default settings	Major
5221	Passwords cleared by jumper	Major
5224	Password clear jumper is Set	Major
8130	Processor 01 disabled	Major
8131	Processor 02 disabled	Major

Error Code	Error Message	Response
8132	Processor 03 disabled	Major
8133	Processor 04 disabled	Major
8160	Processor 01 unable to apply microcode update	Major
8161	Processor 02 unable to apply microcode update	Major
8162	Processor 03 unable to apply microcode update	Major
8163	Processor 04 unable to apply microcode update	Major
8170	Processor 01 failed Self Test (BIST)	Major
8171	Processor 02 failed Self Test (BIST)	Major
8172	Processor 03 failed Self Test (BIST)	Major
8173	Processor 04 failed Self Test (BIST)	Major
8180	Processor 01 microcode update not found	Minor
8181 8182	Processor 02 microcode update not found	Minor
8183	Processor 03 microcode update not found Processor 04 microcode update not found	Minor
8190	Watchdog timer failed on last boot	Minor Major
8198	OS boot watchdog timer failure	Major
8300	Baseboard management controller failed self-test	Major
8305	Hot Swap Controller failure	Major
83A0	Management Engine (ME) failed Selftest	Major
83A1	Management Engine (ME) Failed to respond.	Major
84F2	Baseboard management controller failed to respond	Major
84F3	Baseboard management controller in update mode	Major
84F4	Sensor data record empty	Major
84FF	System event log full	Minor
8500	Memory component could not be configured in the selected RAS mode	Major
8501	DIMM Population Error	Major
8520	DIMM_A1 failed test/initialization	Major
8521	DIMM_A2 failed test/initialization	Major
8522	DIMM_A3 failed test/initialization	Major
8523	DIMM_B1 failed test/initialization	Major
8524	DIMM_B2 failed test/initialization	Major
8525	DIMM_B3 failed test/initialization	Major
8526	DIMM_C1 failed test/initialization	Major
8527	DIMM_C2 failed test/initialization	Major
8528 8529	DIMM_C3 failed test/initialization	Major
852A	DIMM_D1 failed test/initialization	Major Major
852B	DIMM_D2 failed test/initialization DIMM_D3 failed test/initialization	Major
852C	DIMM E1 failed test/initialization	Major
852D	DIMM_E2 failed test/initialization	Major
852E	DIMM_E3 failed test/initialization	Major
852F	DIMM_F1 failed test/initialization	Major
8530	DIMM F2 failed test/initialization	Major
8531	DIMM_F3 failed test/initialization	Major
8532	DIMM_G1 failed test/initialization	Major
8533	DIMM_G2 failed test/initialization	Major
8534	DIMM_G3 failed test/initialization	Major
8535	DIMM_H1 failed test/initialization	Major
8536	DIMM_H2 failed test/initialization	Major
8537	DIMM_H3 failed test/initialization	Major
8538	DIMM_I1 failed test/initialization	Major
8539	DIMM_I2 failed test/initialization	Major
853A	DIMM_I3 failed test/initialization	Major
853B	DIMM_J1 failed test/initialization	Major
853C	DIMM_J2 failed test/initialization	Major
853D	DIMM_J3 failed test/initialization	Major
853E	DIMM_K1 failed test/initialization	Major
853F (Go to	DIMM_K2 failed test/initialization	Major
(Go to 85C0)		
8540	DIMM_A1 disabled	Major
8541	DIMM_A2 disabled	Major
55 11	Diritin_riz diodolog	iviajoi

Error Code	Error Mossogo	Doggongo
8542	Error Message DIMM A3 disabled	Response Major
8543	DIMM_B1 disabled	Major
8544	DIMM_B2 disabled	Major
8545	DIMM_B3 disabled	Major
8546	DIMM_C1 disabled	Major
8547	DIMM_C2 disabled	Major
8548	DIMM_C3 disabled	Major
8549	DIMM_D1 disabled	Major
854A	DIMM_D2 disabled	Major
854B	DIMM_D3 disabled	Major
854C	DIMM_E1 disabled	Major
854D	DIMM_E2 disabled	Major
854E	DIMM_E3 disabled	Major
854F	DIMM_F1 disabled	Major
8550	DIMM_F2 disabled	Major
8551	DIMM_F3 disabled	Major
8552	DIMM_G1 disabled	Major
8553 8554	DIMM_G2 disabled DIMM G3 disabled	Major Major
8555	DIMM_H1 disabled	Major
8556	DIMM_H2 disabled	Major
8557	DIMM_H3 disabled	Major
8558	DIMM_I1 disabled	Major
8559	DIMM_I2 disabled	Major
855A	DIMM_I3 disabled	Major
855B	DIMM_J1 disabled	Major
855C	DIMM_J2 disabled	Major
855D	DIMM_J3 disabled	Major
855E	DIMM_K1 disabled	Major
855F	DIMM_K2 disabled	Major
(Go to		
85D0) 8560	DIMM_A1 encountered a Serial Presence Detection (SPD) failure	Major
8561	DIMM_A2 encountered a Serial Presence Detection (SPD) failure	Major
8562	DIMM_A3 encountered a Serial Presence Detection (SPD) failure	Major
8563	DIMM_B1 encountered a Serial Presence Detection (SPD) failure	Major
8564	DIMM_B2 encountered a Serial Presence Detection (SPD) failure	Major
8565	DIMM_B3 encountered a Serial Presence Detection (SPD) failure	Major
8566	DIMM_C1 encountered a Serial Presence Detection (SPD) failure	Major
8567	DIMM_C2 encountered a Serial Presence Detection (SPD) failure	Major
8568	DIMM_C3 encountered a Serial Presence Detection (SPD) failure	Major
8569	DIMM_D1 encountered a Serial Presence Detection (SPD) failure	Major
856A	DIMM_D2 encountered a Serial Presence Detection (SPD) failure	Major
856B	DIMM_D3 encountered a Serial Presence Detection (SPD) failure	Major
856C	DIMM_E1 encountered a Serial Presence Detection (SPD) failure	Major
856D 856E	DIMM_E2 encountered a Serial Presence Detection (SPD) failure DIMM_E3 encountered a Serial Presence Detection (SPD) failure	Major Major
856F	DIMM_F1 encountered a Serial Presence Detection (SPD) failure	Major
8570	DIMM_F2 encountered a Serial Presence Detection (SPD) failure	Major
8571	DIMM_F3 encountered a Serial Presence Detection (SPD) failure	Major
8572	DIMM_G1 encountered a Serial Presence Detection (SPD) failure	Major
8573	DIMM_G2 encountered a Serial Presence Detection (SPD) failure	Major
8574	DIMM_G3 encountered a Serial Presence Detection (SPD) failure	Major
8575	DIMM_H1 encountered a Serial Presence Detection (SPD) failure	Major
8576	DIMM_H2 encountered a Serial Presence Detection (SPD) failure	Major
8577	DIMM_H3 encountered a Serial Presence Detection (SPD) failure	Major
8578	DIMM_I1 encountered a Serial Presence Detection (SPD) failure	Major
8579	DIMM_I2 encountered a Serial Presence Detection (SPD) failure	Major
857A	DIMM_I3 encountered a Serial Presence Detection (SPD) failure	Major
857B	DIMM_J1 encountered a Serial Presence Detection (SPD) failure	Major
857C 857D	DIMM_J2 encountered a Serial Presence Detection (SPD) failure	Major
0010	DIMM_J3 encountered a Serial Presence Detection (SPD) failure	Major

Error Code	Error Message	Response
857E	DIMM_K1 encountered a Serial Presence Detection (SPD) failure	Major
857F	DIMM_K2 encountered a Serial Presence Detection (SPD) failure	Major
(Go to		_
85E0)		
85C0	DIMM_K3 failed test/initialization	Major
85C1	DIMM_L1 failed test/initialization	Major
85C2 85C3	DIMM_L2 failed test/initialization	Major Major
85C4	DIMM_L3 failed test/initialization DIMM_M1 failed test/initialization	Major
85C5	DIMM_M2 failed test/initialization	Major
85C6	DIMM M3 failed test/initialization	Major
85C7	DIMM_N1 failed test/initialization	Major
85C8	DIMM_N2 failed test/initialization	Major
85C9	DIMM_N3 failed test/initialization	Major
85CA	DIMM_O1 failed test/initialization	Major
85CB	DIMM_O2 failed test/initialization	Major
85CC	DIMM_O3 failed test/initialization	Major
85CD	DIMM_P1 failed test/initialization	Major
85CE	DIMM_P2 failed test/initialization	Major
85CF 85D0	DIMM_P3 failed test/initialization	Major
85D0 85D1	DIMM_K3 disabled DIMM_L1 disabled	Major Major
85D1	DIMM_L2 disabled	Major
85D3	DIMM_L3 disabled	Major
85D4	DIMM_M1 disabled	Major
85D5	DIMM_M2 disabled	Major
85D6	DIMM_M3 disabled	Major
85D7	DIMM_N1 disabled	Major
85D8	DIMM_N2 disabled	Major
85D9	DIMM_N3 disabled	Major
85DA	DIMM_O1 disabled	Major
85DB	DIMM_O2 disabled	Major
85DC	DIMM_O3 disabled	Major
85DD	DIMM_P1 disabled	Major
85DE 85DF	DIMM_P2 disabled DIMM_P3 disabled	Major Major
85E0	DIMM_K3 encountered a Serial Presence Detection (SPD) failure	Major
85E1	DIMM_L1 encountered a Serial Presence Detection (SPD) failure	Major
85E2	DIMM_L2 encountered a Serial Presence Detection (SPD) failure	Major
85E3	DIMM_L3 encountered a Serial Presence Detection (SPD) failure	Major
85E4	DIMM_M1 encountered a Serial Presence Detection (SPD) failure	Major
85E5	DIMM_M2 encountered a Serial Presence Detection (SPD) failure	Major
85E6	DIMM_M3 encountered a Serial Presence Detection (SPD) failure	Major
85E7	DIMM_N1 encountered a Serial Presence Detection (SPD) failure	Major
85E8	DIMM_N2 encountered a Serial Presence Detection (SPD) failure	Major
85E9	DIMM_N3 encountered a Serial Presence Detection (SPD) failure	Major
85EA	DIMM_O1 encountered a Serial Presence Detection (SPD) failure	Major
85EB	DIMM_O2 encountered a Serial Presence Detection (SPD) failure	Major
85EC	DIMM_O3 encountered a Serial Presence Detection (SPD) failure	Major
85ED 85EE	DIMM_P1 encountered a Serial Presence Detection (SPD) failure DIMM_P2 encountered a Serial Presence Detection (SPD) failure	Major Major
85EF	DIMM_P3 encountered a Serial Presence Detection (SPD) failure	Major
8604	POST Reclaim of non-critical NVRAM variables	Minor
8605	BIOS Settings are corrupted	Major
8606	NVRAM variable space was corrupted and has been reinitialized	Major
92A3	Serial port component was not detected	Major
92A9	Serial port component encountered a resource conflict error	Major
A000	TPM device not detected.	Minor
A001	TPM device missing or not responding.	Minor
A002	TPM device failure.	Minor
A003	TPM device failed self test.	Minor
A100	BIOS ACM Error	Major

Intel® Server System R2000GZ/GL Product Family TPS

Error Code	Error Message	Response
A421	PCI component encountered a SERR error	Fatal
A5A0	PCI Express component encountered a PERR error	Minor
A5A1	PCI Express component encountered an SERR error	Fatal
A6A0	DXE Boot Service driver: Not enough memory available to shadow a Legacy Option ROM	Minor

POST Error Beep Codes

The following table lists the POST error beep codes. Prior to system video initialization, the BIOS uses these beep codes to inform users on error conditions. The beep code is followed by a user-visible code on the POST Progress LEDs

Table 41. POST Error Beep Codes

Beeps	Error Message	POST Progress Code	Description
1	USB device action	NA	Short beep sounded whenever a USB device is discovered
			in POST, or inserted or removed during runtime
1 long	Intel® TXT security	0xAE, 0xAF	System halted because Intel® Trusted Execution
	violation		Technology detected a potential violation of system
			security.
3	Memory error	See Tables 28 and 29	System halted because a fatal error related to the memory
			was detected.
2	BIOS Recovery	NA	Recovery boot has been initiated
	started		
4	BIOS Recovery	NA	BIOS recovery has failed. This typically happens so quickly
	failure		after recovery us initiated that it sounds like a 2-4 beep
			code.

The Integrated BMC may generate beep codes upon detection of failure conditions. Beep codes are sounded each time the problem is discovered, such as on each power-up attempt, but are not sounded continuously. Codes that are common across all Intel server boards and systems that use same generation chipset are listed in the following table. Each digit in the code is represented by a sequence of beeps whose count is equal to the digit.

Table 42. Integrated BMC Beep Codes

Code	Reason for Beep	Associated Sensors
1-5-2-1	No CPUs installed or first CPU socket is empty.	CPU1 socket is empty, or sockets are populated incorrectly CPU1 must be populated before CPU2.
1-5-2-4	MSID Mismatch	MSID mismatch occurs if a processor is installed into a system board that has incompatible power capabilities.
1-5-4-2	Power fault	DC power unexpectedly lost (power good dropout) – Power unit sensors report power unit failure offset
1-5-4-4	Power control fault (power good assertion timeout).	Power good assertion timeout – Power unit sensors report soft power control failure offset
1-5-1-2	VR Watchdog Timer sensor assertion	VR controller DC power on sequence was not completed in time.
1-5-1-4	Power Supply Status	The system does not power on or unexpectedly powers off and a Power Supply Unit (PSU) is present that is an incompatible model with one or more other PSUs in the system.

Appendix D: System Configuration Table for Thermal Compatibility

The following list reflects specified notes identified in the "Support Notes" column in the table. Each note reflects support criteria associated with a specific system configuration. Notes not specified in the table will reflect support criteria for a R1000GZ/GL Base System SKU described in the appropriate 1U Technical Product Specification.

Notes:

- 1. The 25°C configuration alone is limited to elevations of 900m or less
- 2. Use of the designated PCI slot is limited to add-in cards that have air flow requirements of 100 LFM or less. See add-in card specs for air flow requirements.
- 3. Base system SKUs R2312GZ/GL#### and R2224GZ/GL####, configured with the following IO modules: AXX10GBTWLIOM, AXX2FDRIBIOM, and AXX1FDRIBIOM, can only be supported when DRx8 DIMMs are used.
- 4. Systems configured with E5-2643(130W-4C), E5-2690(135W-8C), E5-2637 v2(130W-4C) and E5-2643 v2(130W-6C) processors may experience CPU and or memory throttling, impacting system performance.
- 5. Processor throttling may occur with a system fan failure which may impact system performance.
- 6. Specifically for A3/A4 individual Power Supply selection power margin is required to meet thermal specifications:
 - a) For dual power supply configuration, the power budget must fit within single power supply rated load
 - b) For single power supply configuration, the power budget must be sized with 30% margin to single power supply rated load.
- 7. Intel® Xeon Phi™ or non-Intel GPGPU cards may have performance impact during ambient excursions
- 8. When identifying memory in the table, only Rank and Width are required. Capacity is not required.
- 9. LV refers to low voltage DIMMs (1.35V)
- 10. Installation of the AXXRMFBU2 in a 2U system will also require installation of Intel mounting bracket A2UBKTMFBUSSD. The Cache offload Module can only be installed with 95W processor and DRx8 or equivalent memory for HTA A3/A4 with R2312GZ/GL and R2224GZ/GL System.
- 11. Confirm the case temperature specification for the SSD to make appropriate selection
- 12. Fan fail of dual-rotor fans refers to one rotor fail. "Fan Fail Support" indicates if fan fail can be supported with specified configuration in each column.
- 13. System must have contents from the Intel accessory kit AGZCOPRODUCT installed to support Intel® Xeon Phi[™] or non-Intel GPGPU add-in cards with passive cooling solutions. Only systems configured with S2600GZ (24-DIMM) server board will be supported.
- 14. Fan redundancy is not supported in systems configured with 130W-4 Core and 135W-8 Core processors
- 15. Due to thermal specification availability restrictions, Intel has NOT verified the thermal compatibility of non-Intel GPGPU cards that utilize a passive cooling solution in its server systems. System integrators should verify non-Intel add-in card air flow requirements from available vendor specifications before integrating them into the system. See section 5.2 for air flow support limits.

		Base System SKUs: R2312GZ/GL			Base System SKUs: R2224GZ/GL				Ва		tem SKI	Js:	Ва	ase Syst	em SKI	Js:		Base R2	Support NOTES				
ASHRAE	Classifications		A2	А3	A4	25° C	A2	А3	A4	25° C	A2	А3	A4	25° C	A2	А3	A4	25° C	A2	A2	А3	A4	Note 1
	Max Ambient	25° C	35° C	40° C	45° C	25° C	35° C	40° C	45° C	25° C	35° C	40° C	45° C	25° C	35° C	40° C	45° C	25° C	35° C	35° C	40° C	45° C	
Cooling	Redundant Fan Configuration	•	•	•	•	•	•	•	•	•	•	•	•	•	•	•	•	•	•	•	•	•	
Cooling	Redundancy Available	•	•			•	•			•	•			•	•			•		•			
	EP, 60w, 6C (E5-2630L)	•	•			•	•			•	•	•	•	•	•	•	•	•	•	•	•	•	Note 5
	EP, 70w, 8C (E5-2650L)	•	•	•		•	•	•		•	•	•	•	•	•	•	•	•	•	•	•	•	Note 5
	EP, 95w, 6C (E5-2620 , E5-2630, E5- 2640)	•	•	•	•	•	•	•	•	•	•	•	•	•	•	•	•	•	•	•	•	•	Note 5
	EP, 95w, 8C (E5-2650, E5-2660)	•	•	•	•	•	•	•	•	•	•	•	•	•	•	•	•	•	•	•	•	•	Note 5
Intel® Xeon® processor E5-	EP, 115w, 8C (E5-2665, E5-2670)	•	•	•		•	•	•		•	•	•	•	•	•	•	•	•	•	•	•	•	Note 5
2600 product	EP, 130w, 6C (E5-2667)	•	•			•	•			•	•	•		•	•	•		•	•	•	•		Note 5
family	EP, 130w, 8C (E5-2680)	•	•			•	•			•	•	•		•	•	•		•	•	•	•		Note 5
	EP, 135w, 8C (E5-2690)	•				•	•			•	•			•	•			•	•	•			Notes 4,5
	EP, 80w, 2C (E5-2637)	•	•	•	•	•	•	•	•	•	•	•	•	•	•	•	•	•	•	•	•	•	Note 5
	EP, 80w, 4C (E5-2603, E5-2609)	•	•	•	•	•	•	•	•	•	•	•	•	•	•	•	•	•	•	•	•	•	Note 5
	EP, 130w, 4C (E5-2643)	•				•	•			•	•			•	•			•	•	•			Notes 4,5
	EP, 60W, 6C, (E5-2630L v2)	•	•			•	•			•	•	•	•	•	•	•	•	•	•	•	•	•	Note 5
	EP, 70W, 10C, (E5-2650L v2)	•	•	•		•	•	•		•	•	•	•	•	•	•	•	•	•	•	•	•	Note 5
	EP, 80W, 4C, (E5-2603 v2, E5-2609 v2)	•	•	•	•	•	•	•	•	•	•	•	•	•	•	•	•	•	•	•	•	•	Note 5
	EP, 80W, 6C, (E5-2620 v2, E5-2630 v2)	•	•	•	•	•	•	•	•	•	•	•	•	•	•	•	•	•	•	•	•	•	Note 5
	EP, 95w, 8C, (E5-2640 v2, E5-2650 v2)	•	•	•	•	•	•	•	•	•	•	•	•	•	•	•	•	•	•	•	•	•	Note 5
Intel® Xeon®	EP, 95W. 10C, (E5-2660 v2)	•	•	•	•	•	•	•	•	•	•	•	•	•	•	•	•	•	•	•	•	•	Note 5
processor E5- 2600 v2	EP, 115W, 10C, (E5-2670 v2, E5-2680 v2)	•	•	•		•	•	•		•	•	•	•	•	•	•	•	•	•	•	•	•	Note 5
product family	EP, 115W, 12C, (E5-2695 v2)	•	•	•		•	•	•		•	•	•	•	•	•	•	•	•	•	•	•	•	Note 5
	EP, 130W, 10C, (E5-2690 v2)	•	•			•	•			•	•	•		•	•	•		•	•	•	•		Note 5
	EP, 130W, 4C, (E5-2637 v2)	•				•	•			•	•			•	•			•	•	•			Notes 4,5
	EP, 130W, 6C, (E5-2643 v2)	•				•	•			•	•			•	•			•	•	•			Notes 4,5
	EP, 130W, 8C, (E5-2667 v2)	•	•			•	•			•	•	•		•	•	•		•	•	•	•		Note 5
	EP, 130W, 12C, (E5-2697 v2)	•	•			•	•			•	•	•		•	•	•		•	•	•	•		Note 5
	Dual Rank x8	•	•	•	•	•	•	•	•	•	•	•	•	•	•	•	•	•	•	•	•	•	
	Dual Rank x4	•	•			•	•			•	•	•	•	•	•	•	•	•	•	•	•	•	
Memory Type	Quad Rank x8	•	•			•	•			•	•	•	•	•	•	•	•	•	•	•	•	•	
	Quad Rank x4	•	•			•	•			•	•			•	•			•	•	•			
	Load Reduced DIMM	•				•	•			•	•			•	•			•	•	•			

		Base System SKUs: R2312GZ/GL				Base System SKUs: R2224GZ/GL				Ва		tem SKl 6 GZ/GL	Js:	Ва	ase Syst R2308	em SKl B GZ/GL			Base R2	Support NOTES			
ASHRAE	Classifications	25° C	A2	А3	A4	25° C	A2	А3	A4	25° C	A2	А3	A4	25° C	A2	А3	A4	25° C	A2	A2	А3	A4	Note 1
	Max Ambient	25° C	35° C	40° C	45° C	25° C	35° C	40° C	45° C	25° C	35° C	40° C	45° C	25° C	35° C	40° C	45° C	25° C	35° C	35° C	40° C	45° C	
	Riser #1 - Bottom Slot (1U riser and 2U riser)	•		•	•			•	•				•	•		•						•	
	Riser #1 - Middle Slot (2U riser)	•	•	•	•	•		•	•				•	•		•		•				•	Note 2
	Riser #1 - Top Slot (2U riser)	•	•	•	•	•	•	•	•	•	•	•	•	•	•	•	•	•	•	•	•	•	Note 2
Add-in Cards	Riser #2 - Bottom Slot (1U riser and 2U riser)	•	•	•	•	•	•	•	•	•	•	•	•	•	•	•	•		•	•	•	•	11010 2
	Riser #2 - Middle Slot (2U riser)	•	•	•	•	•	•	•	•	•	•	•	•	•	•	•	•	•	•	•	•	•	
	Riser #2 - Top Slot (2U riser)	•	•	•	•	•	•	•	•	•	•	•	•	•	•	•	•	•	•	•	•	•	
	Intel® Integrated RAID Modules (Mezzanine cards)	•	•	•	•	•		•	•			•	•	•	•	•	•		•	•	•	•	
	AXX10GBTWLIOM - Dual 10GBASE-T IO Module	•				•						•	•	•		•	•				•	•	Note 3
	AXX10GBNIAIOM - Dual SFP+ port						Ť			Ť								Ť					Note 5
I/O Modules	10GbE IO Module	•	•	•	•	•	•	•	•	•	•	•	•	•	•	•	•	•	•	•	•	•	
	AXX1FDRIBIOM - Single Port FDR Infiniband IO Module	•	•	•	•	•	•	•	•	•	•	•	•	•	•	•	•	•	•	•	•	•	
	AXX2FDRIBIOM - Dual Port FDR											_	_	_		_							Note 0
	Infiniband IO Module AXX4P1GBPWLIOM - Quad Port 1GbE	•				•	•			•	•	•	•	•	•	•	•	•	•	•	•	•	Note 3
	IO Module	•	•	•	•	•	•	•	•	•	•	•	•	•	•	•	•	•	•	•	•	•	
	Intel® Xeon Phi™ or GPGPU wActive Cooling up to 300W									•				•				•	•		•		Note 7
	Intel® Xeon Phi™ or GPGPU w/Active Cooling up to 225W													•				•	•		•		Note 7
	Intel® Xeon Phi™ w/Passive Cooling up to 225W													•	•	•	•	•	•	•	•	•	Notes 7,13
Intel® Xeon Phi™ or Non-	Intel® Xeon Phi™ w/Passive Cooling up to 245W													•									Notes 7,13
Intel GPGPU	Intel® Xeon Phi™ w/Passive Cooling up to 300W																						Notes 7,13
	Non-Intel GPGPU w/Passive Cooling up to 75W													•									Note 7
	Non-Intel GPGPU w/Passive Cooling > 75W	I												•	•				•	•			Notes 7,13,
RAID Battery Backup	BBU (rated to 45C)	•	•			•	•			•	•			•	•			•	•	•			
	Supercap (rated to 55C)	•	•	•	•	•	•	•	•	•	•	•	•	•	•	•	•	•	•	•	•	•	
	Cache Offload Module (rated to 55C)	•	•	•	•	•	•	•	•	•	•	•	•	•	•	•	•	•	•	•	•	•	Note 10
Internal SSD	Rated to 60C	•				•				•				•				•					
	Rated to 70C	•	•			•	•			•	•			•	•			•	•	•			<u></u>

Glossary

ACA Australian Communication Authority ANSI American National Standards Institute BMC Baseboard Management Controller BIOS Basic Input/Output System CMOS Complementary Metal-oxide-semiconductor DZD DC-to-DC EMP Emergency Management Port FP Front Panel FRB Fault Resilient Boot FRU Field Replaceable Unit Ir'C Inter-integrated Circuit bus LCD Liquid Crystal Display LPC Low-pin Count LSB Least Significant Bit MSB Most Significant Bit MSB Most Significant Bit MTBF Mean Time Between Failure MTTR Mean Time to Repair NIC Network Interface Card NMI Non-maskable Interrupt OTP Over-temperature Protection OVP Over-voltage Protection PCI Peripheral Component Interconnect PCB Printed Circuit Board PCIe* Peripheral Component Interconnect Express* PCI-X Peripheral Component Interconnect Extended PFC Power-on Self Test PSU Power Supply Unit RAM Random Access Memory RI Ring Indicate SCA Single Connector Attachment SDR Sensor Data Record SE Single-Ended THD Total Harmonic Distortion UART Universal Asynchronous Receiver Transmitter USB Universal Serial Bus Voltage Standby Voltage Standby Voltage Standby Voltage Standby Voltage Standby	Word/Acronym	Definition
BMC Baseboard Management Controller BIOS Basic Input/Output System CMOS Complementary Metal-oxide-semiconductor D2D DC-to-DC EMP Emergency Management Port FP Front Panel FRB Fault Resilient Boot FRU Field Replaceable Unit I*C Inter-integrated Circuit bus LCD Liquid Crystal Display LPC Low-pin Count LSB Least Significant Bit MSB Most Significant Bit MSB Most Significant Bit MTBF Mean Time Between Failure MTTR Mean Time to Repair NIC Network Interface Card NMI Non-maskable Interrupt OTP Over-temperature Protection OVP Over-voltage Protection PCI Peripheral Component Interconnect PCB Printed Circuit Board PCIe* Peripheral Component Interconnect Express* PCI-X Peripheral Component Interconnect Extended PFC Power Factor Correction POST Power- Self Test PSU Power Supply Unit RAM Random Access Memory RI Ring Indicate SCA Single-Ended THD Total Harmonic Distortion UART Universal Serial Bus VCCI Voluntary Control Council for Interference	ACA	Australian Communication Authority
BIOS Basic Input/Output System CMOS Complementary Metal-oxide-semiconductor D2D DC-to-DC EMP Emergency Management Port FP Front Panel FRB Fault Resilient Boot FRU Field Replaceable Unit I'C Inter-integrated Circuit bus LCD Liquid Crystal Display LPC Low-pin Count LSB Least Significant Bit MSB Most Significant Bit MSB Most Significant Bit MTBF Mean Time Between Failure MTTR Mean Time to Repair NIC Network Interface Card NMI Non-maskable Interrupt OTP Over-temperature Protection OVP Over-voltage Protection PCI Peripheral Component Interconnect Express* PCI-X Peripheral Component Interconnect Extended PFC Power Factor Correction POST Power- on Self Test PSU Power Supply Unit RAM Random Access Memory RI Ring Indicate SCA Single-Ended THD Total Harmonic Distortion UNRI Universal Asynchronous Receiver Transmitter USB Universal Serial Bus VCCI Voluntary Control Council for Interference	ANSI	American National Standards Institute
CMOS Complementary Metal-oxide-semiconductor D2D DC-to-DC EMP Emergency Management Port FP Front Panel FRB Fault Resilient Boot FRU Field Replaceable Unit i'C Inter-integrated Circuit bus LCD Liquid Crystal Display LPC Low-pin Count LSB Least Significant Bit MSB Most Significant Bit MTBF Mean Time Between Failure MTTR Mean Time to Repair NIC Network Interface Card NMI Non-maskable Interrupt OTP Over-temperature Protection OVP Over-voltage Protection PCI Peripheral Component Interconnect Express* PCI-X Peripheral Component Interconnect Extended PFC Power Factor Correction POST Power- Os Self Test PSU Power Supply Unit RAM Random Access Memory RI Ring Indicate SCA Single-Ended THD Total Harmonic Distortion UNRI Universal Serial Bus VCCI Voluntary Control Council for Interference	BMC	Baseboard Management Controller
D2D DC-to-DC EMP Emergency Management Port FP Front Panel FRB Fault Resilient Boot FRU Field Replaceable Unit I'C Inter-integrated Circuit bus LCD Liquid Crystal Display LPC Low-pin Count LSB Least Significant Bit MSB Most Significant Bit MSB Most Significant Bit MTBF Mean Time Between Failure MTTR Mean Time to Repair NIC Network Interface Card NMI Non-maskable Interrupt OTP Over-temperature Protection OVP Over-voltage Protection PCI Peripheral Component Interconnect PCB Printed Circuit Board PCI-X Peripheral Component Interconnect Extended PFC Power Factor Correction POST Power-on Self Test PSU Power-on Self Test PSU Power Supply Unit RAM Random Access Memory RI Ring Indicate SCA Single Connector Attachment SDR Sensor Data Record SE Single-Ended THD Total Harmonic Distortion USB Universal Serial Bus VCCI Voluntary Control Council for Interference	BIOS	Basic Input/Output System
EMP Emergency Management Port FP Front Panel FRB Fault Resilient Boot FRU Field Replaceable Unit I'C Inter-integrated Circuit bus LCD Liquid Crystal Display LPC Low-pin Count LSB Least Significant Bit MSB Most Significant Bit MTBF Mean Time Between Failure MTTR Mean Time to Repair NIC Network Interface Card NMI Non-maskable Interrupt OTP Over-temperature Protection OVP Over-voltage Protection PCI Peripheral Component Interconnect PCB Printed Circuit Board PCI-X Peripheral Component Interconnect Extended PFC Power Factor Correction POST Power-on Self Test PSU Power Supply Unit RAM Random Access Memory RI Ring Indicate SCA Single Connector Attachment SDR Sensor Data Record SE Single-Ended THD Total Harmonic Distortion UNB Universal Serial Bus VCCI Voluntary Control Council for Interference	CMOS	Complementary Metal-oxide-semiconductor
FP Front Panel FRB Fault Resilient Boot FRU Field Replaceable Unit If C Inter-integrated Circuit bus LCD Liquid Crystal Display LPC Low-pin Count LSB Least Significant Bit MSB Most Significant Bit MTBF Mean Time Between Failure MTTR Mean Time to Repair NIC Network Interface Card NMI Non-maskable Interrupt OTP Over-temperature Protection OVP Over-voltage Protection PCI Peripheral Component Interconnect PCB Printed Circuit Board PCI-X Peripheral Component Interconnect Extended PFC Power Factor Correction POST Power-on Self Test PSU Power Supply Unit RAM Random Access Memory RI Ring Indicate SCA Single-Connector Attachment SDR Sensor Data Record SE Single-Ended Universal Serial Bus VCCI Voluntary Control Council for Interference	D2D	DC-to-DC
FRB Fault Resilient Boot FRU Field Replaceable Unit FC Inter-integrated Circuit bus LCD Liquid Crystal Display LPC Low-pin Count LSB Least Significant Bit MSB Most Significant Bit MTBF Mean Time Between Failure MTTR Mean Time to Repair NIC Network Interface Card NMI Non-maskable Interrupt OTP Over-temperature Protection OVP Over-voltage Protection PCI Peripheral Component Interconnect PCB Printed Circuit Board PCI-X Peripheral Component Interconnect Extended PFC Power Factor Correction PFCI Power- Supply Unit RAM Random Access Memory RI Ring Indicate SCA Single Connector Attachment SDR Sensor Data Record SE Single-Ended Universal Serial Bus VCCI Voluntary Control Council for Interference	EMP	Emergency Management Port
FRU Field Replaceable Unit I'C Inter-integrated Circuit bus LCD Liquid Crystal Display LPC Low-pin Count LSB Least Significant Bit MSB Most Significant Bit MTBF Mean Time Between Failure MTTR Mean Time to Repair NIC Network Interface Card NMI Non-maskable Interrupt OTP Over-temperature Protection OVP Over-voltage Protection PCI Peripheral Component Interconnect PCB Printed Circuit Board PCI-X Peripheral Component Interconnect Extended PFC Power Factor Correction POST Power-on Self Test PSU Power Supply Unit RAM Random Access Memory RI Ring Indicate SCA Single Connector Attachment SDR Sensor Data Record SE Single-Ended Universal Serial Bus VCCI Voluntary Control Council for Interference	FP	Front Panel
Irc Inter-integrated Circuit bus LCD Liquid Crystal Display LPC Low-pin Count LSB Least Significant Bit MSB Most Significant Bit MTBF Mean Time Between Failure MTTR Mean Time to Repair NIC Network Interface Card NMI Non-maskable Interrupt OTP Over-temperature Protection OVP Over-voltage Protection PCI Peripheral Component Interconnect PCB Printed Circuit Board PCI-X Peripheral Component Interconnect Express* PCI-X Peripheral Component Interconnect Extended PFC Power Factor Correction POST Power-on Self Test PSU Power Supply Unit RAM Random Access Memory RI Ring Indicate SCA Single Connector Attachment SDR Sensor Data Record SE Single-Ended THD Total Harmonic Distortion UART Universal Asynchronous Receiver Transmitter USB Universal Serial Bus VCCI Voluntary Control Council for Interference	FRB	Fault Resilient Boot
LCD Liquid Crystal Display LPC Low-pin Count LSB Least Significant Bit MSB Most Significant Bit MTBF Mean Time Between Failure MTTR Mean Time to Repair NIC Network Interface Card NMI Non-maskable Interrupt OTP Over-temperature Protection OVP Over-voltage Protection PCI Peripheral Component Interconnect PCB Printed Circuit Board PCI-X Peripheral Component Interconnect Express* PCI-X Peripheral Component Interconnect Extended PFC Power Factor Correction POST Power-on Self Test PSU Power Supply Unit RAM Random Access Memory RI Ring Indicate SCA Single Connector Attachment SDR Sensor Data Record SE Single-Ended THD Total Harmonic Distortion UART Universal Serial Bus VCCI Voluntary Control Council for Interference	FRU	Field Replaceable Unit
LPC Low-pin Count LSB Least Significant Bit MSB Most Significant Bit MTBF Mean Time Between Failure MTTR Mean Time to Repair NIC Network Interface Card NMI Non-maskable Interrupt OTP Over-temperature Protection OVP Over-voltage Protection PCI Peripheral Component Interconnect PCI-X Peripheral Component Interconnect Express* PCI-X Peripheral Component Interconnect Extended PFC Power Factor Correction POST Power-on Self Test PSU Power Supply Unit RAM Random Access Memory RI Ring Indicate SCA Single Connector Attachment SDR Sensor Data Record SE Single-Ended THD Total Harmonic Distortion UART Universal Serial Bus VCCI Voluntary Control Council for Interference	I ² C	Inter-integrated Circuit bus
LESB Least Significant Bit MSB Most Significant Bit MTBF Mean Time Between Failure MTTR Mean Time to Repair NIC Network Interface Card NMI Non-maskable Interrupt OTP Over-temperature Protection OVP Over-voltage Protection PCI Peripheral Component Interconnect PCB Printed Circuit Board PCIe* Peripheral Component Interconnect Express* PCI-X Peripheral Component Interconnect Extended PFC Power Factor Correction POST Power-on Self Test PSU Power Supply Unit RAM Random Access Memory RI Ring Indicate SCA Single Connector Attachment SDR Sensor Data Record SE Single-Ended THD Total Harmonic Distortion UART Universal Asynchronous Receiver Transmitter USB Universal Serial Bus VCCI Voluntary Control Council for Interference	LCD	Liquid Crystal Display
MSB Most Significant Bit MTBF Mean Time Between Failure MTTR Mean Time to Repair NIC Network Interface Card NMI Non-maskable Interrupt OTP Over-temperature Protection OVP Over-voltage Protection PCI Peripheral Component Interconnect PCB Printed Circuit Board PCle* Peripheral Component Interconnect Express* PCI-X Peripheral Component Interconnect Extended PFC Power Factor Correction POST Power-on Self Test PSU Power Supply Unit RAM Random Access Memory RI Ring Indicate SCA Single Connector Attachment SDR Sensor Data Record SE Single-Ended THD Total Harmonic Distortion UART Universal Asynchronous Receiver Transmitter USB Universal Serial Bus VCCI Voluntary Control Council for Interference	LPC	Low-pin Count
MTBF Mean Time Between Failure MTTR Mean Time to Repair NIC Network Interface Card NMI Non-maskable Interrupt OTP Over-temperature Protection OVP Over-voltage Protection PCI Peripheral Component Interconnect PCB Printed Circuit Board PCI-X Peripheral Component Interconnect Express* PCI-X Peripheral Component Interconnect Extended PFC Power Factor Correction POST Power-on Self Test PSU Power Supply Unit RAM Random Access Memory RI Ring Indicate SCA Single Connector Attachment SDR Sensor Data Record SE Single-Ended THD Total Harmonic Distortion UART Universal Asynchronous Receiver Transmitter USB Universal Serial Bus VCCI Voluntary Control Council for Interference	LSB	Least Significant Bit
MTTR Mean Time to Repair NIC Network Interface Card NMI Non-maskable Interrupt OTP Over-temperature Protection OVP Over-voltage Protection PCI Peripheral Component Interconnect PCB Printed Circuit Board PCI-X Peripheral Component Interconnect Express* PCI-X Peripheral Component Interconnect Extended PFC Power Factor Correction POST Power-on Self Test PSU Power Supply Unit RAM Random Access Memory RI Ring Indicate SCA Single Connector Attachment SDR Sensor Data Record SE Single-Ended THD Total Harmonic Distortion UART Universal Serial Bus VCCI Voluntary Control Council for Interference	MSB	Most Significant Bit
NIC Network Interface Card NMI Non-maskable Interrupt OTP Over-temperature Protection OVP Over-voltage Protection PCI Peripheral Component Interconnect PCB Printed Circuit Board PCle* Peripheral Component Interconnect Express* PCI-X Peripheral Component Interconnect Extended PFC Power Factor Correction POST Power-on Self Test PSU Power Supply Unit RAM Random Access Memory RI Ring Indicate SCA Single Connector Attachment SDR Sensor Data Record SE Single-Ended THD Total Harmonic Distortion UART Universal Asynchronous Receiver Transmitter USB Universal Serial Bus VCCI Voluntary Control Council for Interference	MTBF	Mean Time Between Failure
NMI Non-maskable Interrupt OTP Over-temperature Protection OVP Over-voltage Protection PCI Peripheral Component Interconnect PCB Printed Circuit Board PCIe* Peripheral Component Interconnect Express* PCI-X Peripheral Component Interconnect Extended PFC Power Factor Correction POST Power-on Self Test PSU Power Supply Unit RAM Random Access Memory RI Ring Indicate SCA Single Connector Attachment SDR Sensor Data Record SE Single-Ended THD Total Harmonic Distortion UART Universal Asynchronous Receiver Transmitter USB Universal Serial Bus VCCI Voluntary Control Council for Interference	MTTR	Mean Time to Repair
OTP Over-temperature Protection OVP Over-voltage Protection PCI Peripheral Component Interconnect PCB Printed Circuit Board PCIe* Peripheral Component Interconnect Express* PCI-X Peripheral Component Interconnect Extended PFC Power Factor Correction POST Power-on Self Test PSU Power Supply Unit RAM Random Access Memory RI Ring Indicate SCA Single Connector Attachment SDR Sensor Data Record SE Single-Ended THD Total Harmonic Distortion UART Universal Asynchronous Receiver Transmitter USB Universal Serial Bus VCCI Voluntary Control Council for Interference	NIC	Network Interface Card
OVP Over-voltage Protection PCI Peripheral Component Interconnect PCB Printed Circuit Board PCIe* Peripheral Component Interconnect Express* PCI-X Peripheral Component Interconnect Extended PFC Power Factor Correction POST Power-on Self Test PSU Power Supply Unit RAM Random Access Memory RI Ring Indicate SCA Single Connector Attachment SDR Sensor Data Record SE Single-Ended THD Total Harmonic Distortion UART Universal Asynchronous Receiver Transmitter USB Universal Serial Bus VCCI Voluntary Control Council for Interference	NMI	Non-maskable Interrupt
PCI Peripheral Component Interconnect PCB Printed Circuit Board PCIe* Peripheral Component Interconnect Express* PCI-X Peripheral Component Interconnect Extended PFC Power Factor Correction POST Power-on Self Test PSU Power Supply Unit RAM Random Access Memory RI Ring Indicate SCA Single Connector Attachment SDR Sensor Data Record SE Single-Ended THD Total Harmonic Distortion UART Universal Asynchronous Receiver Transmitter USB Universal Serial Bus VCCI Voluntary Control Council for Interference	OTP	Over-temperature Protection
PCB Printed Circuit Board PCIe* Peripheral Component Interconnect Express* PCI-X Peripheral Component Interconnect Extended PFC Power Factor Correction POST Power-on Self Test PSU Power Supply Unit RAM Random Access Memory RI Ring Indicate SCA Single Connector Attachment SDR Sensor Data Record SE Single-Ended THD Total Harmonic Distortion UART Universal Asynchronous Receiver Transmitter USB Universal Serial Bus VCCI Voluntary Control Council for Interference	OVP	Over-voltage Protection
PCIe* Peripheral Component Interconnect Express* PCI-X Peripheral Component Interconnect Extended PFC Power Factor Correction POST Power-on Self Test PSU Power Supply Unit RAM Random Access Memory RI Ring Indicate SCA Single Connector Attachment SDR Sensor Data Record SE Single-Ended THD Total Harmonic Distortion UART Universal Asynchronous Receiver Transmitter USB Universal Serial Bus VCCI Voluntary Control Council for Interference	PCI	Peripheral Component Interconnect
PCI-X Peripheral Component Interconnect Extended PFC Power Factor Correction POST Power-on Self Test PSU Power Supply Unit RAM Random Access Memory RI Ring Indicate SCA Single Connector Attachment SDR Sensor Data Record SE Single-Ended THD Total Harmonic Distortion UART Universal Asynchronous Receiver Transmitter USB Universal Serial Bus VCCI Voluntary Control Council for Interference	PCB	Printed Circuit Board
PFC Power Factor Correction POST Power-on Self Test PSU Power Supply Unit RAM Random Access Memory RI Ring Indicate SCA Single Connector Attachment SDR Sensor Data Record SE Single-Ended THD Total Harmonic Distortion UART Universal Asynchronous Receiver Transmitter USB Universal Serial Bus VCCI Voluntary Control Council for Interference	PCIe*	Peripheral Component Interconnect Express*
POST Power-on Self Test PSU Power Supply Unit RAM Random Access Memory RI Ring Indicate SCA Single Connector Attachment SDR Sensor Data Record SE Single-Ended THD Total Harmonic Distortion UART Universal Asynchronous Receiver Transmitter USB Universal Serial Bus VCCI Voluntary Control Council for Interference	PCI-X	Peripheral Component Interconnect Extended
PSU Power Supply Unit RAM Random Access Memory RI Ring Indicate SCA Single Connector Attachment SDR Sensor Data Record SE Single-Ended THD Total Harmonic Distortion UART Universal Asynchronous Receiver Transmitter USB Universal Serial Bus VCCI Voluntary Control Council for Interference	PFC	Power Factor Correction
RAM Random Access Memory RI Ring Indicate SCA Single Connector Attachment SDR Sensor Data Record SE Single-Ended THD Total Harmonic Distortion UART Universal Asynchronous Receiver Transmitter USB Universal Serial Bus VCCI Voluntary Control Council for Interference	POST	Power-on Self Test
RI Ring Indicate SCA Single Connector Attachment SDR Sensor Data Record SE Single-Ended THD Total Harmonic Distortion UART Universal Asynchronous Receiver Transmitter USB Universal Serial Bus VCCI Voluntary Control Council for Interference	PSU	Power Supply Unit
SCA Single Connector Attachment SDR Sensor Data Record SE Single-Ended THD Total Harmonic Distortion UART Universal Asynchronous Receiver Transmitter USB Universal Serial Bus VCCI Voluntary Control Council for Interference	RAM	Random Access Memory
SDR Sensor Data Record SE Single-Ended THD Total Harmonic Distortion UART Universal Asynchronous Receiver Transmitter USB Universal Serial Bus VCCI Voluntary Control Council for Interference		
SE Single-Ended THD Total Harmonic Distortion UART Universal Asynchronous Receiver Transmitter USB Universal Serial Bus VCCI Voluntary Control Council for Interference	SCA	Single Connector Attachment
THD Total Harmonic Distortion UART Universal Asynchronous Receiver Transmitter USB Universal Serial Bus VCCI Voluntary Control Council for Interference	SDR	Sensor Data Record
UART Universal Asynchronous Receiver Transmitter USB Universal Serial Bus VCCI Voluntary Control Council for Interference		Single-Ended
USB Universal Serial Bus VCCI Voluntary Control Council for Interference	THD	Total Harmonic Distortion
VCCI Voluntary Control Council for Interference	UART	Universal Asynchronous Receiver Transmitter
·	USB	Universal Serial Bus
VSB Voltage Standby	VCCI	Voluntary Control Council for Interference
	VSB	Voltage Standby

Reference Documents

See the following documents for additional information:

- Intel[®] Server Board S2600GZ/GL Technical Product Specification
- Intel® Server Board S2600GZ/GL Product Family Spares/Parts List and Configuration Guide
- Intel® Server System R2000GZ/GL Service Guide
- Intel[®] Server System R2000GZ/GL Quick Installation Guide
- Intel® S2600GZGL Product Family Power Budget and Thermal Configuration Tool
- BIOS for Platforms Based on Intel® Xeon Processor E5-4600/2600/2400/1600 Product Families External Product Specification
- Platforms Based On Intel Xeon® Processor E5 4600/2600/2400/1600 Product Families BMC Core Firmware External Product Specification
- Intel® Remote Management Module 4 Technical Product Specification
- Intel® Remote Management Module 4 and Integrated BMC Web Console Users Guide
- Intel[®] Server Board S2600GZGL, Intel[®] Server System R1000GZGL, Intel[®] Server System R2000GZGL Monthly Specification Update
- Intel Integrated RAID Module RMS25PB040, RMS25PB080, RMS25CB040, and RMS25CB080 Hardware / Installation Users Guide
- Intel Integrated RAID Module RMT3PB080 and RMT3CB080 Hardware / Installation Users Guide
- Intel Integrated RAID Module RMS25KB040, RMS25KB080, RMS25JB040, and RMS25JB080 Hardware / Installation Users Guide
- Intel® Raid Maintenance Free Backup Unit AXXRMFBU2 User's Guide