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The document following this cover page is marked as "Cypress" document as this is the company that originally developed the product. Please note that Infineon will continue to offer the product to new and existing customers as part of the Infineon product portfolio.
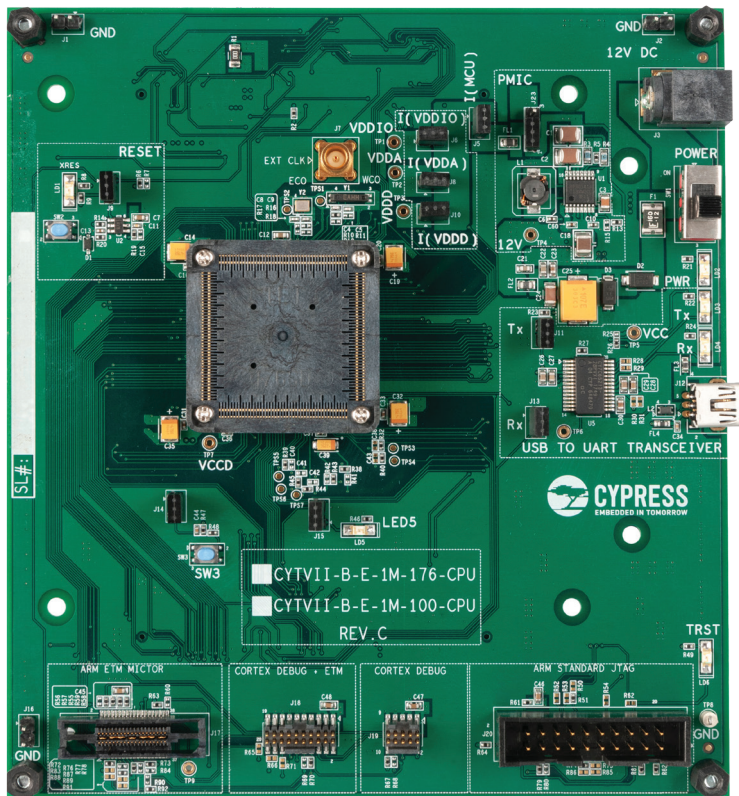
Continuity of document content

The fact that Infineon offers the following product as part of the Infineon product portfolio does not lead to any changes to this document. Future revisions will occur when appropriate, and any changes will be set out on the document history page.

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Infineon continues to support existing part numbers. Please continue to use the ordering part numbers listed in the datasheet for ordering.
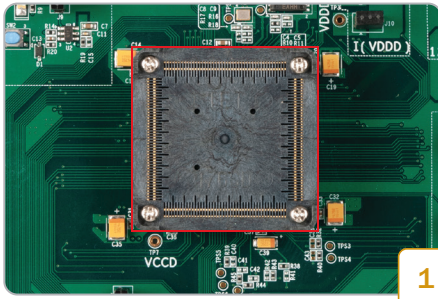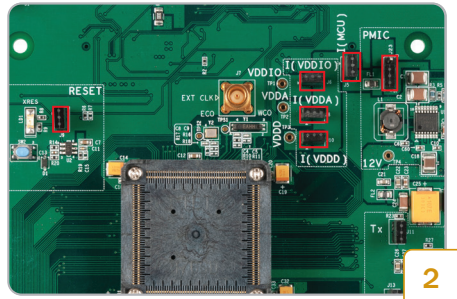
TRAVEO II CPU BOARD

Kit Contents:

1. Traveo II CPU Board CYTVII-B-E-176-SO REV.C
2. Type-B Mini USB Cable
3. 12 V AC-DC Universal Power Adapter
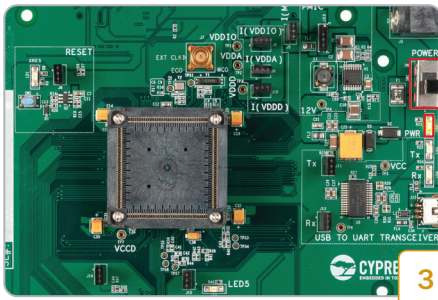4. Quick Start Guide (this document)
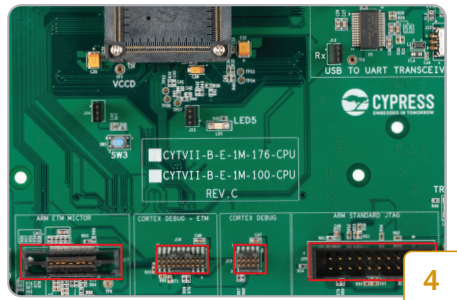
TRAVEO II CPU BOARD

1

- Insert a Traveo II Device into the IC socket (U4), align Pin 1 with the arrow mark near C16 and close the socket cover.

2

- Ensure that jumpers J23(Pin 1 & 2), J5, J6, J8, J9, and J10, are shorted. Connect the 12V adapter to the Power Jack J3.

3

- Toggle the switch SW1 to the upper position, to turn ON the board. The LED marked "PWR" should light up.
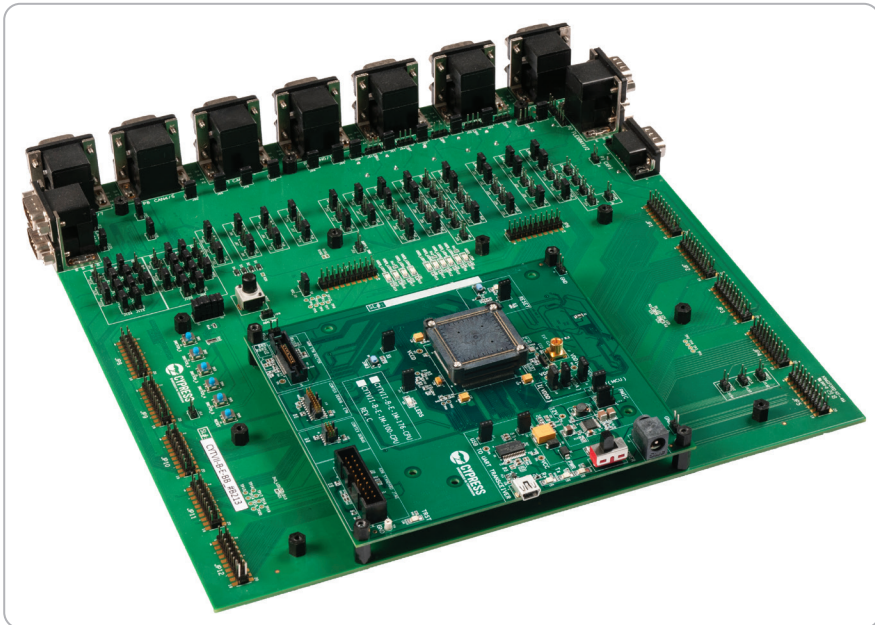

4

- Connect an appropriate programming tool (IAR/GHS/ iSystem/Lauterbach), to one of the debug interfaces – J17, J18, J19, J20

Use an appropriate IDE on a PC, to load a firmware HEX file into the device flash, successfully. The LED Blink firmware is available in the Release-Package.

TRAVEO II CPU BOARD

Instructions:

- The Traveo II CPU Board, can be used standalone to evaluate limited features of the Traveo II MCU.
- The board can be used with the Traveo II Body Controller Base Board, to evaluate all features of the MCU.

- To know more about the Base Board and compatibility, please use the Cypress Customer Support Portal. (cypress.com/support)
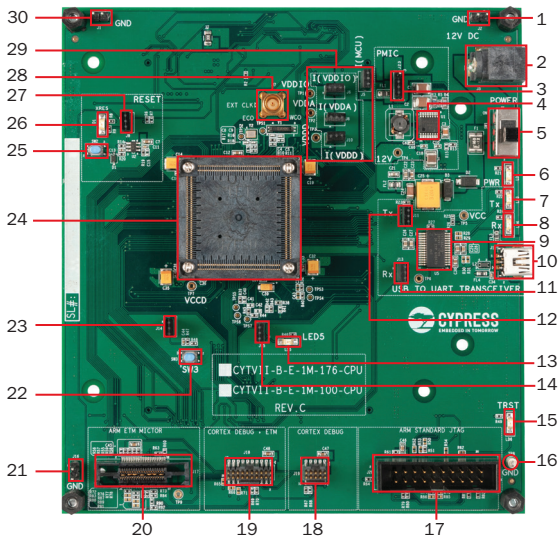
For the following documentation:

- User Guide
- Schematic
- Sample Driver Library (SDL)
- Software examples

Please use the Cypress Customer Support Portal.

TRAVEO II CPU BOARD

Traveo II CPU Board Details

1. J2 - Jumper for GND Probe	16. TP8 – GND Test Point
2. Power Jack (12 V)	17. 20-Pin IDC – Arm Standard JTAG Connector
3. J23 – Jumper for PMIC/LDO Connect	18. MIPI-10 – Cortex Debug Connector
4. Cypress PMIC (5V)	19. MIPI-20 – Cortex Debug + ETM Connector
5. SW1 – Power Switch	20. Mictor-38 – Arm ETM Mictor Connector
6. Power LED	21. J16 – Jumper for GND Probe
7. UART-USB Tx LED	22. SW3 – User Button
8. UART-USB Rx LED	23. J14 – Jumper for User Button
9. Cypress USB-Serial Controller	24. U4 – Cypress Traveo II MCU
10. USB Type-B Mini Connector	25. MCU Reset Button (SW2)
11. J13 – Jumper for UART-USB Rx	26. Reset LED (LD1)
12. J11 – Jumper for UART-USB Tx	27. J9 – Jumper for Reset Controller
13. LED5 – User LED	28. External Clock Input SMA Connector
14. J15 – Jumper for LED5	29. MCU Power Measurement Jumpers
15. LD6 – MCU TAP Controller Reset LED	30. J1 - Jumper for GND Probe