

PRIMERGY RX4770 M1

System configurator and order-information guide

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PRIMERGY Server

Instructions

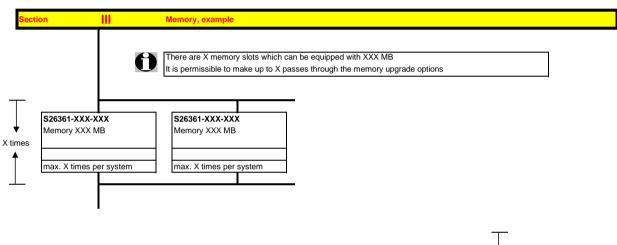
This document contains basic product and configuration information that will enable you to configure your system via PC-/System-Architect

Only these tools will ensure a fast and proper configuration of your PRIMERGY server or your complete PRIMERGY Rack system.

You can configure your individual PRIMERGY server in order to adjust your specific requirements.

The System configurator is divided into several chapters that are identical to the current price list and PC-/System-Architect.

Please follow the lines. If there is a junction, you can choose which way or component you would like to take. Go through the configurator by following the lines from the top to the bottom.



In one chapter you can only select as many components (here 4x) as the arrow indicates.



Please note that there are information symbols which indicate necessary information.



Further information in the internet see:

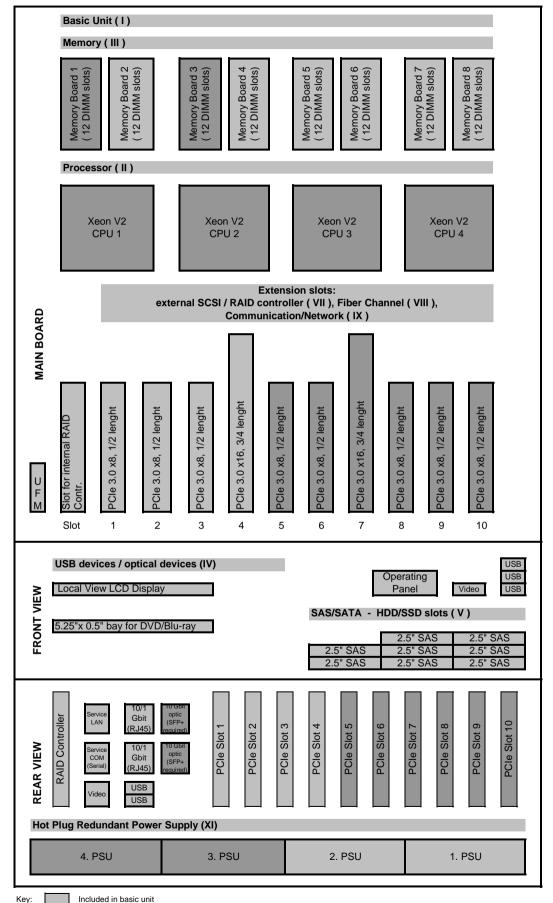
http://ts.fujitsu.com/products/standard_servers/index.html

(internet)

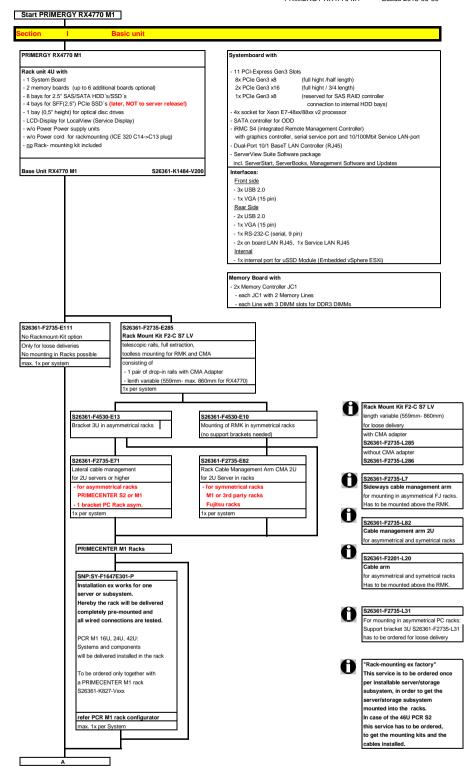
https://partners.ts.fujitsu.com/com/order-supply/configurators/primergy_config/current/P (extranet)

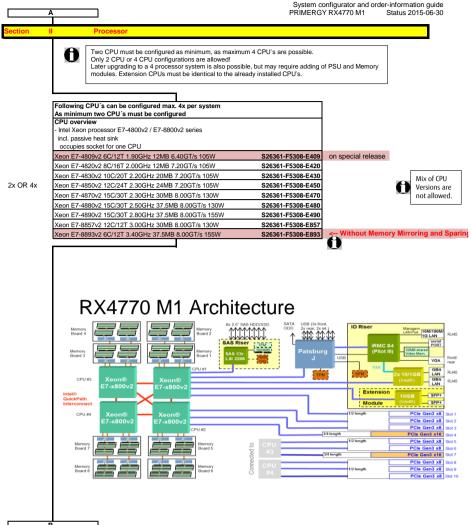
Prices and availability see price list and PC-/System-Architect. Subject to change and errors excepted.

Configuration diagram PRIMERGY RX4770 M1



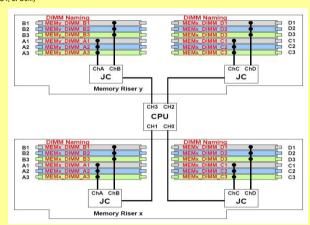
Option







Basically DIMM population orders:
DIMM population order for each CPU is done by DIMM pair (2x DIMMSs) using Round Robin scheme (e.g. CPU1, CPU2, CPU3, CPU4. CPU1...)



- BTO Order for DIMM population (DIMM installation order)

 DIMM population limitation in platform is listed as below.

 Mixing of LRDIMM with any other DIMM type is not allowed per platform.

 Mixing of DDR3 voltages is not validated within a socket or across sockets. (If 1.35V (DDR3L) and 1.50V (DDR3) DIMMs are mixed, the DIMMs will run at 1.50V.)
- Mixing of DDR3 operating frequencies is not validated within a socket or across. (If DIMMs with different frequencies are mixed, all DIMMs will run at the common lowest frequency.)
- A maximum of 8 logical ranks (ranks seen by the host) per channel is allowed. Mixing of Independent and Lockstep channel mode is not allowed per platform.
- Mixing of Non-Mirrored and Mirrored mode is not allowed per platform.
- Mixing of Sparing and Non-Sparing mode is not allowed per platform

Memory configuration modes with minimum need of DIMMs per CPU and further Stepping (see Population #):

Population #	SMI2 Channel Mode	Memory Mirror Mode		DIMM population #
1	Independent mode	Off	Off	2
2	Independent mode	On	Off	2
3	Independent mode	Off	On	4
4	Lockstep mode	Off	Off	4
5	Lockstep mode	On	Off	4
6	Lockstep mode	Off	On	8

Server Rules for Combination CPU and Memory Boards:

- A minimum of one Memory Board per each CPU must be populated.

 If two Memory Boards per CPU will be used, each CPU have to be populated with two Memory Boards.

 Following Configurations of CPU and Memory Boards per Server are possibble:
- -- Two CPU with two Memory Boards. -- Two CPU with four Memory Boards.
- --- Four CPLI with four Memory Boards
- --- Four CPU with eight Memory Boards

Memory population Table:

SMI2 Channel modes are selectable in BIOS setup menu. Lockstep Mode is default Mode if HW configuration makes it possible.

Performance Mode can be switched on.

Lockstep Mode:

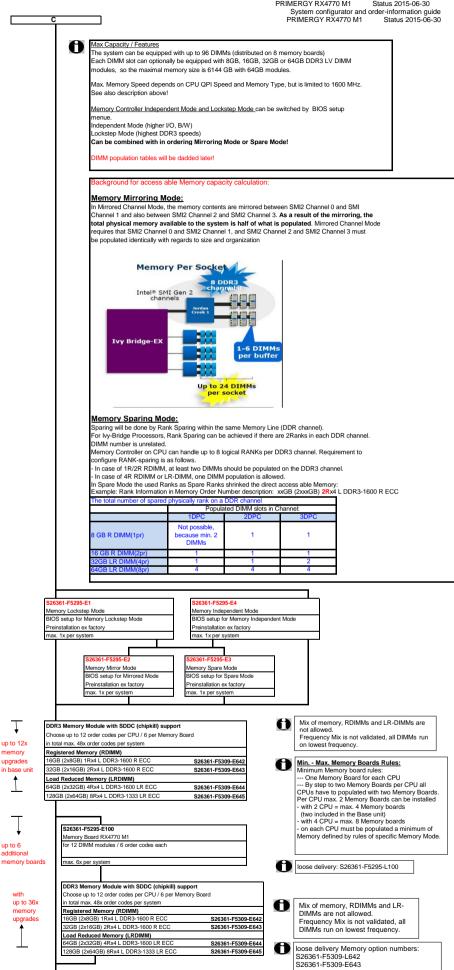
- Lockstep --> delault mode, parallel Thrughput to both MEM Lines of one SMI2.
 Bus frequency ratio SMI2 to MEM Line is 1:1
- Max. SMI2 frequency are 2666 MHz, means in Lockstep-Mode **1600 MHz DIMMs can also used with 1600 MHz.**Memory Interlieving functionate only via 2 level, required for interleaving are same Memory capacity on DDR channels.

- Mirror Mode and Sparing Mode can be combined.

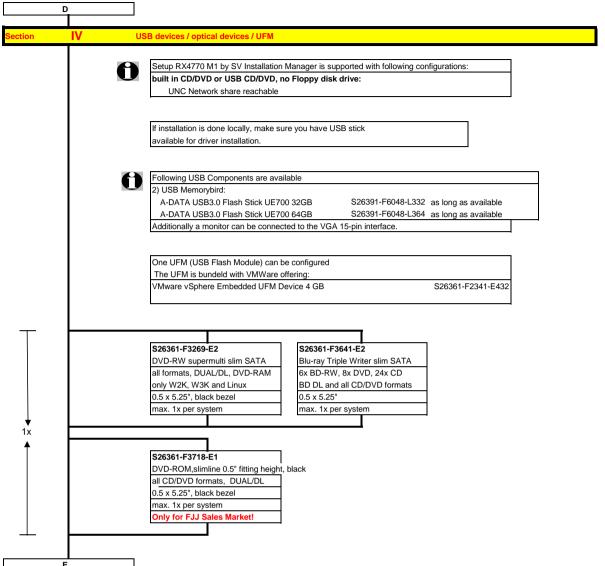
 In Lockstep Channel Mode, each memory access is a 128-bit data access that spans Channel 0 and Channel 1, and Channel 2 and Channel 3 beckstep Channel Mode, each memory access is a 128-bit data access that spans Channel 0 and Channel 1, and Channel 2 and Channel 3 beckstep Channel Mode requires that Channel 0 and Channel 1, and Channel 2 and Channel 3 must be populated identically with regards to size and organization. DIMM slot populations within a channel do not have to be identical but the same DIMM slot location across Channel 0 and Channel 1 and across Channel 2 and Channel 3 must be populated the same

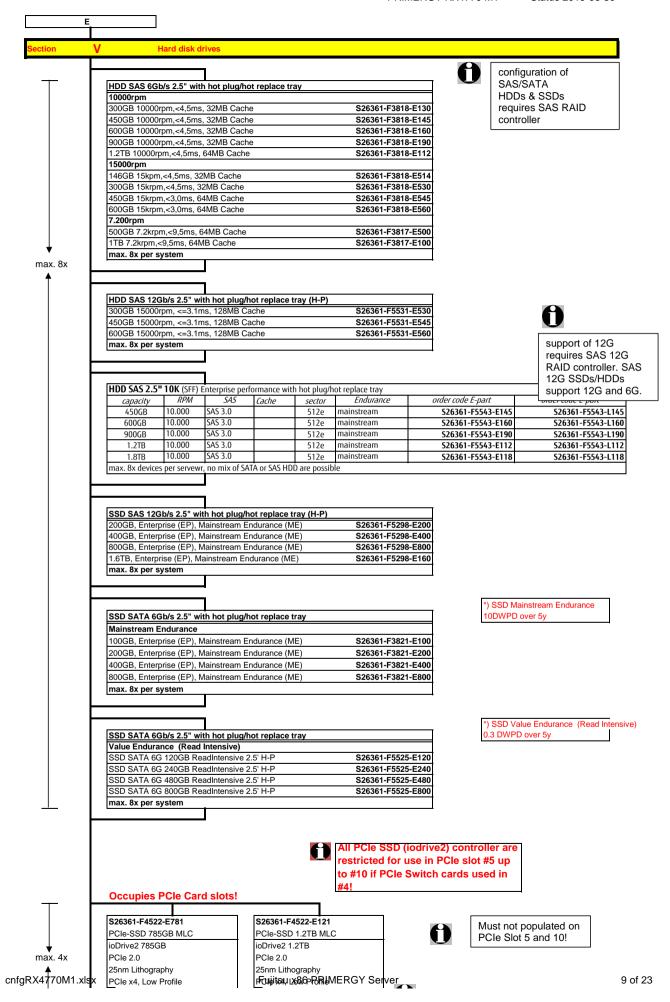
- Intel Independent Mode :
 Bus frequency ratio SMI2 to MEM Line is 2:1
- Max. SMI2 frequency are 2666 MHz, means in Independent -Mode fast 1600 MHz DIMMs can be used with Max. with 1333 MHz.
- Memory Interlieving functionate via all 3 memory level what will be result in faster memory troughput than Lockstep Mode, required for interleaving are same Memory capacity on DDR channels.
- Mirror Mode and Sparing Mode can be combined.
 Channels can be populated in any order in Independent Channel Mode. All four channels may be populated in any order and the combined in an nts. All DDR channels must run at the same interface frequency but individual channels may run at different DIMM timings (RAS latency, CAS latency, and so forth) Independent Channel mode allows SDDC.

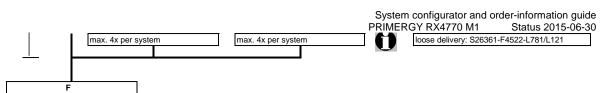
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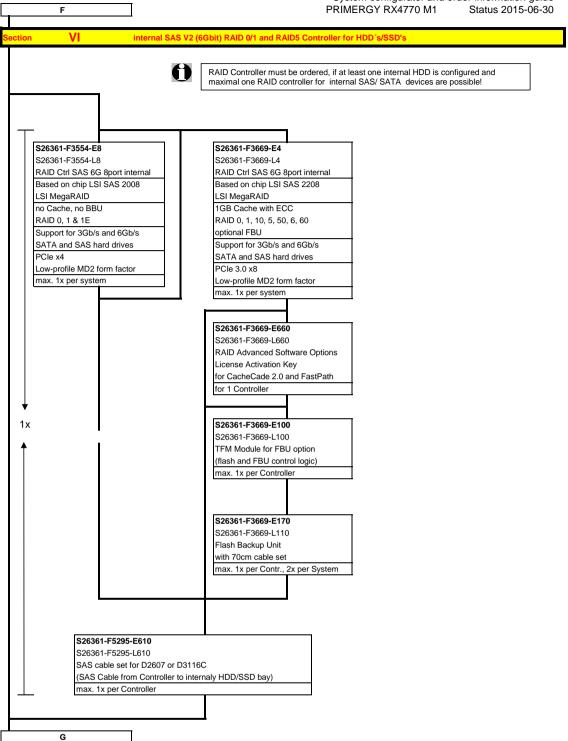


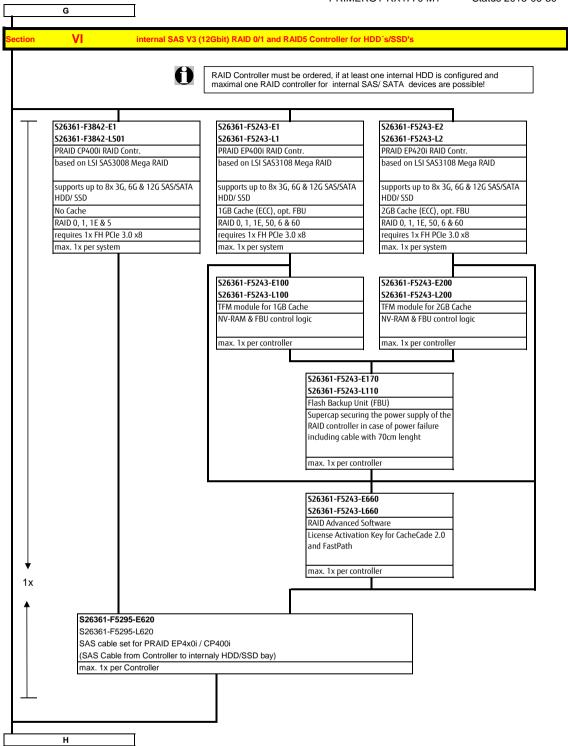
S26361-F5309-F644 S26361-F5309-E645





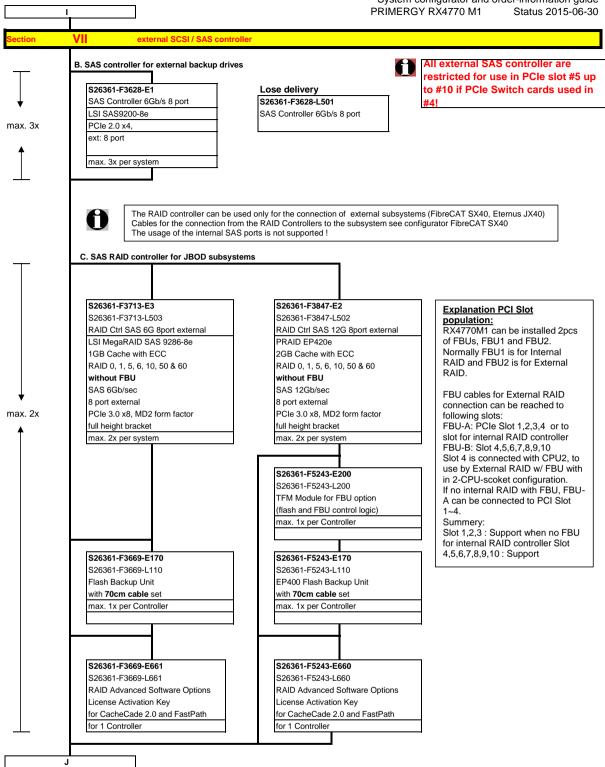


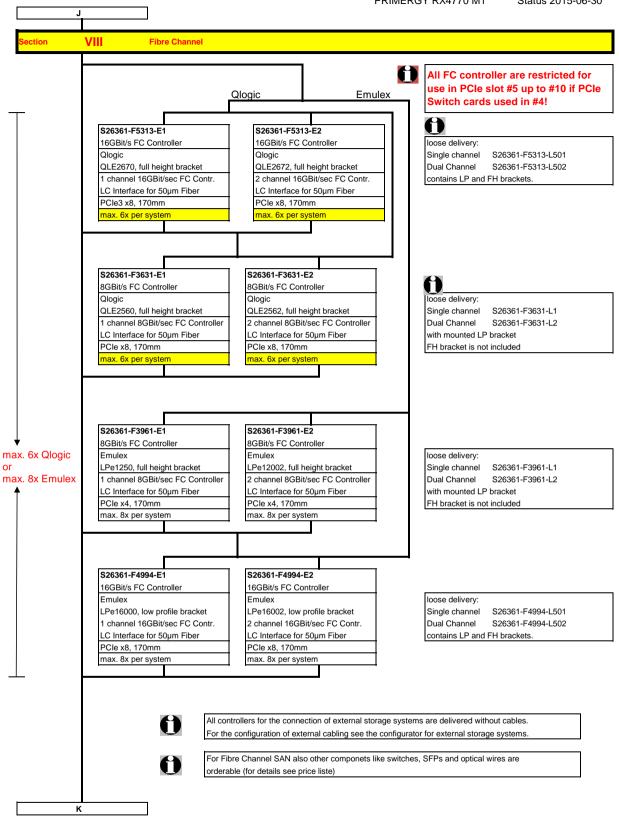


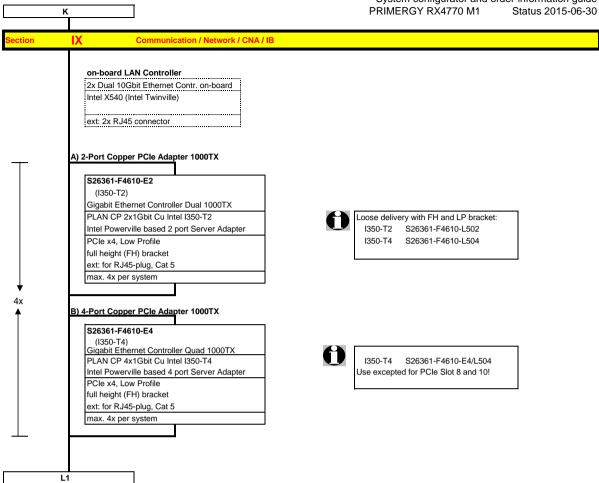


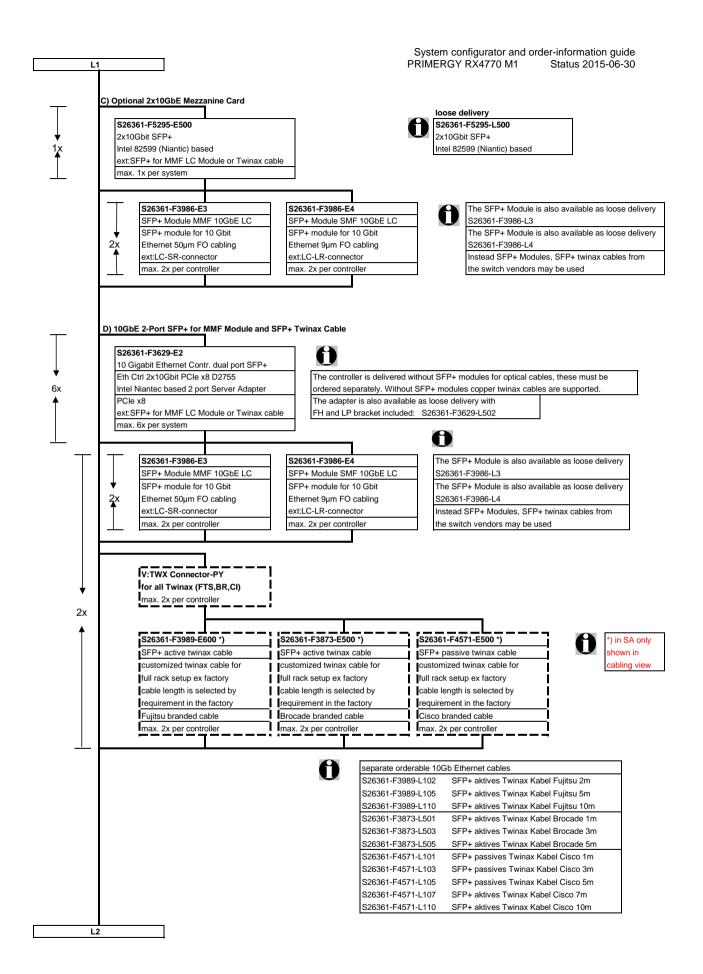
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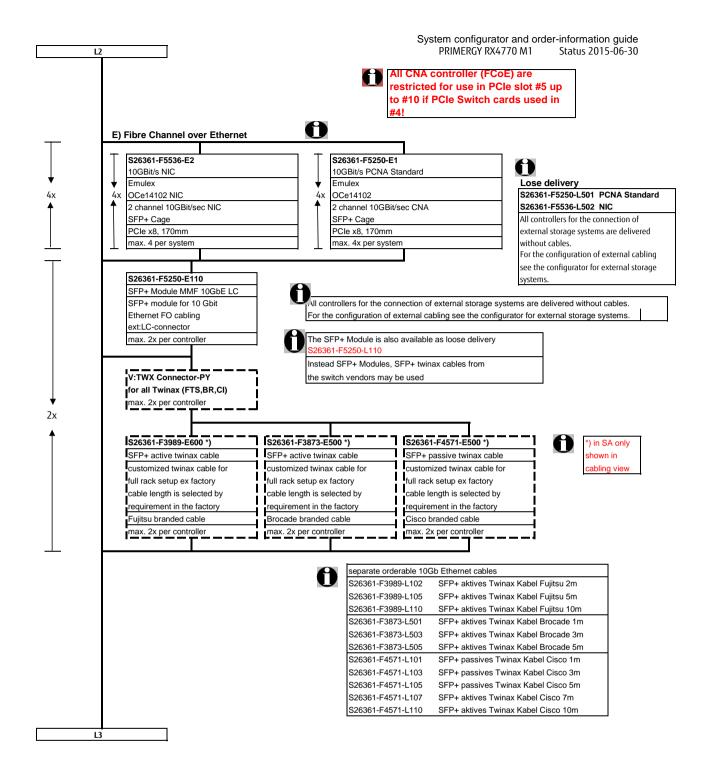
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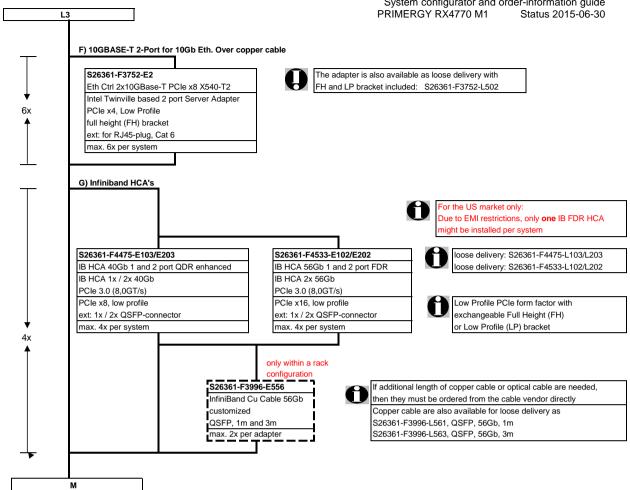












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Section

System Management Products (RemoteView) iRMC S2 advanced pack / TPM

S26361-F1790-E243

S26361-F1790-L244

iRMC S4 advanced pack

integrated remote management controller activation key for

graphical console redirection

and remote media redirection

max. 1x per system

iRMC S4 (integrated Remote Management Controller) onboard server management Controller with dedicated 10/100/1000 Service LAN-port and integrated graphics.



Executing system updates, controlling the hardware setup or running diagnostic tests on components are frequent tasks of IT administrators to ensure a continuous 24x7 server operation. ServerView embedded Lifecycle Management (eLCM) for Fujitsu PRIMERGY servers greatly supports such routine tasks by consolidating and enhancing management functions directly available ("embedded") within the server.

S26361-F1790-E310

embedded Lifecycle Management (eLCM)

Server Online Update

OS driver Update

Hardware firmware update

Server Offline Update

Hardware update via Update Manager Express

PrimeCollect

Autonomous creation of Primecollect archives

Creation and use of PrimeCollect archives over AIS connect

Custom Image (Jukebox function)

Automatic and manual download of CD and DVD Images

Automatic and manual start of CD and DVD Images

max. 1x per system



Loose delivery

eLCM Activation Pack

(Node Locked License)

BDL:ELCM-PACK

options contains: - 16GB SD card

- Paper with TAN for Licensekey

S26361-F3552-E5

TPM Module

Trusted Platform Module on Motherboard Use according to import restrictions

max. 1x per system



Be aware of import restrictions! Loose delivery for later integration possible for customer.

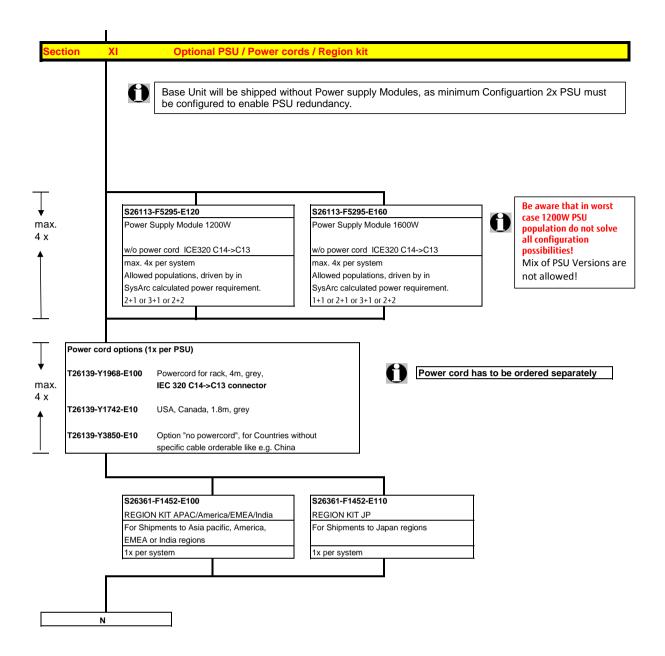
S26361-F3552-L5

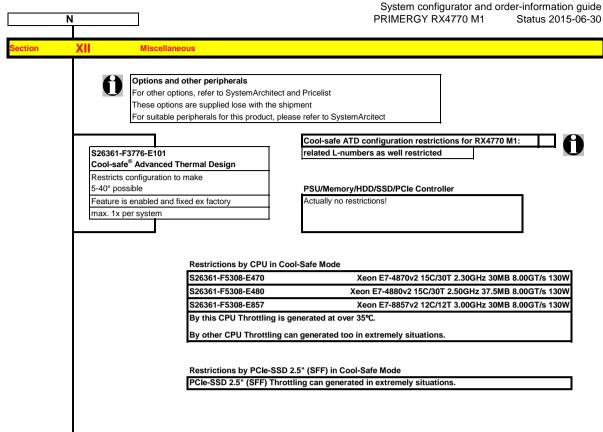
TPM Module add-on kit

for later integration (loose delivery)

Trusted Platform Module on Motherboard

Use according to import restrictions max. 1x per system





End PRIMERGY RX4770 M1

Change Report

Date	Order number	Changes		
2014-08-26	S26361-F5543-E118	Added 2.5" HDD SAS 12G 10Krpm 512e 1.8TB		
2014-08-26	S26361-F5531-E5*	Added 2.5" HDD SAS 12G 15Krpm 300GB, 450GB, 600GB		
2014-07-30	S26361-F2735-E82	defined as "can"-position to RMK		
2014-07-08	S26361-F5308-E409	Updated CPU spec for E7-4809 v2		
2014-06-24	S26361-F5308-E420	Xeon E7-4820v2 no longer special release		
2014-05-20	S26361-F5308-E409	Added another 6 Core CPU on special release (Xeon E7-4809v2)		
2014-04-03		Corrected Basic Unit order number S26361-F4530-E10 and S26361-F4530-E13		
2014-03-31		First Version		