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KA34063A.

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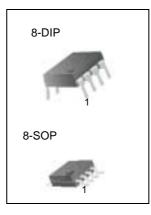
KA34063A SMPS Controller

Features

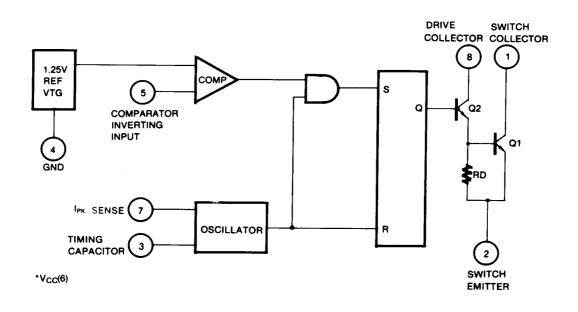
- Operation From 3.0 to 40V Input
- Short Circuit Current Limiting
- Low Standby Current
- Output Switch Current of 1.5A Without External Transistors
- Output Voltage Adjustable
- Frequency Of Operation From 100Hz to 100KHz
- Step-Up, Step-Down or Inverting Switching Regulators

Description

The KA34063A is a monolithic regulator sub System intended for use as DC to DC converter. This device contains a temperature compensated bandgap reference, a dutycycle control oscillator, driver and high current output switch. It can be used for step down, step-up or inverting switching regulators as well as for series pass regulators.



Internal Block Diagram



Absolute Maximum Ratings

Parameter	Symbol	Value	Unit
Supply Voltage	Vcc	40	V
Comparator Input Voltage Range	VI(COMP)	- 0.3 ~ + 40	V
Switch Collector Voltage	VC(SW)	40	V
Switch Emitter Voltage	VE(SW)	40	V
Switch Collector To Emitter Voltage	VCE(SW)	40	V
Driver Collector Voltage	VC(DR)	40	V
Switch Current	ISW	1.5	А

Electrical Characteristics

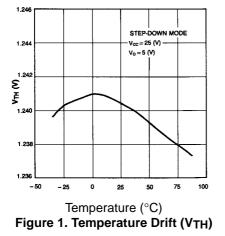
 $(V_{CC} = 5.0V, T_A = 0^{\circ}C \text{ to } +70^{\circ}C, \text{ unless otherwise specified})$

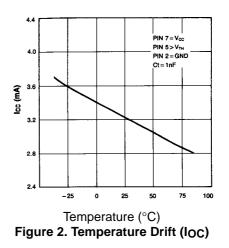
Parameter	Symbol	Conditions	Min.	Тур.	Max.	Unit
OSCILLATOR				-		
Charging Current	ICHG	V _{CC} = 5 to 40V T _A = 25°C	22	31	42	μA
Discharging Current	IDISCHG	V _{CC} = 5 to 40V T _A = 25°C	140	190	260	μΑ
Oscillator Amplitude	V(OSC)	TA = 25°C		0.5	-	V
Discharge To Charge Current Ratio	к	$V_7 = V_{CC}$ $T_A = 25^{\circ}C$	5.2	6.1	7.5	-
Current Limit Sense Voltage	VSENSE(C.L)	ICHG = IDISCHG T _A = 25°C	250	300	350	mV
OUTPUT SWITCH			•			
Saturation Voltage 1 (Note)	VCE(SAT)1	I _{SW} = 1.0A V _C (driver) = V _C (SW)	-	0.95	1.3	V
Saturation Voltage 2 (Note)	VCE(SAT)2	I _{SW} = 1.0A, V _C (driver) = 50mA	-	0.45	0.7	V
DC Current Gain (Note)	GI(DC)	I _{SW} = 1.0A, V _{CE} = 5.0V, T _A = 25°C	50	180	-	-
Collector off State Current (Note)	IC(OFF)	VCE = 40V, TA = 25°C	-	10	100	nA
COMPARATOR	L			•	•	
Threshold Voltage	VTH	-	1.21	1.24	1.29	V
Threshold Voltage Line Regulation	ΔVτη	VCC = 3 to 40V	-	2.0	5.0	mV
Input Bias Current	IBIAS	VI = 0V		50	400	nA
TOTAL DEVICE			•			
Supply Current	ICC	VCC = 5 to 40V CT = 0.001uF V7 = VCC, V5>VTH pin2 = GND	-	2.7	4.0	mA

Note :

Output switch tests are performed under pulsed conditions to minimize power dissipation

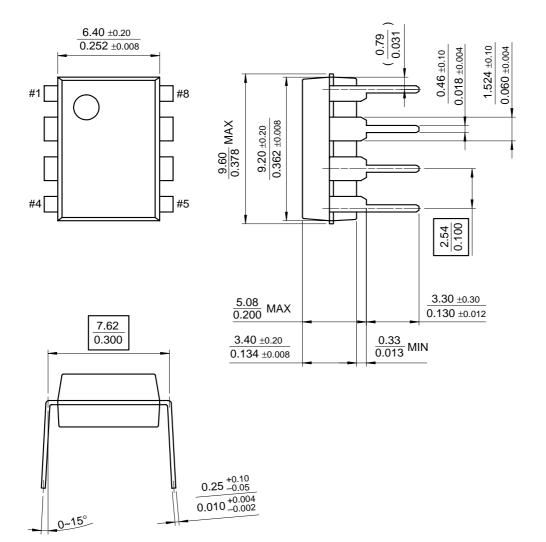
Typical Performance Characteristics





Mechanical Dimensions

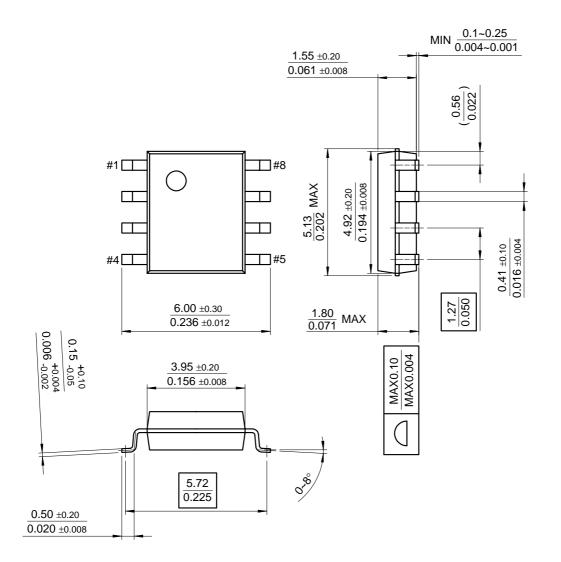
Package



8-DIP

Mechanical Dimensions (Continued)

Package



8-SOP

Ordering Information

Product Number	Package	Operating Temperature	
KA34063A	8-DIP	0 ~ + 70°C	
KA34063AD	8-SOP	0~+700	

KA34063A

KA34063A

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