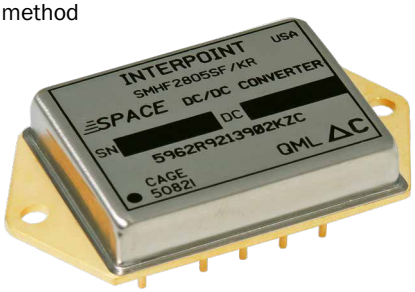
SMHF Single and Dual DC-DC Converters

16 TO 45 VOLT INPUT – 8 TO 15 WATT

FEATURES

- Radiation tolerant space dc-dc converter
  - Single event effects (SEE) LET performance to 86 MeV cm^2/mg
  - Total ionizing dose (TID) guaranteed per MIL-STD-883 method 1019, radiation hardness assurance (RHA)
    P = 30 krad(Si), L = 50 krad(Si), R = 100 krad(Si)
  - 50 - 300 rad(Si)/sec dose rate (Condition A)
  - 10 mrad(Si)/sec dose rate (Condition D)
- Operating temperature -55°C to +125°C
- Qualified to MIL-PRF-38534 Class H and K
- Input voltage range 16 to 45 volts
- Transient protection 50 volts for 50 ms
- Fully isolated
- Fixed high frequency switching
- Inhibit function,
- Synchronization input
- Indefinite short circuit protection
- Undervoltage lockout



MODELS OUTPUT VOLTAGE (V)	
SINGLE	DUAL
3.3	±5
5	±12
5.2	±15
12	
15	

DESCRIPTION

The Interpoint® SMHF Series™ of 28 volt dc-dc converters offers a wide input voltage range of 16 to 45 volts and up to 15 watts of output power. The units are capable of withstanding transients up to 50 volts for up to 50 ms.

SCREENING

SMHF converters offer screening to Class H or K and radiation hardness assurance (RHA) levels P - 30 krad(Si), L - 50 krad(Si) or R - 100 krad(Si). Single event effects (SEE) LET performance to 86 MeV cm^2/mg. See Table 9 on page 15 for more information.

CONVERTER DESIGN

The SMHF converters are switching regulators that use a quasi-square wave, single-ended forward converter design with a constant switching frequency of 550 kHz typical. Isolation between input and output circuits is provided with a transformer in the forward path and a temperature compensated opto-coupler in the feedback control loop. The opto-coupler is radiation tolerant and is especially selected for space applications.

Dual output models maintain cross regulation with tightly coupled output magnetics. Up to 70% of the total output power is available from either output, providing the opposite output is simultaneously carrying 30% of the total output power. Predictable current limit is accomplished by directly monitoring the output load current and providing a constant current output above the overload point.

The SMHF converter's feed-forward compensation system provides excellent dynamic response and noise rejection. Audio rejection is typically 50 dB.

Typical output voltage response for a 50% to 100% step load transient is as low as 1.8% with a 150 µs recovery time, typical. See Table 5 on page 5 for more information.

SMHF converters provide an inhibit terminal that can be used to disable internal switching, resulting in no output and very low quiescent input current. The converter is inhibited when the inhibit pin is pulled low. The unit is enabled when the pin, which is internally connected to a pull-up resistor, is left unconnected or is connected to an open-collector gate. See Table 5 on page 5 for more information.

The SMHF Series' synchronization feature allows the user to match the switching frequency of the converter to the frequency of the system clock. This allows the user to adjust the nominal 550 kHz operating frequency to any frequency within the range of 500 kHz to 600 kHz by applying a compatible input of the desired frequency to pin 5.

SMHF Series converters provide short circuit protection by restricting the output current to approximately 140% of the full load output current. The output current is sensed in the secondary stage to provide highly predictable and accurate current limiting, and to eliminate foldback characteristics.

Undervoltage lockout prevents the units from operating below approximately 14 volts input to keep system current levels smooth, especially during initialization or re-start operations.

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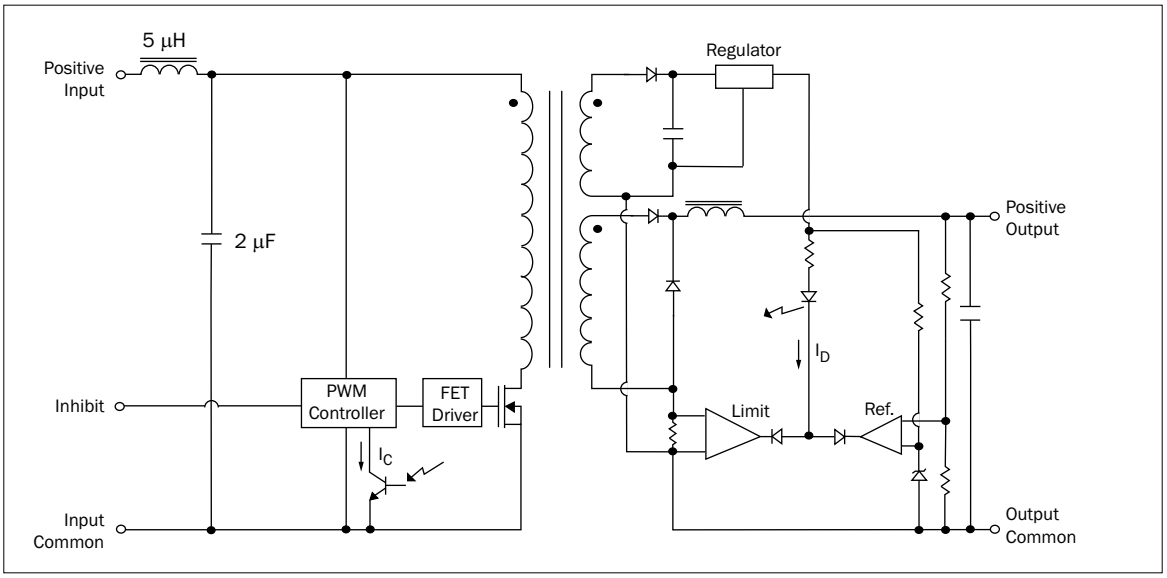

FIGURE 1: SMHF SINGLE OUTPUT, BLOCK DIAGRAM

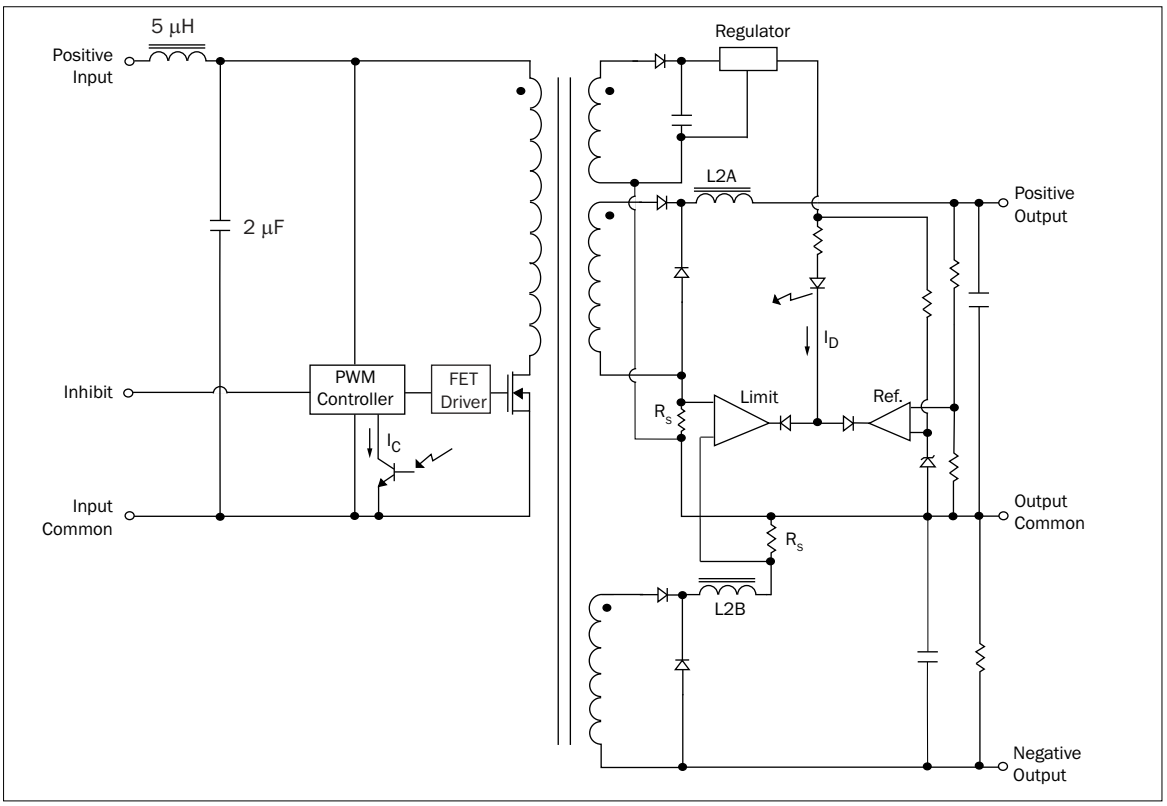

FIGURE 2: SMHF DUAL OUTPUT, BLOCK DIAGRAM

SMHF Single and Dual DC-DC Converters

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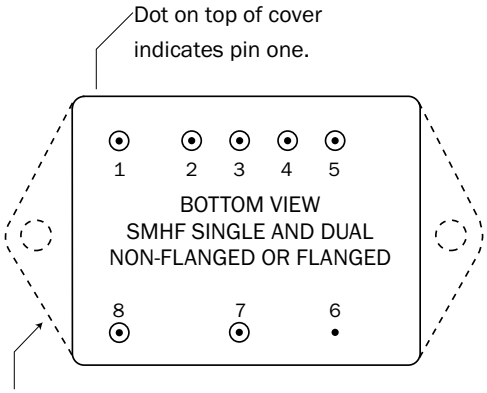
PIN OUT		
Pin	**Single Output**	**Dual Output**
1	Inhibit	Inhibit
2	No connection	Positive Output
3	Output Common	Output Common
4	Positive Output	Negative Output
5	Sync	Sync
6	Case Ground	Case Ground
7	Input Common	Input Common
8	Positive Input	Positive Input

TABLE 1: PIN OUT



See Figure 27 on page 13 and
Figure 28 on page 14 for dimensions.

FIGURE 3: PIN OUT

PINS NOT IN USE	
Inhibit (pin 1)	Leave unconnected
Sync (pin 5)	Connect to Input Common (pin 7)

TABLE 2: PINS NOT IN USE

SMHF Single and Dual DC-DC Converters

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MODEL NUMBERING KEY

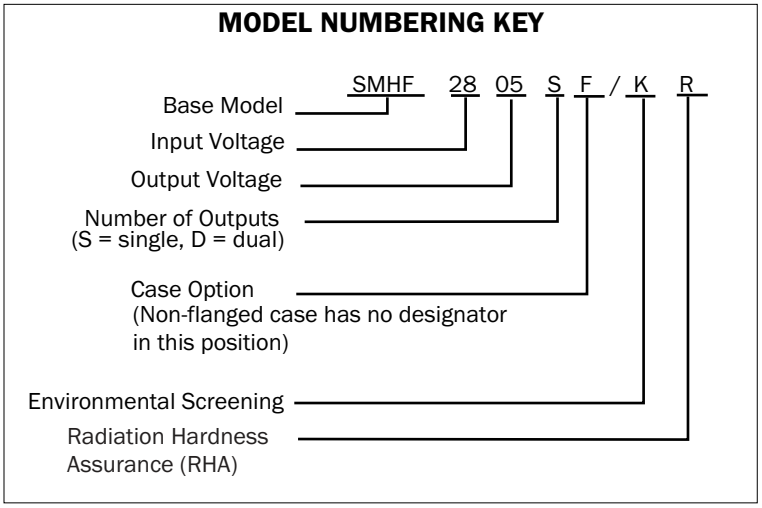

FIGURE 4: MODEL NUMBERING KEY

SMD NUMBERS	
STANDARD MICROCIRCUIT DRAWING (SMD)	**SMHF SIMILAR PART**
5962R0251002KXC	SMHF283R3S/KR
5962R9213902KXC	SMHF2805S/KR
5962R9213903KXC	SMHF285R2S/KR
5962R9166402KXC	SMHF2812S/KR
5962R9160102KXC	SMHF2815S/KR
5962R9555902KXC	SMHF2805D/KR
5962R9214402KXC	SMHF2812D/KR
5962R9161402KXC	SMHF2815D/KR

The SMD numbers shown are for RHA level R, screening level Class K, standard case (X), standard pin seal and non-solder dipped pins (C). For other options please refer to the SMD for the SMD number and the vendor similar number. All SMD numbers are listed on the SMD in the "Bulletin" which is the last page of the SMD. For exact specifications for an SMD product, refer to the SMD. SMDs can be downloaded from https://landandmaritimeapps.dla.mil/programs/smcr

TABLE 3: SMD NUMBER CROSS REFERENCE

MODEL NUMBER OPTIONS [1]

TO DETERMINE THE MODEL NUMBER ENTER ONE OPTION FROM EACH CATEGORY IN THE FORM BELOW.

CATEGORY	Base Model and Input Voltage	Output Voltage [2]	Number of Outputs [3]	Case Option [4]	Screening [5]	RHA [6]
OPTIONS	SMHF28	3R3, 05, 5R2, 12, 15	S	(non-flanged, leave blank)	O	O
		05, 12, 15	D	F (flanged)	H	P
					K	L
						R
FILL IN FOR MODEL #[7]	SMHF28	_____	_____	_____	/ _____	_____

Notes
1. See Figure 4 above for an example of a model number.
2. Output Voltage: An R indicates a decimal point. 3R3 is 3.3 volts out. The values of 3.3 and 5.2 are only available in single output models.
3. Number of Outputs: S is a single output and D is a dual output
4. Case Options: For the standard case (Figure 27 on page 13) leave the Case Option blank. For the flanged case option (Figure 28 on page 14), insert the letter F in the Case Option position.
5. Screening: A screening level of O is a space prototype and is only available with RHA O. See Table 9 on page 15 and Table 10 on page 16 for more information.
6. RHA: Interpoint model numbers use an "O" in the RHA designator position to indicate the "-" (dash) radiation hardness assurance level of MIL-PRF-38534, which is defined as "no RHA." RHA O is only available with screening level O. See Table 10 on page 16 for more information.
7. If ordering by model number add a "-Q" to request solder dipped leads (SMHF2805S-Q).

TABLE 4: MODEL NUMBER OPTIONS

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TABLE 5: OPERATING CONDITIONS - ALL MODELS, 25°C CASE, 28 VIN, UNLESS OTHERWISE SPECIFIED

SMHF SERIES		ALL MODELS			
PARAMETER	CONDITIONS	MIN	TYP	MAX	UNITS
LEAD SOLDERING TEMPERATURE [1]	10 SECONDS MAX.	—	—	300	°C
STORAGE TEMPERATURE [1]		-65	—	+150	°C
CASE OPERATING TEMPERATURE	FULL POWER	-55	—	+125	°C
	ABSOLUTE [1]	-55	—	+135	
DERATING OUTPUT POWER/CURRENT [1]	LINEARLY	From 100% at 125°C to 0% at 135°C			
ESD RATING [1, 2] MIL-PRF-38534, 3.9.5.8.2	MIL-STD-883 METHOD 3015 CLASS 1C	1000-1999			V
ISOLATION: INPUT TO OUTPUT, INPUT TO CASE, OUTPUT TO CASE [3]	@ 500 VDC AT 25°C	100	—	—	Megohms
UNDERVOLTAGE LOCKOUT [1]	V_{IN}	—	14	—	V
INPUT TO OUTPUT CAPACITANCE [1]		—	60	—	pF
CURRENT LIMIT [1, 4]	% OF FULL LOAD	—	140	—	%
AUDIO REJECTION [1]		—	50	—	dB
SWITCHING FREQUENCY	-55°C TO +125°C	480	550	620	kHz
SYNCHRONIZATION	INPUT FREQUENCY	500	—	600	kHz
	DUTY CYCLE [1]	40	—	60	%
	ACTIVE LOW	—	—	0.8	V
	ACTIVE HIGH [1]	4.5	—	5.0	
	REFERENCED TO	INPUT COMMON			
	IF NOT USED	CONNECT TO INPUT COMMON			
INHIBIT ACTIVE LOW (OUTPUT DISABLED) Do not apply a voltage to the inhibit pin [5]	INHIBIT PIN PULLED LOW	—	—	0.8	V
	INHIBIT PIN SOURCE CURRENT [1]	—	—	5	mA
	REFERENCED TO	INPUT COMMON			
INHIBIT ACTIVE HIGH (OUTPUT ENABLED)Do not apply a voltage to the inhibit pin [5]	INHIBIT PIN CONDITION	OPEN COLLECTOR OR UNCONNECTED			
	OPEN INHIBIT PIN VOLTAGE [1]	7.5	—	12	V

For mean time between failures (MTBF) contact Applications Engineering at powerapps@craneae.com or call 1 425-882-3100.

Notes
1. Guaranteed by characterization test and/or analysis. Not a production test.
2. Passes 1000 volts.
3. When testing isolation, input pins are tied together and output pins are tied together. They are tested against each other and against case. Discharge the pins before and after testing.
4. Dual outputs: The over-current limit will trigger when the sum of the currents from both outputs reaches 140% (typical value) of the maximum rated “total” current of both outputs.
5. An external inhibit interface should be used to pull the inhibit low or leave it floating. The inhibit pin can be left unconnected if not used.

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TABLE 6: ELECTRICAL CHARACTERISTICS: -55°C TO +125°C CASE, 28 VIN, 100% LOAD, UNLESS OTHERWISE SPECIFIED.

SINGLE OUTPUT MODELS		SMHF283R3S			SMHF2805S			SMHF285R2S			UNITS
PARAMETER	CONDITIONS	MIN	TYP	MAX	MIN	TYP	MAX	MIN	TYP	MAX	
OUTPUT VOLTAGE		3.20	3.30	3.40	4.85	5.00	5.15	5.05	5.20	5.35	V
OUTPUT CURRENT	V_{IN} = 16 TO 45 V	–	–	2.4	–	–	2.4	–	–	2.4	A
OUTPUT POWER	V_{IN} = 16 TO 45 V	0	–	8	0	–	12	0	–	12.5	W
OUTPUT RIPPLE	T_C = 25°C	–	5	30	–	5	30	–	5	30	mV p-p
10 kHz - 2 MHz	T_C = -55°C TO +125°C	–	5	30	–	5	30	–	5	30	
LINE REGULATION	V_{IN} = 16 TO 45 V	–	1	10	–	1	10	–	1	10	mV
LOAD REGULATION	NO LOAD TO FULL	–	20	50	–	20	50	–	20	50	mV
INPUT VOLTAGE	CONTINUOUS	16	28	45	16	28	45	16	28	45	V
NO LOAD TO FULL	TRANSIENT 50 ms [1]	0	–	50	0	–	50	0	–	50	
INPUT CURRENT	NO LOAD	–	25	50	–	25	40	–	25	40	mA
	INHIBITED	–	6	10	–	6	10	–	6	10	
INPUT RIPPLE CURRENT	10 kHz - 10 MHz	–	–	120	–	–	120	–	–	120	mA p-p
EFFICIENCY	T_C = 25°C	68	71	–	73	76	–	72	78	–	%
	T_C = -55°C TO +125°C	65	–	–	70	–	–	70	–	–	
LOAD FAULT [2, 3]	POWER DISSIPATION	–	5	8	–	3.5	8	–	3.5	8	W
SHORT CIRCUIT	RECOVERY [1]	–	7.5	30	–	7.5	30	–	7.5	30	ms
STEP LOAD RESPONSE [3, 4]	TRANSIENT	–	±150	±400	–	±150	±400	–	±150	±400	mV pk
50% - 100% - 50%	RECOVERY	–	150	300	–	150	300	–	150	300	µs
STEP LINE RESPONSE [1, 3, 5]	TRANSIENT	–	±550	±800	–	±550	±800	–	–	±800	mV pk
16 - 40 - 16 V	RECOVERY	–	200	500	–	200	500	–	–	600	µs
STEP LINE RESPONSE [1, 3, 5]	TRANSIENT	–	±70	–	–	±110	–	–	±110	–	mV pk
22 - 32 - 22 V	RECOVERY	–	200	–	–	180	–	–	180	–	µs
STEP LINE RESPONSE [1, 3, 5]	TRANSIENT	–	±100	–	–	±160	–	–	±160	–	mV pk
36 - 45 - 36 V	RECOVERY	–	250	–	–	180	–	–	180	–	µs
STARTUP [3, 6]	DELAY	–	10	25	–	10	25	–	10	25	ms
	OVERSHOOT [1]	–	15	50	–	15	50	–	15	50	mV pk
CAPACITIVE LOAD [7]	T_C = 25°C	–	–	300	–	–	300	–	–	300	µF

Notes
1. Guaranteed by characterization test and/or analysis. Not a production test.
2. Indefinite short circuit protection not guaranteed above 125°C (case)
3. Recovery time is measured from application of the transient to the point at which Vout is within regulation.
4. Step load test is performed at 10 microseconds typical.
5. Step line test is performed at 100 microseconds ± 20 microseconds
6. Tested on release from inhibit.
7. No effect on dc performance.

SMHF Single and Dual DC-DC Converters

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TABLE 7: ELECTRICAL CHARACTERISTICS: -55°C TO +125°C CASE, 28 VIN, 100% LOAD, UNLESS OTHERWISE SPECIFIED.

SINGLE OUTPUT MODELS		SMHF2812S			SMHF2815S			UNITS
PARAMETER	CONDITIONS	MIN	TYP	MAX	MIN	TYP	MAX	
OUTPUT VOLTAGE		11.76	12.00	12.24	14.70	15.00	15.30	V
OUTPUT CURRENT	V_{IN} = 16 TO 45 V	–	–	1.25	–	–	1.00	A
OUTPUT POWER	V_{IN} = 16 TO 45 V	0	–	15	0	–	15	W
OUTPUT RIPPLE	T_C = 25°C	–	15	40	–	10	40	mV p-p
10 kHz - 2 MHz	T_C = -55°C TO +125°C	–	15	40	–	10	40	
LINE REGULATION	V_{IN} = 16 TO 45 V	–	5	20	–	8	30	mV
LOAD REGULATION	NO LOAD TO FULL	–	20	50	–	20	50	mV
INPUT VOLTAGE	CONTINUOUS	16	28	45	16	28	45	V
NO LOAD TO FULL	TRANSIENT 50 ms [1]	0	–	50	0	–	50	
INPUT CURRENT	NO LOAD	–	25	55	–	25	62	mA
	INHIBITED	–	5	10	–	5	10	
INPUT RIPPLE CURRENT	10 kHz - 10 MHz	–	–	120	–	–	120	mA p-p
EFFICIENCY	T_C = 25°C	76	79	–	78	78	–	%
	T_C = -55°C TO +125°C	72	–	–	74	–	–	
LOAD FAULT [2, 3]	POWER DISSIPATION	–	3.5	8	–	3.5	8	W
SHORT CIRCUIT	RECOVERY [1]	–	7.5	30	–	7.5	30	ms
STEP LOAD RESPONSE [3, 4]	TRANSIENT	–	±150	±500	–	±200	±500	mV pk
50% - 100% - 50%	RECOVERY	–	50	300	–	50	300	µs
STEP LINE RESPONSE [1, 3, 5]	TRANSIENT	–	±550	±800	–	±550	±800	mV pk
16 - 40 - 16 V	RECOVERY	–	300	700	–	300	700	µs
STEP LINE RESPONSE [1, 3, 5]	TRANSIENT	–	±250	–	–	±250	–	mV pk
22 - 32 - 22 V	RECOVERY	–	210	–	–	210	–	µs
STEP LINE RESPONSE [1, 3, 5]	TRANSIENT	–	±350	–	–	±350	–	mV pk
36 - 45 - 36 V	RECOVERY	–	300	–	–	300	–	µs
STARTUP [3, 6]	DELAY	–	10	25	–	10	25	ms
	OVERSHOOT [1]	–	25	50	–	25	50	mV pk
CAPACITIVE LOAD [7]	T_C = 25°C	–	–	100	–	–	100	µF

Notes
1. Guaranteed by characterization test and/or analysis. Not a production test.
2. Indefinite short circuit protection not guaranteed above 125°C (case)
3. Recovery time is measured from application of the transient to the point at which Vout is within regulation.
4. Step load test is performed at 10 microseconds typical.
5. Step line test is performed at 100 microseconds ± 20 microseconds
6. Tested on release from inhibit.
7. No effect on dc performance.

SMHF Single and Dual DC-DC Converters

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TABLE 8: ELECTRICAL CHARACTERISTICS: -55°C TO +125°C CASE, 28 VIN, 100% LOAD, UNLESS OTHERWISE SPECIFIED.

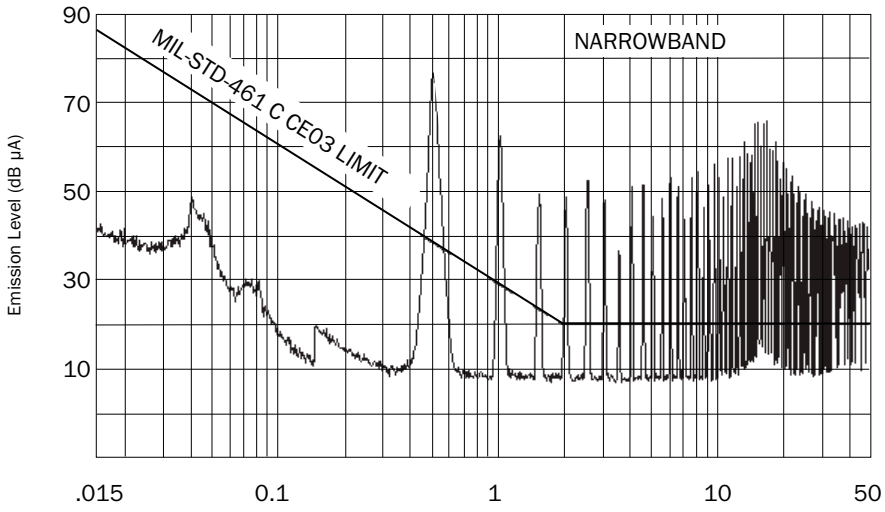
DUAL OUTPUT MODELS		SMHF2805D			SMHF2812D			SMHF2815D			UNITS
PARAMETER	CONDITIONS	MIN	TYP	MAX	MIN	TYP	MAX	MIN	TYP	MAX	
OUTPUT VOLTAGE	$+V_{OUT}$	4.85	5.00	5.15	11.76	12.00	12.24	14.70	15.00	15.30	V
	$-V_{OUT}$	4.82	5.00	5.18	11.70	12.00	12.30	14.63	15.00	15.38	
OUTPUT CURRENT [2]	EITHER OUTPUT	–	±1.2	1.68	–	±0.625	0.875	–	±0.5	0.7	A
V_{IN} = 16 TO 45 V	TOTAL	–	–	2.4	–	–	1.25	–	–	1.0	
OUTPUT POWER [2]	EITHER OUTPUT	–	±6	8.4	–	–	10.5	–	–	10.5	W
V_{IN} = 16 TO 45 V	TOTAL	–	–	12	–	–	15	–	–	15	
OUTPUT RIPPLE	T_C = 25°C	–	30	95	–	30	95	–	30	95	mV p-p
± V_{OUT}, 10 kHz - 2 MHz	T_C = -55°C TO +125°C	–	30	95	–	30	95	–	30	95	
LINE REGULATION [3]	$+V_{OUT}$	–	2	10	–	2	18	–	2	18	mV
V_{IN} = 16 TO 45 V	$-V_{OUT}$	–	10	100	–	10	100	–	10	100	
LOAD REGULATION [3]	$+V_{OUT}$	–	5	25	–	5	25	–	5	25	mV
NO LOAD TO FULL	$-V_{OUT}$	–	80	150	–	60	150	–	40	150	
CROSS REGULATION [4]	EFFECT ON $-V_{OUT}$	–	6	7.5	–	3	6	–	3	6	%
INPUT VOLTAGE	CONTINUOUS	16	28	45	16	28	45	16	28	45	V
NO LOAD TO FULL	TRANSIENT 50 ms [1]	–	–	50	–	–	50	–	–	50	
INPUT CURRENT	NO LOAD	–	25	50	–	30	50	–	30	50	mA
	INHIBITED	–	6	10	–	6	10	–	6	10	
INPUT RIPPLE CURRENT	10 kHz - 10 MHz	–	60	120	–	55	120	–	55	120	mA p-p
EFFICIENCY	T_C = 25°C	75	77	–	76	80	–	76	82	–	%
	T_C = -55°C TO +125°C	72	–	–	74	–	–	74	–	–	
LOAD FAULT [5]	POWER DISSIPATION	–	3	6	–	3	6	–	3	6	W
SHORT CIRCUIT	RECOVERY [1]	–	6	30	–	6	50	–	6	50	ms
STEP LOAD RESPONSE [6, 7, 8]	TRANSIENT	–	±200	±500	–	±300	±600	–	±300	±600	mV pk
50% - 100% - 50% Bal Loads	RECOVERY	–	90	400	–	90	400	–	90	400	µs
STEP LINE RESPONSE [1, 6, 9]	TRANSIENT	–	±500	±800	–	±500	±750	–	±550	±750	mV pk
± V_{OUT}, 16 - 40 - 16 V	RECOVERY	–	200	700	–	300	900	–	300	900	µs
STEP LINE RESPONSE [1, 6, 9]	TRANSIENT	–	±125	–	–	±160	–	–	±160	–	mV pk
± V_{OUT}, 22 - 32 - 22 V	RECOVERY	–	200	–	–	160	–	–	160	–	µs
STEP LINE RESPONSE [1, 6, 9]	TRANSIENT	–	±200	–	–	±250	–	–	±250	–	mV pk
± V_{OUT}, 36 - 45 - 36 V	RECOVERY	–	160	–	–	200	–	–	200	–	µs
STARTUP [6, 10]	DELAY	–	12	25	–	10	20	–	10	20	ms
	OVERSHOOT [1]	0	100	500	0	100	500	0	100	500	mV pk
CAPACITIVE LOAD [11, 12]	T_C = 25°C	–	–	47	–	–	10	–	–	10	µF

Notes
1. Guaranteed by characterization test and/or analysis. Not a production test.
2. Up to 70% of the total output power is available from either output providing the opposite output is simultaneously carrying 30% of the total output power. Each output must carry a minimum of 30% of the total output power in order to maintain regulation on the negative output.
3. Balanced loads.
4. Effect on $-V_{OUT}$ for the following conditions, percentages are of total power:
   $+P_O$ = 50%, $-P_O$ = 10%; $+P_O$ = 10%, $-P_O$ = 50%
   $+P_O$ = 70%, $-P_O$ = 30%; $+P_O$ = 30%, $-P_O$ = 70%
   All conditions are referenced to balanced loads of 50%/50%.
5. Indefinite short circuit protection not guaranteed above 125°C (case)
6. Recovery time is measured from application of the transient to point at which V_{OUT} is within regulation.
7. Response of either output with the opposite output held at half of the total output power.
8. Step load test is performed at 10 microseconds typical.
9. Step line test is performed at 100 microseconds ± 20 microseconds.
10. Tested on release from inhibit.
11. Applies to each output.
12. Does not effect dc performance.

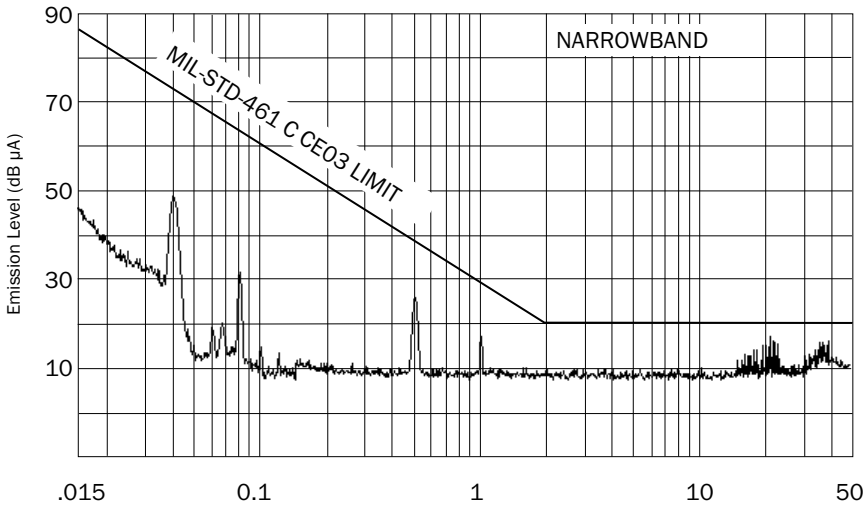
SMHF Single and Dual DC-DC Converters

16 TO 45 VOLT INPUT – 8 TO 15 WATT

TYPICAL PERFORMANCE PLOTS: 25°C CASE, 28 V_{IN}, 100% LOAD, FREE RUN, UNLESS OTHERWISE SPECIFIED.
These are examples for reference only and are not guaranteed specifications.



SMHF2805S, unfiltered,
Representative of the SMHF Series of DC-DC Converters
FIGURE 5



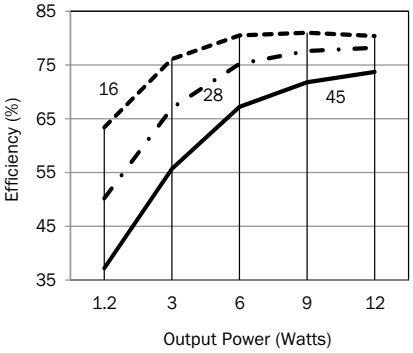
SMHF2805S with Interpoint SFMC28-461 EMI Filter,
Representative of the SMHF Series of DC-DC Converters

FIGURE 6

SMHF Single and Dual DC-DC Converters

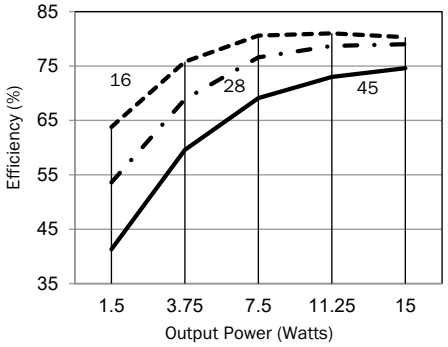
16 TO 45 VOLT INPUT – 8 TO 15 WATT

TYPICAL PERFORMANCE PLOTS: 25°C CASE, 28 V_{IN}, 100% LOAD, FREE RUN, UNLESS OTHERWISE SPECIFIED.
These are examples for reference only and are not guaranteed specifications.



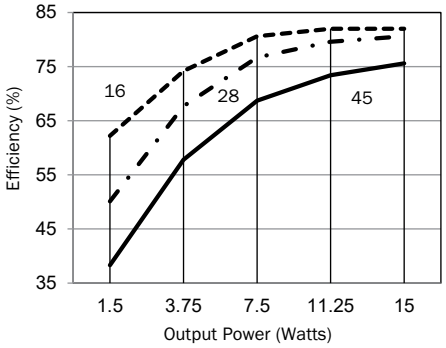
SMHF2805S Efficiency

FIGURE 7



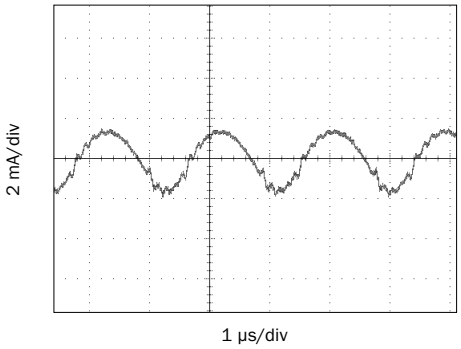
SMHF2812S Efficiency

FIGURE 8



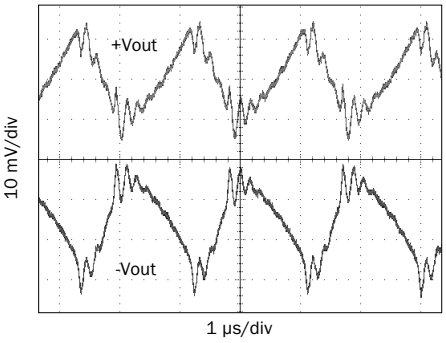
SMHF2815S Efficiency

FIGURE 9



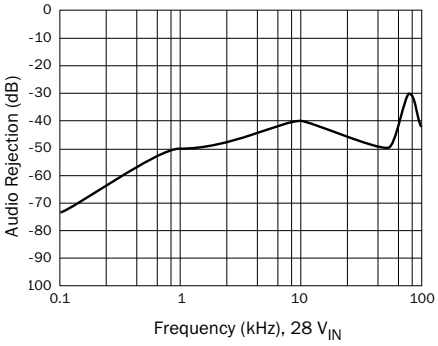
Representative of
SMHF Series I_{IN} Ripple

FIGURE 10



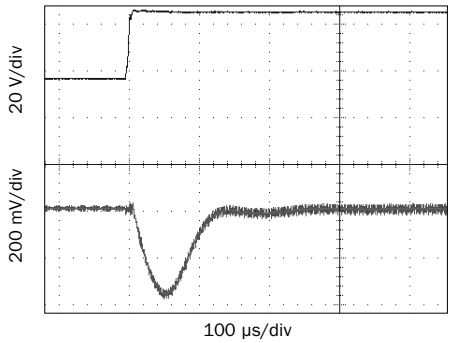
Representative of
SMHF Series V_{OUT} Ripple

FIGURE 11



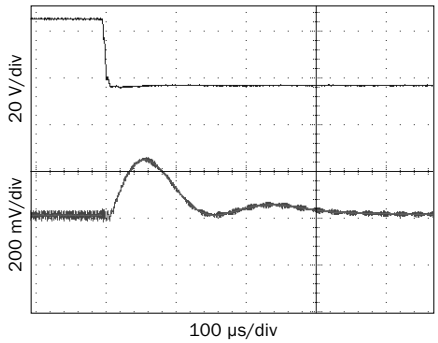
SMHF Series Single Audio Rejection

FIGURE 12



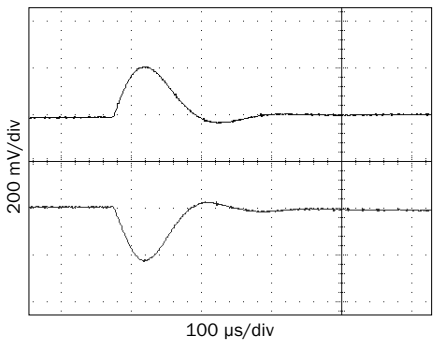
V_{IN} 16 to 45 V, full resistive load
SMHF2805S Representative of
Single Output Line Transient

FIGURE 13



V_{IN} 45 to 16 V, full resistive load
SMHF2805S Representative of
Single Output Line Transient

FIGURE 14



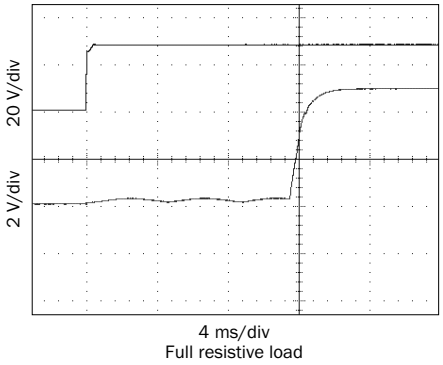
50 - 100 - 50% Load
SMHF2805S Representative of
Single Output Load Transient

FIGURE 15

SMHF Single and Dual DC-DC Converters

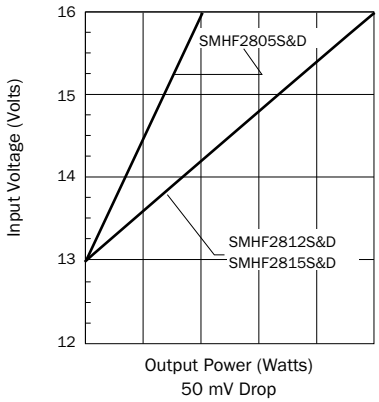
16 TO 45 VOLT INPUT – 8 TO 15 WATT

TYPICAL PERFORMANCE PLOTS: 25°C CASE, 28 V_{IN}, 100% LOAD, FREE RUN, UNLESS OTHERWISE SPECIFIED.
These are examples for reference only and are not guaranteed specifications.



SMHF2805S Representative of
Single Output Turn-On Delay

FIGURE 16



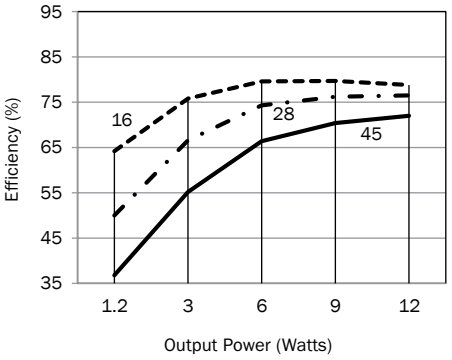
SMHF2805S Representative of
Single Output Low Line Dropout vs. Load

FIGURE 17

SMHF Single and Dual DC-DC Converters

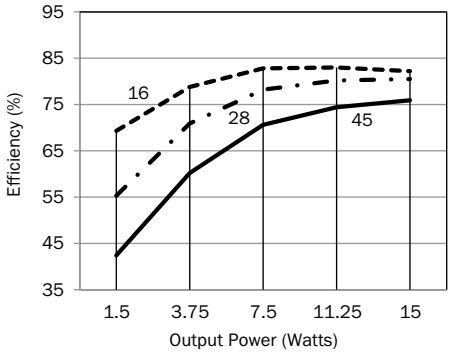
16 TO 45 VOLT INPUT – 8 TO 15 WATT

TYPICAL PERFORMANCE PLOTS: 25°C CASE, 28 V_{IN}, 100% LOAD, FREE RUN, UNLESS OTHERWISE SPECIFIED.
These are examples for reference only and are not guaranteed specifications.



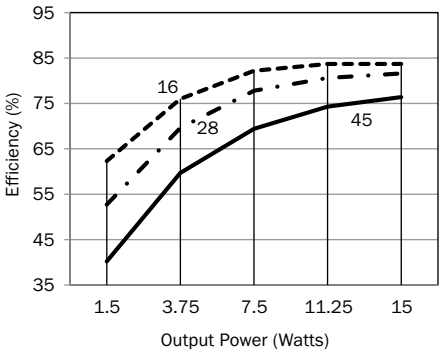
SMHF2805D Efficiency

FIGURE 18



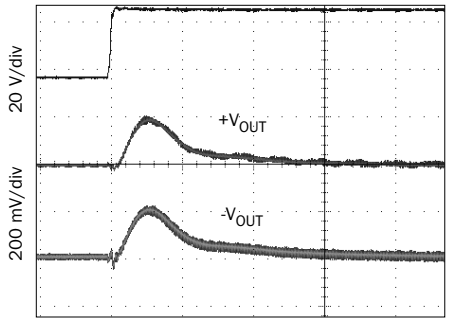
SMHF2812D Efficiency

FIGURE 19



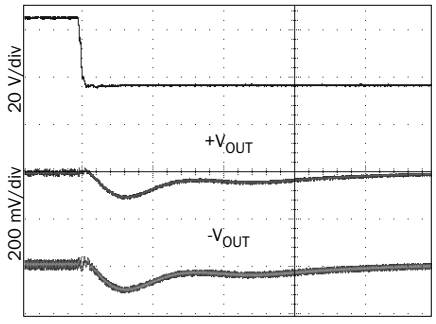
SMHF2815D Efficiency

FIGURE 20



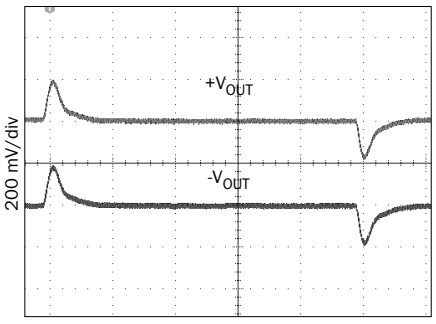
100 µs/div
V_{IN} 16 to 45 V, full resistive load
SMHF2815D Representative of
Dual Output Line Transient

FIGURE 21



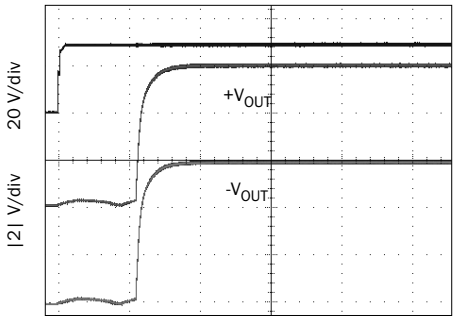
100 µs/div
V_{IN} 16 to 45 V, full resistive load
SMHF2815D Representative of
Dual Output Line Transient

FIGURE 22



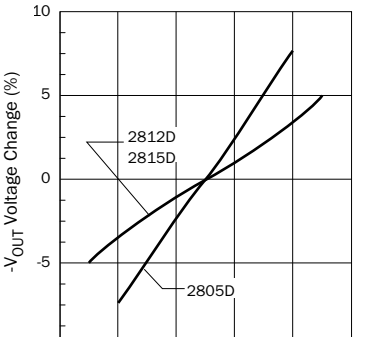
100 µs/div
50 - 100 - 50% Load
SMHF2815D Representative of
Dual Output Load Transient

FIGURE 23



4 ms/div
Full resistive load
SMHF2815D Representative of
Dual Output Turn-On Delay

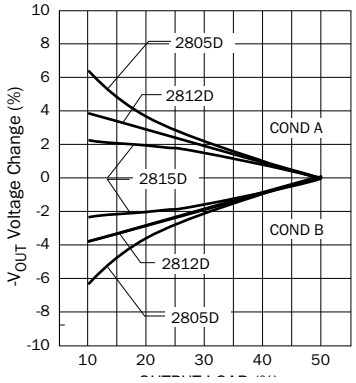
FIGURE 24



Positive Output Load (% of Total Load)
$-V_{OUT}$ with Shift in Load Balance

SMHF Dual Cross Regulation

FIGURE 25



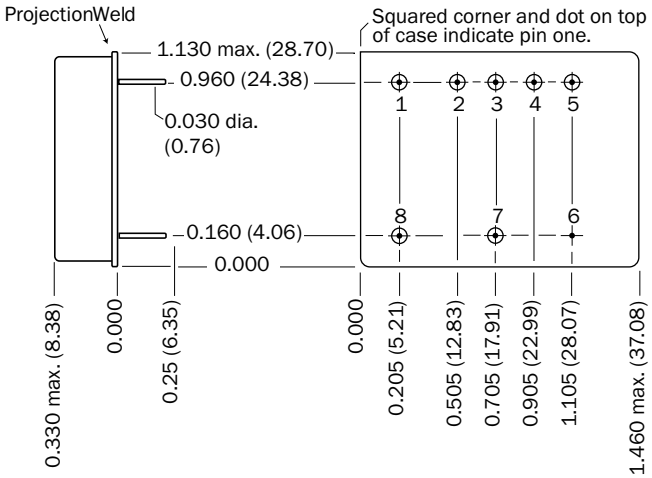
Condition A: +V 50% Load; -V 50% to 10% Load
Condition B: -V 50% Load; +V 50% to 10% Load
SMHF Dual Cross Regulation

FIGURE 26

SMHF Single and Dual DC-DC Converters

16 TO 45 VOLT INPUT – 8 TO 15 WATT

BOTTOM VIEW CASE E1



Weight: 30 grams maximum

Case dimensions in inches (mm)
Tolerance ±0.005 (0.13) for three decimal places
±0.01 (0.3) for two decimal places
unless otherwise specified

CAUTION
Heat from reflow or wave soldering may damage the device.
Solder pins individually with heat application not exceeding 300°C for 10 seconds per pin.

Materials
Header Cold Rolled Steel/Nickel/Gold
Cover Kovar/Nickel
Pins #52 alloy/Gold compression glass seal.
Gold plating of 50 - 150 microinches included in pin diameter
Seal Hole: 0.080 ±0.002 (2.03 ±0.05)

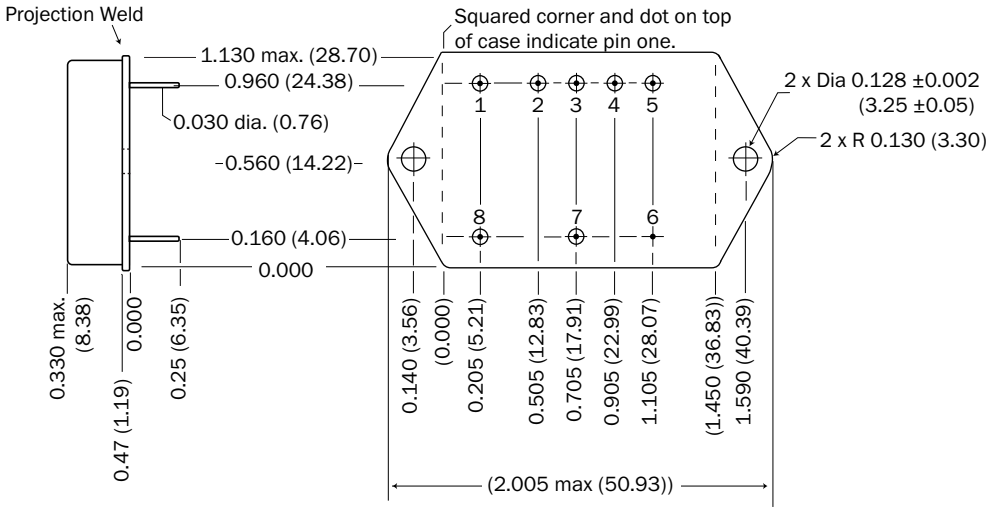
Please refer to the numerical dimensions for accuracy.

FIGURE 27: CASE E1

SMHF Single and Dual DC-DC Converters

16 TO 45 VOLT INPUT – 8 TO 15 WATT

BOTTOM VIEW CASE G1
Flanged cases: Designator "F" required in Case Option position of model number



Weight: 30 grams maximum

Case dimensions in inches (mm)
Tolerance ±0.005 (0.13) for three decimal places
±0.01 (0.3) for two decimal places
unless otherwise specified

CAUTION
Heat from reflow or wave soldering may damage the device. Solder pins individually with heat application not exceeding 300 °C for 10 seconds per pin.

Materials

Header	Cold Rolled Steel/Nickel/Gold
Cover	Kovar/Nickel
Pins	#52 alloy/Gold compression glass seal Gold plating of 50 - 150 microinches included in pin diameter Seal Hole: 0.080 ±0.002 (2.03 ±0.05)

Please refer to the numerical dimensions for accuracy.

FIGURE 28: CASE G1

SMHF Single and Dual DC-DC Converters

16 TO 45 VOLT INPUT – 8 TO 15 WATT

ELEMENT EVALUATION TABLES FOR QML PRODUCTS ARE IN "APP-009 QUALITY AND CERTIFICATION", APPENDIX A, IN COMPLIANCE WITH MIL-PRF-38534 REVISION L. (LINK HTTPS://WWW.CRANEAE.COM/QUALITY-ASSURANCE-MODULAR-POWER)

ENVIRONMENTAL SCREENING SPACE DC-DC CONVERTERS PROTOTYPE, CLASS H AND K

	NON-QML [1]	QML [2, 3]	
TEST PERFORMED	**PROTOTYPE (/O) [4]**	**CLASS H (/H)**	**CLASS K (/K)**
Non-destruct wire bond pull, Method 2023		■ [5]	■
Pre-cap Inspection, Method 2017, 2032	■	■	■
Temperature Cycle (10 times)			
Method 1010, Cond. C, -65°C to +150°C, ambient	■	■	■
Constant Acceleration			
Method 2001, 3000 g	■	■	■
PIND, Test Method 2020, Cond. A		■ [5]	■
Pre burn-in test, Group A, Subgroups 1 and 4	■	■ [5]	■
Burn-in Method 1015, +125°C case, typical [6]			
96 hours	■		
160 hours		■	
2 x 160 hours (includes mid-BI test)			■
Final Electrical Test, MIL-PRF-38534, Group A,			
Subgroups 1 and 4: +25°C case	■		
Subgroups 1 through 6, -55°C, +25°C, +125°C case		■	■
Hermeticity Test, Method 1014			
Gross Leak, Cond. B_2, Kr85			■
Gross Leak, Cond. C_1, fluorocarbon	■	■	
Fine Leak, Cond. B_1, Kr85			■
Fine Leak, Cond. A_2, helium	■	■	
Radiography, Method 2012			■
Post Radiography Electrical Test, +25°C case			■ [5]
Final visual inspection			
Method 2009 of MIL-STD-883		■	■
Magnification 1X [7]	■		

Test methods are referenced to MIL-STD-883 as determined by MIL-PRF-38534.

Notes
1. Non-QML prototype products may not meet all of the requirements of MIL-PRF-38534.
2. All processes are QML qualified and performed by certified operators.
3. Class H or K QML products that have no SMD number are marked "CHP, CHL, CHR, CKP, CKL or CKR" per MIL-PRF-38534, Table III instead of "QML".
4. "O" in the RHA designator position in Interpoint model numbers indicates DLA RHA "-" defined as no RHA.
5. Not required by DLA but performed to assure product quality.
6. Burn-in temperature designed to bring the case temperature to +125°C minimum. Burn-in is a powered test.
7. Visual inspection is performed per an internal document. Product may contain cosmetic irregularities such as dents, dings, scratches, etc. that do not affect form, fit or function.

TABLE 9: ENVIRONMENTAL SCREENING SPACE DC-DC CONVERTERS PROTOTYPE, CLASS H AND K

SMHF Single and Dual DC-DC Converters

16 TO 45 VOLT INPUT – 8 TO 15 WATT

SPACE RADIATION HARDNESS ASSURANCE DC-DC CONVERTERS CLASS H AND K, RHA [1] P, L AND R

	QML [2]					
	CLASS H			CLASS K		
QUALIFICATION PER MIL-STD	/HP	/HL	/HR	/KP	/KL	/KR
RHA P: 30 krad(Si) total dose [3, 4]	■			■		
RHA L: 50 krad(Si) total dose [3, 4]		■			■	
RHA R: 100 krad(Si) total dose [3, 4]			■			■
SEE, LET 86 MeV cm^2/mg [5]	■	■	■	■	■	■

Test methods are referenced to MIL-STD-883 as determined by MIL-PRF-38534.

Notes
1. DLA has approved the RHA plan for Interpoint power products. Our SMD products with RHA "P", "L" or "R" code meet DLA requirements.
2. Class H or K QML products that have no SMD number are marked "CHP, CHL, CHR, CKP, CKL or CKR" per MIL-PRF-38534, Table III instead of "QML".
3. Radiation sensitive components internal to the devices are procured with radiation guarantees or undergo radiation lot acceptance testing (RLAT) performed per condition A, method 1019 of MIL-STD-883.
4. Representative devices were initially High Dose Rate (HDR) tested using condition A of method 1019 of MIL-STD 883 to ensure RHA designator levels. Representative devices have also been Low Dose Rate (LDR) tested using condition D of method 1019 of MIL-STD-883 to the RHA designator levels. Representative devices will also be re-tested after design or process changes that can affect RHA response of this device.
5. Single event testing was performed on a converter to 86 MeV-cm^2/mg using 15 MeV/nucleon gold ions with no latch-up, burn-out, functional interrupts, or gate ruptures exhibited. Single event upsets (output voltage transients) may be present up to 86 MeV-cm^2/mg.

TABLE 10: SPACE RADIATION HARDNESS ASSURANCE DC-DC CONVERTERS CLASS H AND K, RHA P, L AND R

